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OPSP BIOS ENGINEERING

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DATE: June 21, 2000

TO: Intel Customer Support

SUBJECT: VC82010A.86A Standard BIOS, Production Release 11, Build 0031

About This Release

- VC82010A.86A.0031.P11.0006090855
- PXE v. 2.0 (build 74)

Features/Errata Fixed in This Release

P11-0031

- **Boot block needs to be updated for this release.**
- Changed the one shot reset workaround to always reset rather than only after identifying an error.
- Added workaround for RAMBUS initialization issue to S3 wake up.
- Banner change to P11-0031.
- CMOS map changed in this release.
- Added workaround for RAMBUS levelization issue.

P10-0028

- **Boot block needs to be updated for this release.**
- Fixed issue where board serial number did not show up in SMBIOS structures.
- Fixed issue where 850MHz Pentium® III processors were reported as 866MHz.
- Brought VC82010A.86A BIOS to latest core code.
- Standardized SIO function names.
- Using newer flash libraries (functionality should be the same).
- Fixed security driver for FWH.
- Removed unnecessary delays associated with an ATA master on primary channel and an ATAPI master on the secondary channel.
- Fixed int15 load BIOS Default path.
- Fixed the issue of invalid custom defaults being loaded in BIOS Setup even though no custom defaults have been saved previously. Updated Date and Time stamps.
- Added ability to do source level debugging
- Add Fast Table-Driven POST.
- Added support for higher frequency processors.
- Fixed the issue where invalid CMOS/GPNV error occurred after disabled LPT port from Windows* 95/98 APM mode system device manager.
- Added support for Simple Boot Flag specification version 1.0.
- Implement fix for issue with the Load Custom Default option.
- Fix for Fast Table-Driven POST - POST Timer.
- Added support for USB legacy for dual USB controllers.
- Implement Passwords Checksum fix to prevent phantom password issue.
- Implemented PARTIES support.
- Fixed issue where a floppy seek was occurring when Quick Boot was enabled in BIOS SETUP.
- Fixed issue where the SMBUS base address was not listed in an IO device node.
- Fixes SMDXREF issue where the number of type 17s do not match the total shown in Type 16.
- Resolved CMOS checksum issue with Intel specific INT15 which forces custom defaults to CMOS.
- Enhanced Quiet boot support to add custom BIOS functionality.
- Add POST Timer support.
- Fixed issues with mouse detection and high-frequency processors.

- • Fixed issue where BIOS incorrectly detects an option ROM on 3COM EtherLink* XL PCI cards that do not have an option ROM.
- Fixed DMITEST Failures where the SMBIOS Handle is incorrect for the Memory Structures.
- Fixed "Memory Error" with Adaptec* 3940 card.
- Added "Replaceable Module" support.
- Added support for additional processor steppings.

P09-0026
- This BIOS version was not released

P08-0022
- Resolved WHQL ACPI HCT issue regarding the SBTN lowest wakeable state.

P07-0020
- Added support for new, higher-frequency Intel® Pentium® III Processors.

P06-0019
- This version not released

P05-0018
- Added fix for WHQL Unreported I/O failure (9.5 HCT).
- Enabled Windows* 2000 system wake from the S4/Hibernate state via RTC alarm function.
- Added S4 OS sleep state support in the BIOS.
- Updated ACPI Creator Revision to 1.011.
- Added workaround for ICHx frozen RTC issue.
- Fixed issue where COM2 wasn't functional after S3.
- Optimized event log initialization and updates for faster POST execution. This reduces boot time.
- Fixed issue where Processor Serial Number comes up ENABLED in the SETUP when the board is booted for the very first time.
- Fixed issue where an incorrect Diagnostic LED display was occurring at boot-time.
- Added support for full duplex mode for the Serial Port B. Added Setup question for Half-Duplex/Full-Duplex.
- Fixed programming of PCI Subsystem Vendor & Device IDs for CS4281 audio.
- Modified the translation for "PCI SlotX IRQ priority" to fit the screen
- Corrected "Press F1 toRun SETUP" to "Press F1 to Run SETUP" error message following a CMOS checksum error.
- Updated processor support.
- Fixed Setup options which could not be changed with ABSCMOS.
- Disabled I2O boot device initialization.
- Fixed issues with Option ROMs not getting keypresses.
- Improve setup message handling.
- Fix stuck key handling to allow Setup entry key to be held down without generating keyboard error.
- Added support for multiple languages, including Double Byte Character Set(Kanji).
- Fixed Type 17 SMBIOS failure.
- Fixed hang when booting the Windows 98 CD on a SCSI CDROM, and selecting boot to hard drive when the hard drive is an IDE-HDD.
- Fixed hang when an Intel EtherExpress Pro 100 cards with Novell* BootWare is installed the system.
- Fixed ATAPI Zip* Drive not booting as HDD.
- Fixed issue that caused the LAN Setup option in Boot Order to show up blue instead of gray when the
- User enters Setup in Limited Mode.
- Fixed Wake On LAN* issue with certain network adapter.
- Added new speeds to the Maintenance mode "Processor Speed" pick list.
- Fixed issue that prevented the system from going into S3 when serial port loopbacks were installed.
- Changed the Setup default for the Video Memory Cache Mode option from "Write Combining" to "Uncached".

P04-0016

- Added fix for potential hot start issue during manufacturing.
- Updated support for Intel® Pentium® III Processors.
- Updated copyright string for year 2000.

P03-0014
- Initial production release.