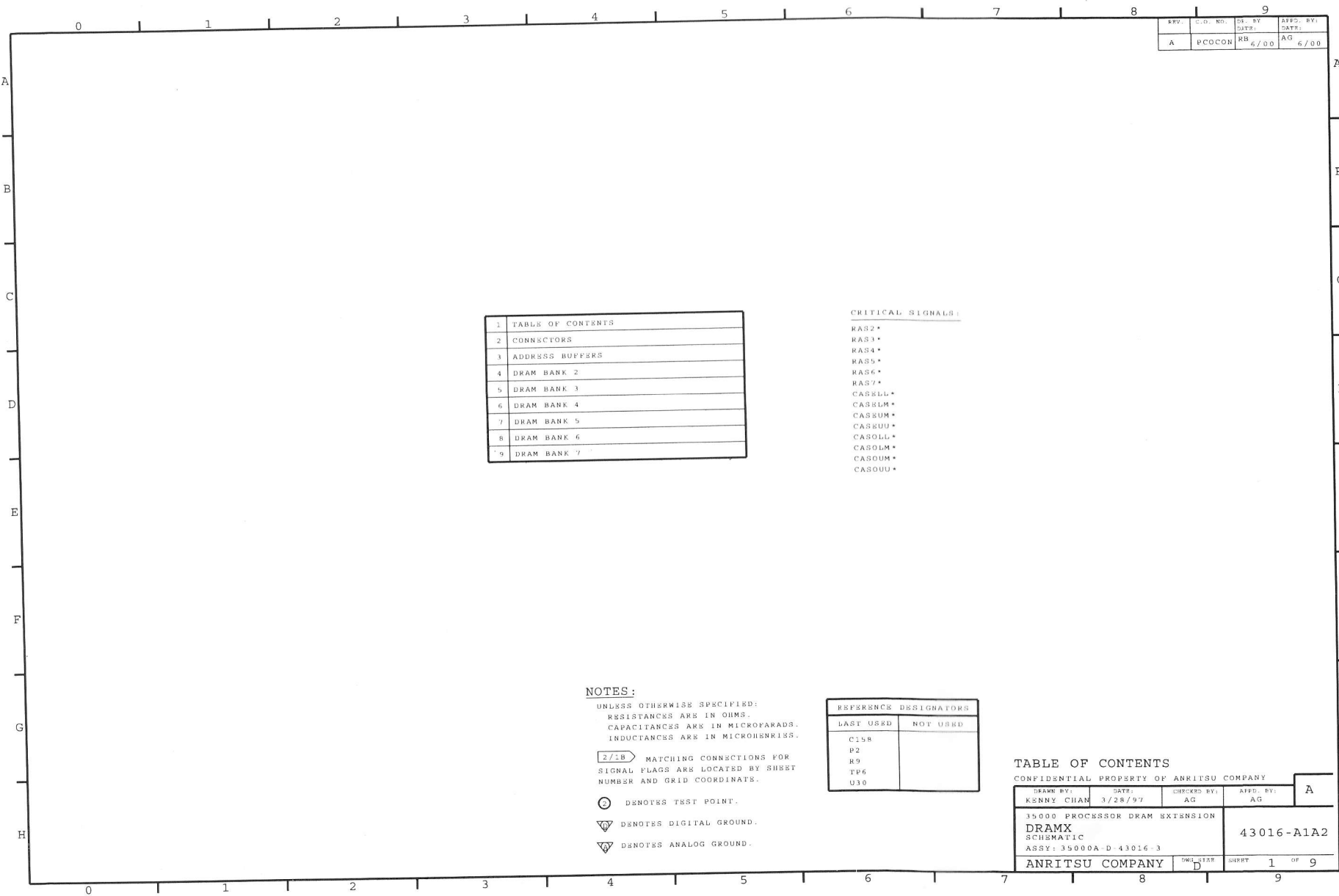


# Ext Memory Board



REV.	LOG. NO.	DES. BY	DATE	APPD. BY	DATE
A	PCOC0N	RB	6/00	AG	6/00

1	TABLE OF CONTENTS
2	CONNECTORS
3	ADDRESS BUFFERS
4	DRAM BANK 2
5	DRAM BANK 3
6	DRAM BANK 4
7	DRAM BANK 5
8	DRAM BANK 6
9	DRAM BANK 7

**CRITICAL SIGNALS:**

- RAS2\*
- RAS3\*
- RAS4\*
- RAS5\*
- RAS6\*
- RAS7\*
- CAS0LL\*
- CAS0LM\*
- CAS0UM\*
- CAS0UU\*
- CAS0LL\*
- CAS0LM\*
- CAS0UM\*
- CAS0UU\*

**NOTES:**

UNLESS OTHERWISE SPECIFIED:  
 RESISTANCES ARE IN OHMS.  
 CAPACITANCES ARE IN MICROFARADS.  
 INDUCTANCES ARE IN MICROHENRIES.

 MATCHING CONNECTIONS FOR  
 SIGNAL FLAGS ARE LOCATED BY SHEET  
 NUMBER AND GRID COORDINATE.

 DENOTES TEST POINT.

 DENOTES DIGITAL GROUND.

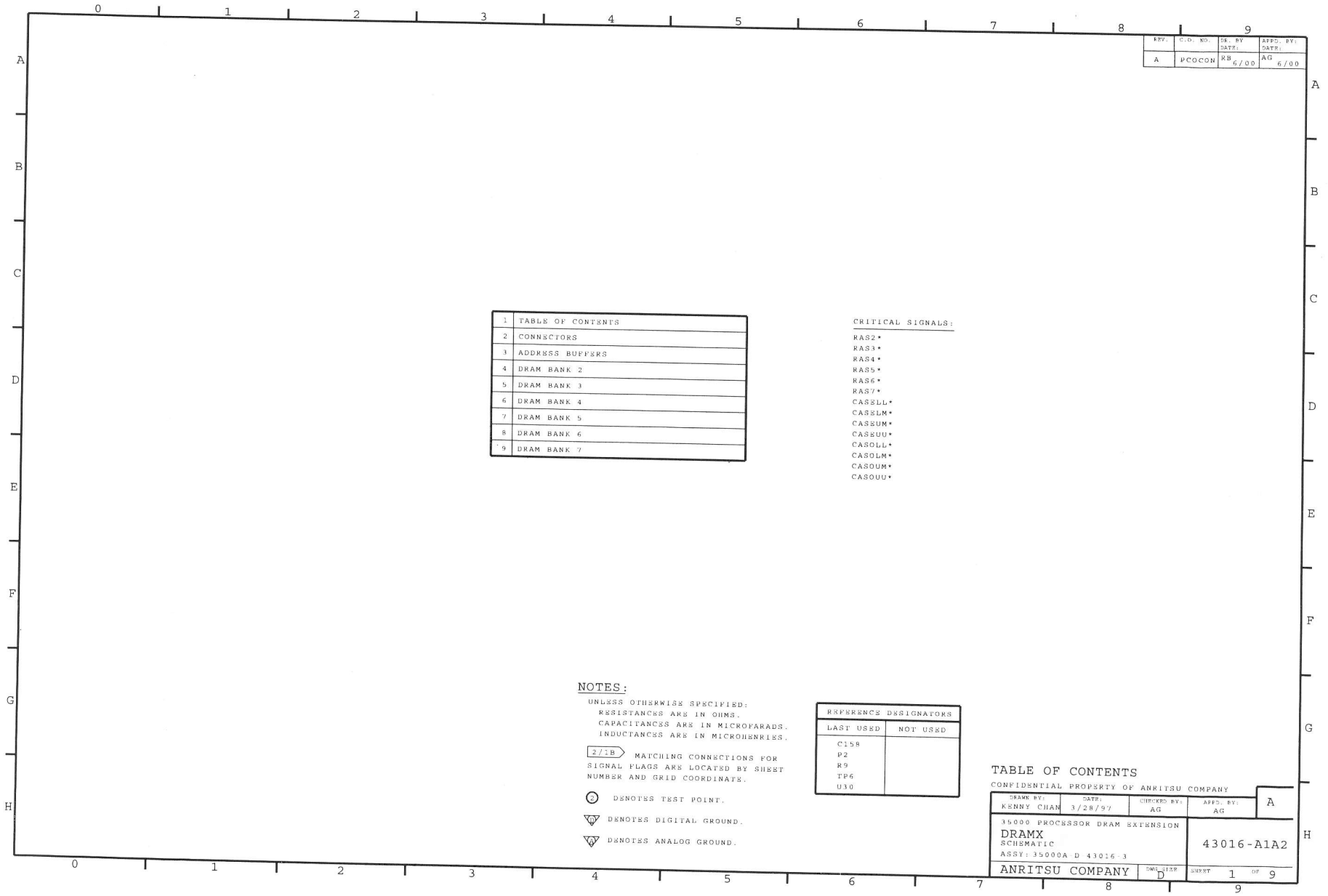
 DENOTES ANALOG GROUND.

REFERENCE DESIGNATORS	
LAST USED	NOT USED
C158	
P2	
R9	
TP6	
U30	

**TABLE OF CONTENTS**

CONFIDENTIAL PROPERTY OF ANRITSU COMPANY				A
DRAWN BY: KENNY CHAN	DATE: 3/28/97	CHECKED BY: AG	APPD. BY: AG	
35000 PROCESSOR DRAM EXTENSION DRAM SCHEMATIC ASSY: 35000A-D-43016-3			43016-A1A2	
ANRITSU COMPANY			DWG. NO. D	SHEET 1 OF 9

# Ext Memory Module



REV.	C.D. NO.	DR. BY	DATE	APPD. BY:	DATE:
A	PCOC00	RB	6/00	AG	6/00

1	TABLE OF CONTENTS
2	CONNECTORS
3	ADDRESS BUFFERS
4	DRAM BANK 2
5	DRAM BANK 3
6	DRAM BANK 4
7	DRAM BANK 5
8	DRAM BANK 6
9	DRAM BANK 7

CRITICAL SIGNALS:

RAS2\*  
 RAS3\*  
 RAS4\*  
 RAS5\*  
 RAS6\*  
 RAS7\*  
 CAS2LL\*  
 CAS2LM\*  
 CAS2UM\*  
 CAS2OU\*  
 CAS2OL\*  
 CAS2LM\*  
 CAS2UM\*  
 CAS2OU\*

NOTES:

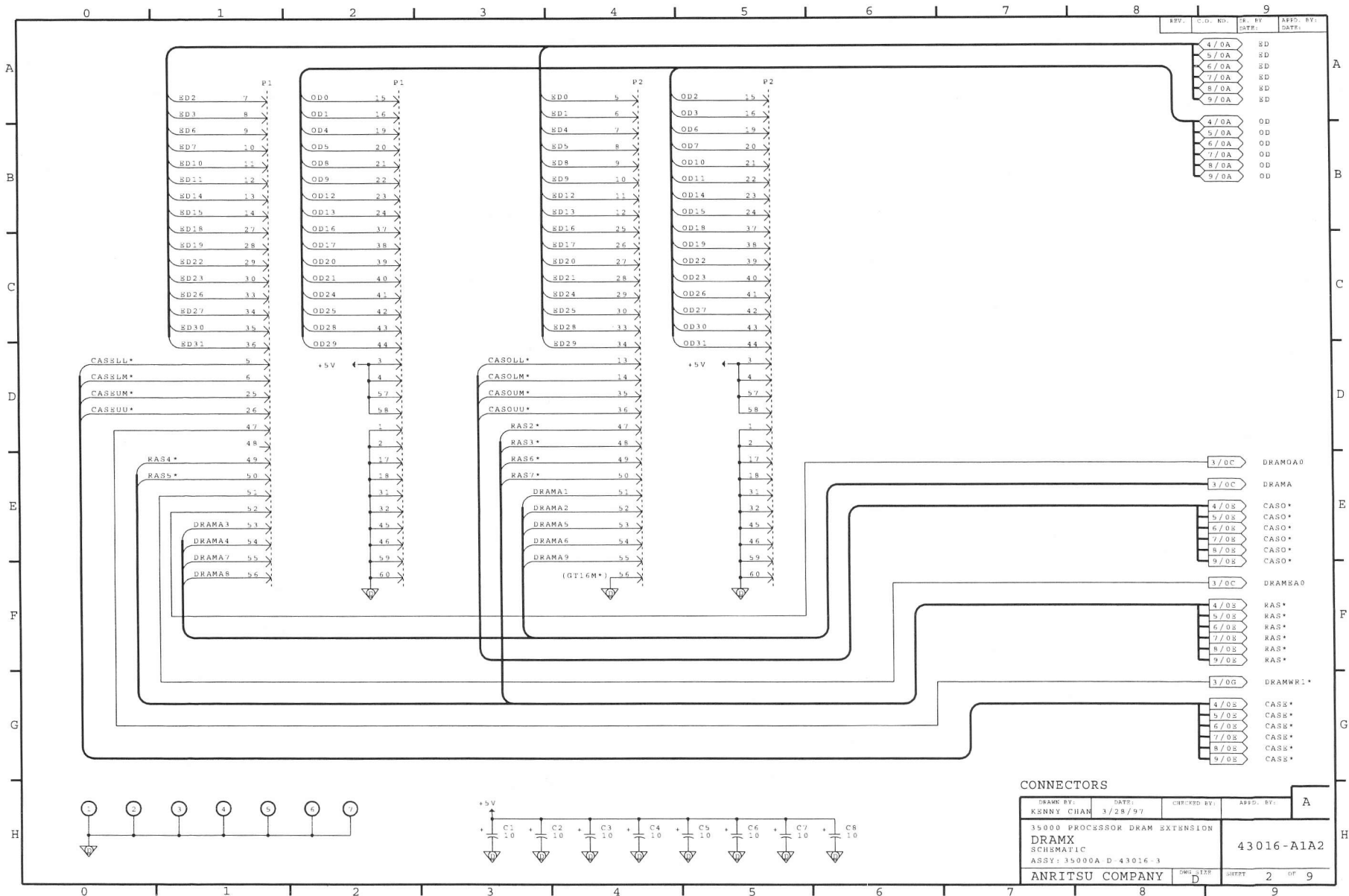
UNLESS OTHERWISE SPECIFIED:  
 RESISTANCES ARE IN OHMS.  
 CAPACITANCES ARE IN MICROFARADS.  
 INDUCTANCES ARE IN MICROHENRIES.

2/1B MATCHING CONNECTIONS FOR SIGNAL FLAGS ARE LOCATED BY SHEET NUMBER AND GRID COORDINATE.

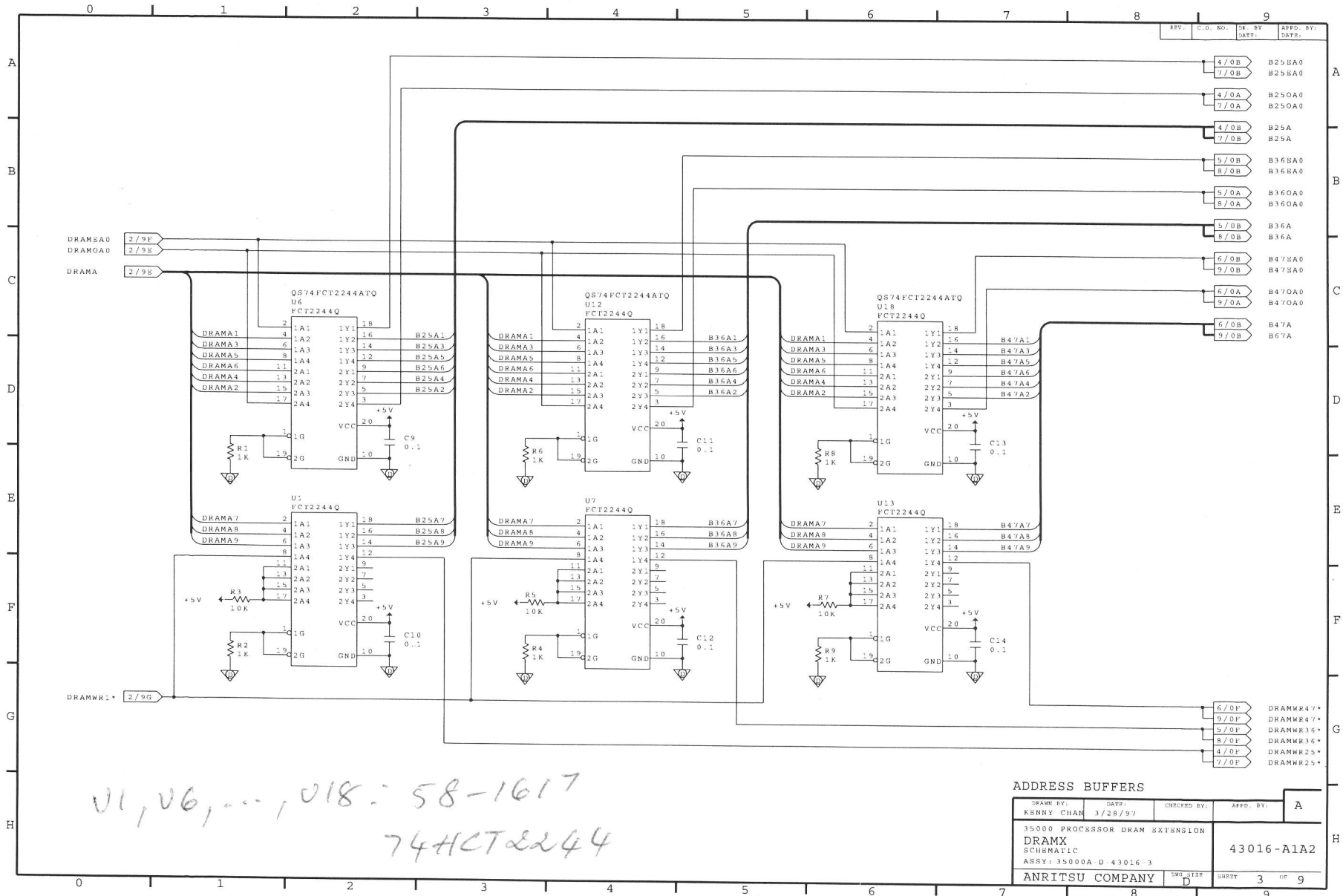
⊙ DENOTES TEST POINT.  
 ▽ DENOTES DIGITAL GROUND.  
 ▽ DENOTES ANALOG GROUND.

REFERENCE DESIGNATORS	
LAST USED	NOT USED
C158	
P2	
R9	
TP6	
U30	

TABLE OF CONTENTS				
CONFIDENTIAL PROPERTY OF ANRITSU COMPANY				
DRAWN BY:	DATE:	CHECKED BY:	APPD. BY:	A
KENNY CHAN	3/28/97	AG	AG	
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC			43016-A1A2	
ASSY: 35000A D 43016-3				
ANRITSU COMPANY		DWG. ISS. D	SHEET	1 OF 9



CONNECTORS			
DRAWN BY: KENNY CHAN	DATE: 3/28/97	CHECKED BY:	APPD. BY: A
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC ASSY: 35000A D 43016-3			43016-A1A2
ANRITSU COMPANY		DWG. LIST D	SHEET 2 OF 9

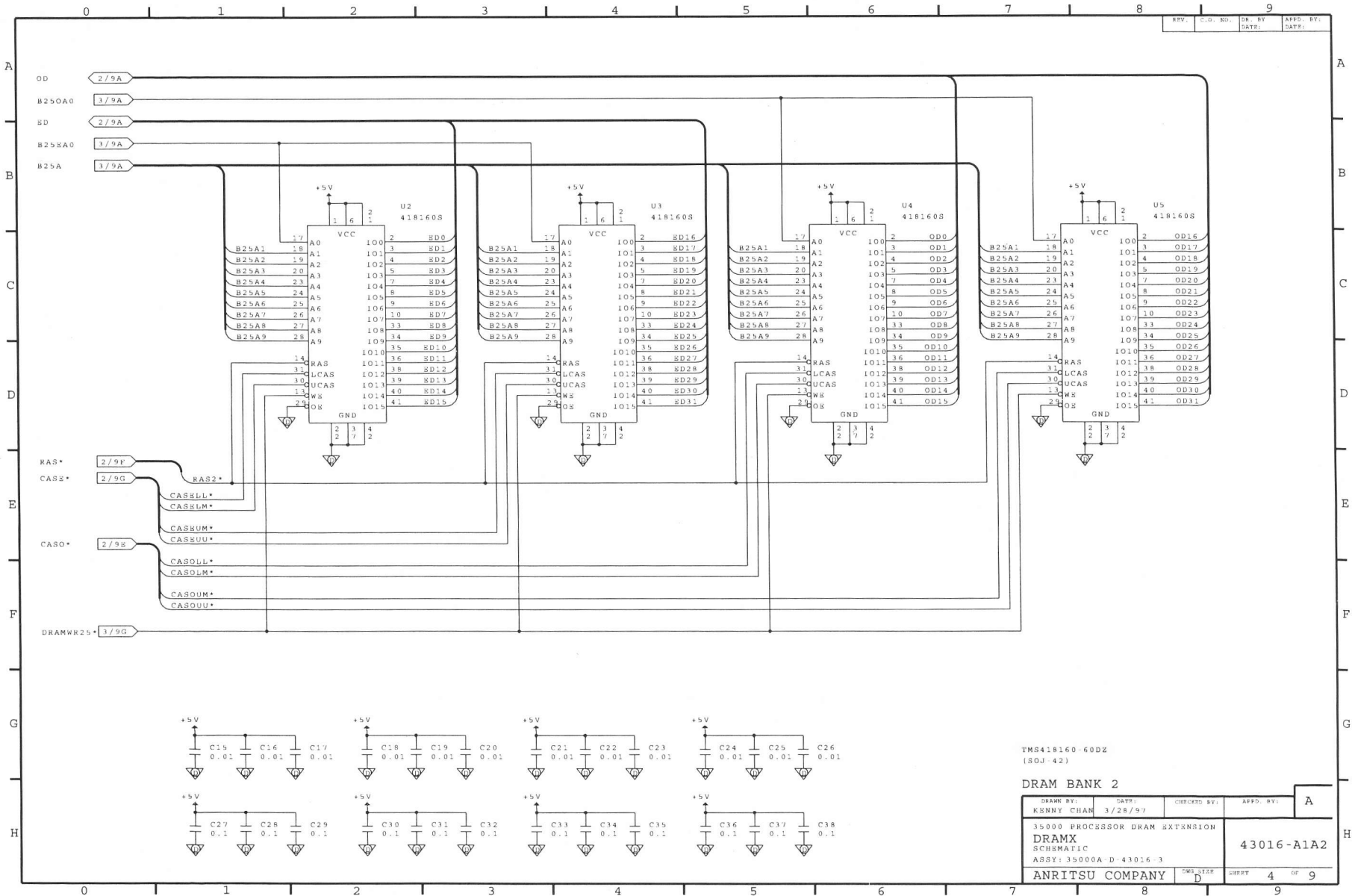


REV.	C.D. NO.	DR BY	DATE	APPD. BY	DATE
------	----------	-------	------	----------	------

4/0B	B25BA0
7/0B	B25BA0
4/0A	B250A0
7/0A	B250A0
4/0B	B25A
7/0B	B25A
5/0B	B36BA0
8/0B	B36BA0
5/0A	B360A0
8/0A	B360A0
5/0B	B36A
8/0B	B36A
6/0B	B47BA0
9/0B	B47BA0
6/0A	B470A0
9/0A	B470A0
6/0B	B47A
9/0B	B47A
6/0F	DRAMWR47*
9/0F	DRAMWR47*
5/0F	DRAMWR36*
8/0F	DRAMWR36*
4/0F	DRAMWR25*
7/0F	DRAMWR25*

ADDRESS BUFFERS

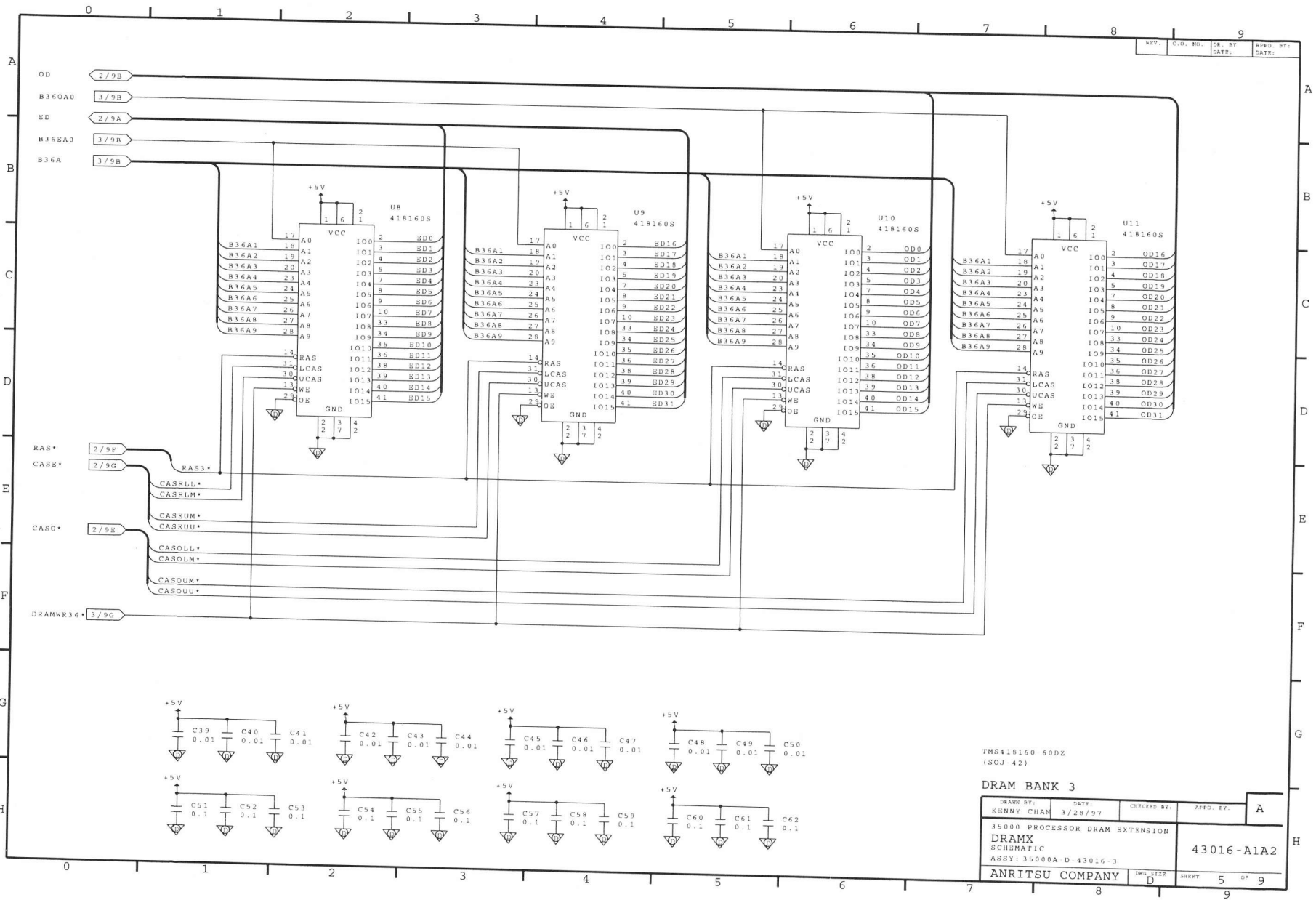
DRAWN BY: KENNY CHAN	DATE: 3/28/97	CHECKED BY:	APPD. BY: A
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC			43016-A1A2
ASSY: 35000A-D 43016-3			
ANRITSU COMPANY		CHK. SIZE D	SHEET 3 OF 9



TMS418160-60DZ  
(SOJ-42)

**DRAM BANK 2**

DRAM BY: KSNRY CHAN	DATE: 3/28/97	CHECKED BY:	APPR. BY: A
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC ASSY: 35000A D 43016 3			43016-A1A2
ANRITSU COMPANY		DWG. SIZE D	SHEET 4 OF 9

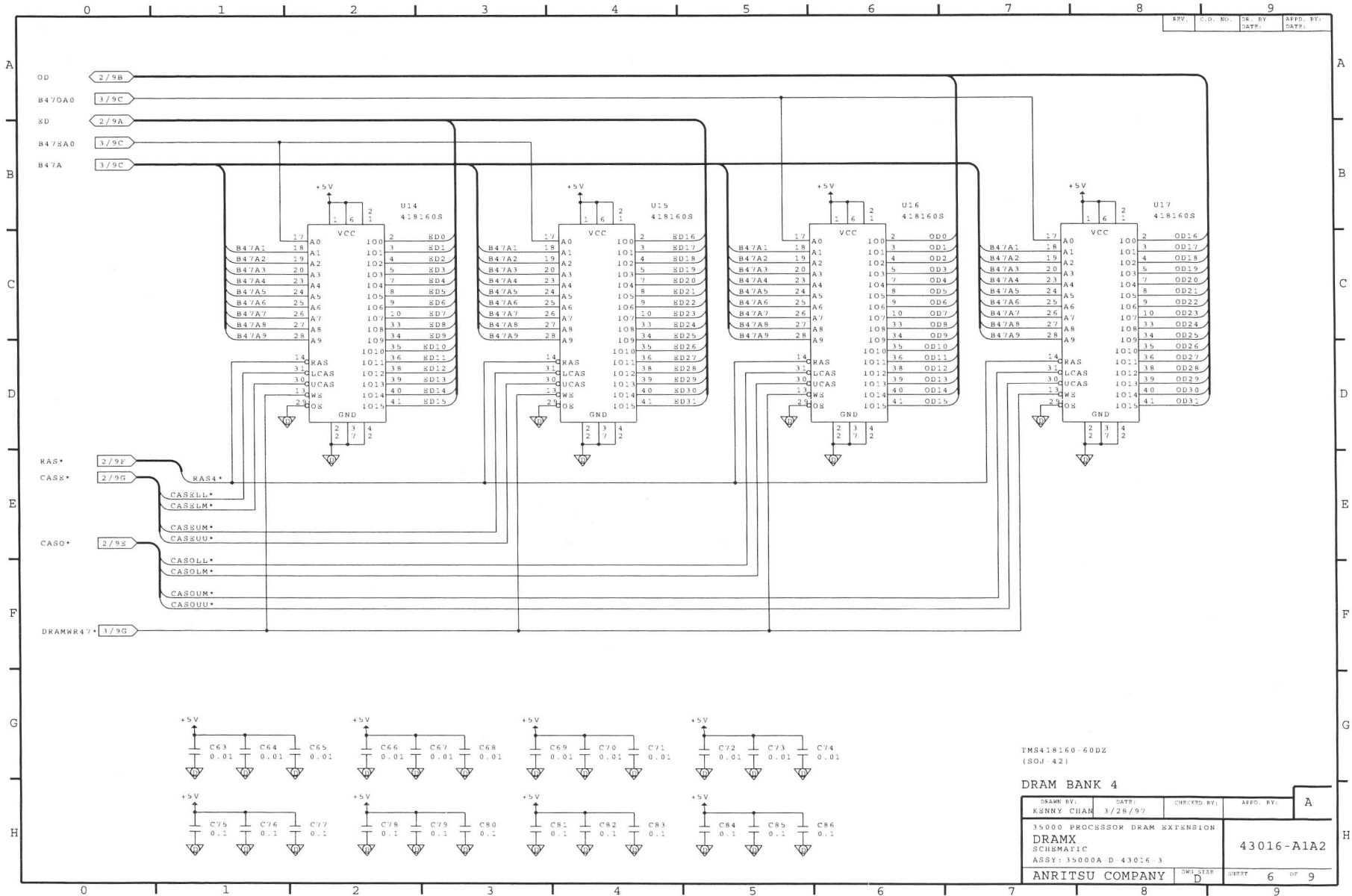


REV.	C.D. NO.	DR. BY	APPD. BY

TMS418160 60DZ  
(SOJ-42)

DRAM BANK 3

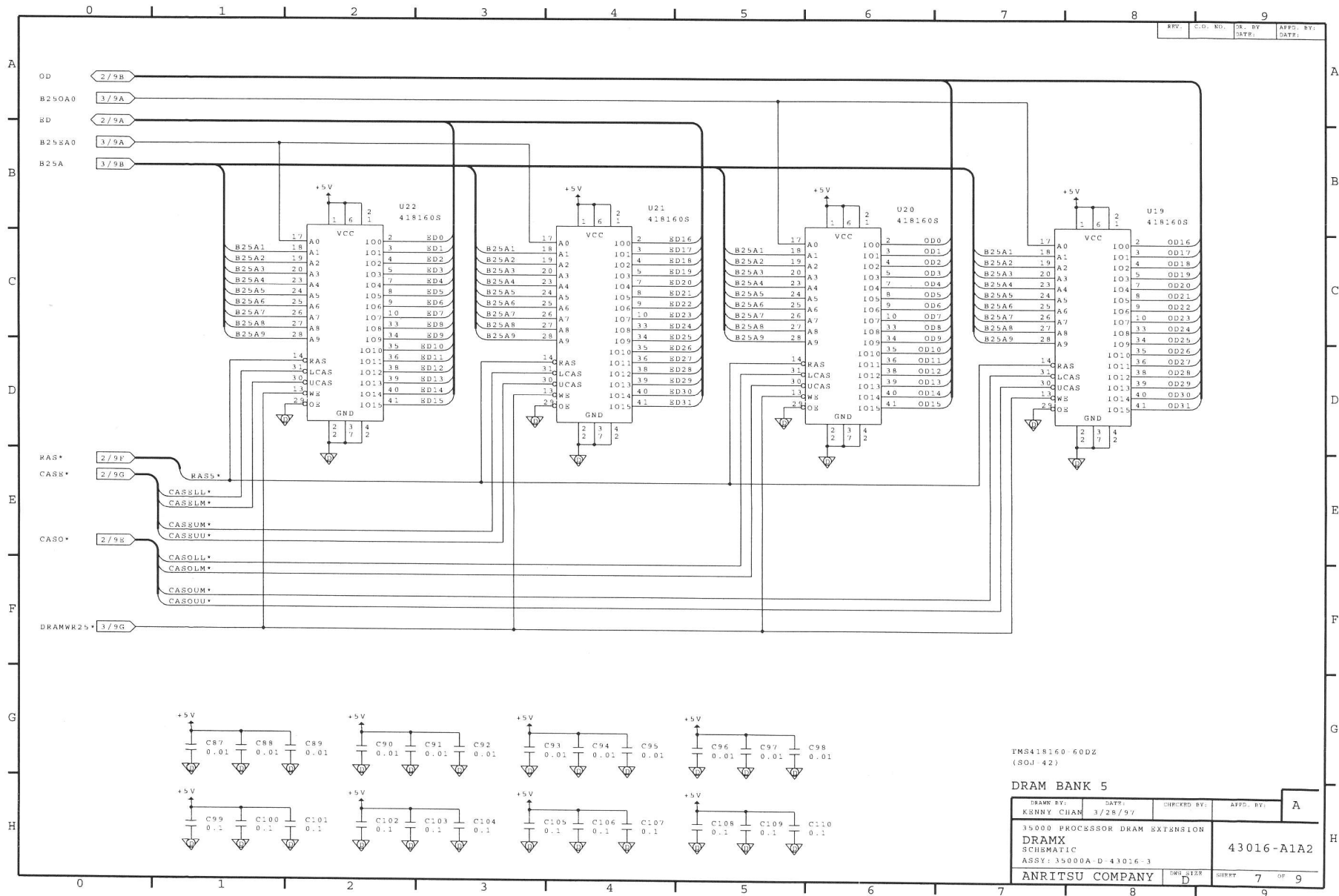
DRAWN BY:	DATE:	CHECKED BY:	APPD. BY:
KENNY CHAN	3/28/97		A
35000 PROCESSOR DRAM EXTENSION			
DRAM			
SCHEMATIC			
ASSY: 35000A D 43016-3			
43016-A1A2			
ANRITSU COMPANY		DRG. 1122	SUPPLY 5 OF 9



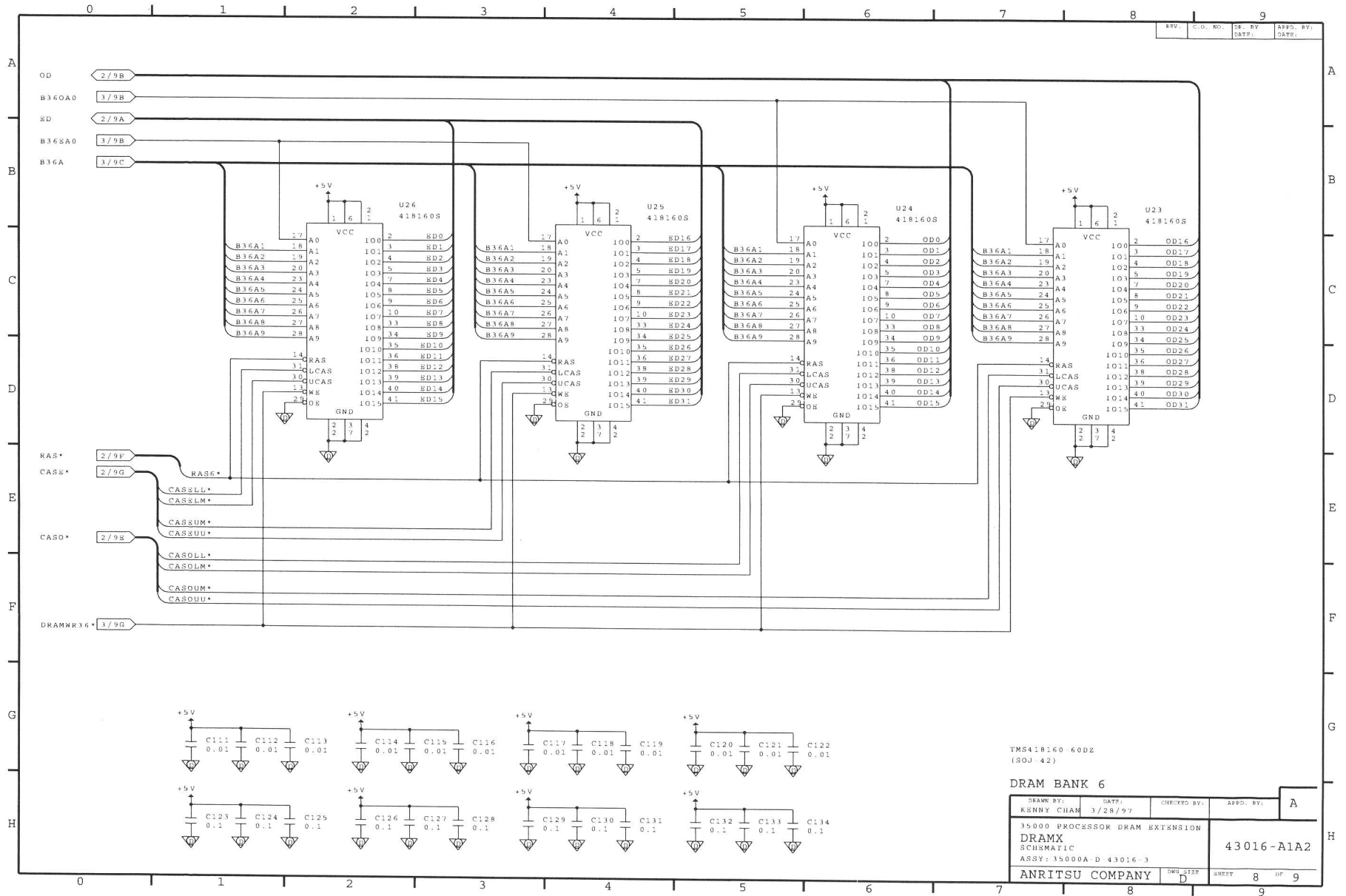
IMS418160-60DZ  
(SOJ 42)

DRAM BANK 4			
DRAWN BY:	DATE:	CHK'D BY:	APP'D BY:
KENNY CHAN	1/28/97		A
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC ASSY: 35000A D 43016 3			43016-A1A2
ANRITSU COMPANY		SW: 115X D	6 OF 9

U19-U30 were intended for bottom side of PCB + are not loaded.





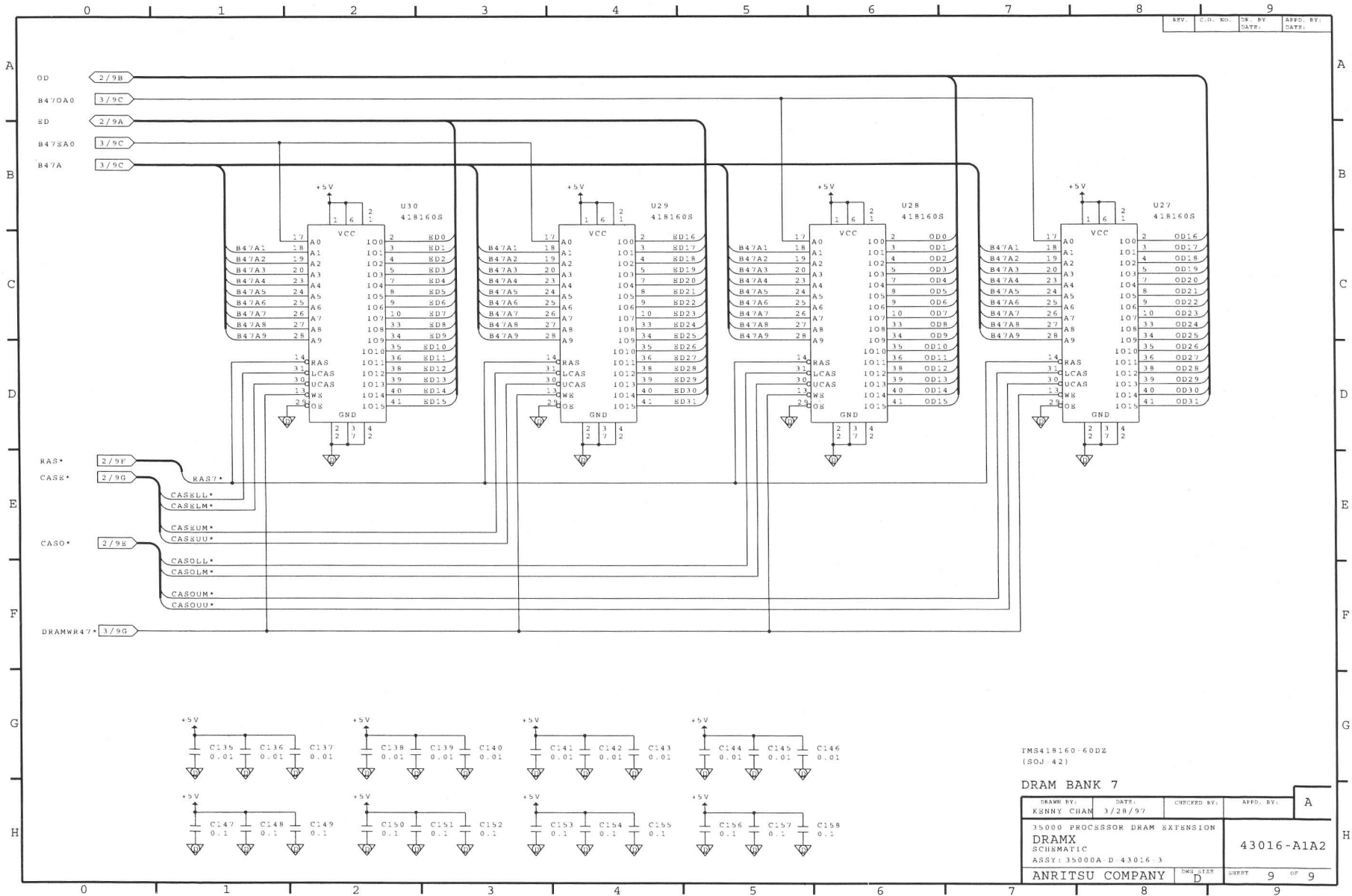


REV. C.O. NO. DR. BY APPD. BY:  
DATE: DATE:

TMS418160-60D2  
(SOJ 42)

**DRAM BANK 6**

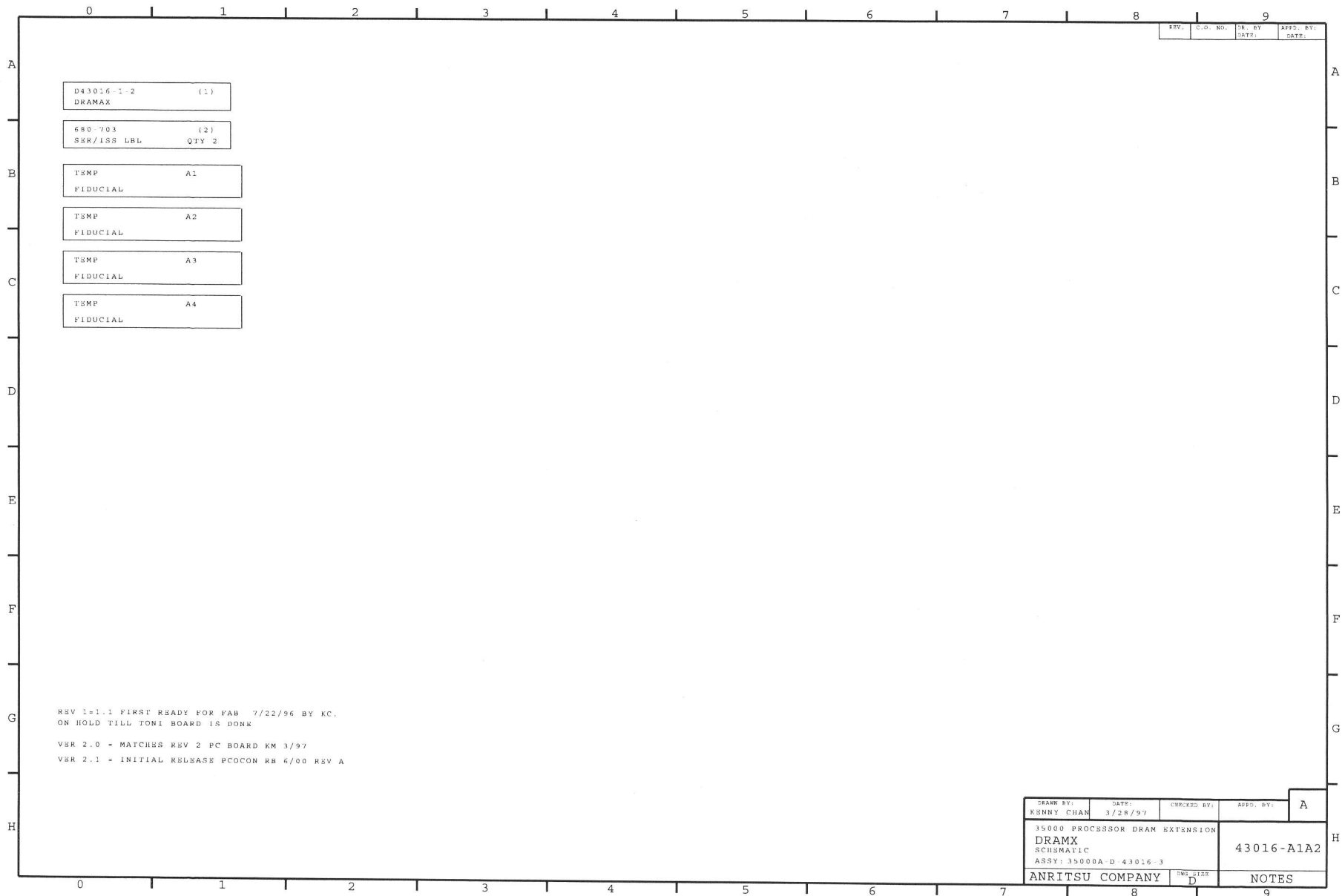
DRAWN BY: KENNY CHAN	DATE: 3/28/97	CHK'D BY:	APPD. BY: A
35000 PROCESSOR DRAM EXTENSION DRAMX SCHEMATIC			43016-A1A2
ASSY: 35000A-D 43016-3			
ANRITSU COMPANY		DWG. SIZE D	SHEET 8 OF 9



TMS418160-60D2  
(SOJ 42)

DRAM BANK 7

DRAM BY:	DATE:	CHECKED BY:	APPD. BY:
KENNY CHAN	3/28/97		A
35000 PROCESSOR DRAM EXTENSION			
DRAMX			
SCHEMATIC			
ASSY: 35000A D 43016 3			
ANRITSU COMPANY			43016-A1A2
DWG. SIZE D		SHEET 9 OF 9	



REV.	C.O. NO.	DR. BY	APPD. BY:
		DATE:	DATE:

DRAWN BY:	DATE:	CHECKED BY:	APPD. BY:	A
KENNY CHAN	3/28/97			
35000 PROCESSOR DRAM EXTENSION				
DRAMX				
SCHEMATIC				43016-A1A2
ASSY: 35000A-D-43016-3				
ANRITSU COMPANY			DWG. DESK	NOTES
		D		