



NOTES:

1. A MULTIPLIER OR OTHER FULL MULTICHANNEL PERIPHERAL CAN BE FITTED ONLY ON TERMINAL B (LOGIC 21D).
2. IN A MULTIPLIER -HESITATION (SINGLE CHARACTER) THE 'NO' LINE IS SENT IN STEP 21. THE REPLY ON 'DI' IS THE CHANNEL ADDRESS AND IS ADDED IN INVERSE FORM TO 128 TO FORM THE CONTROL WORD ADDRESS i.e. FOR CHANNEL J THE WORD ACCESSED IS 181-J. IF THE COUNT DOES NOT GO TO ZERO THE NORMAL HESITATION SEQUENCE IS PERFORMED. HOWEVER IF 'BP4' COMES ON IN STEP 21, THEN AFTER STEP 22, STEPS 26 & 27 ARE CARRIED OUT BEFORE STEP 23/24. THIS IS DONE TO RECHARGE THE CONTROL WORD SO THAT THE TRANSFER CONTINUES IN A NEW AREA.
3. 'STEP 21' IS ON THROUGHOUT & PRODUCES 'CPRT', 'CMCJ', AND 'NO'.

STEP 26 (READ RECHARGE WORD)
PERIPHERAL ADDRESS TO RT (STEP 21)+64
READ; CONTINUE CYCLE
GO TO STEP 27

STEP 27 (RECHARGE THE CONTROL WORD)
PERIPHERAL ADDRESS TO RT (STEP 21)
READ; CONTINUE CYCLE
INHIBIT SIN → RB AND INHIBIT 'CLEAR RB'
GO TO STEP 25 AND 24

22A

MATERIAL	HEAT TREATMENT	SURFACE TREATMENT	
MULTICHANNEL INTERFACE		ISSUE	CAT.
5020380		1	D
TITLE 1902/3 PROCESSOR LOGIC		SHEET 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30	ISSUE NUMBERS OF OTHER SHEETS AT THE TIME OF ISSUING THIS SHEET.