Subject index for papers in Volume 49

Each index entry below is accompanied by an author's name and a page number; the author index contains the title of the paper and the names of coauthors, if any.

Errata

In the paper "Characterization of Simultaneous Multithreading (SMT) Efficiency in POWER5" by H. M. Mathis et al. in the *IBM Journal of Research and Development*, Volume 49, No. 4/5, July/September 2005, the last entry in the SMT gain column of Table 1 (p. 558) should represent a negative percentage, i.e., −11.2.

The names S. A. Cordes and J. L. Speidell were erroneously omitted from the author list of the paper entitled "Low-Cost Wafer Bumping" by P. A. Gruber, L. Bélanger, G. P. Brouillette, D. H. Danovitch, J.-L. Landreville, D. T. Naugle, V. A. Oberson, D.-Y. Shih, C. L. Tessler, and M. R. Turgeon in the *IBM Journal of Research and Development*, Volume 49, No. 4/5, July/September 2005. Their biographies follow.

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Mr. Cordes is a Senior Engineer and Manager at the IBM Thomas J. Watson Research Center. He joined IBM in 1988 after receiving a B.S. degree in electrical engineering from Manhattan College, New York. From 1988 to 2000, his work was focused on developing microfabrication processing techniques and building novel microelectronic devices—e.g., injection molding of solder, microelectromechanical systems, or MEMS, and applying silicon micromachining for various membrane applications. Since 2000 he has managed the Central Scientific Services Microfabrication Department. Mr. Cordes is a co-inventor on 26 U.S. patents.

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Mr. Speidell is a Senior Technical Staff Member and Senior Manager at the IBM Thomas J. Watson Research Center. He joined IBM in 1977, receiving A.A.S. degrees in electrical technology and engineering science from Westchester Community College in 1977 and a B.S. degree in electrical engineering from Manhattan College. From 1977 to 2000, Mr. Speidell's work was focused on developing microfabrication processing techniques and building novel microelectronic devices such as field-effect transistors with submicron channel widths and high-speed photodiodes and applying silicon micromachining to building mechanical structures such as grids for ion sources and energy analyzers. During this same period, Mr. Speidell was heavily involved with photomask technology, building a high-speed laser-based mask writer to support the photomask demands of the Research Division. He was also the co-inventor of dielectric mask technology used in laser ablation processing. Since 2000, Mr. Speidell has been the Senior Manager for the Central Scientific Services (CSS) Department and also the functional manager of the CSS electronics team. He has received 14 IBM Invention Achievement Awards and two IBM Outstanding Technical Achievement Awards.