

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

United States

6,229,173	U. Gruening and C. J. Radens	Hybrid 5F2 cell layout for buried surface strap aligned to vertical transistor
6,229,334	R. A. Kelley and D. Neal	Voltage overshoot control
6,229,338	P. K. Coulman, S. H. Dhong, J. A. Silberman, and O. Takahashi	Method and apparatus for reducing dynamic programmable logic array propagation delay
6,229,358	D. W. Boerstler and J. A. Silberman	Delayed matching signal generator and frequency multiplier using scaled delay networks
6,229,372	D. T. Hui, B. W. Mashak, S. H. Voldman, and R. R. Williams	Active clamp network for multiple voltages
6,229,374	J. Tammone	Variable gain amplifiers and methods having a logarithmic gain control function
6,229,465	R. N. Bulaga, B. Cummings, D. Firth, J. L. Harris, C. Newman, and D. L. Wheeler	Built in self test method and structure for analog to digital converter
6,229,524	R. Chernock, F. Schaffa, and D. Seidman	User interface for interaction with video
6,229,526	V. Berstis	Method and system for simultaneous operation of multiple handheld IR control devices in a data processing system
6,229,544	B. J. Cragun	Tiled image editor
6,229,664	T. R. Albrecht and J. Kuan	Head-disk interface tester with optically servo controlled rotary voice coil motor actuator
6,229,676	W. L. Prater	Ferrofluid seal for actuator bearing
6,229,693	D. W. Hill and J. Karidis	Articulated display for notebook computer
6,229,697	E. J. Selker	Notebook computer having a resilient, portfolio-type case
6,229,699	B. Kerrigan and J. W. Young	Packaging apparatus and method for network computer chassis
6,229,708	J. S. Corbin, D. A. Hall, K. C. Larsen, M. W. Mueller, and P. M. Thomsen	Multi-axis microprocessor docking mechanism
6,229,712	D. E. McConnell and C. Munoz-Bustamante	Printed circuit board for coupling surface mounted optoelectric semiconductor devices
6,229,750	A. Correale and R. J. Lynch	Method and system for reducing power dissipation in a semiconductor storage device
6,229,754	E. L. Carter, M. H. Lee, M. R. Ouellette, G. Q. Wang, and M. H. Wood	Write through function for a memory
6,229,779	R. E. Berry, S. A. Morgan, J. M. Mullaly, D. J. Roberts, C. A. Swearingen, and A. C. Temple	Physical play-in-place audio compact disc jewel case
6,229,801	R. E. Anderson, E. M. Foster, and G. W. Rohrbaugh	Delivery of MPEG2 compliant table data
6,229,853	C. A. Gebler, A. Y. Ngai, M. P. Vachon, and R. L. Woodard	Method and apparatus to accommodate partial picture input to an MPEG-compliant encoder
6,230,038	R. J. Von Gutfeld and J. F. Ziegler	Imaging of internal structures of living bodies by sensing implanted magnetic devices

6,230,121	O. W. Weber	Measurement and validation of interaction and communication
6,230,151	R. Agrawal, C. T. Ho, and M. J. Zaki	Parallel classification for data mining in a shared-memory multiprocessor system
6,230,153	S. K. Howard, D. C. Martin, and M. E. Plutowski	Association rule ranker for web site emulation
6,230,157	M. C. Azua, R. D. Hoffman, and J. W. Malcolm	Flattening complex data structures in Java/JavaScript objects
6,230,161	P. T. Berkland, H. J. Glaser, and S. E. Nickolas	Dynamic expression editor
6,230,167	J. S. Lipscomb, W. L. Luken, J. P. Menon, and B. O. Schneider	Method and apparatus for generating and displaying hotlinks in a panoramic three dimensional scene
6,230,168	P. Gegen, P. T. Leone, T. Rauch, and J. Unger	Method for automatically constructing contexts in a hypertext collection
6,230,171	G. Pacifici and A. S. Youssef	Markup system for shared HTML documents
6,230,183	J. E. Arwe, C. K. Eilert, and P. B. Yocum	Method and apparatus for controlling the number of servers in a multisystem cluster
6,230,196	T. Guenther, F. D. Lawlor, D. H. Lin, and C. Schmitt	Generation of smart HTML anchors in dynamic web page creation
6,230,198	C. S. Dawson, M. A. Kaczmariski, B. J. Vining, and D. P. Warren	Server-to-server event logging
6,230,218	D. Casper and J. C. Elliott	Apparatus for transferring execution of certain channel functions to a control unit having means for combining certain commands and data packets in one sequence
6,230,219	J. S. Fields, Jr. and G. L. Guthrie	High performance multichannel DMA controller for a PCI host bridge with a built-in cache
6,230,242	R. K. Arimilli, J. S. Dodson, and G. L. Guthrie	Store instruction having vertical memory hierarchy control bits
6,230,243	D. A. Elko, J. W. Josten, J. M. Nick, D. H. Surman, and W. W. Wilkinson	Method, system and program products for managing changed data of castout classes
6,230,247	D. M. Cannon and H. N. Martin	Method and apparatus for adaptive storage space allocation
6,230,260	D. A. Luick	Circuit arrangement and method of speculative instruction execution utilizing instruction history caching
6,230,265	G. H. Ahrens, J. C. Kennel, J. M. Patel, and K. P. Szabo	Method and system for configuring resources in a data processing system utilizing system power control information
6,230,283	M. Gambino	Logical connection resynchronization
6,230,290	D. Heidel, W. Hwang, and T. Kiriata	Method of self programmed built in self test
6,230,302	C. I. Gabele, P. T. Patel, S. T. Quay, A. K. Spencer, and P. G. Villarrubia	Method and system for performing timing analysis on an integrated circuit design
6,230,311	S. N. Gerard, S. L. Halter, S. J. Munroe, and E. E. Voldal	Apparatus and method for disabling methods called on an object
6,230,314	P. F. Sweeney and F. Tip	Method and device for program transformation using class hierarchy transformation based upon type constraint analysis
6,231,224	E. T. Gamble and E. McNulty	Light pipe guide and carrier for hard disk drive
6,231,249	R. H. Harris	Duplex printer using a ribbon shifting mechanism
6,231,333	T. G. Ference, P. A. Gruber, B. Hernandez, E. M. Kummer, and A. R. Zingher	Apparatus and method for vacuum injection molding
6,231,526	R. H. Taylor	System and method for augmentation of surgery
6,231,707	D. Gupta, L. W. Herron, J. U. Knickerbocker, D. C. Long, J. P. Nayak, and R. A. Rita	Method of forming a multi-layer ceramic substrate with max-punched vias
6,232,045	C. DeCusatis and L. Jacobowitz	System for creating, reading and writing on rotatable information storage media, a method for producing a recording blank
6,232,143	J. T. Maddix, A. M. Palagonia, P. J. Pikna, and D. P. Vallett	Micro probe ring assembly and method of fabrication
6,232,154	H. Haffner, L. L. Hsu, G. Lehmann, and A. Reith	Optimized decoupling capacitor using lithographic dummy filler
6,232,163	J. S. Brown, R. J. Gauthier, and S. H. Voldman	Semiconductor diode with depleted polysilicon gate structure and method
6,232,170	M. C. Haakey and W. H. Ma	Method of fabricating trench for SOI merged logic DRAM
6,232,173	F. Assaderaghi, L. L. Hsu, and J. A. Mandelman	Process for forming a memory structure that includes NVRAM, DRAM, and/or SRAM memory structures on one substrate and process for forming a new NVRAM cell structure
6,232,222	M. Armacost, R. Conti, J. P. Gambino, and J. K. Stephens	Method of eliminating a critical mask using a blockout mask and a resulting semiconductor structure
6,232,363	F. D. Egitto, L. J. Matienzo, and B. O. Morrison, Jr.	Method for producing a diffusion barrier and polymeric article having a diffusion barrier
6,232,559	M. L. Janecsek	Multi-layer printed circuit board registration
6,232,564	S. F. Arndt, M. Budman, and J. R. Stack	Printed wiring board wireability enhancement
6,232,633	J. Bracchitta and J. S. Nakos	NVRAM cell using sharp tip for tunnel erase

6,232,639	F. D. Baker, J. S. Brown, R. J. Gauthier, S. J. Holmes, R. K. Leidy, E. J. Nowak, and S. H. Voldman	Method and structure to reduce latch-up using edge implants
6,232,667	E. B. Hultmark and B. C. Noble	Technique for underfilling stacked chips on a cavity MLC module
6,232,798	P. K. Coulman, S. H. Dhong, J. A. Silberman, and O. Takahashi	Self resetting circuit timing correction
6,232,799	D. H. Allen and D. L. Stasiak	Method and apparatus for selectively controlling weak feedback in regenerative pass gate logic circuits
6,232,806	B. G. Rudolph, J. D. Strom, and D. M. Woeste	Multiple-mode clock distribution apparatus and method with adaptive skew compensation
6,232,967	N. J. Dauerer and E. E. Kelley	Web browser column control
6,232,968	C. Alimpich, B. Jeffcoat, D. Neuhard, L. L. Vigil, and J. Wittig	Data processor controlled display system with a plurality of switchable customized basic function interfaces for the control of varying types of operation
6,232,971	T. Haynes	Variable modality child windows
6,233,073	M. D. Bowers, D. E. Denning, R. G. Emberty, and C. A. Klein	Diagnostic injection of transmission errors in fiber optic networks
6,233,121	T.-S. S. Pan	Magnetic disk drive suspension pitch motion limiter
6,233,142	B. Kerrigan, H. C. Tanner, and J. W. Young	Mechanical architecture of supporting a hard disk drive into a feature card slot in a network computer
6,233,143	E. T. Gamble and E. McNulty	Shock dampening system for hard disk drive carrier
6,233,184	J. Barth, C. L. Bertin, J. H. Dreibelbis, W. F. Ellis, E. L. Hedberg, W. J. Howell, H. L. Kalter, W. R. Tonti, and D. L. Wheeler	Structure for wafer level test and burn-in
6,233,191	S. W. Gould, J. A. Iadanza, and F. R. Keyser	Field programmable memory array
6,233,206	H. Hamann, Y. Martin, and H. Wickramasinghe	Assembly for writing/reading high density magnetic data
6,233,560	A. R. Tannenbaum	Method and apparatus for presenting proximal feedback in voice command systems
6,233,575	R. Agrawal and B. E. Dom	Feature selection, hierarchical classification and context-dependent signature generation for text databases
6,233,583	R. A. Hoth	Report generator for use within a Lotus Notes database system
6,233,584	K. J. Purcell	Technique for providing a universal query for multiple different databases
6,233,598	R. A. Kertis	Interleaved finite impulse response (FIR) filter functions for servo and data detection in a direct access storage device (DASD)
6,233,599	W. G. Nation, R. N. Newshutz, and J. C. Willis	Apparatus and method for retrofitting multi-threaded operations on a computer by partitioning and overlapping registers
6,233,612	B. Fruchtmann, M. A. Kaczmarzski, and D. P. Warren	Dynamic network protocol management information base options
6,233,622	B. P. Atsatt, S. R. Loomis, and G. R. Turner	Adapter and handler framework for web server extensions
6,233,630	G. W. Wilhelm, Jr.	Resource management system having a control structure to indicate the availability of integer from an integer pool upon responsive to a request
6,233,636	R. A. Kelley, D. Neal, and K. A. Riek	Method and system for allowing PCI bus transactions to be performed at higher operating frequencies
6,233,639	T. Dell, B. G. Hazelzet, M. W. Kellogg, C. R. Ogilvie, and P. C. Stabler	Memory card utilizing two wire bus
6,233,641	C. S. Graham, S. M. Lambeth, D. F. Moertl, and P. Movall	Apparatus and method of PCI routing in a bridge configuration
6,233,642	D. J. Martens and E. Yaniv	Method of wiring a 64-bit rotator to minimize area and maximize performance
6,233,643	L. P. Andrews, R. Beckman, R. C. Eng, J. M. Linger, J. R. Petty, J. Sinibaldi, G. L. Turbeville, and K. B. Williams	Apparatus including a host processor and communications adapters interconnected with a bus
6,233,644	D. J. Dahlen, J. M. Nick, D. H. Surman, and D. W. Westcott	System of performing parallel cleanup of segments of a lock structure
6,233,655	W. H. Li, C. K. Shum, and C. F. Webb	Method for quad-word storing into 2-way interleaved L1 cache
6,233,666	T. S. Mathews, K. V. Nallapati, and D. W. Sheffield	Deferred disk drive space allocation for virtual memory pages with management of disk address recording in multipage tables without external process interrupts for table for input/output to memory
6,233,674	E. N. Elnozahy	Method and system for scope-based compression of register and literal encoding in a reduced instruction set computer (RISC)
6,233,680	D. C. Bossen, A. Kitamorn, and C. A. McLaughlin	Method and system for boot-time deconfiguration of a processor in a symmetrical multi-processing system
6,233,685	S. W. Smith and S. Weingart	Establishing and employing the provable untampered state of a device
6,233,693	N. C. Berglund and D. A. Verburg	Smart DASD spin-up

6,233,714	M. Hassner and N. N. Heise	Generalized method and means for defining and operating a (D, K) partial-response ML detector of binary-coded sequences
6,233,725	B. A. Beadle, M. Brown, M. Paolini, and D. S. Rothert	Method and apparatus to coordinate and control the simultaneous use of multiple just in time compilers with a Java virtual machine
6,233,728	W. A. Carter, A. R. Elderon, T. D. Magee, M. D. Nicholas, H. Y. Saade, G. Sutherland, W. N. Tindall, J. R. Urs, T. E. Weinmann, and M. T. Wheatley	Compiler-assisted or interpreter-assisted solution to the year 2000 problem with debugging option for computer programs
6,233,959	S. Kang, H. V. Mahaney, R. R. Schmidt, and P. Singh	Dehumidified cooling assembly for IC chip modules
6,233,960	S. Kang, H. V. Mahaney, R. R. Schmidt, and P. Singh	Spot cooling evaporator cooling system for integrated circuit chip modules
6,234,009	D. Chen, E. Flecha, J. Hammond, and K. G. Roessler	Controlling motion of a scanning force microscope probe tip moving into engagement with a sample surface
6,234,687	R. R. Hall and H. T. Lin	Self-aligning method and interlocking assembly for attaching an optoelectronic device to a coupler
6,234,695	S. C. Freedman, D. F. Fritz, R. W. Kruppa, and R. D. Yoder	Variable power thermal printer
6,234,807	K. Amini, J. C. Diepenbrock, R. J. Evans, M. L. Scollard, and M. B. Tran	Circuit board connector edge with straddle pattern tab design for impedance-controlled connections
6,234,829	M. W. Mueller, P. M. Thomsen, and L. M. Walter	Method and device for identifying card slots that are required to be populated concurrently in a computer system
6,234,870	D. C. Edelstein and C. Uzoh	Serial intelligent electro-chemical-mechanic wafer processor
6,235,406	C. Uzoh	Copper film including laminated impurities
6,235,411	K. J. Blackwell, P. C. Chen, A. R. Knoll, L. J. Matienzo, and R. D. Weale	Process for coating a substrate with metallic layer
6,235,412	T. J. Cheng, A. Giri, A. K. Malhotra, J. Pennacchia, E. D. Perfecto, and R. Yu	Corrosion-resistant terminal metal pads for thin film packages
6,235,435	S. Peng	Dichroic photo mask and methods for making and inspecting same
6,235,439	C. A. Whiting	Method for controlling image size of integrated circuits on wafers supported on hot plates during post exposure baking of the wafers
6,235,452	J. Collins, L. C. MacDowell, W. M. Moreau, and M. Santarelli	Detection of a gaseous substance emanating from a layer of polymeric composition
6,235,544	P. A. Franklin, C. J. Hendricks, R. Surprenant, S. J. Tirsch, T. A. Wassick, and J. P. Wood	Seed metal delete process for thin film repair solutions using direct UV laser
6,235,567	F. Y. Huang	Silicon-germanium BiCMOS on SOI
6,235,597	G. L. Miles	Semiconductor structure having reduced silicide resistance between closely spaced gates and method of fabrication
6,235,996	S. Farooq, M. J. Interrante, S. K. Ray, and W. E. Sablinski	Interconnection structure and process for module assembly and rework
6,236,060	K. K. Chan and S. Tiwari	Light emitting structures in back-end of line silicon technology
6,236,077	J. P. Gambino, R. Jammy, J. A. Mandelman, and C. J. Radens	Trench electrode with intermediate conductive barrier layer
6,236,103	K. Bernstein, R. M. Geffken, W. D. Pricer, A. K. Stamper, and S. H. Voldman	Integrated high-performance decoupling capacitor and heat sink
6,236,115	A. J. Emerick, M. A. Gaynes, V. Puligandla, C. G. Woychik, and J. M. Zalesinski	High density integrated circuit packaging with chip stacking and via interconnections
6,236,196	D. Guidotti, A. Halperin, M. E. Scaman, and A. R. Zingher	Thermal modulation system and method for locating a circuit defect
6,236,233	K. R. Vannorsdel	Method and system for translating TTL off-chip drive for integrated circuit with negative substrate bias
6,236,253	T. L. Leasure and J. A. Paredes	Apparatus and method for controlling a reset in a self-timed circuit of a multiple-clock system
6,236,258	H. Hoenigschmid and D. Netis	Wordline driver circuit using ring-shaped devices
6,236,267	C. Anzil	Linearization for power amplifiers using feed-forward and feedback control
6,236,535	L. J. Gilliland and C.-H. Lee	Spindle motor with hydrodynamic bearing
6,236,542	B. R. Denhartog, D. L. Fox, J. A. Hagan, C. S. Shen, and K. V. Viswanathan	Substrate independent superpolishing process and slurry
6,236,573	J. G. Gundlach and P. Wormsbecher	Apparatus and method for single-handed manipulation of latching pluggable electronic component
6,236,589	A. Gupta and R. Saraf	Ultra high density storage media and method thereof
6,236,617	L. L. Hsu, H. O. Joachim, H. Wong, and M. R. Wordeman	High performance CMOS word-line driver

6,236,727	M. L. Ciacelli, J. E. Fetkovich, J. L. Kouloheris, W. M. Lam, and J. W. Urda	Apparatus, method and computer program product for protecting copyright data within a computer system
6,236,909	J. C. Colson and S. G. Graham	Method for representing automotive device functionality and software services to applications using JavaBeans
6,236,936	V. Berstis and J. L. Smith	Maintaining a desired separation or distribution in a moving cluster of machines using a time multiplexed global positioning system
6,236,968	D. Kanevsky and W. Zadrozny	Sleep prevention dialog based car system
6,236,985	C. Aggarwal, J. Wolf, and P. S. Yu	System and method for searching databases with applications such as peer groups, collaborative filtering, and e-commerce
6,236,989	S. A. Mandyam and S. Saxena	Network based help architecture
6,237,000	D. J. Dahlen and D. A. Elko	Method and apparatus for previewing the results of a data structure allocation
6,237,004	J. P. Dodson, M. Nguyen, and C. A. Schwendiman	System and method for displaying data using graphical user interface control elements
6,237,008	D. G. Beal, M. S. Milillo, and C. J. West	System and method for enabling pair-pair remote copy storage volumes to mirror data in another storage volume
6,237,019	D. F. Ault and J. M. Forsythe	Method and apparatus for performing a semaphore operation
6,237,030	D. M. Adams, J. J. Maddalozzo, G. F. McBrearty, and J. M. Shieh	Method for extracting hyperlinks from a display document and automatically retrieving and displaying multiple subordinate documents of the display document
6,237,035	M. C. Azua, R. D. Hoffman, and M. Mall	System and method for preventing duplicate transactions in an internet browser/internet server environment
6,237,043	M. Brown, M. T. Collins, W. Gu, P. J. Kilpatrick, and K. R. Lawrence	System and method for adding highly optimized synchronization capability to objects at run-time
6,237,048	J. M. Allen, D. F. Moertl, D. M. Neal, G. M. Nordstrom, and T. J. Osten	Adapter card with vendor unique differentiation and customization using PCI sideband signals
6,237,057	R. A. Kelley and D. Neal	Method and system for PCI slot expansion via electrical isolation
6,237,062	W. C. Carlson and J. W. Peake	Storage and access to scratch mounts in VTS system
6,237,067	R. J. Eberhard, E. Wong, and V. P. Zeyak, Jr.	System and method for handling storage consistency conflict
6,237,068	W. S. Cadden, R. Lee, and O. Skudal	System for multi-volume, write-behind data storage in a distributed processing system
6,237,070	S. W. Ng	Method and system for determining the data layout geometry of a disk drive
6,237,081	H. Q. Le, B. J. Ronchetti, D. J. Shippy, and L. E. Thatcher	Queuing method and apparatus for facilitating the rejection of sequential instructions in a processor
6,237,085	J. L. Burns, S. H. Dhong, and K. J. Nowka	Processor and method for generating less than (LT), greater than (GT), and equal to (EQ) condition code bits concurrent with a logical or complex operation
6,237,092	K. F. Hayes	Client-server system with central application management allowing an administrator to configure user and group contexts during application configuration without relauching the application
6,237,100	D. Cromer, B. Ellison, J. W. Freeman, E. Kern, and R. S. Springfield	Power passwords within a data processing system for controlling a supply of system power
6,237,101	W. Moore and S. T. Ventrone	Microprocessor including controller for reduced power consumption during decoding of instructions by microcode units
6,237,103	N. Y. Lam, L. E. Simmons, and F. M. Liu	Power sequencing in a data processing system
6,237,111	M. Gambino	Method for logical connection resynchronization
6,237,119	M. D. Weber	Method and system for making internal states of an integrated circuit visible during normal operation without the use of dedicated I/O pins
6,237,132	A. A. Dean, K. J. Goodnow, S. W. Gould, K. Torino, and S. T. Ventrone	Toggle based application specific core methodology
6,237,134	N. Sundaresan	Method of, system for, and article of manufacture for providing a generic adaptor for converting from a non-future function pointer to a future function object
6,237,140	W. A. Carter, A. R. Elderon, T. D. Magee, M. D. Nicholas, H. Y. Saade, G. Sutherland, W. N. Tindall, J. R. Urs, T. E. Wienmann, and M. T. Wheatley	Compiler-assisted or interpreter-assisted windowing solution to the year 2000 problem for computer programs
6,237,142	S. Brodsky, D. Gangopadhyay, T. J. Grose, R. M. Lau, and S. Mitra	Method for synchronization between links and group definitions within an object-oriented system
6,237,393	D. B. Ames and M. J. Schade	Wafer center alignment device and method of wafer alignment
6,238,026	J. Adams, R. Crigler, P. Haughton, G. Proctor, R. Samper, and K. Whitehead	System and method for mounting components in a computer chassis
6,238,337	N. Kambhatla, D. Kanevsky, W. Zadrozny, and A. Zlatsin	Medical non-intrusive prevention based on network of embedded systems
6,238,469	M. Ronay	Dual-valent rare earth additives to polishing slurries

6,238,513	K. Arnold, J. W. Lewis, and J. N. Pinto	Wafer lift assembly
6,238,531	M. Pinarbasi	Method and apparatus to improve the properties of ion beam deposited films in an ion beam sputtering system
6,238,532	D. Restaino, S. M. Rossnagel, A. H. Simon, and P. Smetana	Radio-frequency coil for use in an ionized physical vapor deposition apparatus
6,238,589	E. I. Cooper, M. Datta, T. Dinan, T. Kanarsky, M. Pike, and R. Shenoy	Method for monitoring components in the TiW etching bath used in the fabrication of C4s
6,238,599	J. D. Gelorme, S. K. Kang, and S. Purushothaman	High conductivity, high strength, lead-free, low cost, electrically conducting materials and applications
6,238,737	K. K. Chan, E. C. Jones, F. R. McFeely, P. M. Solomon, and J. J. Yurkas	Method for protecting refractory metal thin film requiring high temperature processing in an oxidizing atmosphere and structure
6,238,741	J. M. Blazick, M. E. Cropp, J. N. Humenik, G. H. Leino, J. P. Nayak, F. V. Ranalli, D. A. Sylvester, J. A. Trumpetto, J. Utter, R. V. Vallabhaneni, and R. Weisman	Single mask screening process
6,238,850	O. Bula, D. Cole, E. W. Conrad, and W. C. Leipold	Method of forming sharp corners in a photoresist layer
6,238,935	J. L. Egley, G. M. Gut, D. J. Koch, and M. A. Matusewic	Silicon-on-sapphire wafer having conductive layer for detection with electrical sensors
6,238,946	J. F. Ziegler	Process for fabricating single crystal resonant devices that are compatible with integrated circuit processing
6,238,963	B. Chen and C. Hwang	Damascene process for forming ferroelectric capacitors
6,238,998	E. Leobandung	Shallow trench isolation on a silicon substrate using nitrogen implant into the side wall
6,239,469	R. J. Bolam, R. J. Evans, and A. M. Palagonia	Method for fabrication of silicon on insulator substrates
6,239,484	K. Dore and D. L. Edwards	Underfill of chip-under-chip semiconductor modules
6,239,591	A. Bryant, E. J. Nowak, and M. H. Tong	Method and apparatus for monitoring SOI hysteresis effects
6,239,617	J. J. Cahill, D. L. Guertin, R. R. Williams, and D. G. Young	Mixed voltage output driver with automatic impedance adjustment
6,239,620	N. Aoki, S. H. Dhong, N. Y. Kojima, and J. A. Silberman	Method and apparatus for generating true/complement signals
6,239,649	C. L. Bertin, J. J. Ellis-Monaghan, E. L. Hedberg, T. B. Hook, J. A. Mandelman, E. J. Nowak, W. D. Pricer, M. H. Tong, and W. R. Tonti	Switched body SOI (silicon-on-insulator) circuits and fabrication method therefor
6,239,665	J. D. Strom	Method and apparatus for switching in metal insulator metal capacitors and FET tuning capacitors for low noise oscillators
6,239,685	T. R. Albrecht and T. C. Reiley	Bistable micromechanical switches
6,239,703	M. J. Brady, D. W. Duan, D. Friedman, H. Heinrich, and P. A. Moskowitz	Communication pad structure for semiconductor devices
6,239,714	S. J. Hadderman and W. F. Relyea	Controller for use in an interconnection system
6,239,799	B. J. Owings	Method and system for providing a splitter bar control
6,239,800	D. L. Mayhew, M. Powell, and D. E. Shough	Method and apparatus for leading a user through a software installation procedure via interaction with displayed graphs
6,239,802	L. C. Lahey, D. Neuhard, and D. R. Palmer	File menu option for making file templates from pre-existing files
6,239,933	R. A. Kertis	Method and circuits for high speed powerup of analog reference source for a direct access storage device (DASD)
6,239,936	D. W. Abraham, T. J. Chainer, K. Etzold, H. H. Ottesen, and G. J. Smith	Method and apparatus for calibrating a thermal response of a magnetoresistive element
6,239,939	N. X. Bui, J. Fukuda, G. A. Jaquette, J. A. Koski, and K. Tsuruta	Robust detection of data modulated into a timing based servo
6,239,947	L.-S. Fan, J. Hong, and W. H. Juan	Microactuator with integrated sensor and drivers and method of manufacturing the same
6,239,955	R. E. Fontana, R. Hsiao, and H. A. Santini	Stabilized MR sensor and heat guide joined by contiguous junction
6,239,975	B. Otis	Enhanced arrangement for supplying continuous power to a circuit board
6,240,043	D. Hanson, T. Kirihata, and G. Mueller	SDRAM with a maskable input
6,240,105	J. M. Zetts	Video server streaming synchronization
6,240,139	T. A. Cao, S. Dutta, and D. Luk	Apparatus and method for repeating simultaneously transmitted signals on a single transmission path
6,240,174	J. Silver	Two-way transmission of subscriber provisional data for an intelligent peripheral in an advanced intelligent network

6,240,243	M.-S. Chen, D. D. Kandlur, and P. S. Yu	Method and apparatus for storing and retrieving scalable video data in a disk-array-based video server
6,240,330	J. M. Kurtzberg and M. Levanoni	Method for feedforward corrections for off-specification conditions
6,240,400	Y. L. Chou and A. Garg	Method and system for accommodating electronic commerce in the semiconductor manufacturing industry
6,240,407	D. T. Chang, J. M. Cheng, J. H. Chow, and J. Xu	Method and apparatus for creating an index in a database system
6,240,412	D. Y. Dyko, C. J. Hastings, R. Sobiesiak, and R. Wendt	Integration of link generation, cross-author user navigation, and reuse identification in authoring process
6,240,419	P. A. Franaszek	Compression store addressing
6,240,430	H. Y. Amro and K. W. Deike	Method of multiple text selection and manipulation
6,240,433	M. S. Schmookler and D. Senzig	High accuracy estimates of elementary functions
6,240,444	T. Fin, T. Fujisaki, M. Kobayashi, and M. Shinozaki	Internet web page sharing
6,240,446	D. Casper and J. C. Elliott	Multiplexing of multiple data packets for multiple input/output operations between multiple input/output devices and a channel subsystem having multiple channels
6,240,453	D. Y. Chang and J. S. Wang	Managing unused resources in a service application
6,240,466	M. D. McKeehan, S. J. Munroe, and E. E. Voldal	Object-oriented apparatus and method for determining new object location relative to an existing object
6,240,467	B. C. Beardsley, J. C. Elliott, and J. L. Iskiyan	Input/output operation request handling for concurrently executing in a multi-host system
6,240,468	R. B. Capelli	Interposed graphics device driver module processing function request within module in standard mode, and passing function request to specialized mode device driver in specialized mode
6,240,474	G. L. Guthrie, R. A. Kelley, D. Neal, and S. M. Thurber	Pipelined read transfers
6,240,476	R. Garcia, K. A. Honeycutt, and S. E. Still	System and method for allocating bus master control lines to peripheral devices connected to a bus
6,240,487	K. S. Chan, D. A. Hicks, P. P. Liu, R. P. Singh, and S. Tung	Integrated cache buffers
6,240,489	C. M. Durham and B. P. Hanley	Method for implementing a pseudo least recent used (LRU) mechanism in a four way cache memory within a data processing system
6,240,490	J. W. Lyles, Jr., J. T. Yen, Q. Yin, M. D. Griswold	Comprehensive multilevel cache preloading mechanism in a multiprocessing simulation environment
6,240,492	E. M. Foster, S. B. Herndon, and C. H. Ngai	Memory interface for functional unit of integrated system allowing access to dedicated memory and shared memory, and speculative generation of lookahead fetch requests
6,240,498	S. M. Dickes and P. B. Winterfield	Object oriented storage pool apparatus and method
6,240,499	M. S. Spencer	Method and system for improving data storage and access for programs written in mid-level programming languages
6,240,507	J. E. Derrick, S. Mallick, and R. McDonald	Mechanism for multiple register renaming and method therefor
6,240,512	Y. Fang, I. L. Kao, and G. C. Wilson	Single sign-on (SSO) mechanism having master key synchronization
6,240,521	R. J. Barber and E. J. Selker	Sleep mode transition between processors sharing an instruction set and an address space
6,240,536	J. LeBlanc and D. G. Mikan	Scannable latch circuit and method for providing a scan output from a latch circuit
6,240,546	L. C. Balfour, W. A. Carter, G. A. Dyck, D. E. Lee, B. B. Moore, G. V. Railsback, and J. D. Rotramel	Identifying date fields for runtime year 2000 system solution process, method and article of manufacture