

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

Canada

2,237,441	W. Kou	Secure open tendering protocol in electronic procurement
2,212,316	J. W. Benayon and B. W. Thomson	Method of recognizing fixed and variable sized DADA objects in multiple heaps
2,166,358	A. A. Al-Karmi, S. S. Singh, and B. S. Soor	Programming information for servers and clients in a distributed computing environment
2,201,278	C. L. Brealey, J. G. Johnston, V. Klicnik, D. M. Lauzon, L. T. Loi, and A. A. Seelemann	Hierarchical metadata store for an integrated development environment
2,212,194	B. Fielding and C. R. Plater	Method and apparatus for controlling access in a video distribution network
2,212,477	B. Fielding and C. R. Plater	Method and apparatus for maintaining directory services for a video
2,165,893	V. Klicnik and R. D. McDonald	Visual application part partitioning for creating distributed object oriented applications

China

552,385	J. Le Boudec and L. Truong	Connectionless ATM data services
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Germany

19,611,896.4	S. Hornung and H. Schuy	Load/unload-mechanism
19,633,466.7	W. Haenel	Post-initialization of chip cards
69,704,231.6	H. Hilgers, P. Joeris, and M. Straub	Surface modification of magnetic heads

France

658,021	F. Bauchot, R. Bjorklund, A. Herzberg, S. Kутten, and M. Wetterwald	A method and system for key distribution and authentication in a data communication network
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Great Britain

2,313,703	J. S. Beeteson and A. R. Knox	Current sensing in vacuum electron devices
2,318,030	B. K. Aldred, H. S. Lambert, H. D. Mitchell, D. S. Renshaw, and G. Tuttle	Communication system with exchange of capability information
2,318,486	C. J. Holloway	Data communications system
2,318,539	A. Dobson and R. A. Merrick	Data charting
2,331,387	A. Wilson	Method and apparatus for controlling printers
2,353,633	J. S. Beeteson and A. R. Knox	Display device

Japan

1,105,240	H. Hirano	Industrial design of multi-purpose passbook printer
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3,153,863	A. Ohyama and K. Takahashi	The mapping of data cell to print form field through mouse operations without programming
3,154,325	N. Morimoto, M. Numao, and S. Shimizu	A method for indentifying digital camera and detecting alteration on digital images
3,155,489	D. W. Albrecht and A. Aoyagi	Trace suspension assembly with dimple on polyimide layer
3,155,499	T. Okada, K. Tanaka, and T. Ushio	Integrated demodulator for DVD and CD
3,155,991	T. Fukuda and H. Matsuzawa	A method of parallel processing of multiple aggregate queries on shared-nothing multiprocessors
3,160,201	R. Kubota	Full text search engine
3,165,099	M. Demura, H. Nagura, T. Tamura, and K. Tanaka	Efficient erasure correction method for DVD
3,166,896	H. Hama, K. Iwano, and S. Misono	Apparatus for virtually zooming candidates for selection
3,166,905	M. Kitagawa, H. Nakano, and A. Yanagawa	An image processing method
3,167,931	A. Takeuchi	E2PROM write sequence for PC card terminal
3,167,980	M. Takagi	Candidate sieving method
3,170,455	K. Iwata	RAID with capacity merge function

United States

6,113,649	N. Govindaraj	Object representation of program and script components
6,113,759	C. Uzoh	Anode design for semiconductor deposition having novel electrical contact assembly
6,113,769	P. C. Andricacos, J. Horkans, and C. Uzoh	Apparatus to monitor and add plating solution of plating baths and controlling quality of deposited metal
6,114,019	A. C. Bhatt, D. H. Glatzel, V. R. Markovich, A. F. Moring, K. I. Papathomas, and D. J. Russell	Circuit board assemblies having filled vias free from bleed-out
6,114,082	M. C. Hakey, S. J. Holmes, D. V. Horak, A. D. Katnani, N. Patel, and P. Rabidoux	Frequency doubling hybrid photoresist having negative and positive tone components and method of preparing the same
6,114,096	T. Brunner, R. Mih, and D. C. Wheeler	Asymmetrical resist sidewall
6,114,098	B. K. Appelt, J. S. Kresge, J. M. Lauffer, and K. I. Papathomas	Method of filling an aperture in a substrate
6,114,181	A. J. Gregoritsch	Pre burn-in thermal bump card attach simulation to enhance reliability
6,114,221	W. R. Tonti and R. Q. Williams	Method and apparatus for interconnecting multiple circuit chips
6,114,248	J. P. Gambino and M. Jaso	Process to reduce localized polish stop erosion
6,114,249	D. F. Canaperi, R. Jagannathan, M. Krishnan, C. Morgan, and T. Wright	Chemical mechanical polishing of multiple material substrates and slurry having improved selectivity
6,114,257	P. A. Ronsheim	Process for modified oxidation of a semiconductor substrate using chlorine plasma
6,114,413	S. K. Kang, S. Purushothaman, and E. E. Simonyi	Thermally conducting materials and applications for microelectronic packaging
6,114,450	M. Berger, A. J. Call, F. Pompeo, and K. G. Sachdev	Aryl cyanate and/or diepoxide and tetrahydropyranyl-protected hydroxym ethylated phenolic or hydroxystyrene resin
6,114,458	C. J. Hawker, J. L. Hedrick, and M. Trollsas	Highly branched radial block copolymers
6,114,662	D. Guidotti and K. L. Lee	Continual flow rapid thermal processing apparatus and method
6,114,719	F. H. Dill, R. E. Fontana, T. Lin, S. S. Parkin, and C. Tsang	Magnetic tunnel junction memory cell with in-stack biasing of the free ferromagnetic layer and memory array using the cell
6,114,725	T. Furukawa, M. C. Hakey, S. J. Holmes, D. V. Horak, H. L. Kalter, J. A. Mandelman, P. Rabidoux, and J. J. Welser	Novel structure for folded architecture pillar memory cell
6,114,736	K. Balasubramanyam, S. B. Brodsky, R. Conti, and B. El-Kareh	Controlled dopant diffusion and metal contamination in thin polycide gate conductor of MOSFET device
6,114,937	J. Burghartz, D. C. Edelstein, C. Jahnes, and C. Uzoh	Integrated circuit spiral inductor
6,114,960	R. J. Gilmour	Method and apparatus for an integrated security device providing for automatic disablement
6,115,029	L. E. Gregg and W. Jaaskelainen Jr.	Graphical pointing device and method for controlling a graphical pointer within a data processing system
6,115,030	V. Berstis and T. G. Zimmerman	Improved trackpoint device
6,115,044	C. Alimpich, B. Jeffcoat, D. Neuhard, L. L. Vigil, and J. P. Wittig	Data processor controller display with a tree of items with modification of child item properties designated at parent level without modification of parent item properties
6,115,046	L. P. Chen and X. F. Niu	Enhanced computer aided design system

6,115,066	S. Gowda, H. J. Shin, H. P. Wong, P. Xiao, and J. Yang	Image sensor with direct digital correlated sampling
6,115,070	J. Song and B. L. Yeo	System and method for DCT domain inverse motion compensation using shared information
6,115,200	D. G. Allen and L. J. Serrano	Method and apparatus for preventing write operations in the presence of post-shock motion
6,115,238	R. J. Von Gutfeld	Apparatus for limiting viewing angle and glare of display and portable electronic device employing the same
6,115,249	G. L. Chiu and T. M. Cipolla	Portable computer stand for enhanced cooling
6,115,308	D. Hanson and T. Kirihata	Sense amplifier and method of using the same with pipelined read, restore and write operations
6,115,310	B. Ji, T. Kirihata, and D. Netis	Wordline activation delay monitor using sample wordline located in data-storing array
6,115,339	D. J. Winarski	Method and system in an optical storage disc drive for conserving laser power
6,115,361	G. R. Frazier and R. G. Hathorn	Link incident reporting extended link service for networks
6,115,371	V. Berstis and J. L. Smith	Satellite uplink separation using time multiplexed global positioning system cell location beacon system
6,115,422	R. E. Anderson and E. M. Foster	Protocol and procedure for time base change in an MPEG-2 compliant datastream
6,115,509	F. J. Yeskel	High volume image archive subsystem
6,115,544	S. H. Mueller	Method and system for displaying error messages
6,115,616	A. Halperin, J. D. Rutledge, A. Schrott, C. Tresser, R. J. Von Gutfeld, and C. W. Wu	Hand held telephone set with separable keyboard
6,115,648	F. D. Gallo	System and method for non-invasive accessor recalibration
6,115,703	B. Bireley, L. R. Curran, F. L. Lin, A. B. Lobo, and J. Q. Wong	A two-level caching system for prepared SQL statements in a relational database management system
6,115,712	N. Islam and V. Singh	Mechanism for combining data analysis algorithms with databases on the internet
6,115,720	T. Bleizeffer, N. Church, K. L. Devine, P. Hughes, B. J. Kilburn, and D. E. Shough	Method and apparatus for performing a health check on a database system
6,115,723	D. C. Fallside	Algorithm to generate ML statements from the positions of objects on a canvas
6,115,736	M. Devarakonda, A. Mohindra, A. Purakayastha, and D. J. Zukowski	System and method for automatically localizing access to remote network components using implicit agent relocation
6,115,745	V. Berstis and J. R. Thompson	Scheduling of distributed agents for a dialup network
6,115,763	M. C. Cogswell, G. R. Currier, S. M. Douskey, J. R. Elliott, S. D. Vincent, J. M. Wallin, and P. Wiltgen	Multi-core chip providing external core access with regular operation function interface and predetermined service operation service interface comprising core interface units and master interface unit
6,115,764	D. R. Chisholm and A. B. McNeill	Acyclic cable bus having redundant path access
6,115,773	L. B. Capps Jr., R. C. Dixon, T. Nguyen, and K. H. Pham	Circuit for detecting improper bus termination on a SCSI bus
6,115,789	C. M. Durham, P. J. Klim, and H. Ngo	Method and system for determining which memory locations have been accessed in a self timed cache architecture
6,115,794	R. K. Arimilli and J. S. Dodson	Method and system of providing a pseudo-precise inclusivity scheme in a sectored cache memory for maintaining cache coherency within a data-processing system
6,115,795	G. D. Gilda and S. L. Gregor	Method and apparatus for configurable multiple level cache with coherency in a multiprocessor system
6,115,804	G. D. Carpenter, M. E. Dean, and D. Glasco	Non-uniform memory access (NUMA) data processing system that permits multiple caches to concurrently hold data in a recent state from which data can be sourced by shared intervention
6,115,820	J. R. Lewis	Determining theft of grammar code
6,115,829	R. E. Murray and T. J. Slegel	Computer system with transparent processor sparing
6,115,912	A. H. Crudo, J. G. Davis, C. R. Le Coz, M. V. Pierson, A. K. Sarkhel, and A. K. Trivedi	Apparatus and method for printed circuit board repair
6,115,928	N. J. Dauerer	Fiber optic position indicator using liquid/air light transmission
6,116,127	W. D. Carbaugh, L. W. Herron, M. W. Kapfhammer, M. LaPlante, D. C. Long, N. A. Rizk, and J. Wylder	Flex die punching apparatus and method
6,116,797	J. G. Brewington and R. H. Harris	Double acting wrap spring drive
6,117,182	C. J. Alpert, A. Devgan, and S. T. Quay	Optimum buffer placement for noise avoidance
6,117,185	W. J. Schmidt	Skip list data storage during compilation
6,117,212	A. W. Buechele, J. T. Butler, and K. O. Muggenburg	System for evacuating air from a viscous media
6,117,283	P. C. Chen, G. L. Gorman, C. Hwang, V. Raman, and R. Simmons	Silicon coating on air bearing surface for magnetic thin film heads

6,117,367	R. J. Bezama, M. Cohn, B. V. Fasano, G. M. Johnson, R. A. Rita, R. V. Vallabhaneni, and N. Wier-Cavalieri	Pastes for improved substrate dimensional control
6,117,498	K. Chondroudis, D. Mitzi, and M. Prikas	Single source thermal ablation method for depositing organic-inorganic hybrid films
6,117,569	T. Lin and D. Mauri	Spin valves with antiferromagnetic exchange pinning and high uniaxial anisotropy reference and keeper layers
6,117,620	J. H. Brannon, W. I. Imaino, A. Juliana, M. R. Latta, C. H. Lee, W. C. Leung, and H. J. Rosen	Method of producing a calibration disk
6,117,771	W. J. Murphy and P. Tiwari	Method for depositing cobalt
6,117,778	B. P. Jones and V. Y. Sardesai	Semiconductor wafer edge bead removal method and tool
6,117,784	C. Uzoh	Process for integrated circuit wiring
6,117,796	G. Gale, R. Jagannathan, K. P. Madden, K. J. McCullough, H. F. Okorn-Schmidt, K. Pope, and D. I. Rath	Removal of silicon oxide
6,118,155	S. H. Voldman	Integrated ESD structures for use in ESD circuitry
6,118,192	J. Karidis	Applications of micro-electro-mechanical wobble motors as radio frequency transceiver components
6,118,261	C. K. Erdelyi and J. E. Gersbach	Slew rate control circuit
6,118,318	J. A. Fifield and L. G. Heller	Self biased differential amplifier with hysteresis
6,118,428	J. M. Blackman, B. A. Feigenbaum, M. A. Fiechtner, J. T. Galea, A. M. Gilbert, D. A. Kerr, P. R. King, T. Lay, I. Lomelli, D. Miclette, C. V. Powell, G. T. Puchtel, and J. M. Wagner	Method and system for simultaneous presentation of multiple windows supported by different graphic user interfaces
6,118,518	P. C. Hobbs	Assembly comprising a pocket 3-D scanner
6,118,609	K. Gong	Method to adjust head switch time for improved disk drive performance
6,118,622	H. S. Gill	Technique for robust resetting of spin valve head
6,118,623	C. Chiu, J. J. Fatula, R. Hsiao, and L.-C. Lee	A high definition chevron type MR sensor
6,118,630	A. Argumedo	Compliant edge guiding and following system for tape
6,118,632	T. R. Albrecht, P. M. Baumgart, T. A. Nguyen, K. A. Rubin, and A. C. Tam	Magnetic disk stack having laser-bump identifiers on magnetic disks
6,118,659	S. T. Adams, W. V. Cranston, W. E. Hamel, J. K. Koenig, and J. McCloskey	Heat sink clamping spring additionally holding a ZIF socket locked
6,118,667	C. Grosser, M. S. Mansuria, and L. H. Webster	Card insulator and guide for a computer system having hot plugable adapter cards
6,118,683	S. Butt, G. Kunkel, and C. Radens	Dynamic random access memory cell layout
6,118,707	S. W. Gould, J. A. Iadanza, F. R. Keyser, and T. Zitttrisch	Field-programmable memory array
6,118,719	T. Dell, B. G. Hazelzet, and M. W. Kellogg	Self-initiated self-refresh mode for memory modules
6,118,726	B. Ji and T. Kirihata	Shared row decoder
6,118,740	C. DeCusatis and L. Jacobowitz	In a system for creating, reading and writing on rotatable information storage media, a method for writing closely spaced information tracks
6,118,745	R. A. Hutchins, G. Jaquette, and K. Tsuruta	Acquisition of highly variable data frequency by digital edge processing
6,118,780	J. M. Dunn, A. G. Ganek, E. H. Stern, and B. E. Willner	Communication network and method of operation for real time user selection of voice and/or data paths in the network
6,118,788	P. Kermani, M. Naghshineh, C. M. Olsen, T. Ozugur, and B. Rezvani	Balanced media access methods for wireless networks
6,118,823	J. D. Carr	Control scheme for shared-use dual-port predicted error array
6,118,873	J. B. Lotspiech and K. S. McCurley	System for encrypting broadcast programs in the presence of compromised receiver devices
6,118,880	G. D. Kokkosoulis and D. A. Temple	Method and system for dynamically maintaining audio balance in a stereo audio system
6,118,890	A. W. Senior	System and method for broad classification of biometric patterns
6,118,939	R. L. Foshee, J. J. Hawkins, and C. J. Nack	Method and system for a replaceable application interface at the user task level
6,118,940	R. F. Berry	Method and apparatus for benchmarking byte code sequences
6,119,085	J. R. Lewis and K. A. Ortega	Reconciling recognition and text to speech vocabularies
6,119,086	A. P. Ittycheriah, S. Maes, and D. Nahamoo	Speech coding via speech recognition and synthesis based on pre-enrolled tokens
6,119,112	R. R. Bush	Optimum cessation of training in neural networks
6,119,116	R. A. Rose	System and method for accessing and distributing audio CD data over a network

6,119,128	D. K. Courter, M. H. Hu, L. M. Kunioka-Weiss, T. T. Majithia, D. A. Matamoros, J. A. Ruddy, and Y. Y. Wang	Recovering different types of objects with one pass of the log
6,119,133	J. Celi Jr. and W. L. Nusbickel	Extensible method and apparatus for retrieving files having unique record identifiers as file names during program execution
6,119,143	D. M. Dias, J. Wolf, and P. S. Yu	A computer system and method for load balancing with selective control
6,119,151	T. G. Cantrell, S. Jaji, A. A. Shaheen, and R. B. Ward	System and method for efficient cache management in a distributed file system
6,119,161	C. Lita and J. R. Thompson	Managing connection requests in a dialup computer network
6,119,166	L. D. Bergman and J. Turek	Controlling communications with local application using a browser application
6,119,169	B. J. Schuh	Network system having disk drive bypass circuits
6,119,191	D. Neal and S. M. Thurber	Performing PCI access cycles through PCI bridge hub routing
6,119,201	J. E. Kulakowski, R. J. Means, and D. J. Winarski	Disk under-run protection using formatted padding sectors (amended)
6,119,202	J. M. Borkenhagen and J. I. Brookhouser	Method and apparatus to interleave level 1 data cache line fill data between system bus and level 2 data cache for improved processor performance
6,119,204	J. Chang, J. M. Nunez, T. A. Petersen, and Unknown	Data processing system and method for maintaining translation lookaside buffer (TLB) coherency without enforcing complete instruction serialization
6,119,209	E. M. Bauman, R. E. Galbraith, and M. Johnson	Backup directory for a write cache
6,119,219	D. Bair, M. S. Farrell, B. W. Krumm, P. K. Mak, J. A. Navarro, T. J. Slegel, and C. F. Webb	System serialization with early release of individual processor
6,119,224	C. P. Roth	Fast shift amount decode for VMX shift and VPERM instructions
6,119,241	M. S. Michail, W. D. Pricer, and S. T. Ventrone	Self regulating temperature/performance/voltage scheme for micros (X86)
6,119,246	A. Kitamorn, C. A. McLaughlin, and S. Nulu	Error collection coordination for software-readable and non-software readable fault isolation registers in a computer system
6,119,247	D. E. House and B. J. Owings	Remote debugging of internet applications
6,122,289	P. Brown, K. A. Cousins, J. R. Flanagan, B. Isrel, B. Rajaraman, A. J. Staggs, and M. Zee	Methods, systems and computer program products for controlling data flow through a communications adapter
6,119,331	T. Y. Chang, H. P. Lee, and J. Lo	Notching the first pole piece of a write head with a notching layer in a combined magnetic head
6,119,555	F. D. DiMaria	Precision punch and die design and construction
6,120,179	D. E. Houser and F. S. Poch	Method and apparatus for measuring drill hole temperatures
6,120,552	J. M. Kurtzberg, M. Levanoni, and J. S. Lew	A method to exhibit parallelism for computer implementation of a computational processing
6,120,885	A. J. Call, S. A. DeLaurentis, S. Farooq, S. K. Kang, S. Purushothaman, and K. A. Stalter	Structure, materials, and methods for socketable ball grid
6,120,909	N. A. Bojarczuk, S. Guha, and R. Haight	Monolithic silicon-based nitride display device
6,121,064	J. Lasky, B. Philips, A. Speranza, J. Wong, and M. Yu	STI fill for SOI which makes SOI inspectable
6,121,069	C. M. Boyko, A. P. Ingraham, V. R. Markovich, and D. J. Russell	Interconnect structure for joining a chip to a circuit card
6,121,078	J. DeBrosse and M. R. Wordeman	Integrated circuit planarization and fill biasing design method
6,121,106	W. F. Ellis, R. J. Houghton, M. G. Levy, and W. R. Tonti	Method for forming an integrated trench capacitor
6,121,122	J. Dunn, P. B. Gray, K. K. Kieft, N. Schmidt, and S. A. St. Onge	Method of contacting a silicide-based Schottky diode
6,121,128	M. C. Hakey, S. J. Holmes, D. V. Horak, and W. P. Noble	Method for making borderless wordline for DRAM cell
6,121,129	N. A. Greco, S. E. Greco, and T. Wagner	Method of contact structure formation
6,121,536	J. W. Malcolm	Method and apparatus for encoding test in a mid data stream
6,121,539	J. A. Casey, S. Dwyer, G. M. Johnson, D. C. Long, and K. Prettyman	Thermoelectric devices and methods for making the same
6,121,595	D. Lewis, A. Viehbeck, and S. J. Whitehair	Applicator to provide uniform electric and magnetic fields over a large area and for continuous processing
6,121,642	D. M. Newns	Junction mott transition field effect transistor (JMTFET) and switch for logic and memory applications
6,121,651	T. Furukawa, M. C. Hakey, S. J. Holmes, D. V. Horak, and J. A. Mandelman	DRAM cell with three-sided-gate transfer device

6,121,659	T. Christensen and J. E. Sheets	Buried patterned conductor planes for semiconductor-on-insulator integrated circuit
6,121,661	F. Assaderaghi, L. L. Hsu, and J. A. Mandelman	Silicon-on-insulation structure for electrostatic discharge protection and improved heat dissipation
6,121,771	A. Moser	Magnetic force microscopy probe with bar magnet tip
6,121,796	M. K. Ciraula and D. G. Mikan	Power-saving dynamic circuit
6,121,810	R. A. Philpott	Integrated delay line calibration method and apparatus for direct access storage device (DASD)
6,121,850	U. Ghoshal	Digitally adjustable inductive element adaptable to frequency tune an LC oscillator
6,121,971	D. D. Bardon, R. E. Berry, S. H. Isensee, and D. J. Roberts	Method and system for providing visual hierarchy of task groups and related viewpoints of a three dimensional environment in a display of a computer system
6,121,973	T. Furuhashi, T. Itoh, and K. Shimada	Quadrilateral mesh generation method and apparatus
6,122,056	M. Hibbs and S. Peng	Direct phase shift measurement between interference patterns using aerial image measurement tool (as amended)
6,122,125	D. L. Clare, G. M. Frees, and L. J. Serrano	Head selectable servo notch filter and method for improving servo system performance
6,122,138	V. D. Khanna, S. Kumar, and M. Sri-Jayantha	Disk drive with vibration synchronizing rib
6,122,139	H. Dang, N. Kagami, V. D. Khanna, G. McVicker, Y. Nakagawa, K. Satoh, A. Sharma, and M. Sri-Jayantha	Disk drive rotary actuator system including synchronous counter torque generator
6,122,144	T. Y. Chang, D. E. Heim, and H. P. Lee	Contoured pole tip structures for minimizing side track erasure
6,122,150	H. S. Gill	Antiparallel (AP) pinned spin valve sensor with giant magnetoresistive (GMR) enhancing layer
6,122,254	M. Aydemir and C. Jeffries	A method and apparatus for network flow control with perceptive parameters
6,122,256	N. C. Berglund	Fail-safe communications network for use in system power control
6,122,277	D. L. Garmire, D. G. Grice, and T. Zhang	Improved parallel computer network broadcasting and acknowledgement
6,122,627	M. J. Carey and G. G. Kiernan	A system, method, and program for object building in queries over object views
6,122,628	V. Castelli, C. S. Li, and A. Thomasian	Multidimensional data clustering and dimension reduction for indexing and searching
6,122,631	R. Berbec, J. L. Brady, J. M. Caffrey, J. Crimi, A. P. Fenaroli, M. C. Iatridis, G. S. Puchkoff, R. Sanchez, M. R. Whalen, and J. A. Williams	Dynamic server-managed access control for a distributed file system
6,122,634	S. Brodsky, S. Mitra, and R. Panwar	Fractal nested layout for hierarchical systems
6,122,636	W. E. Malloy, S. Moore, G. Robinson, and C. Tomlyn	Relational emulation of multi-dimensional database index
6,122,638	G. Huber and D. W. McCauley	Object-oriented processor and method for caching intermediate data in an object-oriented processor
6,122,659	H. T. Olnowich	Memory controller for controlling memory accesses across networks in distributed shared memory processing systems
6,122,660	Y. Baransky, H. Franke, and P. Pattnaik	Method for distributing digital TV signal and selection of content
6,122,666	J. Beurket, P. K. Malkin, W. Rubin, P. S. Yu, and G. Ziedins	Method for collaborative transformation and caching of web objects in a proxy network
6,122,674	H. T. Olnowich	Bi-directional network adapter for interfacing local node of shared parallel processing system to multi-stage switching network for communications with remote node
6,122,683	Y. M. Ku and T. Q. Nguyen	Handshake minimizing serial-to-parallel interface with shift register coupled by parallel bus to address logic and control logic
6,122,691	R. K. Arimilli, J. S. Dodson, J. D. Lewis, and D. E. Williams	Apparatus and method of layering cache and architectural specific functions to permit generic interface definition
6,122,692	A. E. Okpiz and T. A. Petersen	Eliminate adjacent address collisions on a pipelined response bus
6,122,710	M. Kumar and H. Pham	Dynamic word line driver for cache
6,122,716	J. L. Combs	Method of authentication for use in computer system with mapped memory
6,122,723	K. G. Day, D. E. Denning, R. G. Emberty, C. A. Klein, D. D. McBride, and E. Pawlowski	Switching multi-initiator SCSI devices to a singular target bus
6,122,754	W. Litwin, M. J. Menon, and T. Risch	Method and system for data recovery using a distributed and scalable data structure
6,122,760	D. A. Grosch and M. D. Knox	Burn-in technique for chips containing different types of IC circuitry
6,122,926	S. Kang, H. V. Mahaney, R. R. Schmidt, and P. Singh	Low thermal conductance insulated cooling assembly for IC chip modules

6,123,825	S. H. Boettcher, P. DeHaven, C. C. Parks, A. H. Simon, and C. Uzoh	Electromigration-resistant copper microstructure and process of making
6,124,041	F. Y. Audi, L. D. David, R. S. Divakaruni, S. Farooq, L. W. Herron, H. M. Lasky, A. Mastreani, G. Natarajan, S. N. Reddy, V. Sura, R. V. Vallabhaneni, and D. R. Wall	Copper-based paste containing copper aluminate for microstructural and shrinkage control of copper-filled vias
6,124,074	J. F. Maniscalco and P. R. Varanasi	Photoresist compositions with cyclic olefin polymers and hydrophobic non-steroidal multi-alicyclic additives
6,124,141	K. P. Muller and V. C. Prakash	Non-destructive method and device for measuring the depth of a buried interface
6,124,199	G. B. Bronner, J. P. Gambino, D. E. Kotecki, and C. J. Radens	Method for simultaneously forming a storage-capacitor electrode and interconnect
6,124,639	A. G. Domenicucci, L. M. Gignac, Y. Wang, H. S. Wildman, and K. H. Wong	Flat interface for a highly disordered metal-silicon contact barrier film
6,124,856	E. P. Bryan, W. H. Jones, and P. B. Moody	Method and apparatus for displaying modeless bar interfaces in a computer system
6,124,857	I. Inoue, T. Itoh, S. Karube, K. Shimada, and A. Yamada	Meshing method and apparatus
6,124,914	P. Chaudhari, C. E. Farrell, J. A. Lacey, and S. C. Lien	Method and apparatus for forming an alignment pattern on a surface using a particle beam useful for a liquid crystal
6,124,994	D. J. Malone	System and method for providing nonadjacent redundancy synchronization bytes
6,125,005	F. C. Lee, U. V. Nayak, and R. N. Payne	Altitude insensitive air bearing using pitch compensation for data storage applications
6,125,007	J. L. Beck, T. A. Gregory, and C. G. Keller	Membrane seal secured to inner and outer oscillating bearing members for small angle oscillations within confined axial space
6,125,036	S. Kang, H. V. Mahaney, R. R. Schmidt, and P. Singh	Moisture barrier seals for cooled IC chip module assemblies
6,125,119	R. J. Cherukuri and D. A. Sinicrope	Dynamic permanent virtual connection (DPVC) at user-to-network interfaces in frame relay networks
6,125,361	S. Chakrabarti and B. E. Dom	Feature diffusion across hyperlinks
6,125,364	F. Castellucci, A. R. Greef, and T. Maguire	A flexible object persistence framework using data cursor objects loaded from superclasses
6,125,370	D. K. Courter, P. S. Desai, C. A. Friske, J. M. Garth, K. D. Gougherty, L. M. Kunioka-Weiss, D. E. Raiman, J. A. Ruddy, J. A. Watts, and K. R. Ziedenstein	Repartitioning data
6,125,382	C. H. Brobst, C. H. Chou, and S. N. Gerard	Distributed thread mechanism and method
6,125,384	M. L. Brandt, K. Brown, P. Dykes, E. D. Lindberg, D. E. Olson, J. E. Selden, D. D. Snyder, and J. O. Walts	Computer apparatus and method for communicating between software applications and computers on the world-wide web
6,125,393	C. E. Clark, S. J. Greenspan, J. D. Haggar, and D. R. Sutherland	System of compressing the tail of a sparse log stream of a multisystem environment
6,125,400	G. A. Cohen, D. L. Kaminsky, and J. Seeber	Method and system of running object oriented programs across a network through compression and distillation of remote method invocation
6,125,417	R. T. Bailis, T. Bonds, R. L. Draughn, A. D. Genzlinger, D. J. Jensen, C. S. Lingafelt, B. S. Oakley, and M. J. Ward	Hot plug of adapters using optical switches
6,125,444	M. A. Check, M. S. Farrell, J. S. Liptay, T. J. Slegel, and C. F. Webb	Millimode capable computer system providing global branch history table disables and separate millicode disables which enable millicode disable to be turned off for some sections of code execution but not disabled for all
6,125,461	L. M. Huisman, D. R. Knebel, P. Nigh, P. N. Sanda, and X. Xiao	Method for identifying long paths in integrated circuits
6,125,465	W. V. Huott, T. J. Koprowski, and T. G. McNamara	Isolation/removal of faults during LBIST testing
6,125,467	R. C. Dixon	Method and apparatus for partial word read through ECC block
6,125,496	L. C. Stevens	Pad and roller assembly for cleaning semiconductor wafers
6,125,531	D. S. Farquhar, V. R. Markovich, K. I. Papathomas, and L. L. Schmidt	Method of making a printed circuit board having filled holes and a fill member for use therewith including reinforcement means
6,125,687	G. M. McClelland and C. T. Rettner	Apparatus for measuring outgassing of volatile materials from an object
6,126,203	C. Dwork, M. Naor, and F. Pestoni	Machine-readable checks
6,126,456	J. S. Campbell and R. W. Nesky	Solder column tip compliancy modification for use in BGA socket connector

6,126,761	P. Dehaven, C. C. Goldsmith, J. L. Hurd, S. Kaja, M. S. Legere, and E. D. Perfecto	Process of controlling grain growth in metal films
6,126,798	R. J. Contolini, J. O. Dukovic, and J. Reid	Membrane partition system for plating of wafers
6,126,806	C. Uzoh	Enhancing copper electromigration resistance with indium and oxygen lamination
6,126,848	J. A. Gilhooly, L. Li, C. Morgan, W. Surovic, and C. Wei	Indirect endpoint detection by chemical reaction and chemiluminescence
6,127,025	A. C. Bhatt, D. H. Glatzel, V. R. Markovich, A. F. Moring, K. I. Papathomas, and D. J. Russell	Circuit board with wiring sealing filled holes
6,127,045	H. S. Gill	Magnetic tunnel junction device with optimized ferromagnetic layer
6,127,053	T. Lin, D. Mauri, and J. F. Smyth	Spin valves with high uniaxial anisotropy reference and keeper layers
6,127,069	J. G. Balz, M. W. Kapfhammer, M. LaPlante, and D. C. Long	A method of visually inspecting positioning of a pattern on a substrate
6,127,071	N. Lu	Serif mask design for correcting severe corner rounding and line end shortening in lithography
6,127,097	N. R. Bantu, A. Bhatt, A. C. Bhatt, G. W. Jones, J. A. Kotylo, R. J. Owen, K. I. Papathomas, and A. K. Vardya	Photoresist develop and strip solvent compositions and method for their use
6,127,204	P. D. Isaacs, C. J. Mathison, and M. Swain	Column grid array or ball grid array pad on via
6,127,215	H. O. Joachim, J. A. Mandelman, and R. Rengarajan	Deep divot mask for enhanced buried-channel PFET performance and reliability
6,127,253	J. M. Rubino and R. Saraf	Lead-free interconnection for electronic devices
6,127,686	C. E. Obszarny	Thin resist process by sub-threshold exposure
6,127,721	B. Dinkel and C. Narayan	Soft passivation layer in semiconductor fabrication
6,127,731	M. K. Hoffmeyer	Capped solder bumps which form an interconnection with a tailored reflow melting point
6,127,735	D. G. Berger, G. Brouillette, D. Danovitch, P. A. Gruber, B. L. Humphrey, M. Liehr, W. T. Motsiff, and C. J. Sambucetti	Interconnect for low temperature chip attachment
6,127,832	R. R. Comulada, M. Goldowsky, J. P. Karidis, G. McVicker, and Y. Y. Yu	Electrical test tool having easily replaceable electrical probe
6,127,840	P. W. Coteus, D. M. Dreps, and F. D. Ferraiolo	Dynamic line termination clamping circuit
6,127,851	C. E. Boice, J. M. Kaczmarczyk, and M. P. Vachon	Circuit and method for differentiating multiple modules
6,127,866	A. M. Chu, J. M. Dortu, J. A. Fifield, and J. E. Rotella	Delay-locked-loop (DLL) having symmetrical rising and falling clock edge type delays
6,127,867	T. C. Coughlin Jr. and W. F. Lawson	Zero set-up high speed CMOS latched-receiver
6,127,878	D. Hanson and G. Mueller	Driver circuit
6,128,017	C. Alimpich, B. Jeffcoat, D. Neuhard, L. L. Vigil, and J. P. Wittig	Data processor controlled display interface for control of operations with unintrusive error message monitoring
6,128,025	A. A. Bright, S. Kosonocky, and K. W. Warren	Embedded frame buffer system and synchronization method
6,128,089	C. P. Ausschnitt and D. Nyyssonen	Combined segmented and nonsegmented bar-in-bar targets
6,128,149	R. Y. Li	Method and system for detecting common mode disturbances from a dual stripe magnetoresistive head
6,128,178	D. M. Newns	Very thin film, capacitor for dynamic random access memory (DRAM)
6,128,262	C. DeCusatis and L. Jacobowitz	System for creating, reading and writing on rotatable information storage media, a method for customizing said media with timing information
6,128,267	C. DeCusatis and L. Jacobowitz	Active tracking method for optical disk storage
6,128,484	K. L. Milsted, K. D. Nguyen, and P. Singkornrat	Wireless transceivers for remotely controlling a computer
6,128,510	T. J. Beukema, M. Cina, E. B. Flint, B. P. Gaucher, Y. Kwark, M. Oprysko, W. E. Pence, and S. Ponnappalli	Cordless data/facsimile modem
6,128,611	B. Doan and S. M. Ho	An internet enabled generic application program for accessing hierarchical data
6,128,612	J. Brereton, A. R. Coden, and M. S. Schwartz	Method and system for translating an ad-hoc query language using common table expressions
6,128,619	A. J. Fogarasi, S. M. Ho, D. W. Ling, and K. M. McBride	Generating an internet application for accessing a hierarchical database

6,128,622	M. A. Bach, I. H. Chung, J. K. Flanigan, C. A. Garcia, J. E. Hill, S. T. Kuo, T. Lai, K. M. McBride, and H. M. Rowe-Anderson	A user interface for creating programs specifications for accessing database persistent objects
6,128,630	D. M. Shackelford	Data processor controlled display interface for control of operations with unintrusive error message monitoring
6,128,648	M. S. Chen, K. L. Wu, and P. S. Yu	An information handling system and method for maintaining coherency between servers and mobile terminals
6,128,655	D. Fields, S. D. Hassinger, and W. Hurley	Distribution mechanism for filtering, formatting and reuse of web based content
6,128,674	B. Beukema, P. A. Buckland, W. T. Chen, D. A. Elko, I. D. Judd, and R. J. Recio	Method of minimizing host CPU utilization in driving an adapter by residing in system memory a command/status block a soft interrupt block and a status block queue
6,128,683	M. A. Griese, B. N. Harding, and D. C. Mairet	Method and system for allowing a plurality of SCSI hosts to interact with a DASD storage subsystem
6,128,705	R. K. Arimilli, J. M. Kaiser, and D. E. Williams	Method and apparatus for executing multiply-initiated, multiply-sourced variable delay system bus operations
6,128,707	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Adaptive writeback of cache line data in a computer operated with burst mode transfer cycles
6,128,710	S. J. Greenspan, K. E. Plambeck, and C. A. Scalzi	Method utilizing a set of blocking-symbol resource-manipulation instructions for protecting the integrity of data in noncontiguous data objects of resources in a shared memory of a multiple processor computer system
6,128,720	J. Glossner, L. D. Larsen, G. G. Pechanek, and S. Vassiliadis	Distributed processing array with component processors performing customized interpretation of instructions
6,128,722	R. E. Fry, D. Q. Nguyen, and A. T. Williams	Data processing system having an apparatus for exception tracking during out-of-order operation and method therefor
6,128,738	R. Doyle, J. R. Hind, and J. H. King	Certificate based security in SNA data flows
6,128,746	S. D. Clark, R. S. Jensen, J. A. Kirscht, P. W. Rudrud, and M. G. Veldhuizen	Continuously powered mainstore for large memory subsystems
6,128,752	K. R. Griess, A. C. Merenda, and D. L. Pierce	Fault tolerant design for AC defects
6,128,755	S. E. Bello, K. A. Hua, and J. K. Peir	Fault tolerant multiple processor system with signature voting
6,129,476	R. T. Berman, C. R. Mendel, and W. Sprague	Breakaway precision robot end effector
6,129,610	J. K. Stephens	Polish pressure modulation in CMP to preferentially polish raised features
6,129,955	D. L. Dittrich, S. J. Fuerniss, K. I. Papathomas, and D. W. Wang	Encapsulating a solder joint with a photo cured epoxy resin or cyanate
6,130,145	J. Faltermeier, M. Ilg, and R. Srinivasan	Improved polycide
6,130,161	L. Ashley, H. M. Dalal, D. B. Nguyen, and H. S. Rathore	A method of forming copper interconnections with enhanced electromigration and reduced defect sensitivity
6,130,170	L. D. David and L. Fanti	Process improvements for titanium-tungsten etching in the presence of electroplated C4's
6,130,182	M. Naeem	Dielectric catalyst structures
6,130,432	H. C. Pfeiffer, J. J. Senesi, and M. A. Sturans	Particle beam system with dynamic focusing
6,130,453	S. N. Mei and E. J. Vishnesky	Flash memory structure with floating gate in vertical trench
6,130,469	J. Bracchitta and W. D. Pricer	Electrically alterable antifuse using FET
6,130,472	C. Feger and J. P. Hummel	Moisture and ion barrier for protection of devices and interconnect structures
6,130,475	T. Dell, G. C. Feng, and M. W. Kellogg	Clock distribution system for synchronous circuit assemblies
6,130,476	W. R. LaFontaine Jr., P. A. Mescher, and C. G. Woychik	Semiconductor chip package having chip-to-carrier mechanical/electrical connection formed via solid state diffusion
6,130,479	E. D. Blackshear and P. Chalco	Nickel alloy films for reduced intermetallic formation in solder
6,130,733	A. C. Lowe	A light-scattering display having a structured solid state selectivity reflective layer formed inside the display cell
6,130,750	C. P. Ausschnitt and T. Brunner	Optical metrology tool and method of using same
6,130,795	D. A. Freitas and K. R. Vannorsdel	Method and apparatus to sense and report connection integrity of a differential ECL transmission line having proper parallel termination
6,130,809	H. A. Santini	Write head before read head constructed merged magnetic head with track width and zero throat height defined by first pole tip
6,130,814	J. Z. Sun	Current-induced magnetic switching device and memory including the same
6,130,835	R. E. Scheuerlein	Voltage biasing for magnetic RAM with magnetic tunnel memory cells
6,130,854	S. W. Gould, J. A. Iadanza, F. R. Keyser, and T. Zittirsch	Field-programmable memory array
6,130,889	R. H. Boivie, N. K. Feldman, A. Viswanathan, and R. M. Woundy	Determining and maintaining hop-count for switched networks

6,131,110	C. Bates and P. R. Day	System and method for predicting user interest in unaccessed sites by counting the number of links to the unaccessed sites in previously accessed sites
6,131,113	E. Ellsworth, L. H. Evans, S. S. Ghoman, T. C. Jarvis, M. J. Kalos, R. O'Neill, L. Phan, D. B. Schreiber, and B. D. Watson	Managing a shared resource in a multi-processor system
6,131,182	M. Beakes, B. A. Chappell, T. I. Chappell, G. S. Ditlow, B. L. Dorfman, B. M. Fleischer, V. Narayanan, R. Philhower, G. A. Sai Halasz, G. Shahidi, and D. Widiger	Method and apparatus for synthesizing and optimizing control logic based on SRCMOS logic array macros
6,131,185	N. Coskun and B. A. Tate	Method and system for visually debugging on object in an object oriented system
6,131,187	A. C. Chow and Y. Liao	Method and system for translating exception handling semantics of a bytecode class file
6,131,258	J. H. Comfort, A. Grill, D. E. Kotecki, and K. L. Saenger	Sidewall capacitor with L-shaped dielectric
6,131,271	R. E. Fontana, H.-C. Guthrie, W. L. Guthrie, E. J. Lee, L.-C. Lee, and F. Martin	Method of planarizing first pole piece layer of write head by lapping without delamination of first pole piece layer from wafer substrate
6,131,278	S. W. MacQuarrie, W. R. Storr, and J. W. Wilson	Metal substrate having an IC chip and carrier mounting
6,131,279	G. W. Jones, R. W. Keesler, V. R. Markovich, W. J. Rudik, J. W. Wilson, and W. E. Wilson	Integrated manufacturing packaging process
6,131,794	J. A. Burke, D. C. Olson, J. Tersigni, and J. S. Wolfe	Shaving blade for chip site dressing
6,131,796	S. Kaja, S. N. Reddy, and D. R. Wall	Direct brazing of refractory metal features
6,132,510	A. W. Buechele, J. T. Butler, K. O. Muggenburg, and M. G. Reichel	Nozzle apparatus for extruding conductive paste
6,132,632	D. E. Haney, R. Huber, C. Hwang, D. Perez, and J. W. Williams	Method and apparatus for achieving etch rate uniformity in a reactive ion etcher
6,132,644	M. Angelopoulos, E. D. Babich, I. Babich, K. J. Chen, W. Moreau, and D. E. Seeger	Energy sensitive electrically conductive admixtures, uses thereof, methods of fabrication and structures fabricated therewith
6,132,813	P. C. Chen, R. Hsiao, S. V. Nguyen, and A. C. Ting	High density plasma surface modification for improving antiwetting properties
6,132,940	R. Mih and F. X. Zach	Method for producing constant profile sidewalls
6,133,136	D. C. Edelstein, V. McGahay, H. A. Nye III, B. R. Ottey, and W. Price	Robust interconnect structure
6,133,139	H. M. Dalal, D. B. Nguyen, and H. S. Rathore	Self-aligned composite insulator with sub-half-micron multilevel high density electrical interconnections and process thereof
6,133,540	J. U. Knickerbocker, M. Laplante, D. C. Long, and T. Weiss	Apparatus and method for producing punch pin with spherical head
6,133,608	F. Assaderaghi, R. C. Flaker, L. L. Hsu, and J. A. Mandelman	SOI-body selective link method and apparatus
6,133,610	R. J. Bolam, S. Kulkarni, and D. J. Schepis	Silicon-on-insulator chip having an isolation barrier for reliability and process of manufacture
6,133,633	D. G. Berger, G. Brouillette, D. Danovitch, P. A. Gruber, R. S. Patel, S. Roux, C. J. Sambucetti, and J. L. Speidell	Method for building interconnect structures by injection molded solder and structures built
6,133,749	P. R. Hansen and H. Pilo	Variable impedance output driver circuit using analog biases to match driver output impedance to load input impedance
6,133,758	C. M. Durham and P. J. Klim	Selectable self-timed replacement for self-resetting circuitry
6,133,759	J. Beck, R. P. Masleid, and T. R. Toms	Decoupled reset dynamic logic circuit
6,133,799	W. H. Favors, E. W. MacDonald, S. Mukherjee, and L. Warriner	Voltage-controlled oscillator utilizing threshold voltage control silicon on insulator MOSFETs
6,133,916	A. E. Bukszar, C. A. Pickover, and D. J. Winarski	Graphical user interface providing access to files downloaded over a network
6,134,068	J. E. Donaldson	Disk drive spindle synchronization apparatus and method
6,134,080	M. Y. Chang and M. A. Church	Magnetic head with precisely defined zero throat height
6,134,107	M. L. Buller, B. Kerrigan, and J. W. Young	Reverse convective airflow cooling of computer enclosure
6,134,164	G. M. Lattimore and G. A. Yeung	Sensing circuit for a memory cell array
6,134,182	J. J. Covino and H. Pilo	Cycle independent data to echo clock tracking circuit
6,134,284	N. James	Circuit and method for receiving system clock signals
6,134,366	M. L. Loeb and S. E. Wallace	Reduced dispersion optical waveguide and methods for fabricating the same
6,134,540	M. J. Carey and G. G. Kiernan	A system, method, and program for applying query rewrite technology to object building

6,134,541	V. Castelli, C. S. Li, and A. Thomasian	Searching multidimensional indexes using associated clustering and dimension reduction information
6,134,555	A. Chadha, B. Iyer, H. Messatfa, and J. Yi	Dimension reduction using association rules for data mining application
6,134,588	T. Guenthner, F. D. Lawlor, and C. Schmitt	High availability web browser access to servers
6,134,597	P. F. Rieth and J. S. Stevens	CRC hash compressed server object identifier
6,134,616	D. L. Beatty	Method and apparatus for dynamic reenumeration and reconfiguration of computer devices after system hibernation
6,134,620	C.-S. Hui, K. J. Venkatramani, and B. J. Wolford	Tri-state bus contention circuit preventing false switching caused by poor synchronization
6,134,621	R. A. Kelley, D. Neal, J. Nicholson, and S. M. Thurber	Variable slot configuration for multi-speed bus
6,134,623	R. E. Garcia and J. R. Paveza	Method and system for taking advantage of a pre-stage of data between a host processor and a memory system
6,134,645	D. Q. Nguyen	Instruction completion logic distributed among execution units for improving completion efficiency
6,134,646	K. A. Feiste, T. Ngo, and A. M. Youngs	System and method for executing and completing store instructions
6,134,657	W. J. Johnson, R. S. Keller, G. C. Manthuruthil, and M. L. Williams	Method and system for access validation in a computer system
6,134,676	C. B. Ford and G. A. Van Huben	Programmable hardware event monitoring method
6,134,682	S. F. Oakland	Testable bus control logic circuitry and method for using same
6,134,684	J. Baumgartner, K. D. Klapproth, and D. Mui	Method and system for error detection in test units utilizing pseudo-random data
6,134,699	J. A. Steenburgh and S. S. Woodward	Method and apparatus for detecting virtual address parity error for a translation lookaside buffer
6,134,704	J. Adams, G. Clarke, K. J. Goodnow, S. W. Gould, and S. T. Ventrone	Integrated circuit macro apparatus
6,134,710	F. E. Levine and C. P. Roth	Adaptive method and system to minimize the effect of long cache misses
6,134,772	J. D. Kulesza, V. R. Markovich, K. I. Papathomas, and J. G. Sabia	Via fill compositions for direct attach of devices and methods of applying same
6,134,776	M. K. Hoffmeyer	Heatsink and package structure for wirebond chip rework and replacement
6,134,858	J. B. Gutelius, S. C. McIntosh, and B. D. Notohardjono	Computer equipment having an earthquake damage protection mechanism
6,135,379	A. J. Argumedo	Magnetic recording tape leader for single reel tape cartridge
6,135,865	G. J. Beardsley, C. Huynh, S. Messier, and D. Walker	CMP apparatus with built-in slurry distribution and removal
6,136,419	B. V. Fasano, R. F. Indyk, S. M. Kamath, J. U. Knickerbocker, S. I. Langenthal, D. O'Connor, and S. N. Reddy	Ceramic substrate having a sealed layer
6,136,498	P. Jagannathan, L. L. Linehan, W. M. Moreau, and R. J. Smith	Polymer-bound sensitizer
6,136,513	A. P. Angelopoulos, G. W. Jones, L. J. Matienzo, T. R. Miller, and W. D. Taylor	Method of uniformly depositing seed and a conductor and the resultant printed circuit structure
6,136,655	F. Assaderaghi, C. L. Bertin, J. P. Gambino, L. L. Hsu, and J. A. Mandelman	Method of making low voltage active body semiconductor device
6,136,656	J. S. Brown, R. J. Gauthier, W. R. Tonti, and S. H. Voldman	Method to create a depleted poly MOSFET
6,136,664	L. Economikos, D. E. Kotecki, and J. A. Mandelman	Filling of high aspect ratio trench isolation
6,136,669	F. A. Flitsch and M. S. Fung	Mobile charge immune process
6,136,686	J. P. Gambino, M. Jaso, and H. Wong	Fabrication of interconnects with two different thicknesses
6,136,709	L. A. Clevenger, R. C. Iggulden, and S. Weber	Improved metal line deposition process
6,136,733	L. R. Blumberg, R. M. Japp, W. J. Rudik, and J. F. Surowka	Method of reducing coefficient of thermal expansion in chip attach packages
6,137,042	J. M. Kurtzberg and J. S. Lew	Visual display for music generated via electric apparatus
6,137,128	S. J. Holmes, H. L. Kalter, S. Tiwari, and J. J. Welser	Self-isolated and self-aligned 4F-square vertical FET-trench cells
6,137,129	C. L. Bertin, J. E. Cronin, E. L. Hedberg, and J. A. Mandelman	High performance direct coupled FET memory cell
6,137,158	M. S. Cohen, M. Gauvin, G. W. Johnson, D. Kuchta, A. A. Lacerte, N. A. Lee, S. E. Ouimet, S. Spanoudis, S. Tremblay, and J. Trehwella	Leadframe and leadframe assembly for parallel optical computer link

6,137,161	D. A. Gilliland and J. L. Peacock	Interposer array module for capacitive decoupling and filtering
6,137,299	R. R. Cadieux, G. C. Correia, G. Hill, and A. P. Ingraham	Method and apparatus for testing integrated circuit chips
6,137,376	J. Imbornone and J. M. Maurant	Printed balun circuits
6,137,468	A. E. Martinez and S. J. Martinez	Method and apparatus for altering a display in response to changes in attitude relative to a plane
6,137,476	M. Hocker and D. Winarski	Data mouse
6,137,487	M. L. Mantha	Method and apparatus for manipulating graphical objects in a data processing system
6,137,494	S. I. Nin	Method and means for evaluating a tetrahedral linear interpolation function
6,137,578	C. P. Ausschnitt	Segmented bar-in-bar target
6,137,594	W. C. Decker, H. C. Lee, and J. L. Zable	System, method and program for converting an externally defined four dimensional colorant (CMYK) into an equivalent four dimensional colorant defined in terms of the four inks (C'M'Y'K')
6,137,596	W. C. Decker, H. C. Lee, and J. L. Zable	System, method, and program for converting three dimensional colorants to more than three dimensional colorants
6,137,643	D. T. Flynn	Thermal asperity detection with long magnet rejection for direct access storage device (DASD)
6,137,644	S. R. Hetzler, P. Kasiraj, and R. H. New	Method for adaptive formatting and track traversal in data storage devices
6,137,684	D. N. Ayd, R. M. Ecker, F. Edlinger, T. S. Farrow, and P. W. Kelly	Camming mechanism for joining modular electronic enclosures
6,137,694	D. P. Beaman, B. Kerrigan, and J. W. Young	Integral EMC shielding for computer enclosures
6,137,797	B. M. Bass, J. S. Chorpenning, D. R. Henderson, E. H. Ku, K. H. Potter Jr., S. B. Schrum Jr., M. S. Siegel, and N. C. Strole	Process definition for source route switching
6,137,804	S. S. Allison, K. J. Barker, S. H. Johnson, and J. K. Lee	A system and method for automatic retry of a transmit, independent of a host processor, after an underrun occurs in a LAN
6,137,805	V. Berstis	Method and apparatus to remotely configure a data processing system
6,138,099	J. R. Lewis and M. E. Smith	Automatically updating language models
6,138,103	F. Cheng, D. P. Connors, T. R. Ervolina, and R. Srinivasan	Method for production planning in an uncertain demand environment
6,138,115	R. Agrawal, M. Mehta, and J. C. Shafer	Method and system for generating a decision-tree classifier in parallel in a multi-processor system
6,138,117	R. J. Bayardo	Method and system for mining long patterns from databases
6,138,124	S. R. Beckhardt	Field level replication method
6,138,127	M. Pasch	Apparatus and method for recycling an object
6,138,150	S. R. Nichols, K. N. Schroeder, and S. L. Wentz	Method for remotely controlling computer resources via the internet with a web browser
6,138,156	J. C. Fletcher, D. L. Kaminsky, and C. Kessler	Selecting and applying content-reducing filters based on dynamic environmental factors
6,138,160	S. J. Boies, S. Levy, and M. J. Ryan	System with wake up server and wake up monitor for facilitating dial up connection between two application partners via internet
6,138,169	T. J. Freund, S. A. Holdsworth, and S. A. Smith	System and method for creating an object oriented transaction service that achieves interoperability with encina procedural transactions
6,138,187	D. E. Denning, R. G. Emberty, and C. A. Klein	Method and system for increasing spatial reuse in a serial storage architecture subsystem
6,138,200	C. R. Ogilvie	System for allocating bus bandwidth by assigning priority for each bus duration time slot to application using bus frame and bus duration
6,138,206	M. T. Fisher and G. D. Gilda	Data register for multicycle data cache read
6,138,208	S. H. Dhong, H. P. Hofstee, D. Meltzer, and J. A. Silberman	Multiple level cache memory with overlapped L1 and L2 memory access
6,138,209	D. J. Krolak and S. B. Levenstein	Data processing system and multi-way set associative cache utilizing class predict data structure and method thereof
6,138,215	J. H. Bartik and M. A. Check	Method for absolute address history table synonym resolution
6,138,218	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Forward progress on retried snoop hits
6,138,223	M. A. Check and J. S. Liptay	Absolute address history table index generation for predicting instruction and operand cache accesses
6,138,224	R. Lisle	Method for paging software wavetable synthesis samples
6,138,242	L. H. Massman and W. W. Plyler	Power adapter for powering a remote device through a computer data port
6,138,243	B. G. Mealey and M. S. Williams	Method and system for keeping time across a multiprocessor system

6,138,350	A. C. Bhatt, D. H. Glatzel, V. R. Markovich, A. F. Moring, K. I. Papathomas, and D. J. Russell	Process for manufacturing a circuit board with filled holes
6,138,839	W. V. Cranston, J. McCloskey, J. J. Tout, and F. C. Yentz	Pivoted latching bar for retaining expansion cards
6,138,893	D. V. Caletka, K. Knadle, and C. G. Woychik	Method for producing a reliable BGA solder joint interconnection
6,139,078	T. Bodiker and J. P. Hollowell	Self-aligning end effector for small components
6,139,198	S. H. Danforth, I. R. Forman, and H. H. Madduri	System and method for enabling tracing of program execution in an object-oriented system
6,139,647	M. Armacost, S. Grundon, D. Harmon, and D. M. Kenney	Selective removal of vertical portions of a film
6,139,661	J. E. Cronin, J. D. Poole, and M. C. Weller	Two step SMT method using masked cure
6,139,666	B. V. Fasano, R. F. Indyk, S. M. Kamath, S. I. Langenthal, and S. N. Reddy	Method and material for producing ceramic surfaces with easily removable contact sheets
6,139,759	L. A. Doezenia, P. Kaszuba, L. Moszkowicz, J. M. Never, and J. A. Slinkman	Method of manufacturing silicided silicon microtips for scanning probe microscopy
6,139,778	M. Angelopoulos, R. A. DiPietro, and J. M. Shaw	Vibrational methods of deaggregation of electrically conductive polymers and precursors thereof
6,140,015	A. Katnani, M. Khojasteh, R. Kwong, and P. R. Varanasi	Photoresist compositions with pendant polar-functionalized aromatic groups and acid-labile branching
6,140,171	A. Allen, J. Lasky, R. W. Mann, J. Pekarik, J. Rankin, E. Sengle, and F. R. White	FET device containing a conducting sidewall spacer for local interconnect and method for its fabrication
6,140,175	R. L. Kleinhenz and C. J. Radens	Self-aligned deep trench DRAM array device
6,140,208	F. Agahi, G. B. Bronner, B. Flietner, E. N. Hammerl, H. Ho, and R. Srinivasan	Shallow trench isolation (STI) with bilayer of oxide-nitride for VLSI applications
6,140,217	H. C. Jones and J. G. Ryan	Technique for extending the limits of photolithography
6,140,226	A. Grill, J. P. Hummel, C. Jahnes, V. V. Patel, and K. Saenger	Dual damascene processing for semiconductor chip interconnects
6,140,234	S. E. Greco and C. Uzoh	Method to selectively fill recesses with conductive metal
6,140,462	M. Angelopoulos, A. G. MacDiarmid, and W. G. Zheng	Oxidative/reductive methods of deaggregation of electrically conductive polymers and precursors thereof and methods of fabricating articles therewith
6,140,846	F. Chan, S. P. Koch, and D. W. Stout	Driver circuit configured for use with receiver
6,140,855	D. Hanson, T. Kirihaata, and G. Mueller	Dynamic-latch-receiver with self-reset pointer
6,140,885	W. W. Abadeer, J. Connor, and P. R. Hansen	On-chip automatic system for impedance matching in very high speed input-output chip interfacing
6,140,990	E. S. Schlig	Active matrix liquid crystal display incorporating pixel inversion with reduced drive pulse amplitudes
6,141,005	D. J. Hetherington, D. B. Kumhyr, and M. McNally	Combined display of locale-sensitive calendars in a distributed computer enterprise environment
6,141,008	M. A. Bloomfield	Method and system for providing size adjustment for a maximized window in a computer system graphical user interface
6,141,093	B. Argyle, A. Halperin, M. E. Scaman, and E. J. Yarmchuk	Method and apparatus for locating power plane shorts using polarized light microscopy
6,141,176	M. Blaum, B. Kabelac, L. Serrano, and M. H. Yu	Data storage disk with information encoded in the position error signal fields
6,141,191	W. Y. Lee, T. Lin, D. Mauri, and R. J. Wilson	Spin valves with enhanced GMR and thermal stability
6,141,200	F. F. Hinedi and L. Mamileti	Stacked PFET off-chip driver with a latch bias generator for overvoltage protection
6,141,209	D. P. Beaman, M. L. Buller, L. T. Cooper, B. Kerrigan, J. R. Pugley, and J. W. Young	Quick-release computer chassis
6,141,216	J. A. Ho-Lung, V. Kamath, and T. Wong	Quick-release hinge joint for heat pipe
6,141,242	F. Assaderaghi, L. L. Hsu, and J. A. Mandelman	Low cost mixed memory integration with substantially coplanar gate surfaces
6,141,245	C. L. Bertin, J. A. Fifield, E. L. Hedberg, R. J. Houghton, T. D. Sullivan, S. W. Tomashot, and W. R. Tonti	Impedance control using fuses
6,141,267	L. L. Hsu, T. K. Kirihaata, and C. Narayan	Defect management engine for semiconductor memories and memory systems
6,141,325	O. Gerstel	Paradigm for enabling interoperability between different subnetworks

6,141,351	K. J. Goodnow, M. S. Michail, W. D. Pricer, and S. T. Ventrone	Radio frequency bus for broadband microprocessor communications
6,141,403	F. W. Dain, C. B. Dillon, L. V. Marks, and S. D. Rhodes	Modem protocol
6,141,445	V. Castelli, I. Kontoyiannis, J. T. Robinson, and J. Turek	Multiresolution lossless/lossy compression and storage of data for efficient processing thereof
6,141,632	F. Assaderaghi, P. D. Muench, G. E. Smith, L. F. Wagner, and T. L. Walters	Method to use in simulation of an SOI device
6,141,634	E. B. Flint, B. P. Gaucher, Y. Kwark, and D. Liu	AC power line network simulator
6,141,659	K. S. Barker, A. O'Donnell, D. A. Schell, and B. T. Smith	Systems, methods and computer program products for retrieving documents from multiple document servers via a single client session
6,141,660	M. A. Bach, K. J. Charlet, S. M. Ho, K. M. McBride, H. M. Rowe-Anderson, T. B. Sander, and T. Vogel	A command line interface for creating business objects for accessing a hierarchical database
6,141,699	J. L. Gentry, J. Luzzi, S. M. Reys, J. F. Taylor, and G. Zhu	Interactive display system for sequential retrieval and display of a plurality of interrelated data sets
6,141,714	R. K. Arimilli, J. M. Kaiser, and D. E. Williams	Method and apparatus for executing self-snooped unresolvable system bus operations
6,141,731	B. C. Beardsley, M. T. Benhase, D. A. Martin, R. L. Morton, and M. A. Reid	Method and system for managing data in cache using multiple data structures
6,141,733	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Cache coherency protocol with independent implementation of optimized cache operations
6,141,758	M. Benantar, R. H. High Jr., and M. Rath	Method and system of maintaining client server security associations in a distributed computing system
6,141,771	D. G. Hostetter and J. T. O'Brien	Method and system for providing a trusted machine state
6,141,783	J. J. Ashley, M. Blaum, B. Marcus, and C. M. Melas	Error propagation limiting encoder/decoder for multilevel decision feedback equalization
6,141,784	G. T. Davis and J. H. Derby	Method and system in a data communications system for the retransmission of only an incorrectly transmitted portion of a data packet
6,141,786	C. E. Cox, M. Hassner, B. M. Trager, and S. Winograd	Method and apparatus for performing arithmetic operations on Galois fields and their extensions
6,141,792	L. E. Acker, M. H. Conner, and A. R. Martin	Object oriented framework for specifying the format of compiler output with a template facility
6,141,858	J. W. Berberich and N. B. Schirle	Head suspension lift CAM adjustment
6,141,866	J. G. Davis, J. W. Heater, and A. T. Mays	Universal tool for uniformly applying a force to a plurality of components on a circuit board
6,142,361	F. J. Downes Jr., R. M. Japp, and M. V. Pierson	Chip C4 assembly improvement using magnetic force and adhesive
6,142,726	T. Jantz, R. Kuder, D. C. Linnell, R. A. Meyen, and R. P. Westerfield	Automated chamfering method
6,142,802	J. Berg, D. P. Gaio, and W. K. Hogan	Guide rail and CAM system with integrated connector for removable transceiver
6,142,829	R. D. O'Groske, G. Oldenburg, S. M. Thorvilson, and R. V. Tointon	Ferrite block in a cable connector premold
6,143,117	K. Kelly, A. K. Malhotra, E. D. Perfecto, and R. Yu	Process for transferring a thin-film structure to a temporary carrier
6,143,388	X. Bian, M. F. Doerner, S. Duan, J. Li, and S. M. Mirzamaani	Thin film disk with onset layer
6,143,635	D. C. Boyd, S. M. Burns, H. I. Hanafi, Y. Taur, and W. C. Wille	Field effect transistor with improved implants and method for making such transistors
6,143,640	J. E. Cronin and B. Luther	Method of fabricating a stacked via in copper/polyimide beol
6,143,643	K. Carter, C. J. Hawker, J. L. Hedrick, V. Y. Lee, R. D. Miller, W. Volksen, and D. Y. Yoon	Process for manufacture of integrated circuit device using organosilicate insulative matrices
6,144,013	R. C. Chu, M. J. Ellsworth Jr., and R. E. Simons	Local humidity control system for low temperature electronic module
6,144,037	A. B. El-Kareh, B. El-Kareh, and J. G. Ryan	Capacitor charging sensor
6,144,054	F. Agahi, L. L. Hsu, and J. A. Mandelman	DRAM cell having an annular signal transfer region
6,144,081	C. M. Hsieh, L. L. Hsu, L. R. Logan, J. A. Mandelman, and S. Ogura	Method to suppress subthreshold leakage due to sharp isolation corners in submicron FET structures
6,144,086	J. S. Brown, R. J. Gauthier, R. W. Mann, and S. H. Voldman	Structure for improved latch-up using dual depth STI with impurity implant
6,144,178	T. Hirano and K. Satoh	Disk drive with controlled reduced internal pressure
6,144,224	J. F. Lee and D. L. Ostapko	Clock distribution network with dual wire routing
6,144,325	T. Chiu, D. G. Mikan, and J. Nguyen	Register file array having a two-bit to four-bit encoder
6,144,361	J. G. Gordon II, M. W. Hart, and S. A. Swanson	Transmissive electrophoretic display with vertical electrodes

6,144,367	V. Berstis	Method and system for simultaneous operation of multiple handheld control devices in a data processing system
6,144,371	G. J. Clary and K. Nathan	Thinkscribe combined electronic and paper based scheduling
6,144,379	R. L. Bertram, F. S. Krauss, and M. LaFauci	A computer controlled user interactive display system for presenting graphs with interactive icons for accessing related graphs
6,144,381	A. P. Brush, K. R. Kluttz, and D. Lection	Systems, methods and computer program products for compass navigation of avatars in three dimensional worlds
6,144,554	L. S. Mok	Laptop computer with slidable keyboard for exposing a heat generating surface for more efficient heat dissipation
6,144,609	G. M. Lattimore, D. Wendel, and F. C. Wernicke	Multiport memory cell having a reduced number of write wordlines
6,144,668	B. M. Bass, D. R. Henderson, E. H. Ku, S. Lemke, J. Rash, L. B. Reiss, and T. E. Ryle	Simultaneous cut through and store-and-forward frame support in a network device
6,144,744	E. D'Avignon, R. S. DeBellis, R. M. Smith, and P. C. Yeh	Method and apparatus for the secure transfer of objects between cryptographic processors
6,144,945	A. Garg and J. R. Kalagnanam	Method for fast and accurate evaluation of periodic review inventory policy
6,144,967	C. M. Nock	Object oriented processing log analysis tool framework mechanism
6,144,970	C. R. Bonner and R. W. Lyle	Technique for inplace reorganization of a LOB table space
6,144,990	M. L. Brandt, P. Dykes, and E. D. Lindberg	Computer apparatus and method for communicating between software applications and computers on the world-wide web using universal variable handling
6,145,021	M. A. Dawson III and J. Landry	Using programmable plug and play resource data to provide relief for resource-constrained personal computer systems
6,145,031	S. D. Mastie and M. Xie	Multiple insertion point queue to order and select elements to be processed
6,145,032	J. Bannister, G. D. Carpenter, and D. Glasco	System for recirculation of communication transactions in data processing in the event of communication stall
6,145,036	M. A. Barenys, C. R. Genz, J. G. Goodwin, and F. C. Gray	Polling of failed devices on an I2C bus
6,145,038	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Method and system for early slave forwarding of strictly ordered bus operations
6,145,053	G. J. Smith	Data security method using head disk stiction
6,145,057	R. K. Arimilli and J. S. Dodson	Precise method and system for selecting an alternative cache entry for replacement in response to a conflict between cache operation requests
6,145,059	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Cache coherency protocols with posted operations and tagged coherency states
6,145,119	D. E. House, B. J. Owings, and S. S. Pei	Programming development environment for intranet and internet applications employing unique project data structure
6,145,188	W. W. Brooks Jr., J. T. Coffey, T. P. Fracek, R. E. Lagergren, J. M. Rigotti, and M. A. Schlimmer	Fan and fold head lead termination method using flex cable with edge termination pads
6,146,150	N. K. Roberts	Circuit card with separate interfaces for different bus architectures
6,146,755	S. Guha and A. Gupta	High density magnetic recording medium utilizing selective growth of ferromagnetic material
6,147,009	A. Grill, C. Jahnes, V. V. Patel, and L. Perraud	Hydrogenated oxidized silicon carbon material
6,147,177	R. D. Allen, J. Opitz, R. Sooriyakumaran, and T. I. Wallow	Polycyclic resist compositions with increased etch resistance
6,147,402	J. J. Cuomo, H. M. Dalal, L. L. Hsu, and R. V. Joshi	Refractory metal capped low resistivity metal conductor lines and vias
6,147,506	U. M. Ahmad and E. R. Atwood	Wafer test fixture using a biasing bladder and methodology
6,147,508	J. Beck, D. W. Boerstler, C. M. Durham, and P. J. Klim	Power consumption control mechanism and method therefor
6,147,683	A. E. Martinez and M. D. Rahn	Graphical selection marker and method for lists that are larger than a display window
6,147,687	M. C. Wanderski	Dynamic and selective buffering tree view refresh with viewable pending notification
6,147,698	W. C. Decker and J. L. Zable	Density control for a printer
6,147,730	R. A. Budd, G. L. Chiu, M. J. Cordes, S. Cordes, and J. P. Doyle	Color filters for flat panel displays
6,147,769	B. K. Jackson and M. L. Williams	Display of selected printer response for distinct applications
6,147,856	J. Karidis	Variable capacitor with wobble motor disc selector
6,147,871	J. R. DeWitt and J. H. Neer	Low profile riser card assembly for a computer system
6,148,000	R. H. Boivie, N. K. Feldman, A. Viswanathan, and R. M. Woundy	Merging of data cells at network nodes
6,148,278	C. Castaldo	Emulating the S/390 "read backwards" tape operation on SCSI attached tape devices
6,148,290	A. Dan and F. N. Parr	Service contract for managing service systems

6,148,295	T. R. Albrecht and C. H. Heino	Composite ramp structure for data storage devices
6,148,295	N. Megiddo and U. Shaft	Method for computing near neighbors of a query point in a database
6,148,295	T. W. Wu	Composite ramp structure for data storage devices
6,148,328	G. A. Cuomo and S. K. Singhal	Method and system for signaling presence of users in a networked environment
6,148,335	N. Haggard, C. Molloy, G. Quesenberry, R. Robinson, and H. Stuck	Performance/capacity management framework over many servers
6,148,340	R. Bitteringer, N. C. Brubaker, B. C. Housel, and S. Wang	Method and system for differencing container files
6,148,352	J. L. Coale and S. R. Van Gundy	Scalable modular data storage system
6,148,361	G. D. Carpenter, M. E. Dean, P. L. Debacker, D. Glasco, and R. L. Rockhold	Interrupt architecture for a non-uniform memory access (NUMA) data processing system
6,148,375	S. J. Baylor and Y. Hsu	Hierarchical bus simple coma architecture for shared memory multiprocessors
6,148,383	W. F. Micka and Y. Novick	Storage system employing universal timer for peer-to-peer asynchronous maintenance of consistent mirrored storage
6,148,394	K. A. Chiarot, D. S. Ray, S. Tung, and B. D. Williamson	Apparatus and method for tracking out of order load instructions to avoid data coherency violations in a processor
6,148,410	M. E. Baskey, D. E. Dillenberger, G. S. Goldszmidt, G. D. Hunt, E. M. Levy-Abegnoli, J. M. Nick, and D. W. Schmidt	Fault tolerant recoverable TCP/IP connection router
6,148,412	D. M. Cannon, M. A. Kaczmariski, and D. P. Warren	Availability and recovery of files using copy storage pools
6,148,419	G. H. Ahrens, M. C. Duron, B. Faust, F. C. Gray, and K. P. Szabo	System diagnostic location codes
6,134,546	R. J. Bestgen, R. L. Egan, and C. L. Ramler	Method and computer program product for implementing subquery join
6,149,048	W. Brearley, L. Economikos, P. F. Findeis, K. Kelly, B. W. Leenstra, A. G. Merryman, E. D. Perfecto, C. Prasad, J. P. Wood, and R. Yu	Apparatus and method for use in manufacturing semiconductor devices
6,149,464	B. J. Debauche, P. D. Patel, and S. H. Weingart	Apparatus for detecting cable attachment
6,149,465	J. Berg, D. P. Gaio, and W. K. Hogan	Guide rail system with integrated wedge connector for removable transceiver
6,149,498	T. G. Van Kessel and C. R. Whitaker	Semiconductor wafer handling system
6,150,001	J. L. Hedrick, C. M. Mate, and J. Wu	Magnetic recording disk with polycyanate ester overlayer and method for making it
6,150,061	J. R. Stack	Clover-leaf solder mask opening
6,150,212	R. Divakaruni, J. P. Gambino, J. A. Mandelman, C. J. Radens, and W. R. Tonti	Shallow trench isolation method utilizing combination of spacer and fill
6,150,230	D. E. Kotecki and W. H. Ma	Trench separator for self-defining discontinuous film
6,150,255	F. J. Downes Jr., S. J. Fuerniss, G. R. Hill, A. P. Ingraham, V. R. Markovich, and J. A. Molla	Method of planarizing a curved substrate and resulting structure
6,150,258	T. W. Mountsier and M. J. Shapiro	Plasma deposited fluorinated amorphous carbon films
6,150,282	G. Gale, R. Jagannathan, K. P. Madden, K. J. McCullough, H. F. Okorn-Schmidt, K. Pope, and D. I. Rath	Selective removal of etching residues
6,150,536	K. Chondroudou and D. Mitzi	Dye doped organic-inorganic hybrid materials
6,150,670	J. Faltermeier, U. Gruening, S. G. Hegde, R. Jammy, B. S. Lee, and H. Tews	Process for fabricating a uniform gate oxide of a vertical transistor
6,150,707	R. K. Cook, C. Gruszecki, S. E. Luce, M. A. Passaro, and F. Scholl	Metal-to-metal capacitor having thin insulator
6,150,716	S. W. MacQuarrie, W. R. Storr, and J. W. Wilson	Metal substrate having an IC chip and carrier mounting
6,150,723	R. M. Geffken and J. M. Harper	Copper stud structure with refractory metal liner
6,150,726	N. B. Feilchenfeld, S. J. Fuerniss, M. A. Gaynes, P. Hoontrakul, and M. V. Pierson	Component carrier with raised bonding sites
6,150,834	M. K. Ciraula, C. M. Durham, and P. J. Klim	Elimination of SOI parasitic bipolar effect
6,150,869	S. N. Storino, J. V. Tran, and R. R. Williams	Method and apparatus for body control in silicon-on-insulator (SOI) domino circuits

6,150,961	N. Alewine, J. C. Colson, A. P. Ittycheriah, S. Maes, and P. A. Moskowitz	Automated traffic mapping
6,151,024	C. Alimpich, B. Jeffcoat, D. Neuhard, L. L. Vigil, and J. P. Wittig	Data processor controlled display interface with tree hierarchy of elements view expandable into multiple detailed views
6,151,179	J. M. Poss	Signal processing circuit with feedback extracted from a sampled analog error signal
6,151,198	G. J. Chung, T. Lincoln, and W. L. Prater	Overmolding of actuator e-block by thixotropic of semisolid forging
6,151,200	L. L. Hsu, J. B. Kuang, S. Ratanaphanyarat, and M. J. Saccamango	Method of discharging SOI floating body charge
6,151,202	M. W. Mueller, P. M. Thomsen, and L. M. Walter	Discharging electrostatic charge during hot card insertion
6,151,211	R. A. Dayan and H. J. Locker	Active ventilation of a packaged electronic device during operation
6,151,217	P. M. Thomsen	Apparatus and method for enabling hot plugging of a integrated circuit
6,151,266	W. H. Henkels, W. Hwang, and R. V. Joshi	Asynchronous multiport register file with self resetting write operation
6,151,589	C. Aggarwal and P. S. Yu	Methods for performing large scale auctions and online negotiations
6,151,599	M. Ault, G. L. Child, E. Plassmann, B. A. Rich, T. J. Shrader, and K. K. Soper	Web client scripting test architecture for web server-based authentication
6,151,622	M. Fraenkel and S. K. Singhal	Method and system for portably enabling view synchronization over the world-wide web using frame hierarchies
6,151,623	C. G. Harrison, S. Nichols, and R. Spagna	Agent activity report via object embedding
6,151,655	M. Blake, M. Fee, C. Jones, P. K. Mak, and G. E. Strait	Computer system deadlock request resolution
6,151,661	H. W. Adams III, T. B. Genduso, and W. L. Leung	Cache memory storage space management system and method
6,151,664	J. M. Borkenhagen, G. G. Fagerness, J. D. Irish, and D. J. Krolak	Programmable SRAM and DRAM cache interface with preset access priorities
6,151,685	J. Li, D. K. Nguyen, M. S. Shih, and H. F. Yun	System and method for recovering a segment directory for a log structured array
6,151,700	O. O. Fox	An object oriented distributed programming system for computer controlled networks with selective capture of program property data identifying a particular program version
6,153,043	D. C. Edelstein, W. J. Horkans, S. E. Luce, N. E. Lustig, K. R. Pope, and P. D. Roper	Elimination of photo-induced electrochemical dissolution in chemical mechanical polishing
6,153,474	D. Dobuzinsky, S. D. Halle, E. N. Hammerl, H. Ho, M. Jaso, J. A. Mandelman, and R. Srinivasan	Method of controllably forming a locos oxide layer over a portion of a vertically extending sidewall of a trench extending into a semiconductor substrate
6,153,491	D. E. Kotecki and W. H. Ma	Overhanging separator for self-defining discontinuous film
6,153,505	L. Bolde, P. A. Gruber, and C. C. Lei	Plastic solder array using injection molded solder
6,153,536	D. Brouillette and R. L. Mendelson	Method for mounting wafer frame at backside grinding (BSG) tool
6,153,696	G. Breyta, D. J. Dawson, H. Ito, M. Khojasteh, R. Kwong, E. Macy, D. P. Merritt, W. M. Moreau, S. E. Perreault, H. S. Sachdev, and R. L. Wood	Process for forming carbonates of hydroxyaromatic compounds
6,153,725	M. Angelopoulos and R. A. DiPietro	Control of polymerization kinetics and rate of polymer precipitation as a means of controlling the aggregation and morphology in conductive polymers and precursors thereof
6,153,885	R. A. Kendall	Toroidal charged particle deflector with high mechanical stability and accuracy
6,153,902	T. Furukawa, U. Gruening, D. V. Horak, J. A. Mandelman, C. J. Radens, and T. S. Rupp	Vertical DRAM cell with wordline self-aligned to storage trench
6,153,934	A. Allen, J. Lasky, R. W. Mann, J. Rankin, and F. R. White	Buried butted contact and method for fabricating
6,153,935	T. J. Dalton, D. C. Edelstein, J. G. Gaudiello, M. Krishnan, S. Malhotra, M. McGlashan-Powell, E. O'Sullivan, and C. J. Sambucetti	Dual etch stop/diffusion barrier for damascene interconnects
6,154,091	J. P. Pennings, G. E. Smith, and M. H. Wood	SOI sense amplifier with body contact structure
6,154,200	D. Challener, J. L. Levine, and M. A. Schappert	Computer input stylus and system

6,154,361	D. H. Anderson, K. C. Green, O. M. Herrera, K. R. Schneebeli, and P. P. Urbisci	Disk drive chassis for reducing transmission of vibrations between disk-drive units of a disk-drive array
6,154,751	M. Ault, G. L. Child, E. Plassmann, B. A. Rich, T. J. Shrader, and K. K. Soper	Method for executing a user requested CGI program in a new authentication context while protecting operation of a default webserver program
6,154,768	Q. Chen, T. E. Murphy Jr., P. F. Rieth, and J. S. Stevens	System and method for negotiating functions and features
6,154,790	G. W. Kilmer, G. B. Pruett, and J. P. Ward	System for monitoring and reporting serial numbers of hard disk drives in a computer system using radio frequency identification (RFID) technology
6,154,792	T. P. Giordano, B. W. Knapp, R. P. Mech, and D. R. Welsh	Method and computer program product for paging control using a reference structure including a reference bitmap
6,154,822	M. S. Spencer	Method and system for improving data storage and access for programs written in mid-level programming languages
6,154,836	M. A. Dawson III and J. Landry	Method and system for configuring plug and play devices for a computer operating system
6,154,852	D. L. Amundson, D. R. Halley, P. D. Koeller, L. W. Koser, and L. M. Smith	Method and apparatus for data backup and recovery
6,154,858	H. H. Ottesen and G. J. Smith	<i>In situ</i> method and apparatus for detecting surface defects to identify handling damage in a disk drive
6,154,868	C. E. Cox, M. D. Flickner, J. L. Hafner, M. Hassner, B. M. Trager, and S. Winograd	Method and means for computationally efficient on-the-fly error location correction in linear cyclic codes using ultra-fast error location
6,155,289	K. A. Carlsen	Method and system for sub-atmospheric gas delivery with backflow control
6,156,091	J. A. Casey	Controlled porosity for ceramic contact sheets and setter tiles
6,156,165	M. V. Pierson and T. B. Youngs, Jr.	Method of forming a metallization feature on an edge of an IC chip
6,156,221	K. L. Covert, J. M. Lauffer, and P. A. Moschak	Copper etching compositions, processes and products derived therefrom
6,156,375	H. L. Hu, R. E. Lee, H. A. Santini, and C. Tsang	Method of making read/write magnetoresistive (MR) head with sunken components
6,156,445	F. C. Burns, W. W. Fleming, V. Y. Lee, and R. W. Snyder	Ablatively photodecomposable compositions
6,156,461	B. J. Grenon, R. A. Haight, D. M. Hayden, M. Hibbs, J. P. Levin, T. E. Neary, R. A. Rochefort, D. A. Schmidt, J. G. Smolinski, and A. Wagner	Method for repair of photomasks
6,156,484	E. Bassous, G. Das, F. D. Egitto, N. B. Feilchenfeld, E. Foster, S. J. Fuerniss, J. S. Kamperman, D. J. Mikalsen, M. R. Scheuermann, and D. B. Stone	Gray scale etching for thin flexible interposer
6,157,067	L. L. Hsu and D. Netis	Metal oxide semiconductor capacitor utilizing dummy lithographic patterns
6,157,168	R. S. Malik	Secondary power supply for an uninterruptable power system
6,157,216	G. M. Lattimore, D. G. Mikan, B. Patel, and G. A. Yeung	Circuit driver on SOI for merged logic and memory circuits
6,157,336	T. C. Nguyen and J. M. Poss	Target specific folded analog to digital converter for direct access storage device (DASD)
6,157,374	E. K. Evans and R. M. West	Graphics display system and method for providing internally timed time-varying properties of display attributes
6,157,380	P. W. Bennett, D. S. Desai, S. Harnett, S. Kulkarni, A. M. Mizell, and D. S. Wood	Generic mechanism to create opendoc parts from workplace shell objects
6,157,381	C. L. Bates and P. R. Day	Computer system, user interface component and method utilizing non-linear scroll bar
6,157,530	J. P. Pequignot, T. Rahman, J. Sloan, D. W. Stout, and S. H. Voldman	Method and apparatus for providing ESD protection
6,157,555	B. Hemena and R. S. Malik	Current limiter responsive to changing parallel power supply capacity
6,157,740	D. J. Buerkle and C. H. Ngai	Compression/decompression engine for enhanced memory storage in MPEG decoder
6,157,910	K. A. Ortega	Deferred correction file transfer for updating a speech file by creating a file log of corrections
6,157,915	K. Bhaskaran, D. P. Connors, F. F. Heath, and N. Nayak	Method and apparatus for collaboratively managing supply chains
6,157,933	J. Celi, Jr., V. S. Moore, W. L. Nusbickel, and G. R. Walters	Method and apparatus for loading multiple animated images on a web-page with limited network throughput
6,157,940	F. R. Marullo and D. H. Randall	Automated client-based web server stress tool simulating simultaneous multiple user server accesses
6,157,960	D. L. Kaminsky, A. King, and Z. Li	Technique for programmatically creating distributed object programs

6,157,962	P. Hodges, M. G. Hurley, N. K. Ouchi, and M. S. Shih	Multipath I/O storage systems with multipath I/O request mechanisms
6,157,980	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	Cache directory addressing scheme for variable cache sizes
6,157,981	B. Blaner, H. Burkhart, R. Herzl, K. Lauricella, C. R. Ogilvie and A. Tran	Real time invariant behavior cache
6,158,020	D. Cromer, H. J. Locker, D. Rhoades, R. S. Springfield, and J. P. Ward	Remote jumper set and reset
6,158,024	M. Mandal	Method and apparatus for structured memory analysis of data processing systems and applications
6,158,027	G. S. Bush, R. Cideciyan, J. D. Coker, E. S. Eleftheriou, R. L. Galbraith, and D. J. Stanek	Enhanced noise-predictive maximum likelihood (NPML) data detection method and apparatus for direct access storage device (DASD)
6,158,032	G. R. Currier, L. L. Day, S. M. Douskey, P. A. Ganfield, and J. M. Wallin	Data processing system, circuit arrangement and program product including multi-path scan interface and methods thereof
6,158,107	T. Chang and J. Lo	Inverted merged MR head with plated notched first pole tip and self-aligned second pole tip
6,158,108	D. J. Seagle	Method of making magnetic head with vialess lead layers from MR sensor to pads
6,158,118	D. E. McConnell and C. M. Munoz-Bustamante	Method and apparatus for aligning surface mounted optoelectric semiconductor devices on a printed circuit board
6,158,644	P. J. Brofman, M. G. Courtney, S. Farooq, M. J. Interrante, R. A. Jackson, G. B. Martin, S. K. Ray, W. E. Sablinski, and K. A. Stalter	Method for enhancing fatigue life of ball grid arrays
6,159,354	R. J. Contolini, J. O. Dukovic, J. Feng, E. Patton, J. Reid, and S. Taatjas	Electric potential shaping method for electroplating
6,159,641	T. H. Baum, P. B. Comita, and F. M. Schellenberg	Method for the repair of defects in lithographic masks
6,159,787	J. M. Aitken and A. W. Strong	Structures and processes for reduced topography trench capacitors
6,159,807	A. Bryant and E. J. Nowak	Self-aligned dynamic threshold CMOS device
6,159,870	A. B. Chakravarti, R. Conti, F. V. Liucci, and D. Restaino	Borophosphosilicate glass incorporated with fluorine for low thermal budget gap fill
6,160,292	R. C. Flaker, L. L. Hsu, and J. B. Kuang	Circuit and methods to improve the operation of SOI devices
6,160,302	A. M. Palagonia	Laser fusible link
6,160,386	B. Hemena and R. S. Malik	Parallel power system which includes over voltage protection
6,160,557	C. Narayanaswami	Method and apparatus providing efficient rasterization with data dependent adaptations
6,160,687	M. Pinarbasi	Method of providing improvement in the prevention of shield-to-lead shorts in MR/GMR heads, MR/GMR heads produced thereby and storage devices using MR/GMR heads produced thereby
6,160,724	B. Hemena and R. S. Malik	Boost doubler circuit wherein an AC bridge rectifier is not required
6,160,748	M. Kumar	Apparatus and method for maintaining bit line charge state during a read operation
6,161,090	D. Kanevsky and S. Maes	Apparatus and methods for speaker verification/identification/classification employing non-acoustic and/or acoustic models and databases
6,161,109	D. A. Matamoros and J. A. Ruddy	Accumulating changes in a data base management system by copying the data object to the image copy if the data object identifier of the data object is greater than the image identifier of the image copy
6,161,112	B. J. Cragun and P. R. Day	Web page presentation control mechanism and method
6,161,145	A. Bainbridge, S. Cocks, D. F. Ferguson, T. J. Freund, A. Leff, G. Normington, J. T. Rayfield, and R. A. Storey	Updating server-related data at a client
6,161,164	S. H. Dhong and J. A. Silberman	Content addressable memory accessed by the sum of two operands
6,161,166	R. W. Doing, R. N. Kalla, and S. J. Schwinn	Instruction cache for multithreaded processor
6,161,178	D. Cromer, J. W. Freeman, and R. S. Springfield	Data processing system and method for specification of one of a plurality of password requirements for each boot device
6,161,180	S. M. Matyas and S. W. Smith	Authentication for secure devices with limited cryptography
6,161,189	R. K. Arimilli, J. D. Lewis, and D. E. Williams	Latch and hold circuit that permits subcircuits of an integrated circuit to operate at different frequencies
6,161,208	P. F. Dutton, S. L. Gregor, and H. H. Li	Storage subsystem including an error correcting cache and means for performing memory to memory transfers
6,161,388	U. Ghoshal	Enhanced duty cycle design for microthermoelectromechanical coolers
6,162,305	R. Hsiao, D. Mauri, and N. L. Robertson	Method of making spin valve read head with plasma produced metal oxide insulation layer between lead and shield layers

6,162,365	A. Bhatt and J. G. Gaudiello	PD etch mask for copper circuitization
6,162,532	C. Black, S. M. Gates, C. B. Murray, and S. Sun	Magnetic storage medium formed of nanoparticles
6,162,565	V. Chao, S. A. Estes, T. B. Faure, and T. Wagner	Dilute acid rinse after develop for chrome etch
6,162,582	R. Hsiao, T. Lin, and H. A. Santini	Method of making high resolution lead to shield short-resistant read head
6,162,659	L. Wu	Method for manufacturing a package structure having a heat spreader for integrated circuit chips
6,162,660	W. R. LaFontaine, P. A. Mescher, and C. G. Woychik	Method for joining a semiconductor chip to a chip carrier substrate and resulting chip package
6,162,997	I. Memis	Circuit board with primary and secondary through holes
6,163,045	J. Alsmeier, L. L. Hsu, J. A. Mandelman, and W. R. Tonti	Reduced parasitic leakage in semiconductor devices
6,163,073	J. G. Patel	Integrated heatsink and heatpipe
6,163,090	R. E. Brown, C. Bunker, and J. C. Lo	Probe position actuator with an elongated stroke
6,163,169	W. F. Lawson	CMOS tri-state control circuit for a bidirectional I/O with slew rate control
6,163,173	S. N. Storino, G. Uhlmann, and R. R. Williams	Method and apparatus for implementing adjustable logic threshold in dynamic circuits for maximizing circuit performance
6,163,250	P. A. Moskowitz	System and method for sensing objects on surface of vehicle
6,163,367	C. E. Obszarny	Apparatus and method for <i>in-situ</i> adjustment of light transmission in a photolithography process
6,163,420	J. M. Poss	Interleaved analog tracking timing and gain feedback loops for partial response maximum likelihood (PRML) data detection for direct access storage device (DASD)
6,163,442	H. S. Gill, H. P. Lee, M. P. Salo, J. F. Smyth, J. D. Westwood, and S. W. Yuan	High moment bilayer first pole piece layer of a write head with high magnetic stability for promoting read signal symmetry of a read head
6,163,465	M. Criscuolo, B. Kerrigan, S. Sung, and H. C. Tanner	Cable storage compartment for network computer
6,163,510	P. Lee, E. H. Stern, and B. E. Willner	Multimedia search and indexing system and method of operation using audio cues with signal thresholds
6,163,536	J. M. Dunn, E. H. Stern, and B. E. Willner	Communication system including a client controlled gateway for concurrent voice/data messaging with a data server
6,163,683	J. M. Dunn, P. Lee, E. H. Stern, and B. E. Willner	Broadcast data radio system and receiver apparatus therefor
6,163,773	G. T. Kishi	Data storage system with trained predictive cache management engine
6,163,779	V. Berstis, M. L. Mantha, M. Modh, and S. Radhakrishnan	Method of saving a web page to a local hard drive to enable client-side browsing
6,163,791	M. S. Schmookler and D. Senzig	High accuracy estimates of elementary functions
6,163,802	B. G. Ernst, G. E. Hedgefield, M. S. Lees, and K. K. Lin	Message tracking system
6,163,812	A. S. Gopal, R. Neves, and S. Vajracharya	Adaptive fast path architecture for commercial operating systems and information server applications
6,163,813	E. H. Jenney	Pattern for instantiating objects of unknown type in object-oriented applications
6,163,815	Y. Baumgartner, J. S. Fields, Jr., G. L. Guthrie, and K. A. Riek	Dynamic disablement of a transaction ordering in response to an error
6,163,820	H. M. Garrett, W. G. Holland, J. F. Logan, J. G. McDonald, and K. Stacy	Efficient data transfer mechanism for input/output devices
6,163,857	P. K. Mak, P. J. Meaney, W. W. Shen, and G. E. Strait	Computer system UE recovery logic
6,163,862	R. D. Adams, E. S. Cooley, and P. R. Hansen	On-chip test circuit for evaluating an on-chip signal using an external test signal
6,163,880	G. Ramalingam and H. Srinivasan	Object model for java
6,163,950	T. Bodiker and J. P. Hollowell	Self-aligning end effector for small components
6,164,076	R. C. Chu, M. J. Ellsworth, Jr., and R. E. Simons	Thermoelectric cooling assembly with thermal space transformer interposed between cascaded thermoelectric stages for improved thermal performance
6,164,633	V. P. Mulligan, C. Tompkins, and W. F. Yuen	Multiple size wafer vacuum chuck
6,165,170	J. Felsenstein, S. H. Gomory, and J. J. Wynne	Laser dermablator and dermablation
6,165,278	P. J. Hart, V. Sirtori, S. Varinelli, M. Verderio, and F. Zambon	Removing thermal grease from electronic cards
6,165,629	B. M. Kellner, K. M. McGuire, K. G. Sachdev, and P. J. Sorce	Structure and process for thin film interconnect

6,165,649	B. J. Grenon, R. A. Haight, D. M. Hayden, M. Hibbs, J. P. Levin, T. E. Neary, R. A. Rochefort, D. A. Schmidt, J. G. Smolinski, and A. Wagner	Methods for repair of photomasks
6,165,651	W. J. Adair, D. S. O'Grady, and S. Peng	Apparatus for tuning an attenuating phase shift mask
6,165,673	R. D. Allen, G. Breyta, R. A. DiPietro, D. C. Hofer, H. Ito, J. Opitz, and T. I. Wallow	Resist composition with radiation sensitive acid generator
6,165,678	R. D. Allen, R. A. DiPietro, R. Sooriyakumaran, T. I. Wallow, and G. M. Wallraff	Lithographic photoresist composition and process for its use in the manufacture of integrated circuits
6,165,885	M. A. Gaynes and M. V. Pierson	Method of making components with solder balls
6,165,917	J. Batey, P. M. Fryer, and J. H. Souk	Passivation of copper with ammonia-free silicon nitride and application to TFT/LCD
6,166,332	T. S. Farrow, T. Keung, and J. Walker	Method and apparatus for controlling the bend in a ribbon cable
6,166,420	R. J. Gauthier, D. J. Schepis, and S. H. Voldman	Method and structure of high and low K buried oxide for SOI technology
6,166,423	J. P. Gambino, M. Jaso, and D. E. Kotecki	Integrated circuit having a via and a capacitor
6,166,437	S. H. Dhong, K. J. Nowka, and M. J. Shapiro	Silicon on silicon package with precision align macro
6,166,561	J. A. Fifield and C. P. Miller	Method and apparatus for protecting off chip driver circuitry employing a split rail power supply
6,166,688	D. Cromer, R. A. Dayan, B. Ellison, E. Kern, H. J. Locker, and J. P. Ward	Data processing system and method for disabling a portable computer outside an authorized area
6,166,735	B. E. Dom and D. Petkovic	Video story boarding interface for selective downloading and displaying of desired portions of remote-stored video data object
6,166,901	J. R. Drake, E. T. Gamble, and J. Pacheco	Vibration dampening system for removable hard disk drive carriers
6,166,948	S. S. Parkin	Magnetic memory array with magnetic tunnel junction memory cells having flux: closed free layers
6,166,981	T. Kirihaata	Method for addressing electrical fuses
6,167,032	S. S. Allison, K. J. Barker, S. H. Johnson, and J. K. Lee	System and method for avoiding host transmit underruns in a communication network
6,167,055	A. G. Ganek, E. H. Stern, and B. E. Willner	Apparatus for provision of broadband signals over installed telephone wiring
6,167,332	J. M. Kurtzberg and M. Levanoni	Method and apparatus suitable for optimizing an operation of a self-guided vehicle
6,167,408	D. M. Cannon and D. R. Crockett	Comparative update tracking to synchronize local operating parameters with centrally maintained reference parameters in a multiprocessing system
6,167,441	M. C. Azua	Customization of web pages based on requester type
6,167,444	E. B. Boden, P. A. Gebler, Jr., and F. A. Gruber	Method and system for exchanging routing information
6,167,453	C. H. Becker and J. I. Knutson	Method and apparatus for utilizing URLs to specify local or remote java bean repositories
6,167,459	B. C. Beardsley, A. S. Meritt, M. A. Paulsen, and H. M. Yudenfriend	System for reassigning alias addresses to an input/output device
6,167,475	J. D. Carr	Data transfer method/engine for pipelining shared memory bus accesses
6,167,494	R. W. Cheston, R. P. Hoggarth, R. I. Knox, H. J. Locker, and D. Rhoades	Method and system for recovering from operating system failure
6,167,500	K. A. Feiste, J. S. Muhich, and S. W. White	Mechanism for queuing store data and method therefor
6,167,521	S. W. Smith and S. H. Weingart	Securely downloading and executing code from mutually suspicious authorities
6,167,524	K. J. Goodnow, M. S. Michail, J. G. Patel, W. D. Pricer, and S. T. Ventrone	Apparatus and method for efficient battery utilization in portable personal computers
6,167,557	P. Kudva, D. S. Kung, and L. Stok	Method and apparatus for logic synthesis employing size independent timing optimization
6,167,615	B. T. Ma, A. K. Sarkhel, and P. K. Seto	Method for producing circuit board assemblies using surface mount components with finely spaced leads
6,167,621	G. F. Goth and J. J. Loparco	Center feed parallel flow cold plate for dual refrigeration systems
6,167,753	D. Chen, E. Flecha, J. Hammond, and K. G. Roessler	Detecting fields with a single-pass, dual-amplitude-mode scanning force microscope
6,168,693	G. Landry and C. Uzoh	Apparatus and methods to control the uniformity of electroplated workpiece
6,168,732	A. Afzali, M. Angelopoulos, V. A. Bourgault, L. D. Comerford, M. W. Mirre, S. Molis, N. M. Patel, R. Saraf, J. M. Shaw, and P. Spellane	Inherently conducting blends of polyaniline with commodity and engineering resins

6,168,845	R. E. Fontana, R. Hsiao, E. E. Marinero, H. A. Santini, and B. D. Terris	Patterned magnetic media and method of making the same using selective oxidation
6,169,281	D. Chen, E. Flecha, J. Hammond, Y. Martin, and K. G. Roessler	Apparatus and method for determining side wall profiles using a scanning probe microscope having a probe dithered in lateral directions
6,169,412	R. A. Byers	Adjustable tooling pin for a card test fixture
6,169,541	R. M. Smith	Method, apparatus and system for integrating television signals with internet access
6,169,548	T. Haynes	Easily dismissed message windows
6,169,639	M. P. Salo and A. J. Wallash	Unipolar last write for improved magnetic stability
6,169,795	J. M. Dunn and E. H. Stern	An internet telephony callback system and method of operation
6,169,805	J. M. Dunn, A. G. Ganek, E. H. Stern, and B. E. Willner	System and method of operation for providing user's security on-demand over unsecure networks
6,169,930	M. D. Blachek and G. J. Smith	Method and apparatus for preventing cold temperature induced damage in a disk drive
6,169,989	M. Eichstaedt, Q. Lu, and S. H. Teng	Method and apparatus for parallel profile matching in a large scale webcasting system
6,170,022	J. Linville, B. A. Makrucki, E. Suffern, and J. R. Warren	Method and system for providing congestion control in a data communications network
6,170,023	B. C. Beardsley, J. L. Iskiyan, J. E. McIlvain, P. R. Mills, M. A. Paulsen, W. G. Thompson, and H. M. Yudenfriend	System for accessing an input/output device using multiple addresses
6,170,029	R. A. Kelley and D. Neal	Voltage overshoot control in hot plug system
6,170,045	R. A. Bobak, S. B. Compton, J. K. Johnson, A. F. Martens, M. M. Maurer, D. L. Meck, W. R. Richardson, and M. A. Wright	Cross-system data piping using an external shared memory
6,170,059	G. W. Kilmer, G. B. Pruett, and J. P. Ward	Tracking memory modules within a computer system
6,170,078	M. Erle, M. Graf, L. M. Huisman, and Z. Zhang	Fault simulation using dynamically alterable behavioral models
6,173,294	A. Azagury, E. Kolodner, E. Petrank, and Z. Yehudai	A method for combining card marking with remembered set for generational garbage collection with more than two generations
6,170,630	V. J. Goss, R. A. McCord, Jr., and J. N. Smith	Transportation apparatus for system monitored operations testing
6,171,436	C. Huynh, H. G. Linde, P. Marmillion, A. M. Palagonia, B. A. Pierson, and M. J. Rutten	Method of removing slurry particles
6,171,513	R. R. Comulada, K. M. Davis, F. F. Jamin, B. P. Jones, F. Krug, and M. F. Lofaro	Chemical-mechanical polishing system having a bi-material wafer backing film and two-piece wafer carrier
6,171,757	M. Angelopoulos, A. Aviram, C. R. Guarnieri, and R. Kwong	Organometallic polymers and use thereof
6,171,873	J. Blondin, D. Brouillette, R. F. Cook, D. F. Diefenderfer, E. G. Liniger, and R. L. Mendelson	Method and apparatus for preventing chip breakage during semiconductor manufacturing using wafer grinding striation information
6,171,918	J. S. Brown, R. J. Gauthier, E. J. Nowak, M. H. Tong, and S. H. Voldman	Depleted poly MOSFET structure and method
6,171,971	W. Natzle	Freestanding multilayer IC wiring structure
6,171,988	B. V. Fasano and R. A. Rita	Low loss glass ceramic composition with modifiable dielectric constant
6,172,385	P. R. Duncombe, R. B. Laibowitz, D. Neumayer, K. Saenger, and T. M. Shaw	Multilayer ferroelectric capacitor structure
6,172,399	K. L. Lee, D. Lewis, and R. G. Viswanathan	Formation of ultra-shallow semiconductor junction using microwave annealing
6,172,512	R. J. Evans, D. F. Heidel, J. A. Kash, D. R. Knebel, and J. C. Tsang	Image processing methods for the optical detection of dynamic errors in integrated circuits
6,172,522	M. K. Kerr and W. F. Lawson	Slew rate controlled predriver circuit
6,172,529	P. J. Klim and J. E. Dunning	A compound domino logic circuit having output noise elimination
6,172,531	A. G. Aipperspach and P. T. Freiburger	Low power wordline decoder circuit with minimized hold time
6,172,676	B. O. Wood	Method and computer program product for implementing multiple drag and drop operations for large objects without blocking an operating system interface
6,172,769	R. Rao, G. R. Thompson, C. Tresser, and C. W. Wu	Printer dependent descreening method
6,172,833	S. M. Fry, S. D. Johnson, and S. B. Wilson	Retrieval of serpentine pattern data using a memory device of a tape cartridge

6,172,848	H. A. Santini	Write head with self aligned pedestal shaped pole tips that are separated by a zero throat height defining layer
6,172,871	J. A. Ho-Lung, V. Kamath, M. S. Mansuria, and T.-L. Wong	Method and system in a data processing system for efficiently cooling a portable computer system
6,172,920	S. H. Dhong, M. Ohkubo, S. Onishi, and O. Takahashi	Data transfer using two stage bit switch in memory circuit
6,172,981	K. J. Christensen, N. E. Cox, J. P. Ervin, and R. C. Matlack, Jr.	Method and system for distributing network routing functions to local area network stations
6,173,044	P. D. Hortensius, L. Lumelsky, and A. Narasimhan	Multipoint simultaneous voice and data services using a media splitter gateway architecture
6,173,112	G. G. Gruse, G. E. Hamblin, M. M. Hurtado, and K. L. Milsted	Method and system for recording in-process broadcast programs
6,173,234	C. H. Lee	Axial bearing fly height measurement to detect oil deficiency
6,173,281	R. J. Bestgen, T. P. Giordano, B. W. Knapp, and D. R. Welsh	Method and computer program product for processing and combining data sets including bitmaps
6,173,288	R. A. Diedrich, S. T. Evans, and J. K. Finkenaur	System and method for building a data structure of converted values without keywords from an input string including keyword/text value pairs
6,173,292	R. J. Barber, D. M. Herbert, C. Mohan, A. Somani, S. Watts, and M. Zaharioudakis	Data recovery in a transactional database using write-ahead logging and file caching
6,173,358	J. L. Combs	Computer system having dual bus architecture with audio/video/CD drive controller/coprocessor having integral bus arbitrator
6,173,359	W. C. Carlson and J. W. Peake	Storage and access to scratch mounts in VTS system
6,173,360	B. C. Beardsley, K. F. Day, M. H. Hartung, and W. F. Micka	Apparatus and method for allowing existing ECKD MVS DASD for using an escon interface to be used by an open storage using SCSI-type interface
6,173,371	R. K. Arimilli, J. S. Dodson, J. D. Lewis, and D. E. Williams	Demand-based issuance of cache operations to a processor bus
6,173,376	C. B. Fowler, W. W. Grunbok, Jr., G. F. Martino, and P. J. Vasek, Jr.	Data backup and restore method and system in a multisystem environment
6,173,382	T. Dell and M. W. Kellogg	Dynamic configuration of memory module using presence detect data
6,173,402	R. A. Chapman	Technique for localizing keyphrase-based data encryption and decryption
6,173,415	W. Litwin, M. J. Menon, and T. Risch	System for a scalable distributed data structure having scalable availability
6,173,439	B. A. Carlson, T. Graser, J. U. Lindblom, A. Nilsson, and T. Pernbeck	Interface mechanism and method for accessing non-object oriented data from within an object oriented framework
6,173,720	R. Arndt, G. Gale, F. W. Kern, K. P. Madden, H. F. Okorn-Schmidt, G. F. Ouimet, D. Salgado, and R. Wuthrich	Process for treating a semiconductor substrate
6,173,759	G. T. Galyon, R. G. Kemink, and R. R. Schmidt	Method of cooling electronic devices using a tube in plate heat sink
6,173,760	D. L. Gardell, K. Gaspar, and G. G. Morin	Co-axial bellows liquid heatsink for high power module test
6,173,887	D. I. Mead and M. V. Pierson	Method of making electrically conductive contacts on substrates
6,173,948	R. R. Hall, F. Marconi, and P. M. Nichols	Dimensional compensating vacuum fixture
6,174,096	R. H. Harris	Printing apparatus
6,174,175	A. Behfar-Rad, D. M. McHerron, and C. H. Perry	High density Z axis connector
6,174,218	C. Arcona, M. A. Church, A. M. Desouches, and G. M. Moorefield	Bow compensated lapping
6,174,232	M. F. Holahan, D. J. Lidberg, and J. T. Stoll	Helically conforming axial fan check valve
6,174,582	X. Bian, S. Duan, J. Li, and S. M. Mirzamaani	Thin film magnetic disk having reactive element doped refractory metal seed layer
6,174,606	V. A. Brusic, J. M. Rubino, and R. Saraf	Corrosion and dissolution protection of a conductive silver-polymer composite
6,174,762	G. B. Bronner, J. P. Gambino, L. L. Hsu, J. A. Mandelman, C. J. Radens, and W. R. Tonti	Salicide device with borderless contact
6,174,763	K. E. Beilstein, C. L. Bertin, J. E. Cronin, and F. R. White	Three-dimensional SRAM trench structure and fabrication method therefor
6,174,814	R. F. Cook, E. Garcia, N. A. Greco, S. E. Greco, and E. N. Levine	Method for producing a crack stop for interlevel dielectric layers
6,174,824	J. Michael, J. Miller, G. Pittman, and R. A. Previti-Kelly	Post-processing a completed semiconductor device
6,175,087	R. W. Keesler, V. R. Markovich, J. P. Paoletti, M. B. Perrino, and W. E. Wilson	Composite laminate circuit structure and method of forming the same

6,175,122	T. R. Groves and R. A. Kendall	Method for writing a pattern using multiple variable shaped electron beams
6,175,128	M. C. Hakey, D. V. Horak, W. H. Ma, and W. P. Noble	Process for building borderless bitline, wordline and DRAM structure and resulting structure
6,175,245	N. W. Bowe and R. D. Rossi	CMOS SOI contact integrity test method
6,175,365	A. Gueziec	Surface simplification preserving error tolerances
6,175,392	D. B. Dove	Method and apparatus for improving the color convergence of projection-based displays
6,175,457	D. T. Flynn	Dual polarity thermal asperity detector for direct access storage device (DASD)
6,175,462	P. W. Chung and S. A. Jove	High input impedance single ended, low supply voltage magnetoresistive preamplifier circuits
6,175,475	T. Lin, D. Mauri, J. F. Smyth, and C. Tsang	Fully-pinned, flux-closed spin valve
6,175,477	T. Lin and D. Mauri	Spin valve sensor with nonmagnetic oxide seed layer
6,175,499	S. T. Adams, W. V. Cranston, W. E. Hamel, J. K. Koenig, and J. McCloskey	Heat sink clamping spring additionally holding a ZIF socket locked
6,175,535	S. H. Dhong, M. Ohkubo, S. Onishi, and O. Takahashi	Cycle control circuit for extending a cycle period of a dynamic memory device subarray
6,175,543	G. W. Burr, H. Coufal, and S. Kobras	Encoding technique of data patterns to improve signal-to-noise ratio during content-addressable retrieval
6,175,556	J. Allen and R. J. Cooper	Remote powered ethernet repeater
6,175,569	W. Ellington, F. E. Noel, and L. A. Tomek	Extending asynchronous transfer mode (ATM) QOS across local area networks
6,175,820	T. A. Dietz	Capture and application of sender voice dynamics to enhance communication in a speech-to-text environment
6,175,832	J. Luzzi and G. Zhu	Method, system and program product for establishing a data reporting and display communication over a network
6,175,852	S. H. Dhong, H. C. Ngo, and K. J. Nowka	High-speed binary adder
6,175,860	B. P. Gaucher	Method and apparatus for an automatic multirate wireless/wired computer network
6,175,862	S. C. Chen and E. Feig	Hot objects with sequenced links in web browsers
6,175,866	M. H. Holloway and L. Temoshenko	Method and system for generating unsupported network monitoring objects
6,175,876	M. J. Branson, S. G. Halverson, D. L. Rackham, A. J. Streit, and S. M. Townsend	Mechanism for routing asynchronous state changes in a 3-tier application
6,175,888	G. L. Guthrie, R. A. Kelley, D. Neal, and S. M. Thurber	Dual host bridge with peer to peer support
6,175,899	S. J. Baylor and Y. Hsu	Method for providing virtual atomicity in multi-processor environment having access to multilevel caches
6,175,924	T. W. Arnold	Method and apparatus for protecting application data in secure storage areas
6,175,927	D. Cromer, B. Ellison, E. Kern, and J. P. Ward	Alert mechanism for service interruption from power loss
6,175,930	R. K. Arimilli, J. S. Dodson, J. D. Lewis, and D. E. Williams	Demand based sync bus operation
6,175,933	W. S. Cadden	Recovery of file transfers in a data processing system
6,175,947	T. Lehner, S. Ponnappalli, and S. Upreti	Method of extracting 3-D capacitance and inductance parasitics in sub-micron VLSI chip designs using pattern recognition and parameterization
6,175,949	G. D. Gristede, W. Hwang, and C. R. Tretz	Method and system for selecting sizes of components for integrated circuits
6,175,956	D. R. Hicks and W. J. Schmidt	Method and computer program product for implementing method calls in a computer system
6,175,957	K. Muthukumar, S. Ramaswamy, and B. Simons	A method of, system for, and computer program product for providing efficient utilization of memory hierarchy through code restructuring
6,175,984	R. B. Prime and R. L. Weaver	Apparatus for cleaning precision components
6,176,005	G. A. Garfunkel, W.-C. D. Hsiao, H. P. Lee, and D. J. Werner	Method of making read head with improved lead layers
6,176,374	J. Blondin, J. Jones, D. J. Rich, and W. Smith	Component carrier having a wave pattern tension reduction surface
6,176,461	D. P. Beaman, M. L. Buller, L. T. Cooper, B. Kerrigan, T. A. Merino, and J. R. Pugley	Enclosure apparatus with removable base
6,176,765	J. A. Gilhooly, L. Li, R. G. Lipori, C. Morgan, W. Surovic, and C. Wei	Accumulator for sampling
6,176,883	M. H. Holloway and L. Temoshenko	Method and system for generating unsupported network information indicators

6,176,931	E. C. Cooney, D. Restaino, S. M. Rossnagel, A. H. Simon, and P. Smetana	Wafer clamp ring for use in an ionized physical vapor deposition apparatus
6,176,967	C. E. Obszarny	Reactive ion etch chamber wafer masking system
6,176,985	F. J. Downes, Jr., R. T. Galasco, R. M. Japp, and J. F. Surowka	Laminated electroplating rack and connection system for optimized plating
6,177,184	J. A. Casey, M. Cohn, M. E. Cropp, C. Sullivan, R. J. Sullivan, and A. Vogel	Method of making an interface layer for stacked lamination sizing and sintering
6,177,228	R. D. Allen, R. A. DiPietro, R. Sooriyakumaran, T. I. Wallow, and G. M. Wallraff	Photoresist composition and process for its use
6,177,286	D. Chidambarrao and M. Naeem	Reducing metal voids during BEOL metallization
6,177,289	J. D. Crow, S. J. Koester, D. Kuchta, D. L. Rogers, D. K. Sadana, and S. Tiwari	Lateral trench optical detectors
6,177,299	L. L. Hsu and J. A. Mandelman	Transistor having substantially isolated body and method of making the same
6,177,337	M. Naeem	Method of reducing metal voids in semiconductor device interconnection
6,177,344	R. Conti, M. Galiano, L. Q. Xia, and E. Yieh	Improved method for filling high aspect ratio gaps with flowable glass
6,177,348	J. P. Gambino, P. D. Hoh, M. Jaso, and E. N. Levine	Low temperature via fill using liquid phase transport
6,177,360	K. Carter, R. F. Cook, M. A. Harbison, C. J. Hawker, J. L. Hedrick, V. Y. Lee, E. Liniger, R. D. Miller, W. Volksen, and D. Y. Yoon	Process for manufacture of integrated circuit device
6,177,680	G. J. Dick, A. Ganong, J. G. Hartley, J. W. Pavick, and D. M. Puisto	Correction of pattern-dependent errors in a particle beam lithography system
6,177,696	G. B. Bronner, L. Economikos, R. Jammy, B. Park, C. J. Radens, and M. Schrems	Integration scheme enhancing deep trench capacitance in semiconductor integrated circuit devices
6,177,708	J. B. Kuang, J. P. Pennings, G. E. Smith, and M. H. Wood	SOI FET body contact structure
6,177,728	R. A. Susko and J. W. Wilson	Integrated circuit chip device having balanced thermal expansion
6,177,729	J. Benenati, C. L. Bertin, W. T. Chen, T. E. Dinan, W. F. Ellis, W. J. Howell, G. A. Kelley, J. U. Knickerbocker, W. R. Tonti, and J. Zalesinski	Rolling ball connector
6,177,790	R. G. Emberty and C. Klein	System and method for distributing power between components via a magnetic coupling
6,177,805	D. Pih	High density test connector for disk drives in a high volume manufacturing environment
6,177,806	J. Burghartz	Signal sensor for RF integrated systems
6,177,807	C. L. Bertin, J. A. Fifield, E. L. Hedberg, R. J. Houghton, and W. R. Tonti	High frequency valid data strobe
6,177,809	C. L. Bertin, A. R. Bonaccio, J. P. Fifield, H. L. Kalter, J. A. Mandelman, and W. R. Tonti	Redundant input/output driver circuit
6,177,815	S. J. Baumgartner, A. D. Ngo, and D. W. Siljenberg	High bandwidth mean absolute value signal detector with hysteresis and tunability
6,177,817	J. A. Fifield, R. J. Houghton, and A. B. Wilson	Compensated-current mirror off-chip driver
6,177,818	C. L. Bertin, A. R. Bonaccio, H. L. Kalter, T. M. Maffitt, J. A. Mandelman, and W. R. Tonti	Complementary depletion switch body stack off-chip driver
6,177,833	J. A. Gabric, M. A. Roberge, and E. P. Thoma	Integrated circuit module having reduced impedance and method of providing the same
6,177,860	D. Cromer, N. Dion, M. J. Steinmetz, and J. P. Ward	Method and economical direct connected apparatus for deploying and tracking computers
6,177,873	B. J. Cragun	Weather warning apparatus and method
6,177,918	E. Colgan, J. L. Levine, G. F. Russell, and M. A. Schappert	Liquid crystal display device employing a guard plane between a layer for measuring touch position and common electrode layer
6,177,929	J. J. Maddalozzo, G. F. McBrearty, and J. M. Shieh	User interactive editing of computer displayed information of a plurality of information types through multiple information type buffers and corresponding multiple editors
6,177,930	R. S. Chernock, P. Dettori, M. Milenkovic, and A. Ruiz	System and method for enabling a user to move between cyclically transmitted image streams
6,177,936	B. J. Cragun	Browser hierarchical contextual information for web pages
6,177,941	T. Haynes, C. K. Hunt, and J. Paradise	Representative mapping between tool bars and menu bar pulldowns

6,177,944	M. C. Fowler, K. S. Haskell, R. S. Horton, T. Y. Kwok, S. Mastrianni, C. Narayanaswami, B. O. Schneider, M. van Horn, and J. van Welzen	Two phase rendering for computer graphics
6,177,948	D. Estabrooks and J. L. Zable	PQE for font vs. large dark patch
6,177,972	A. Afzali, G. Held, S. C. Lien, and D. Yoon	Polymer stabilized in-plane switched LCD
6,177,975	S. L. Buchwalter, D. Lewis, S. C. Lien, S. Purushothaman, and J. J. Ritsko	Precision alignment of plates
6,178,000	J. A. Hoffnagle	Monolithic, symmetric interferometer for generation of variable-periodicity patterns for lithography
6,178,018	J. A. Herlocker, D. J. Kekas, and M. L. Loeb	Process and method employing dynamic holographic display medium
6,178,059	G. M. Frees	Method for writing servo information on a magnetic recording disk
6,178,072	H. S. Gill	Keeper layer without sense current shunting in a giant magnetoresistive (GMR) head
6,178,074	H. S. Gill	Double tunnel junction with magnetoresistance enhancement layer
6,178,082	M. S. Farooq, D. E. Kotecki, R. A. Rita, and S. M. Rossnagel	High temperature, conductive thin film diffusion barrier for ceramic/metal systems
6,178,093	A. C. Bhatt, D. H. Glatzel, V. R. Markovich, A. F. Moring, K. I. Papathomas, and D. J. Russell	Manufacturing high density computer systems with circuit board assemblies having filled vias free from bleed-out
6,178,126	P. W. Coteus, T. K. Kirihata, W. E. Maule, and S. W. Tomashot	Memory and system configuration for programming a redundancy address in an electric system
6,178,171	C. A. Alexander, A. Gorti, and M. B. Squire	Route switching mechanisms for source-routed ATM networks
6,178,174	H. Franke, R. K. Govindaraju, P. Pattnaik, M. T. Raghunath, and R. Straub	Optimistic, eager rendezvous transmission mode and combined rendezvous modes for message processing systems
6,178,180	R. C. Eng, K. L. Graves, H. Parikh, and J. Sinibaldi	Communication adapter for processing ATM and ISDN data
6,178,183	M. C. Buskirk	Method and apparatus for receiving conventional telephone calls while connected to the internet
6,178,185	L. V. Marks	Network interface device, method and article of manufacture for providing high bit rate access over robbed bit
6,178,401	M. Franz and M. Novak	Method for reducing search complexity in a speech recognition system
6,178,421	D. J. Dahlen, J. M. Nick, D. H. Surman, and D. W. Westcott	Method of performing parallel cleanup of segments of a lock structure
6,178,435	M. S. Schmookler	Method and system for performing a power of two estimation within a data processing system
6,178,437	S. H. Dhong, H. C. Ngo, and K. J. Nowka	Method and apparatus for anticipating leading digits and normalization shift amounts in a floating-point processor
6,178,441	E. N. Elnozahy	Method and system in a computer network for the reliable and consistent ordering of client requests
6,178,445	G. J. Dawkins and V. H. Lee	System and method for determining which processor is the master processor in a symmetric multi-processor environment
6,178,449	R. F. Forman, J. M. Pechacek, and W. H. Schwane	Apparatus and method for measuring transaction time in a computer system
6,178,460	J. J. Maddalozzo, G. F. McBrearty, and J. M. Shieh	Method of efficiently retrieving data on a computer network by monitoring performance of mirrored network locations
6,178,462	B. M. Bass, D. A. Doidge, E. H. Ku, S. Lemke, J. Rash, and L. B. Reiss	Protocol for using a PCI interface for connecting networks
6,178,467	M. Faucher and P. T. Gutwin	Microprocessor system requests burstable access to noncacheable memory areas and transfers noncacheable address on a bus at burst mode
6,178,471	R. C. Barrett, M. J. Howland, and S. M. Pritko	Method of sharing buffers for the streaming of unchanged data
6,178,472	G. D. Carpenter, D. Glasco, and R. N. Iachetta	Queue having distributed multiplexing logic
6,178,484	R. K. Arimilli, J. S. Dodson, and J. D. Lewis	DCBST with ICBI mechanism to maintain coherency of bifurcated data and instruction caches
6,178,485	R. K. Arimilli, J. M. Kaiser, and D. E. Williams	Method and apparatus for executing singly-initiated singly-sourced variable delay system bus operations of differing character
6,178,489	S. Singh	Method and apparatus for managing linear address mapped storage under selective compression and recency of usage constraints
6,178,493	P. S. Lenk, M. J. Mayfield, R. J. Reese, and M. T. Vaden	Multiprocessor stalled store detection
6,178,495	M. A. Check and T. J. Slegel	Processor E-unit to I-unit interface instruction modification
6,178,497	M. Frederick, B. J. Ronchetti, and C. N. Tran	System and method for determining the relative age of instructions in a processor