

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

Germany

565,759	H. Scherzer	Method and arrangement for decoding F2F signals read from a magnetic data carrier
4,438,522.6	L. Schaefers	Device for the transmission of data streams in data communication networks
19,531,004.7	H. E. Korth	Method and apparatus for perception-optimized transmission of video and audio signals
19,533,569.4	A. Renner, G. Ruehle, M. Spieth, S. Richter, and W. Recktenwald	Device for making a connection with a printed circuit board
59,208,900.2	G. Mohr, H. Arnold, P. J. Lueck, and T. Zur Nieden	Printed circuits with local higher wiring density and method for manufacturing such printed circuits

Japan

948,145-1	K. Nakada, K. Yamazaki, and T. A. Merino	Industrial design of SST display
999,471	K. Nakada and K. Yamazaki	Industrial design of external I/R unit
2,131,331	A. Ikeda, T. Koseki, and T. Ueki	Liquid crystal displays
2,680,260	T. E. Semba	A control method of seek settling for a disk drive
2,683,489	K. Takayanagi and S. Watanabe	Programmable bus protocol
2,683,506	H. Ueda	Image data compression
2,683,870	N. Itoh	Fast approximate retrieval method for tri-structured dictionary
2,688,164	S. Shimotono and T. Kawamura	An emulation method of FDD change status
2,691,101	H. Minakata	A method of inputting line drawings for a paper-like interface
2,691,138	S. Ikami and T. Asano	Pattern randomization circuit for reducing R-EMI from bus-lines
2,691,140	H. Sugi, H. Tagai, M. Takahashi, N. Harada, and S. Shimotono	SCSI ID control
2,694,126	M. Nishida, M. Shinohara, and M. U. Uda	Projection display program

Switzerland

492,025	A. Engbersen and W. E. Denzel	High-speed multi-port FIFO buffer circuit
537,408	R. Oechsle	Routing in a network of bridge-connected LAN segments

United Kingdom

451,384	J. R. Lewis	Data processing system
2,064,923	J. Tierney	G72 17" monitor
2,289,970	C. D. McCall	Interleaved pattern ordering for training of neural networks

United States

5,657,425	W. J. Johnson	Location dependent verbal command execution in a computer-based control system
5,657,439	J. M. Menon and M. R. Jones	Distributed subsystem sparing
5,657,440	R. W. Shomler and W. F. Micka	Asynchronous remote data copying using subsystem-to-subsystem communication
5,657,447	D. Y. Chen, F. T. Sharp, J. Ng, J. Sidik, M. H. Young, and P. C. Leung	Platform-transparent registration and build of stored procedures and user-defined functions
5,658,060	D. B. Dove	Arrangements for projection displays employing reflective light valves
5,658,152	E. J. Selker	Three-prong swivel plug
5,658,185	C. Morgan, D. A. Schmidt, and P. Theodoseau	Chemical-mechanical polishing apparatus with slurry removal system and method
5,658,423	A. Lee, D. Angell, M. C. Sturzenbecker, and P. B. Chou	Monitoring and controlling plasma processes via optical emission
5,658,475	A. Tam, B. Karni, H. Seing, M. Barenboim, P. Baumgart, P. J. Kerstens, P. P. Chrusch, and T. Nguyen	Apparatus for laser texturing disks
5,658,734	G. McGall, G. M. Wallraff, J. Labadie, P. J. Brock, and W. D. Hinsberg	Process for synthesizing chemical compounds
5,658,816	T. V. Rajeevakumar	Method of making DRAM cell with trench under device for 256MB DRAM and beyond
5,659,153	C. Narayan and J. M. Shaw	Thermoformed three-dimensional wiring module
5,659,187	B. S. Meyerson and F. Le Goues	Low defect density/arbitrary lattice constant heteroepitaxial layers
5,659,208	C. J. Kimble and P. K. Egan	Power supply with multiple isolated regulators and isolation mode
5,659,256	A. P. Ingraham, G. Hill, G. Osborne, G. C. Correia, K. B. Horsford, M. A. Couture, M. D. Lowell, M. V. Pierson, R. G. Charlton, and V. Markovich	Method and apparatus for testing integrated circuit chips
5,659,307	G. Mc Vicker, H. Agata, J. P. Karidis, K. Nakada, and M. Goldowsky	Keyboard with biased movable keyboard sections
5,659,436	B. C. Webb, E. J. Yarmchuk, M. Schultz, and T. J. Chainer	Radial self-propagational pattern generation for disk file servowriting
5,659,440	D. C. Acosta, D. C. Graves, J. A. Jackmond, L. K. Whitted, and P. Y. Hu	Data cartridge magazine with interface to automatic cartridge accessing devices
5,659,441	B. L. Rappel, E. A. Eckberg, G. D. Malagrino, Jr., and T. D. Weller	Mechanical device enclosure for high-performance tape drive
5,659,447	C. G. Keller and T. A. Gregory	Magnetic slider design for precision wear-in
5,659,449	D. J. Wanek, J. T. Coffey, and W. W. Brooks, Jr.	Direct access storage device with a molded actuator motor coil support
5,659,492	A. Halperin, L. Li, and S. G. Barbee	Chemical mechanical polishing endpoint process control
5,659,508	H. Pilo, K. Petrunich, P. Zerr, R. Verhelst, R. D. Rossi, and S. Lamphier	Special mode enable transparent to normal mode operation
5,659,551	P. Patel, T. Lo, T. J. Slegel, and W. V. Huott	Programmable computer system element with built-in self-test method and apparatus for repair during power-on
5,659,596	J. M. Dunn	System for location of communication end users
5,659,630	D. C. Forslund	Advanced manufacturing inspection system
5,659,671	A. D. Bowen, D. Tannenbaum, and R. S. Horton	Method and apparatus for shading graphical images in a data processing system
5,659,676	R. J. Redpath	Method and apparatus for automatically generating database definitions of indirect facts from entity-relationship diagrams
5,659,678	C. A. Branson and F. J. Aichelmann, Jr.	Fault-tolerant memory
5,659,682	M. Devarakonda and W. Kish	Scheme to determine completion of directory operations for server recovery
5,659,696	N. Amini and R. Horne	Method and apparatus for determining address location and taking one of two actions depending on the type of read/write data transfer required
5,659,697	C. D. Dietz	Translation lookaside buffer for faster processing in response to availability of a virtual address portion before a second virtual address portion
5,659,699	M. Breternitz, Jr.	Method and system for managing cache memory utilizing multiple hash functions
5,659,708	Unknown, J. M. Kaiser, R. K. Arimilli, and W. K. Lewchuk	System and method for determining source of data in a system with intervening caches
5,659,710	J. E. Derrick and K. L. Sherman	Cache coherency method and system employing serially encoded snoop responses
5,659,722	B. Blaner and L. D. Larsen	Multiple condition code branching system in a multi-processor environment

5,659,723	A. H. Lindsey, F. S. Haynes, M. E. Lorenz, and P. P. Dimitrios	Entity/relationship to object-oriented logical model conversion method
5,659,728	B. Iyer, G. Bhargava, and P. Goel	System and method for generating uniqueness information for optimizing and SQL query
5,659,730	E. E. Kelley and J. F. Paleveda	Method of comparing data files
5,659,752	R. R. Heisch and S. R. Saiyed	System and method for improving branch prediction in compiled program code
5,659,755	O. Strohacker	Method and system in a data processing system for efficiently compressing data using a sorting network
5,659,756	E. Hefferon, J. P. Kubala, J. S. Trotter, and R. A. Paul	Method and system for providing access to logical partition information on a per resource basis
5,659,757	J. S. Peek, J. T. O'Quin II, and L. M. Browning	Method and system for lock instrumentation in a data processing system
5,659,773	D. H. Huynh, L. T. Tran, P. R. Vadapalli, and R. R. Trombly	Personal computer with input/output subsystem
5,659,785	G. G. Pechanek, J. Glossner, L. D. Larsen, and S. Vassiliadis	Array processor communication architecture with broadcast processor instructions
5,659,786	B. Glendening, D. E. Stucki, J. George, J. T. Rodell, N. E. Shafa, P. G. Greenstein, R. E. Hough, and R. R. Guyette	System and method for dynamically performing resource reconfiguration in a logically partitioned data processing system
5,659,790	J. Song and M. Y. Kim	System and method for globally scheduling multimedia stories
5,659,795	A. Bugg, A. F. Stuart, G. Felderman, K. E. Duvall, R. L. Heiney, and S. Walker	System and method for controlling computer output devices by utilizing both their static and dynamic attributes
5,659,951	D. B. Stone, J. R. Wilcox, R. T. White, and T. P. Gall	Method for making printed circuit board with flush surface lands
5,660,672	A. Halperin, L. Li, S. G. Barbee, and T. F. Heinz	In-situ monitoring of the change in thickness of films
5,660,921	A. Viehbeck, G. F. Walker, J. Labadie, J. L. Hedrick, K. W. Lee, K. Y. Ahn, and R. Twieg	Poly(aryl ether benzimidazoles) their use capping layers in microelectric structures
5,661,089	J. W. Wilson	Semiconductor chip package with enhanced thermal conductivity
5,661,330	A. C. Forcier, A. M. Palagonia, B. A. Pierson, B. R. Aimi, D. A. Schmidt, J. E. Cronin, J. M. Leas, and P. Marmillion	Fabrication, testing and repair of multichip semiconductor structures having connect assemblies with fuses
5,661,395	D. E. Skooglund, D. K. Johnson, and M. A. Sorna	Active, low VDS, field effect transistor current source
5,661,524	A. Y. Ngai, E. G. Vail III, and J. A. Murdock	Method and apparatus for motion estimation using trajectory in a digital video encoder
5,661,614	A. J. Wallash, R. Eaton, and R. G. Hirko	Method and apparatus for error recovery for unstable magnetorestrictive heads using bias current reversal
5,661,618	A. M. Homola, C. A. Brown, D. R. Gillis, G. W. Tyndall, M. S. Crowder, and V. Raman	Magnetic recording device having an improved slider
5,661,657	B. E. Osborn, C. Y. Wu, H. H. Nick, and S. Jordan	System and method for performing multiple random asynchronous electrostatic discharge
5,661,674	R. M. Smith	Divide to integer
5,661,680	D. P. Swart and M. A. Bergquist	An analog output buffer circuit
5,661,684	R. C. Wong, S. Ogura, and T. N. Buti	Differential sense amplifier
5,661,727	B. Rezvani, D. McKay, D. N. McKay, F. P. Novak, M. Naghshineh, and P. Kermani	Schemes to determine presence of hidden terminals in wireless networks environment and to switch between them
5,661,732	T. Lo	Programmable ABIST microprocessor for testing arrays with two logical views
5,661,807	D. Coppersmith, D. B. Johnson, R. Guski, R. Larson, and S. M. Matyas	Authentication system using one-time passwords
5,661,896	A. D. Erpelding	Termination pad manipulator for a laminated suspension in a data storage system
5,663,036	J. T. Fahey, K. M. Welsh, R. Sooriyakumaran, W. E. Conley, and W. M. Moreau	Composition for controlling chemically amplified deep UV photoresist structure sidewall profiles
5,663,101	J. E. Cronin	Semiconductor structure having multiple levels of self-aligned interconnection metallization, and methods for its preparation
5,663,517	D. Oppenheim	Interactive system for compositional morphing of music in real-time
5,663,526	D. F. Card, E. Dittmann, and M. K. Saraiya	Optical module with tolerant wave soldered joints
5,663,578	J. F. Shepard, L. Hsu, and M. J. Saccamango	Thin-film transistor with self-aligned bottom gate

5,663,637	A. Halperin, A. McGuire, F. W. Kazak, G. R. Doyle, K. L. Holland, L. Li, R. Nadeau, R. G. Lipori, S. G. Barbee, and W. Surovic	Rotary signal coupling for chemical mechanical polishing endpoint detection with a Westech tool
5,663,652	M. R. Freeman	Method for measuring current distribution in an integrated circuit by detecting magneto-optic polarization rotation in an adjacent magneto-optic film
5,663,663	L. A. Walls, S. Dutta, T. A. Cao, T. D. Trinh, and T. Q. Nguyen	Mixed voltage interface converter
5,663,669	N. R. Vanderschaaf	Circuitry and method for latching information
5,663,670	J. A. Iadanza and M. Ueda	Controllable variable delay inverter for a process tolerant delay circuit
5,663,689	D. W. Siljenberg, R. A. Philpott, and S. J. Baumgartner	Method and apparatus for providing a high-speed charge pump with low static error
5,663,785	J. P. Kirk and R. M. Martino	Diffraction pupil filler modified illuminator for annular pupil fills
5,663,806	G. D. Grise and J. Zalesinski	Non-destructive target marking for image stitching
5,663,854	A. Grill, C. D. Snyder, C. E. Yeack-Scranton, M. A. Moser, and V. V. Patel	Prebent ceramic suspension
5,663,924	H. L. Kalter and J. E. Barth	Boundary independent bit decode for a SDRAM
5,663,938	C. H. Dang and C. T. Dang	Checkerboard data storage library
5,663,950	J. L. Smith and N. P. Lee	Methods and systems for fault isolation and bypass in a dual ring communication system
5,663,965	E. M. Seymour	Central test controller of embedded arrays in large-scale processors
5,663,966	L. L. Day, P. A. Ganfield, and S. M. Douskey	System and method for minimizing simultaneous switching during scan-based testing
5,663,969	H. H. Tsou	Parity-based error detection in a memory controller
5,663,991	I. I. Novof, R. Kelkar, and S. Wyatt	Integrated circuit chip having built-in self-measurement for PLL jitter and phase error
5,664,044	M. S. Ware	Synchronized variable-speed playback of digitally recorded audio and video
5,664,061	G. H. Daggett, J. Karat, J. C. Andreshak, J. M. Lucassen, R. L. Mack, and S. Levy	Interactive computer system recognizing spoken commands
5,664,063	O. Weber and W. J. Johnson	Method and system for user-defined response to the posting of a calendar event
5,664,080	A. A. Rodriguez, A. J. Saenz, B. D. Lucas, and M. Pietras	System and method for generating a universal palette and mapping an original color space to the universal palette
5,664,097	M. D. Smith, M. L. Williams, and W. J. Johnson	System for delaying the activation of inactivity security mechanisms by allowing an alternate input of a multimedia data processing system
5,664,120	Unknown and S. Mallick	Method for executing instructions and execution unit instruction reservation table within an in-order completion processor
5,664,124	A. Kulik, D. Cronin, S. Katz, and W. A. Wall	A bridge between two buses of a computer system that latches signals from the bus for use on the bridge and responds according to the bus protocols
5,664,142	G. D. Boldt, R. E. Vogelsberg, and S. D. Hanna	Chained DMA devices for crossing common buses
5,664,143	A. K. Olbrich	Rotational position queue sorting without rotational position sense
5,664,146	J. A. Fisher, M. R. Bolin, and R. S. Goncharsky	Operator-communications interface for a manual data storage library
5,664,147	M. J. Mayfield	Progressive data cache
5,664,150	C. M. Herring, E. Yee, G. G. Isaac, J. J. Szarek, J. K. Langgood, K. Sendlein, and W. L. Leung	Computer system with a device for selectively blocking writebacks of data from a writeback cache to memory
5,664,155	D. A. Elko, J. A. Frey, J. M. Nick, K. G. Rothwell, and M. D. Swanson	A dumping service facility for multisystem environments
5,664,165	B. J. Wolford and S. E. Curry	Generation of a synthetic clock signal in synchronism with a high-frequency clock signal and corresponding to a low-frequency clock signal
5,664,171	A. N. Swami and R. Agrawal	System and method for query optimization using quantile values of a large unordered data set
5,664,174	H. Sawhney, K. Shim, K. I. Lin, and R. Agrawal	System and method for discovering similar time sequences in databases
5,664,175	B. K. Jackson, M. L. Williams, P. L. Miller, and W. E. Warren	Method and system for reprioritizing calendar items on a data processing system
5,664,176	H. H. Madduri	Moving write lock for replicated objects
5,664,183	B. James, D. M. Telfer, P. D. Heinlein, and P. J. Cirulli	Application of groupware to ISO 9000 registration via facilitated work sessions
5,664,186	J. M. Showalter, R. B. Bennett, and R. M. Nugent	Computer file management and backup system

5,664,190	C. A. Jones, D. D. King, M. L. Cohen, and S. M. Consolatti	System and method for enabling an event-driven interface to a procedural program
5,664,210	R. J. Torres and S. S. Fleming	Method and system of providing multiple selections in text on a computer display
5,664,215	Unknown, D. Burgess, L. R. Kling, and S. P. Song	Data processor with an execution unit for performing load instructions and method of operation
5,664,219	J. S. Trotter, M. Halma, and S. G. Glassen	Method and system for controlling serviceability of adapters connected by an I/O channel interface to a computer system
5,664,223	B. J. Nathanson, C. Bender, G. M. Salem, R. A. Swetz, and S. Zee	Flexible bridge between processor micro channel and switching mechanism
5,664,226	B. M. Czako, B. W. Lawton, S. A. O'Loughlin, and W. L. Sharp	System for merging plurality of atomic data elements into single synchronized file by assigning output rate to each channel in response to presentation time duration
5,664,316	C. H. Tsang, J. H. Lee, J. J. Lo, K. Kung, M. M. Chen, M. T. Krounbi, P. K. Wang, and R. E. Fontana	Magnetoresistive read transducer having a contiguous longitudinal bias layer
5,665,195	G. Natarajan, J. U. Knickerbocker, and R. J. Bezama	Apparatus and method for forming cavities without using an insert
5,665,203	J. Stiebritz, K. R. Milkove, and Y. H. Lee	Silicon etching method
5,665,251	C. D. Snyder, H. Santini, and N. Robertson	RIE image transfer process plating
5,665,493	R. M. Macfarlane	Gated recording of holograms using rare-earth doped ferroelectric materials
5,665,527	G. M. Wallraff and R. D. Allen	Process for generating negative tone resist images
5,665,608	D. L. Wilson, J. Wong, J. A. O'Neill, J. D. Chapple-Sokol, N. Sarma, and R. Conti	Aluminum oxide low pressure chemical vapor deposition (LPCVD) system-Fourier transform infrared (FTIR) source chemical control
5,665,622	K. P. Muller and W. Natzle	Folded trench and RIE/deposition processes for high value capacitors
5,665,626	J. E. Cronin	Method of making a chimney capacitor
5,665,629	B. Chen and G. S. Starkey	Four transistor SRAM process
5,665,650	D. H. Glatzel, D. J. Russell, and J. M. Lauffer	Method for manufacturing a high-density electronic circuit assembly
5,665,655	E. J. White	Process for producing crackstops on semiconductor devices and devices containing the crackstops
5,666,000	E. J. White and M. Dusablon	Microcavity capacitive device
5,666,078	F. J. Towler, H. Pilo, H. Schneiderwind, and S. Lamphier	Programmable impedance output driver
5,666,114	C. Y. Ng, J. Cheng, J. M. Menon, L. J. Garibay, R. B. Brodie, and T. T. Nguyen	Method and means for managing linear mapped address spaces storing compressed data at the storage subsystem control unit or device level
5,666,118	J. E. Gersbach	Self-calibrating segmented digital-to-analog converter
5,666,246	D. E. Heim and H. S. Gill	Magnetic storage system with canted hard bias magnetoresistive head
5,666,248	H. S. Gill	Magnetizations of pinned and free layers of a spin valve sensor set by sense current fields
5,666,320	R. C. Wong, S. Ogura, and T. N. Buti	Storage system
5,666,337	C. H. Dang and C. T. Dang	Data storage apparatus having a columnar array of storage receptacles with physical interlock means
5,666,344	H. J. Rosen, K. A. Rubin, T. C. Strand, W. I. Imaino, and W. W. Tang	Multiple data surface optical data storage system
5,666,354	C. W. Preuss, D. Schulte, and D. R. Cecchi	CMOS bi-directional differential link
5,666,383	A. Ruiz, K. S. Huang, and S. Monti	Variable rate discrete multiple tone bidirectional transmission
5,666,469	H. S. Lee	Method for generating solutions for sequencing problems
5,666,498	H. Y. Amro	Method, memory and apparatus for automatically resizing a window
5,666,500	K. W. Roberson	Workstation user interface for use in conjunction with a host data processing system
5,666,501	Unknown and P. Moreland	Method and apparatus for installing software
5,666,506	J. H. Hesson, S. J. Ciavaglia, and T. J. LeBlanc	Apparatus to dynamically control the out-of-order execution of load/store instructions in a processor capable of dispatching, issuing and executing multiple instructions in a single processor cycle
5,666,533	A. W. Shannon, S. S. Miller, T. Horiguchi, and W. D. Wallace	Method and system for program execution management using process, enclave and thread units
5,666,550	T. Sulzbach	Bus operation circuit using CMOS ratio logic circuits
5,666,557	B. Cassidy and R. Islam	Method and apparatus for automatically assigning device identifiers on a parallel data bus
5,666,560	B. M. Walk, D. F. Moertl, G. S. Van Grinsven, and J. D. Stevens	Storage method and hierarchical padding structure for direct access storage device (DASD) data compression

5,666,985	D. A. Lord and W. Smith	Programmable method and apparatus for cleaning semiconductor elements
5,667,586	A. Powell, B. A. Ek, F. Guarin, S. M. Gates, and S. S. Iyer	A method for forming a single crystal semiconductor on a substrate
5,667,934	A. A. Mehta, D. W. Wang, E. R. Skarvinko, and V. R. Markovich	Thermally stable photoimaging composition
5,667,938	A. Katnani, D. Yang, G. J. Hefferon, M. Khojasteh, N. R. Bantu, R. Sooriyakumaran, W. R. Brunsvold, and W. S. Huang	Acid scavengers for use in chemically amplified photoresists
5,668,018	J. E. Cronin and J. E. Gortych	Method for defining a region on a wall of a semiconductor structure
5,668,059	F. R. Christie, K. I. Papathomas, and D. W. Wang	Solder interconnection structure and process for making
5,668,399	J. A. Hildebeitel and J. E. Cronin	Semiconductor device with increased on chip decoupling capacitance
5,668,492	M. Pedersen and P. Wohl	Integrated circuit clocking technique and circuit therefor
5,668,503	J. E. Gersbach and M. Hayashi	System and method for calibrating damping factor of analog PLL
5,668,507	D. M. Dreps and D. W. Boerstler	Noise generator for evaluating mixed signal integrated circuits
5,668,525	D. G. Mikan, J. S. Muhich, and T. Chiu	Comparator circuit using two-bit to four-bit encoder
5,668,598	E. Linzer, E. E. Viscito, and H. Peterson	Motion video compression system with guaranteed bit production limits
5,668,599	C. H. Ngai, D. P. Cheney, J. D. Myers, M. L. Ciacelli, and S. B. Herndon	Memory management for an MPEG2 compliant decoder
5,668,684	A. D. Erpelding, D. D. Palmer, and S. Pattanaik	Electrical interconnect for a head/arm assembly of computer disk drives
5,668,687	C. H. Tsang, K. Kung, and M. M. Chen	Magnetoresistive sensor employing an exchange-bias enhancing layer with a variable-composition transition region
5,668,709	J. E. Gersbach and M. Hayashi	Switched capacitor current source
5,668,761	J. S. Muhich, L. B. Phillips, and R. P. Masleid	Fast read Domino SRAM
5,668,800	J. G. Stevenson	Path testing in communications networks
5,668,816	P. W. Wong and S. M. Douskey	Method and apparatus for injecting errors into an array built-in-self-test
5,668,830	C. J. Georgiou, K. W. Lee, and T. A. Larsen	Digital phase alignment and integrated multichannel transceiver employing same
5,668,943	C. A. Polyzois, C. R. Attanasio, J. L. Peterson, M. A. Butrico, and S. E. Smith	Virtual shared disks with application transparent recovery
5,668,944	R. F. Berry	Method and system for providing performance diagnosis of a computer system
5,668,952	A. A. Slane	Method for resolving network address by sending reresolve request to nodes at selected time period after establishing address table, and updating the table with received reply thereto
5,668,957	G. T. Davis, M. Krause, P. Kam, P. Patrick, and R. R. Devins	Method and apparatus for providing virtual DMA capability man adapter connected to a computer system bus with no DMA support
5,668,958	E. Johnson, E. J. Bendert, E. S. Berman, M. E. Vendryes, R. B. Bennett, R. M. Nugent, and S. M. Farrell	Heterogeneous filing system with common API and reconciled file management rules
5,668,959	J. W. Malcolm	Creating multiple versions of panels from a single panel definition file
5,668,961	H. Huang, T. Nguyen, and V. Healy	System and method for fast display of data objects in a table
5,668,968	C. F. Wu	Two-level virtual/real set-associate cache system and method with improved synonym detection
5,668,972	Unknown and P. P. Liu	Method and system for miss sequence cache line allocation
5,668,979	R. J. Mitchell and W. Lawless	Storage of clipping plane data in successive bit planes of residual frame buffer memory
5,668,984	M. P. Taborn, N. Bailey, and S. Burchfiel	Processing system and method of operation
5,668,986	D. Garcia and K. Nilsen	Method and apparatus for handling data storage requests in a distributed database environment
5,668,988	M. S. Chen and P. S. Yu	Method for mining path traversal patterns in a web environment by converting an original log sequence into a set of traversal sub-sequences
5,669,001	J. H. Moreno	Object code compatible representation of very long instruction word programs
5,669,006	J. Song, L. J. Joskowicz, and M. Y. Kim	Method for automatically obtaining spatial layout for multimedia presentations
5,669,136	R. A. Magee	IGH input/output density MLC flat pack
5,669,137	J. A. Varcoe, J. V. Ellerson, and R. J. Noreika	Method of making electronic package assembly with protective encapsulant material

5,669,437	B. Hernandez, I. C. Noyan, M. J. Palmer, and R. Horton	High-efficiency thermal interposer
5,669,749	C. T. Danielson, G. C. Stern, J. Overacker, and M. D. Williams	Movable flexible ribbon cable and support band assembly
5,669,775	D. B. Stone, J. D. Herard, J. R. Slack, J. S. Campbell, and R. P. Nowak	Assembly for mounting components to flexible cables
5,669,972	C. F. Littell, H. Cox, J. A. Trumpetto, M. S. Vanca, and R. M. Schroedl	Flex tab thick film metal mask
5,670,187	B. D. Sewing and L. Scalia	Apparatus for in-situ green sheet slitting
5,670,388	B. Machesney, E. J. Nowak, and J. A. Mandelman	Method of making contacted body silicon-on-insulator field effect transistor
5,670,418	B. Ghosal	Method of joining an electrical contact element to a substrate
5,670,428	C. L. Bertin, K. E. Beilstein, T. H. Daubenspeck, and W. J. Howell	Semiconductor chip kerf clear method for forming semiconductor chips and electronic module therefrom
5,670,750	D. J. Russell, J. J. Hansen, and J. M. Lauffer	Electric circuit card having a donut-shaped land
5,670,803	C. L. Bertin, F. R. White, J. E. Cronin, and K. E. Beilstein	Three-dimensional SRAM trench structure and fabrication method therefor
5,670,812	E. Adler, L. Ternullo, R. W. Mann, S. Kulkarni, and W. Rausch	Field effect transistor having contact layer of transistor gate electrode material
5,670,837	K. J. Erickson and Z. E. Boutaghou	Disk drive spindle motor having split windings for each phase
5,671,152	L. W. Liebmann and M. A. Lavin	Efficient generation of negative fill shapes for chips and packages
5,671,281	J. E. Campbell, M. Chan, P. Wiltgen, and S. F. Hajek	Self-modifying access code for altering capabilities
5,671,328	G. Fitzpatrick, M. L. Williams, and W. J. Johnson	Method and apparatus for automatic creation of a voice recognition template entry
5,671,334	K. J. Lynne	Neural network model for reaching a goal state
5,671,356	D. T. Wang	Method and apparatus for microcode loading in a multi-nodal network exhibiting distributed control
5,671,360	G. M. Hambrick and J. T. Rogers	Project management tool implementing authority for a people-oriented work environment tool
5,671,370	M. S. Allen, J. M. Kaiser, R. K. Arimilli, and W. K. Lewchuk	Alternating data valid control signals for high-performance data transfer
5,671,372	K. A. Uplinger and W. E. Price	Data processing system with microprocessor/cache chip set directly coupled to memory bus of narrower data width
5,671,378	G. P. Andrews and W. P. Acker	Method and system for sizing of graphical user interface objects for localization
5,671,390	A. B. Johnson, J. Lui, J. M. Menon, J. T. Brady, and S. Y. Tzou	Log structured arrays using LSA sub-directory
5,671,403	E. J. Shekita and H. C. Young	Interactive dynamic programming system for query optimization with bounded complexity
5,671,405	K. L. Wu and P. S. Yu	Apparatus and method for adaptive logical partitioning of workfile disks for concurrent merge sorts
5,671,417	D. Miclette and M. D. Rogalski	Method and system for inserting floating code hooks into multiple versions of code
5,671,419	M. Hind, M. G. Burke, and P. R. Carini	Interprocedural data-flow analysis that supports recursion while only performing one flow-sensitive analysis of each procedure
5,671,432	A. R. Bertolet, B. A. Worth, E. E. Millham, K. Ferguson, R. Palmer, S. W. Gould, and T. Zitttrisch	Programmable array I/O-routing resource
5,671,441	E. Hefferon, F. E. Johnson, M. Halma, and S. G. Glassen	Method and apparatus for automatic generation of I/O configuration descriptions
5,671,442	G. W. Wilhelm, Jr. and J. W. Feeney	System having device driver operates in first mode for allowing concurrent access to adapter by applications and second mode for limiting access to one application
5,671,443	D. R. Stauffer and R. C. Stempski	Microchannel direct memory access (DMA) acceleration circuit
5,672,015	C. W. Robinson, F. C. Miller, and J. B. Lyrly	Apparatus and method for a printer
5,672,260	C. F. Carey, D. O. Powell, G. P. Vlasak, K. M. Fallon, R. S. Zarr, and V. R. Markovich	Process for selective application of solder to circuit packages
5,672,537	D. A. Carl, D. M. Kenney, S. V. Nguyen, and W. E. Mlynko	Angle-defined trench
5,672,548	H. Shaukatullah, M. A. Gaynes, P. K. Seto, and T. M. Culnane	Method for attaching heat sinks directly to chip carrier modules using flexible-epoxy
5,672,760	F. C. Burns, R. W. Snyder, V. Y. Lee, and W. W. Fleming	Ablatively photodecomposable compositions
5,672,892	N. Rovedo, R. C. Wong, and S. Ogura	Process for making and programming a flash memory array
5,672,901	J. A. Springer, J. R. Abernathey, P. C. Parries, and R. W. Mann	Structure for interconnecting different polysilicon zones on semiconductor substrates for integrated circuits

5,672,980	A. P. Ingraham, G. Hill, G. Osborne, G. C. Correia, K. B. Horsford, M. A. Couture, M. D. Lowell, M. V. Pierson, R. G. Charlton, and V. Markovich	Method and apparatus for testing integrated circuit chips
5,672,991	N. G. Thoma and T. Nguyen	Differential delay line circuit for outputting signal with equal pulse widths
5,672,994	E. J. Nowak, M. H. Tong, and W. M. Au	Antifuse circuit using standard MOSFET devices
5,673,005	W. D. Pricer	A time standard circuit with delay line oscillator
5,673,037	C. Cesar, H. Heinrich, K. Goldman, K. P. McAuliffe, S. L. Greene, S. S. Chan, and T. Cofino	System and method for radio frequency tag group select
5,673,132	G. Miracle, P. Potvin, and Q. J. Carbone	Apparatus for repowering and monitoring serial links
5,673,177	G. L. Kehley, S. B. Sathe, and W. L. Brodsky	Heat sink structure with corrugated wound wire heat conductive elements
5,673,201	C. L. Meiley and R. L. Malm	Sub-problem extraction method for wiring localized congestion areas in VLSI wiring design
5,673,206	R. H. Jones	Real-time digital audio compression/decompression system
5,673,209	A. Bugg, A. F. Stuart, G. Felderman, K. E. Duvall, R. L. Heiney, and S. M. Scott	Apparatus and associated method for compressing and decompressing digital data
5,673,215	A. S. Tsay	Non-restoring fixed-point divider apparatus
5,673,216	A. M. Ahsan, E. E. Kelley, F. Motika, and N. J. Dauerer	Process and system for adding or subtracting symbols in any base without converting to a common base
5,673,244	A. E. Rosenbluth, J. C. Chastang, and K. B. Kirtley	Oblique viewing microscope system
5,673,315	T. J. Wolf	Method and system for software asset usage detection and management
5,673,316	C. S. Chow, J. Auerbach, J. C. Crigler, and M. A. Kaplan	Creation and distribution of cryptographic envelope
5,673,318	M. Bellare, P. W. Rogaway, and R. A. Guerin	Method and apparatus for data authentication in a data communication environment
5,673,319	M. Bellare and P. W. Rogaway	Block cipher mode of operation for secure, length-preserving encryption
5,673,336	A. Edgar	Automatic cross color elimination
5,673,369	M. Y. Kim	Authoring knowledge-based systems using interactive directed graphs
5,673,382	D. M. Cannon and M. A. Kaczmariski	Automated management of off-site storage volumes for disaster recovery
5,673,391	C. F. Webb, M. S. Farrell, and S. A. Swaney	Hardware retry trap for millicoded processor
5,673,399	D. Neal, E. Silha, G. L. Guthrie, and S. M. Thurber	System and method for enhancement of system bus to mezzanine bus transactions
5,673,403	Unknown, C. V. Powell, L. B. Hickerson, R. L. Tycast, and R. L. Verburg	Multiple graphical user interfaces on a single display
5,673,413	J. M. Kaiser and S. R. Deshpande	Method and apparatus for coherency reporting in a multiprocessing system
5,673,414	B. F. Boury, N. Amini, R. Horne, and S. Brannon	Snooping of I/O bus and invalidation of processor cache for memory data transfers between one I/O device and cacheable memory in another I/O device
5,675,315	J. G. Wacławsky and P. C. Hershey	System and method for ring latency measurement and correction