

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

France

503,207	M. Lamberton, M. M. Molinengo, and N. Ishii	Adaptation device and method for efficient interconnection of data processing devices and networks
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Germany

19,651,615.3	D. W. Meyer, H. Haese, H. Strecker, and M. Mueller	Method for applying carbon layers by reactive magnetron sputtering
69,500,346.1	D. F. Wendel, O. Torreiter, and R. Metzger	Enhanced self-test of memories

Japan

980,769-1	K. Yamazaki, S. A. Lucente, and T. Takahashi	Industrial design of firebird CD + large display model
996,505	K. Nakada	Industrial design of paper
2,657,958	H. Tsujino	Process improvement to assemble VCM in cleanroom
2,659,900	A. Ogura, M. Oie, and N. Tanaka	Screen interpolation algorithm using frame rate control
2,660,481	H. Maruyama and S. Toriyama	Data extraction method from patterned sentence
2,660,490	H. Masuda and H. Mitsuzawa	An efficient solid generation method from two 2D views
2,662,120	A. Kuroda, K. Toshioka, and M. Nishimura	Speech recognition system
2,664,817	H. Hashihara and J. U. Takahashi	A fast frame reconstruction method for coded image sequences
2,664,838	H. Hoshiba, K. A. Tago, and N. Negishi	A high-speed data transfer mechanism
2,666,033	K. Kajitani and K. Kosaka	A storage management method for a continuous digital video sequence
2,667,352	J. K. Hong, K. Iwano, and S. Misono	Fast search mechanism for movie data
2,667,369	K. Sakurai	Hold IC-card
2,667,372	H. Matsuda, I. Koyanagi, M. Jayantha, M. Sri-Jayantha, S. Kumar, V. Khanna, and V. D. Khanna	High shock proof HDD
2,667,373	Y. Kobayashi and Y. Sakaguchi	TFT/LCD analog driver
2,669,437	K. Kondoh	Character set inference by the phonetic analysis method
2,669,599	A. Doi, K. Shimada, and T. Itoh	Discontinuity-mesh generation device for radiosity
2,669,601	R. Kubota	High-speed full text search by position index
2,669,603	H. Komatsu and T. Ogasawara	Innermost loop pipelining of loops with loop invariant variables in array subscripts
2,673,097	K. Kuroki and T. Matsui	Driving method for thin-film head
2,673,098	H. Ohkuma and T. Nakanishi	Advanced solder resist pattern for high-density packaging
2,673,101	F. Nakamura, K. Kawase, and T. Moriyama	An effective texture mapping mechanism

Switzerland

511,450	D. G. Schlom, F. Lichtenberg, and J. D. Mannhart	Superconducting circuit elements with metallic substrate and method for manufacturing the same
567,711	J. Le Boudec, L. Truong, and R. Oechsle	Support to connectionless services in ATM network using partial connections
593,836	D. W. Pohl	Near-field photon tunneling devices
711,480	G. Tsudik, P. Janson, and P. A. Janson	Method and system for authenticated secure key distribution in a communication system

United Kingdom

445,454	T. Winlow	Hardware simulator
495,279	A. M. West and T. Anthias	Object-oriented programming platform
513,451	N. D. Butler and R. M. West	Memory device
546,227	A. W. Leonard, D. T. James, and P. J. Elliott	A method of servowriting a magnetic disk drive
593,161	M. A. Jewson and P. A. Beaven	Data processing system
2,064,803	M. Oates	Computer monitor
2,278,030	R. C. Speed	Smooth text display system

United States

5,543,348	E. N. Hammerl, H. Ho, J. Shiozawa, J. A. Mandelman, and R. Stengl	Controlled recrystallization of the buried strap in a 256M capacitor DRAM
5,634,268	G. J. Gaudenzi, H. M. Dalal, and K. Fallon	Method for making direct chip attach circuit card
5,634,729	M. J. Kinney, R. Myers, and R. W. Kruppa	Point-of-sale printer with magnetic reader
5,634,973	C. Cabral, J. M. Harper, J. O. Chu, and K. K. Chan	Low-temperature selective growth of silicon or silicon alloys
5,635,000	C. Sullivan, C. J. Calli, D. Cook, D. Goland, D. Mackin, D. C. Long, J. Saltarelli, J. A. Casey, J. U. Knickerbocker, K. Prettyman, K. C. O'Neil, K. M. McGuire, M. LaPlante, and M. Puchalski	Apparatus and method for screening using electrostatic adhesion
5,635,242	P. D. Agnello and T. O. Sedgwick	Method and apparatus for preventing rupture and contamination of an ultra-clean APCVD reactor during shutdown
5,635,253	D. F. Canaperi, M. Krishnan, and R. Jagannathan	Method of replenishing electroless gold plating baths
5,635,285	A. S. Bergendahl and P. E. Bakeman, Jr.	Method and system for controlling the relative size of images formed in light-sensitive media
5,635,419	D. Kenney and P. J. Geiss	Porous silicon trench and capacitor structures
5,635,693	R. J. Von Gutfeld, S. Benson, and T. Cofino	System and method for tracking vehicles in vehicle lots
5,635,719	P. F. Petric	Variable curvilinear axis deflection means for particle optical lenses
5,635,761	H. I. Stoller, L. A. Walls, T. A. Cao, and T. D. Trinh	Internal resistor termination in multi-chip module environments
5,635,836	J. R. Kirtley, M. B. Ketchen, and S. H. Blanton	Mechanical apparatus with rod, pivot and translation means for positioning a sample for use with a scanning microscope
5,635,846	B. S. Beaman, D. Y. Shih, G. F. Walker, K. E. Fogel, M. H. Norcott, and P. A. Lauro	Test probe having elongated conductor embedded in an elastomeric material which is mounted on a space transformer
5,635,847	V. P. Seidel	Apparatus for testing circuits and/or burning-in chips
5,635,848	J. A. Mahlbacher, J. M. Hammond, K. G. Roessler, L. C. Zai, and M. Servedio	Method and system for controlling high-speed probe actuators
5,635,849	H. Kolan, J. Hammond, J. C. Lo, J. E. Boyette, and M. Servedio	A miniature probe positioning actuator
5,635,858	C. A. Chang and S. H. Dhong	Zero-stopping incrementers
5,635,861	B. T. Patel and F. Chan	Off chip driver circuit
5,635,869	E. J. Nowak, F. D. Ferraiolo, I. I. Novof, and J. E. Gersbach	Current reference circuit
5,635,931	J. A. Thomas, P. Tsoucas, and P. A. Franaszek	System and method for compressing data information
5,636,086	D. S. Chhabra, S. Gopalakrishna, and S. A. Bolasna	Roll insensitive air bearing slider
5,636,090	D. W. Johnson, J. F. Boigenzahn, J. M. Rigotti, and R. E. Lagergren	Tilt limiting inertial actuator latch for a data storage system (as amended)
5,636,131	L. Liebmann, M. A. Lavin, and P. N. Sanda	Geometric autogeneration of "hard" phase-shift designs for VLSI

5,636,133	D. G. Chesebro, D. N. Maynard, M. A. Lavin, and Y. O. Kim	Efficient generation of fill shapes for chips and packages
5,636,144	J. M. Kurtzberg and M. Levanoni	Evaluation and ranking of manufacturing line non-numeric information
5,636,156	D. G. Mikan and M. S. Schmookler	Adder with improved carry lookahead structure
5,636,157	J. H. Hesson	Method and apparatus for performing 64-bit integer additions
5,636,291	D. Nahamoo, E. Bellegarda, J. Bellegarda, and K. Nathan	Continuous parameter hidden Markov model approach to automatic handwriting recognition
5,636,320	C. Yu, D. E. Kotecki, and W. Natzle	Reactor with through-chamber temperature control
5,636,325	P. W. Farrett	Speech synthesis and analysis of dialects
5,636,352	M. R. Turner and R. Bealkowski	Method and apparatus for utilizing condensed instructions
5,636,359	B. C. Beardsley, C. S. Michod, C. Y. Ng, G. E. Morain, J. Paveza, J. H. Cord, J. S. Hyde, L. R. Shipman, and V. J. Legvold	Performance enhancement system and method for a hierarchical data cache using a RAID parity scheme
5,636,361	D. Ingerman	Method and apparatus for multi-processor bus and memory bandwidth expansion
5,636,364	J. W. Knight, P. G. Emma, and T. R. Puzak	Method for enabling concurrent misses in a cache memory
5,636,372	D. J. Hathaway, J. R. Alvarez, and K. Belkhale	Network timing analysis method which eliminates timing variations between signals traversing a common circuit path
5,636,373	B. Glendening, K. Udy, R. E. Hough, and W. S. Wang	System for synchronizing logical clock in logical partition of host processor with external time source by combining clock adjustment value
5,636,376	D. Y. Chang	System and method for selectively and contemporaneously monitoring processes in a multiprocessing server
5,636,781	D. C. Olson and R. B. Phillips	Apparatus and method for removing known good die using hot shear process
5,637,442	A. C. Bhatt, G. S. Ksenak, J. A. Shurtleff, J. J. Wagner, and K. I. Papathomas	Method for treating photolithographic developer and stripper waste streams containing resist or solder mask and gamma butyrolactone or benzyl alcohol
5,637,912	J. Maltabes, L. J. O'Connor, M. A. Cockerill, and S. H. Voldman	Three-dimensional monolithic electronic module having stacked planar arrays of integrated circuit chips
5,638,019	R. F. Frankeny	Accurately generating precisely skewed clock signals
5,638,065	M. Hassner, S. Winograd, and T. Tamura	Maximum-likelihood symbol detection for RLL-coded data
5,638,130	E. Linzer	Display system with switchable aspect ratio
5,638,176	P. C. Hobbs and T. Van Kessel	Inexpensive interferometric eye tracking system
5,638,235	D. E. Heim and H. S. Gill	Magnetic storage system with canted hard bias magnetoresistive head
5,638,237	A. J. Wallash, E. T. Schreck, and P. B. Phipps	Fusible-link removable shorting of magnetoresistive heads for electrostatic discharge protection
5,638,287	W. D. Appel	Reflected wave compensation on high-speed processor cards
5,638,315	D. A. Evans, G. M. Bracerias, and R. Wistort	Content addressable memory for a data processing system
5,638,347	C. H. Dang, C. T. Dang, F. A. Baca, K. Dimitri, R. Yardy, and R. J. Means	Disk cartridge storage system
5,638,385	D. E. Galbi, H. Lee, and J. A. Fifield	Fast check bit write for a semiconductor memory
5,638,517	B. A. Bartek, C. A. Musta, and M. S. McIntyre	Method and apparatus for transmitting a message from a computer system over a network adapter to the network by performing format conversion
5,638,531	D. T. Crump and S. Pancoast	Video processor with video refresh logic
5,638,539	J. C. Goti	Tool for defining complex systems
5,638,597	C. S. Milkovich, E. A. Johnson, J. M. Zalesinski, J. S. Perkins, L. R. Cutting, M. A. Gaynes, M. V. Pierson, and S. E. Poetzinger	Manufacturing flexible circuit board assemblies with common heat spreaders
5,639,163	C. K. Vakirtzis, E. E. Davidson, and F. E. Bosco	An on-chip temperature sensing system
5,639,311	A. Sauer, B. R. Holley, and H. F. Schmitt	Method of cleaning brushes used in post CMP semiconductor wafer cleaning operations
5,639,334	A. Canale, D. Grimard, R. D. Cox, and T. Hetrick	Uniform gas flow arrangements
5,639,562	G. Natarajan and S. N. Reddy	Co-sintered surface metallization for pin-join, wire-bond and chip attach
5,639,694	H. Shaukatullah and S. J. Diffenderfer	Method for making single layer leadframe having groundplane capability
5,640,030	D. Kenney	Double dense ferroelectric capacitors cell memory
5,640,108	C. P. Miller	Single stage dynamic receiver/decoder
5,640,159	G. Furlan and J. Rissanen	Quantization method for image data compression employing context modeling algorithm

5,640,242	H. K. Wickramasinghe, J. C. Panner, M. O'Boyle, T. Van Kessel, and T. E. Sandwick	Assembly and method for making in process thin-film thickness measurements
5,640,339	A. Davis and D. W. Milton	Cache memory including master and local word lines coupled to memory cells
5,640,343	J. H. Kaufman, R. E. Scheuerlein, S. S. Parkin, and W. J. Gallagher	Magnetic memory array using magnetic tunnel junction devices in the memory cells
5,640,381	A. Juliana, D. E. Call, G. A. Jaquette, J. E. Hurst, and S. Hrinya	Laser write driver with power level scaling for generating a plurality of write power levels
5,640,402	A. C. Anderson, D. O. Forlenza, and F. Motika	Fast flush load of LSSD SRL chains
5,640,487	R. Lau, R. Rosenfeld, and S. Roukos	Building scalable n-gram language models using maximum likelihood maximum entropy n-gram models
5,640,503	A. I. Alpert and M. G. Mall	Method and apparatus for verifying a target instruction before execution of the target instruction using a test operation instruction which identifies the target instruction
5,640,513	E. E. Kelley and N. J. Dauerer	Notification of disconnected service machines that have stopped running
5,640,518	C. G. Wright, J. S. Muhich, L. J. Merkel, and R. X. Arroyo	Addition of the pre-last transfer acknowledge signal to bus interface to eliminate data bus turnaround on consecutive read and write tenures
5,640,526	L. H. Moulton III, P. D. Kartschoke, S. Rich, S. J. Ciavaglia, S. M. Conon, and S. W. Mahin	Mechanism to identify instruction boundaries in cache
5,640,530	B. C. Beardsley, G. A. Spear, and R. R. Knowlden	Use of configuration registers to control access to multiple caches and nonvolatile stores
5,640,534	Unknown and P. P. Liu	Method and system for concurrent access in a data cache array utilizing multiple match line selection paths
5,640,540	C. M. Ansberry, J. D. Freer, and T. W. Fuqua	Method and apparatus for translating key codes between servers over a conference networking system
5,640,555	E. T. Lin, J. Kleewein, and Y. Wang	Performance optimization in a heterogeneous, distributed database environment
5,640,557	F. McKiel	Flow-model engine for processing generic connectable logic blocks
5,640,558	S. G. Li	Identifying and analyzing multiple level class relationships in an object-oriented system by parsing source code without compilation
5,640,559	A. M. Silberbauer and S. J. Tabari-Belshe	System and method of encoding units of data including entity/relationship data, function calls, and file data using a common format (CDF) according to formal CDF grammar rules
5,640,561	S. Satoh and Y. Takase	A computerized method and system for replicating a database using log records
5,640,563	D. E. Carmon	Multi-media computer operating system and method
5,640,570	J. C. St. Clair and S. M. Thurber	Information handling system for transmitting contents of line register from asynchronous controller to shadow register in another asynchronous controller determined by shadow register address buffer
5,640,586	G. G. Pechanek, J. G. Delgado-Frias, and S. Vassiliadis	Scalable parallel group partitioned diagonal-fold switching tree computing apparatus
5,640,595	A. P. Stephens and M. J. Baugher	Multimedia resource reservation system with graphical interface for manual input of resource reservation value
5,640,603	A. L. Sheber, A. S. Meritt, and H. M. Yudenfriend	Logical path scheduler for disconnecting currently connected paths to enable logical paths awaiting connection to connect based on path control tables thereof
5,641,114	C. Narayan, M. J. Palmer, and R. Horton	Controlled temperature bonding
5,641,694	D. Kenney	Method of fabricating vertical epitaxial SOI transistor
5,641,838	H. G. Linde, R. A. Previti-Kelly, and T. J. Reen	Thermostable coating materials
5,641,841	A. Diaz, H. S. Nagaraj, M. Nguyen, R. D. Johnson, and T. E. Karis	Conductive lubricant for magnetic disk drives
5,642,045	G. A. Keefe and R. H. Koch	Magnetic field gradiometer with improved correction circuits
5,642,116	J. E. Gersbach	Self-calibrating segmented digital-to-analog converter
5,642,151	I. H. Schneider and W. L. Nusbickel	Analog video server
5,642,217	G. Miracle, P. Potvin, and Q. J. Carbone	Apparatus for repowering and monitoring serial links
5,642,339	C. H. Dang	Sub-carousel data storage library
5,642,362	J. Savir	Scan-based delay tests having enhanced test vector pattern generation
5,642,421	G. Tsudik, J. P. Gray, M. Peyravian, and S. M. Matyas	Encryption of low data content ATM cells
5,642,430	T. D. Greer and W. F. Phillips	Visual presentation system which determines length of time to present each slide or transparency

5,642,477	B. D. Noe, B. W. Lawton, K. Kelly, L. A. DeCarmo, and R. H. Jones	Dynamic realtime insertion of multimedia converters, effects and filters into an active multimedia data stream
5,642,478	C. Chen, D. J. Lang, and M. C. Johnson	Distributed trace data acquisition system
5,642,489	D. Moeller, L. M. Venarchick, P. M. Bland, and R. G. Hofmann	Bridge between two buses of a computer system with a direct memory access controller with accessible registers to support power management
5,642,490	J. H. Lewis, K. D. Johnson, and S. A. Morgan	Providing icon placement alternatives for dynamically added container record
5,642,491	A. C. Wynn and R. A. Rose	Method for expanding addressable memory range in real-mode processing to facilitate loading of large programs into high memory
5,642,511	A. C. Chow, J. W. Fountain, and R. L. Llamas	System and method for providing a visual application builder framework
5,642,514	S. B. Peckham	Method and system for constructing compact executable files by eliminating redundant debugging strings
5,642,515	D. N. Jones and R. A. Kalka, Jr.	Network server for local and remote resources
5,642,948	K. H. Vorhees, M. J. Kinney, and R. H. Harris	Biaxial printer for portrait and landscape printing
5,643,435	G. F. Guhman and M. Joshi	Method of fabricating semiconductor device
5,643,640	A. B. Chakravarti, M. J. Shapiro, S. V. Nguyen, and T. Cheng	Fluorine doped plasma enhanced phospho-silicate glass, and process
5,643,649	B. J. Offutt, D. H. Piltingsrud, D. L. Fox, J. A. Hagan, and S. F. Starcke	Method for improving the flatness of glass disk substrates
5,643,813	C. Galli, J. E. Acocella, J. F. Shepard, L. Hsu, N. Rovedo, and S. Ogura	Packing density for flash memories
5,643,818	K. G. Sachdev, T. Lombardi, and V. Peterson	Removal of residues from metallic insert used in manufacture of multi-layer ceramic substrate with cavity for microelectronic chip
5,643,822	J. A. Mandelman, T. Furukawa, and W. R. Tonti	Method for forming trench-isolated FET devices
5,644,038	A. Aviram, D. Bucca, D. E. Seeger, W. Brunsvold, and W. E. Conley	Quinone diazo compound containing non-metallic atom
5,644,162	C. L. Bertin, K. E. Beilstein, T. H. Daubenspeck, and W. J. Howell	Semiconductor chip having chip metal layer and transfer metal layer composed of same metal, and corresponding electronic module
5,644,221	A. Halperin, L. Li, and S. G. Barbee	Endpoint detection for chemical mechanical polishing using frequency or amplitude mode
5,644,223	R. L. Verkuil	Uniform density charge deposit source
5,644,265	D. W. Stout and J. S. Austin	Off-chip driver for mixed voltage applications
5,644,417	D. C. Bogdan, D. L. Pearl, D. T. Pribula, G. W. Hutt, M. I. Hussain, and N. R. Aulet	Automated system, and corresponding method, for measuring transmitter data-dependent jitter of electro-optic modules
5,644,428	H. Heinrich	Continuously variable beam splitter
5,644,432	F. E. Doany	Three prism color separator
5,644,453	B. L. Rappel, E. A. Eckberg, and G. D. Malagrino, Jr.	Tape drive actuator including a one piece internally pre-loaded linear bearing
5,644,470	C. R. Waters, D. Neal, M. K. Benedict, P. A. Buckland, P. M. Oman, and R. A. Kelley	Autodocking hardware for installing and/or removing adapter cards without opening the computer system cover
5,644,504	A. Y. Ngai, C. E. Boice, J. M. Kaczmarczyk, and R. L. Woodard	Dynamically partitionable digital video encoder processor
5,644,517	C. T. Ho	Method for performing matrix transposition on a mesh multiprocessor architecture having multiple processor with concurrent execution of the multiple processors
5,644,536	C. P. Miller	High gain feedback latch
5,644,555	H. Notarys, H. J. Rosen, K. A. Rubin, and T. W. McDaniel	Multiple data surface magneto-optical data storage system
5,644,577	F. E. Noel, K. J. Christensen, and L. C. Haas	Preserving data frame continuity across full-duplex LAN interface with disparate data capacities
5,644,583	E. Q. Garcia and S. M. Paranjape	Soft error correction technique and system for odd weight row error correction codes
5,644,652	D. Nahamoo, E. Bellegarda, J. Bellegarda, and K. Nathan	A system and method for automatic handwriting recognition with a writer-independent chirographic label alphabet
5,644,686	A. Hekmatpour	Expert system and method employing hierarchical knowledge base, and interactive multimedia/hypermedia applications
5,644,687	A. Vimba and D. Agonafer	Methods and system for thermal analysis of electronic packages
5,644,695	A. Vardy and M. Blaum	Array combinatorial decoding with multiple error and erasure detection and location using cyclic equivalence testing
5,644,696	A. S. Pearson and J. W. Pence	Recovering multi-volume data sets during volume recovery
5,644,698	D. M. Cannon	Configurable reuse delay period for storage pool volumes
5,644,705	T. E. Stanley	Method and apparatus for addressing and testing more than two ATA/IDE disk drive assemblies using an ISA bus

5,644,712	A. S. Coscarella, J. L. Temple III, and M. W. Sachs	Indirect addressing of channels via logical channel groups
5,644,715	M. J. Baugher	System for scheduling multimedia sessions among a plurality of endpoint systems wherein endpoint systems negotiate connection requests with modification parameters
5,644,729	B. F. Boury, N. Amini, R. Horne, S. Brannon, and T. J. Lohman	Bidirectional data storage facility for bus interface unit
5,644,736	H. Huang, T. Nguyen, and V. Healy	System and method for selecting components of a hierarchical file structure
5,644,744	L. H. Moulton III, P. D. Kartschoke, S. Rich, S. J. Ciavaglia, S. M. Conor, and S. W. Mahin	Mechanism to identify instruction boundaries in cache
5,644,751	R. C. Burnett	Fair cache policy in a distributed file system (DFS)
5,644,766	H. R. Coy, L. F. Cabrera, and R. M. Rees	System and method for managing a hierarchical storage system
5,644,771	J. C. Endicott, R. P. Resch, and S. J. Munroe	Efficient method router that supports multiple simultaneous object versions
5,644,775	R. L. Bertram and S. M. Thompson	Method and system for facilitating language translation using string-formatting libraries
5,644,779	S. P. Song	Processing system and method of operation for concurrent processing of branch instructions with cancelling of processing of a branch instruction
5,644,780	D. A. Luick	Multiple port high-speed register file with interleaved write ports for use with very long instruction word (VLIW) and n-way superscalar processors
5,644,791	D. Segev, J. Cheng, J. T. Brady, J. W. Eldridge, N. K. Ouchi, O. Cohn, and Y. Novick	Improving response time for small compressed record reads
5,645,673	B. Peterson, B. V. Fasano, D. C. Long, G. A. Pomerantz, K. C. O'Neil, M. LaPlante, and T. T. Popp	Lamination process for producing non-planar substrates
5,645,764	J. Kuczynski, J. D. Gelorme, and M. Angelopoulos	Electrically conductive pressure sensitive adhesives
5,646,053	D. J. Schepis and J. F. Shepard	Method and structure for front-side gettering of silicon-on-insulator substrates
5,646,058	H. P. Wong and Y. Taur	Method for fabricating a self-aligned double-gate MOSFET by selective lateral epitaxy
5,646,095	W. Eidelloth and W. J. Gallagher	Selective insulation etching for fabricating superconductor microcircuits
5,646,368	B. J. Wilkie, G. A. Parker, and J. E. Muyshondt	Printed circuit board with an integrated twisted pair conductor
5,646,425	D. B. Beach	Supersaturated rare earth doped semiconductor layers by chemical vapor deposition
5,646,509	N. C. Berglund, S. W. Steele, and T. J. Rosedahl	Battery capacity test and electronic system utilizing same
5,646,513	S. R. Riggio	Dynamic loop compensation for continuous mode power converters
5,646,544	J. A. Iadanza	System and method for dynamically reconfiguring a programmable gate array
5,646,546	A. R. Bertolet, B. A. Worth, C. M. Fuller, E. E. Millham, F. R. Keyser, G. Yasar, J. A. Iadanza, K. Clinton, S. Hartman, S. W. Gould, T. Reny, and T. Zitttrisch	Programmable logic cell
5,646,557	E. B. Schorn and S. L. Runyon	Data processing system and method for improving performance of domino-type logic using multiphase clocks
5,646,566	K. A. Batson and R. Ross	Low power clocked set/reset fast dynamic latch
5,646,572	R. P. Masleid	Power management system for integrated circuits
5,646,647	A. C. Chow	Automatic parking of cursor in a graphical environment
5,646,648	R. L. Bertram	Musically enhanced computer keyboard and method for entering musical and textual information into computer systems
5,646,651	A. G. MacInnis and J. Spannaus	Block mode, multiple access multi-media/graphics memory
5,646,676	T. J. Dewkett and W. T. Boyd	Scalable interactive multimedia server system for providing on demand data
5,646,731	F. Zenhausern, H. K. Wickramasinghe, M. O'Boyle, and Y. Martin	Interferometric detection/imaging method based on multi-pole sensing
5,646,801	D. E. Goodman, J. F. Boigenzahn, R. E. Lagergren, and R. J. Bornhorst	Direct access storage device with improved reliability flex cable mounting
5,646,875	D. T. Matheny, M. P. Taborn, and S. Burchfiel	Denormalization system and method of operation
5,646,918	J. E. Kulakowski, K. Dimitri, and R. J. Means	Operating a multi-gripper accessor in an automated storage system

5,646,923	M. J. Shea	System and method for hiding P-list entries for a disk from an initiator using a small computer system interface
5,646,939	A. D. Walker, B. S. Trubey, C. Lindeborg, D. Bartolini, E. Carroll, J. Griesing, J. Moran, and L. Lindell	A method and apparatus for correcting a defect in a token ring network
5,647,058	A. N. Swami, C. Faloutsos, M. D. Flickner, R. Agrawal, and W. Equitz	Method for high-dimensionality indexing in a multi-media database
5,648,005	J. M. Cobb and P. K. Parker	Modified quartz plate to provide non-uniform light source
5,648,113	D. L. Wilson, J. Wong, J. A. O'Neill, J. D. Chapple-Sokol, N. Sarma, R. Conti, R. Hsiao, S. Zuhoski, and S. G. Barbee	Aluminum oxide LPCVD system
5,648,188	S. K. Doran	Real time alignment system for a projection electron beam lithographic system
5,648,268	J. S. Batchelder, K. R. Pope, M. M. Plechaty, and P. C. Hobbs	Radionuclide exchange detection of ultra trace ionic impurities in water
5,648,684	C. L. Bertin, H. L. Kalter, and W. J. Howell	Endcap chip with conductive monolithic L-connect for multichip stack
5,648,720	E. J. Yarmchuk	Apparatus and method for producing a magnetic image of a conductive pattern using eddy currents
5,648,776	A. X. Widmer	Serial-to-parallel converter using alternating latches and interleaving techniques
5,648,801	F. Lee, G. Beardsley, and G. S. Keller	Grayscale printing system
5,648,912	L. T. Liu, P. S. Honsinger, and V. Narayanan	Interconnection resource assignment method for differential current switch nets
5,649,060	D. Kanevsky, D. Nahamoo, H. A. Ellozy, M. A. Picheny, M. Y. Kim, and W. Zadrozny	Automatic indexing and aligning of audio and text using speech
5,649,070	J. H. Connell, R. Mohan, and R. M. Bolle	Learning system with prototype replacement
5,649,078	J. A. Gerth, S. A. Hall, and W. L. Luken	Efficient two-pass rasterization scheme utilizing visibility
5,649,085	T. F. Lehr	Method and system for storing and displaying system operating traces with asynchronous event-pairs
5,649,097	B. K. Bullis, D. L. Stanley, R. L. Schoenike, S. A. King, and T. B. Brodnax	Synchronizing a prediction RAM
5,649,101	J. M. Mathewson	System and method for improving 3270 data stream performance by reducing transmission traffic
5,649,111	J. Stoltz, R. Rehquate, and T. E. Stammely	Data port expander
5,649,121	D. E. Riehm, D. J. Hunt, M. Budman, and M. J. Kuzawinski	Method and apparatus for providing a remotely located outrigger card electrically coupled to a control card
5,649,135	G. G. Pechanek, J. Glossner, L. D. Larsen, and S. Vassiliadis	Parallel processing system and method using surrogate instructions
5,649,140	B. W. Krumm, C. F. Webb, D. McCauley, K. J. Duvalisaint, and M. S. Farrell	System for use in translating virtual addresses into absolute addresses
5,649,150	M. Pedersen	Scannable last-in first-out register stack
5,649,153	B. McNutt, B. N. Harding, G. E. Morain, and R. Azevedo	Aggressive adaptation algorithm for selective record caching
5,649,155	B. W. Krumm, C. F. Webb, D. Wong, J. Ji, J. S. Liptay, S. Q. Ying, and S. T. Comfort	Cache memory accessed by continuation requests
5,649,158	G. F. McBrearty, J. E. Lahr, J. M. Shieh, and L. B. Tropicano	Method, memory and apparatus for archiving storage based on physical partitions
5,649,164	J. F. Skovira and P. L. Childs	Sets and holds in virtual time logic simulation for parallel processors
5,649,168	C. J. Huang and Y. Wang	A computer program product for a query pass through in a heterogeneous, distributed database environment
5,649,170	B. A. Chappell, G. A. Sai-Halasz, P. Patel, and P. Phan	Interconnect and driver optimization for high-performance processors
5,649,177	D. J. Krolak, D. W. Marquart, and L. E. Grosbach	Control logic for very fast clock speeds
5,649,178	B. Blaner and S. Vassiliadis	Apparatus and method for storing and initializing branch prediction with selective information transfer
5,649,185	D. B. Hildebrand, H. M. Gladney, J. Antognini, R. M. Cubert, R. W. Schmiedeskamp, and S. F. Horne	Method and means for providing access to a library of digitized documents and images
5,649,195	L. W. Tai and R. K. Scott	Systems and methods for synchronizing databases in a receive-only network
5,649,351	J. J. Lo, J. L. Su, and R. W. Cole	Method of making thin-film magnetic write head

5,650,032	J. Heidenreich, J. C. Forster, J. H. Keller, and M. S. Barnes	Apparatus for producing an inductive plasma for plasma processes
5,650,345	N. Rovedo and S. Ogura	Self-aligned stacked gate EEPROM with improved coupling ratio
5,650,377	D. P. Kern, K. Y. Lee, M. I. Lutwyche, and R. B. Laibowitz	Selective epitaxial growth of high- T_c superconductive material
5,650,595	K. M. Fallon, L. H. White, and M. R. Bentlage	Electronic module with multiple solder dams in soldermask window
5,650,628	D. F. Haire, J. D. Rockrohr, M. S. Gordon, and P. F. Petric	Simultaneous deflections in charged-particle beams
5,650,632	H. Coufal and R. K. Grygier	Focal plane phase-shifting lithography
5,650,654	W. P. Noble	MOSFET device having controlled parasitic isolation threshold voltage
5,650,731	M. S. Fung, B. H. Yun, and R. L. Verkuil	Photovoltaic oxide charge measurement probe technique
5,650,733	J. J. Covino	Dynamic CMOS circuits with noise immunity
5,650,823	A. Y. Ngai and R. S. Svec	Half pel motion estimation method for B pictures
5,650,887	B. Gurney, M. M. Dovek, and V. S. Speriosu	System for resetting sensor magnetization in a spin valve magnetoresistive sensor
5,650,892	L. K. Dorius and L. S. Samuelson	Negative pressure slider with optimized leading pocket for profile control
5,650,893	D. S. Chhabra, S. Gopalakrishna, and S. A. Bolasna	Roll insensitive air bearing slider
5,650,958	J. C. Slonczewski, J. Z. Sun, S. S. Parkin, and W. J. Gallagher	Magnetic tunnel junctions with controlled magnetic response
5,651,000	J. L. Smith and N. P. Lee	Method and systems for determining the nearest downstream reconfiguration unit in a dual ring communication system
5,651,013	J. A. Iadanza	Programmable circuits for test and operation of programmable gate arrays
5,651,033	D. Casper, F. D. Ferraiolo, R. S. Capowski, and T. A. Gregg	Inter-system data communication channel comprised of parallel electrical conductors that simulates the performance of a bit serial optical communications link
5,651,069	P. W. Rogaway	Software-efficient message authentication
5,651,136	J. L. Denton, K. C. Griffin, M. H. Lipasti, R. E. Johnson, R. J. Eickemeyer, S. K. Ryan, and S. R. Kunkel	System and method for increasing cache efficiency through optimized data allocation
5,651,139	J. B. Cripe, M. E. Medford, M. R. Primm, and S. L. Sanders	System partition read/write access under OS/2
5,651,493	J. D. Bielick, M. K. Hoffmeyer, and P. D. Isaacs	Method of performing solder joint analysis of semi-conductor components
5,651,712	M. D. Potter	Multi-chromic lateral field emission devices with associated displays and methods of fabrication
5,651,857	A. M. Palagonia, B. A. Pierson, D. A. Schmidt, J. E. Cronin, and P. Marmillion	Methods for preparing a sidewall spacer using an overhang
5,651,868	A. C. Wall, D. A. Edmonson, G. J. Johnson, M. L. Canady, and P. D. Teig	Method and apparatus for coating thin-film data storage disks
5,652,015	E. V. Denison, H. G. Kluge, J. A. Aboaf, J. D. Friedman, and V. N. Kahwaty	Arbitrary pattern write head
5,652,410	S. E. Wheeler and S. P. Hobbs	Conductive insert for providing electromagnetic charge protection
5,652,529	B. A. Worth, F. Furtek, F. R. Keyser, S. W. Gould, and T. Zitttrisch	Programmable array clock/reset resource
5,652,581	D. S. Sheinwald, G. Furlan, and J. Rissanen	Distributed coding and prediction by use of contexts
5,652,629	C. A. Gonzales, E. Linzer, and X. Li	Bidirectional motion estimation in a motion video compression system
5,652,630	J. J. Hurd, L. Black, R. L. Bertram, and T. K. Worthington	Video receiver display, three axis remote control, and microcontroller for executing programs
5,652,668	D. C. Bogdan, D. L. Pearl, D. T. Pribula, G. W. Hutt, M. I. Hussain, and N. R. Aulet	Automated system, and corresponding method for determining average optical output power of electro-optic modules
5,652,687	H. Santini, K. Ju, M. M. Chen, and N. Robertson	Planarized thin-film magnetic write head with submicron track width
5,652,742	C. H. Dang, F. A. Baca, and J. W. Hammar	Cartridge retention in storage cell arrays
5,652,749	D. W. Davenport, G. S. Delp, J. J. Lynch, K. G. Plotz, and P. L. Leichty	Apparatus and method for segmentation and time synchronization of the transmission of a multiple program multimedia data stream
5,652,774	A. Saha, C. Ward, N. Malik, and R. Eickemeyer	Method and apparatus for decreasing the cycle times of a data processing system
5,652,829	S. J. Hong	Feature merit generator

5,652,848	B. C. Drerup, H. Q. Bui, and S. E. Curry	Low-latency cadence selectable interface for data transfers between busses of differing frequencies
5,652,853	B. W. Krumm, D. McCauley, J. F. Scanlon, K. J. Duvalstaint, M. J. Kim, and P. H. Gum	Multi-zone relocation facility computer memory system
5,652,864	G. A. Hine	Concurrent storage allocations or returns without need to lock free storage chain
5,652,868	D. D. Williams	Data processor having BIOS decryption of emulated media images
5,652,878	D. J. Craft	Method and apparatus for compressing data
5,652,887	D. W. Dewey and W. M. Doumas	Dynamic meta commands for peripheral devices
5,652,899	R. G. Mays and T. M. Stubbs	Software understanding aid for generating and displaying simplified code flow paths with respect to target code statements
5,652,908	C. Rahn, C. A. Mastrangelo, D. Glowny, F. B. Douglas, J. Shih, P. Mayer, and P. Shier	Method and apparatus for establishing communications sessions in a remote resource control environment
5,652,914	J. Maergner, J. S. Trotter, M. Halma, and W. Eckert	Method and system for superimposing, creating and altering I/O applications and controls within an I/O subsystem by using an I/O subchannel
5,653,013	D. J. Werner and H. S. Gill	Two terminal single stripe orthogonal MR head
5,654,221	J. E. Cronin, S. E. Luce, and S. H. Voldman	Method for forming semiconductor chip and electronic module with integrated surface interconnects/components
5,654,238	G. S. Starkey, J. E. Cronin, and M. D. Potter	Method for etching vertical contact holes without substrate damage caused by directional etching
5,654,376	C. J. Knors, E. Macy, and W. M. Moreau	Polymeric dyes for antireflective coatings
5,654,570	P. D. Agnello	CMOS gate stack
5,654,695	A. Varma, B. D. Rathi, H. T. Olnowich, P. Heidelberger, P. A. Franaszek, and T. N. Barker	Multi-function network
5,654,747	G. J. Smith, G. W. VanLeeuwen, and H. H. Ottesen	Intelligent multimedia set-top control method and apparatus in which billing signals are communicated to an information network
5,654,875	D. B. Lawson	Isolator mounting system for small form factor hard disk drive
5,654,876	P. Singh and S. Sathe	Demountable heat sink
5,654,911	E. M. Schwarz and R. M. Bunce	Carry select and input select adder for late arriving data
5,654,917	N. Rovedo, R. C. Wong, and S. Ogura	Process for making and programming a flash memory array
5,654,990	R. H. Jones	Real-time digital audio compression/decompression system
5,655,080	C. A. Polyzois, D. M. Dias, K. W. Pinnow, R. L. Egan, R. L. Hoffman, and R. P. King	Distributed hash group—by cooperative processing
5,655,090	S. H. Weingart	Externally controlled DSP with input/output FIFOs operating asynchronously and independently of a system environment
5,655,094	J. H. Werner, R. L. Poston, S. H. Isensee, and T. L. Cline	Pop up scroll bar
5,655,103	J. C. Wang, K. Cheng, and K. So	System and method for handling stale data in a multiprocessor system
5,655,106	B. A. Smith	Personal computer with riser connector for alternate master
5,655,107	M. Bull	Digital logic wire delay simulation
5,655,112	A. G. MacInnis	Method and apparatus for enabling data paths on a remote bus
5,655,141	A. D. Ogden and N. R. Vanderschaaf	Method and system for storing information in a processing system
5,655,146	B. Sinha, B. R. Iyer, C. A. Scalzi, G. A. Brent, G. S. Rao, H. Ghafir, I. S. Narang, L. H. Wilson, R. I. Baum, and S. P. Sharma	Coexecuting method and means for performing parallel processing in conventional types of data processing system
5,655,209	D. Gupta, J. A. Casey, J. P. Gauci, R. A. Rita, and R. J. Sullivan	Multilayer ceramic substrates having internal capacitor, and process for producing same
5,655,213	G. Natarajan and S. N. Reddy	Co-sintered surface metallization for pin-join, wire-bond and chip attach
5,655,286	R. E. Jones, Jr.	Integrated transducer-suspension structure for longitudinal recording
5,655,475	D. J. Seagle, D. W. Crandell, and H. P. Lee	Method of grinding thin-film magnetic heads using optical grinding markers
5,655,703	A. K. Sarkhel, L. H. White, and M. A. Jimarez	Solder hierarchy for chip attachment to substrates
5,656,139	C. F. Carey, D. O. Powell, G. P. Vlasak, K. M. Fallon, R. S. Zarr, and V. R. Markovich	Electroplating apparatus
5,656,205	E. L. Thomas and J. Rabolt	Optical components from phase separated block polymers
5,656,514	D. A. Sunderland, D. C. Ahlgren, J. O. Chu, M. Revitz, M. J. Saccamango, and P. A. Ronsheim	Heterojunction bipolar transistor with self-aligned retrograde emitter profile

5,656,535	D. Dobuzinsky, E. N. Hammerl, H. Ho, J. A. Mandelman, M. Jaso, R. Srinivasan, and S. D. Halle	Simplified storage node process for deep trench-based DRAM
5,656,544	A. S. Bergendahl, C. H. Lam, C. L. Bertin, D. Kenney, H. Lee, H. L. Kalter, and J. E. Cronin	Process for forming a polysilicon electrode in a trench
5,656,553	J. M. Leas and S. H. Voldman	Method for forming a monolithic electronic module by dicing wafer stacks
5,656,554	M. Desai and M. S. Pak	Semiconductor chip reclamation technique involving multiple planarization processes
5,656,830	J. H. Zechman	Integrated circuit chip composite having a parylene coating
5,656,849	J. Burghartz, K. A. Jenkins, M. Soyuer, and S. Ponnappalli	Two-level spiral inductor structure having a high inductance to area ratio
5,656,862	D. W. Wang, F. R. Christie, and K. I. Papathomas	Solder interconnection structure
5,656,963	L. B. Phillips and R. P. Masleid	Clock distribution network for reducing clock skew
5,656,977	I. I. Novof and R. Kelkar	Frequency-lock indicator circuit with frequency-only detection
5,657,091	R. L. Bertram	Video display controller, user interface and programming structure for such interface
5,657,235	L. Liebmann, R. M. Martino, and T. J. Weed	Continuous scale optical proximity correction by mask maker dose modulation