

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

France

394,598	E. Gouze, I. Ong, J. M. Piccino, M. Bonneau, and R. Hornung	An Improved Gate Array Cell Having FETs of Different and Optimized Sizes
410,063	G. Boudon	Differential Cascode Current Switch (DCCS) Logic Circuit Family with Input Diodes
469,217	C. Cederbaum, M. Combes, P. Mone, and R. Chancelou	Method of Forming Stacked Self-Aligned Polysilicon PFET Devices and Structures Resulting Therefrom
489,214	A. Bruno, J. Fieschi, J. Martin, and R. Vautier	Coupling Device to Be Connected to a DCE for Allowing the Connection to Different Public Switched Telephone Networks DCE and WOR
489,215	A. Bruno, J. Fieschi, J. Martin, and R. Vautier	Coupling Device to Be Connected to a DCE for the Connection to Public Switched Telephone Network Having a Local Power Supply
523,307	A. Blanc, G. Richter, J. C. Abbiate, and P. Jeannot	Decimation Filter for a Σ - Δ Converter and Data Circuit Erminating Equipment Including the Same

Germany

4,407,998.2	U. Maier and W. Ruppert	Written Character Recognition
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Japan

2,027,948	T. Shima	Character Recognition System by Means of Extracting Approximated Stroke Lines
2,029,650	M. Kubo, Y. Masuda, and Y. Nakagawa	Hard Disk Drive Which Has an Actuator Lock Driven by Air
2,029,657	J. Inazumi, J. Nakamoto, and S. Kobayashi	Memory Management Method with Address Exchange
2,029,675	A. Tobari, K. Kurachi, and K. Ogasawara	BIAS Read Method for Reducing Peak Shift
2,029,730	K. Ohsaki and M. Ueda	High Speed Word Decoder of DRAM with Logic Level Converter
2,029,739	H. Kinoshita and Y. Kato	Effective and Reliable Power Management System for Notebook PC
2,029,741	T. E. Semba	Model Following SEEK Servo for Optical Disk Drive
2,029,744	H. Nomura, T. Ohtani, and Y. Uchiyama	HDD Lock Mechanism with Security Feature
2,029,746	M. Takekoshi and N. Kagami	Focus Acquisition Method for Optical Disk Drive
2,034,239	M. Hasegawa and T. Temmyo	Mutual Exclusive Resource Management Under Event Driven C
2,034,351	M. Kobayashi, M. Mori, and N. Morimoto	A High Speed Synchronized Power for Small Size Multi-Mode CRT Display
2,037,003	H. Komooka, M. Mizuno, and T. Matsuyama	Enabled Outline Drawing of Color Graphics Print
2,037,713	H. Minakata, K. Shimizu, N. Urano, and S. H. Kuriyama	Multisided Patch Generator by Two Stage Blending of Curve Sweeps
2,037,714	H. Yasuda, M. Sohda, and S. Asada	Dither Pattern Rotation Method in Expanded Screen

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2,038,815	A. Masuyama and T. Saitoh	Removal of Heavy Metal Impurity Ions
2,039,766	M. Ogata, T. Aihara, T. Kurosawa, and Y. L. Lien	A Front-end System for Supporting User Interface Mapping
2,500,858	E. Hattori, R. Ohbuchi, T. Iwami, and Y. Miyazaki	BITBLT System for Performing Operations Between a Plureli
2,500,998	F. Nakamura, M. Suzuki, T. Ohtani, and Y. Uchiyama	Multi-Functional Lock Mechanism
2,501,010	S. Hazeki, S. Ishikawa, S. H. Asaka, and T. Echigo	Integration of Multiple Sensory Data by Using Hypothesis-and-Test Method for Navigation of an Autonomous Mobile Robot
2,501,288	H. Etoh, H. Hama, and S. Misono	Gaze Estimation Mechanism with Pupil-Area Measurement
2,501,289	A. Aoyagi, H. Terashima, and M. Take	Tight Tube/Wire Clamping Method Using Adhesives Inside Tube
2,501,746	H. Asano, K. Shimomura, and M. Murakami	Drive Configuration Setting Method for AT Files
2,501,758	K. Katoh, N. Satoh, and S. Yuge	Kana to Kanji Conversion with Trackpoint on Thinkpad
2,502,468	M. Kasuya, Y. Kanada, Y. Katoh, and Y. Terashita	A Calling System for Mobile IVD Network
2,502,802	H. Takano	Controlled Reverse TNLCD
2,502,902	M. Shirai and S. Tsuchida	Wall Via Land
2,502,920	N. Ohba	A Parallel Arbitration Mechanism for ATM Switches
2,507,235	Q. He	A Client-Server Cache System Based on Asynchronous Update
United Kingdom		
462,109	A. R. Hearn, G. N. Dew, and M. W. Hall	Shaft Mounting for a Disk File
487,795	G. Luck and T. K. Gibbs	Linearity Correction Apparatus
544,954	I. D. Judd and N. Apperley	Disk Drive Synchronisation
602,821	A. Heilper, A. Kolsky, D. S. Chevion, E. Walach, I. Gilat, O. Kagan, and Y. Medan	Data Entry System
617,570	A. N. Hamper, D. Newmarch, D. S. Gaunt, E. Johnson, I. Gollledge, J. E. Veal, and S. J. Cutts	Modular Housing
618,561	D. Sawdon	Display System
2,052,738	M. Oates	Video Camera
United States		
5,467,452	A. Blum, G. Goldrian, and W. Kumpf	Routing Control Information via a Bus Selectively Controls Whether Data Should be Routed Through a Switch or a Bus According to Number of Destination Processors
5,467,473	J. A. Kahle and C.-C. Kau	Out of Order Instruction Load and Store Comparison
5,467,881	H. S. Gill	Method of Manufacturing an MR Read Head Which Eliminates Lead-to-Shield Shorts at the ABS of the MR Read Head
5,468,159	W. L. Brodsky and J. D. Herard	Portable External Flexible Cable and Package Using Same
5,468,326	J. J. Cuomo and J. E. Yehoda	Apparatus for Polishing a Diamond or Carbon Nitride Film by Reaction with Oxygen Transported to the Film Through a Superionic Conductor in Contact with the Film
5,468,445	J. A. Casey, R. S. Divakaruni, G. Natarajan, S. S. N. Reddy, and M. Sammet	Ceramic Via Composition, Multilayer Ceramic Circuit Containing Same, and Process for Using Same
5,468,663	C. L. Bertin, D. J. DiMaria, M. Miyakawa, and Y. Sakaue	Method of Fabricating Three-Dimensional Direct-Write EEPROM Arrays
5,468,687	D. Carl, D. M. Dobuzinsky, S. V. Nguyen, and T. Nguyen	Method of Making Ta ₂ O ₅ Thin Film by Low Temperature Ozone Plasma Annealing (Oxidation)
5,468,790	K. I. Papathomas	Triazine Polymer and Use Thereof
5,468,917	W. L. Brodsky, J. D. Herard, T. G. Macek, T. L. Sharp, and G. J. Shovlowsky	Circuitized Structure Including Flexible Circuit with Elastomeric Member Bonded Thereto
5,468,955	L. Chen, D. Podlesnik, and A. Sekiguchi	Neutral Beam Apparatus for In-Situ Production of Reactants and Kinetic Energy Transfer
5,468,996	B. Chan, F. L. Chapin, V. M. Fiocco, J. R. Mankus, and R. S. Pokrzywa	Electronic Package Assembly and Connector for Use Therewith
5,469,029	F. S. Jackson and D. Leaver	Deflection Apparatus for Raster Scanned CRT Displays
5,469,051	E. J. Yarmchuk	Electrical Defect Detector for Circuit Lines
5,469,161	J. D. Bezek	Algorithm for the Implementation of Ziv-Lempel Data Compression Using Content Addressable Memory
5,469,259	J. A. Golby, M. J. Padgett, and S. P. Woodall	Inspection Interferometer with Scanning Autofocus, and Phase Angle Control Features
5,469,333	J. V. Ellerson, R. J. Noreika, and J. A. Varcoe	Electronic Package Assembly with Protective Encapsulant Material on Opposing Sides Not Having Conductive Leads

5,469,446	R. E. Fuhs, W. A. Hammond, Jr., G. W. Nation, S. L. Rogers, and J. C. Willis	Retry Filter and Circulating Echo Method and Apparatus
5,469,541	J. A. Kingman, S. P. Larky, and M. T. Vanover	Window Specific Control of Overlay Planes in a Graphics Display System
5,469,560	T. W. Beglin	Prioritizing Pending Read Requests in an Automated Storage Library
5,469,568	K. B. Schiefer and A. N. Swami	Method for Choosing Largest Selectivities Among Eligible Predicates of Join Equivalence Classes for Query Optimization
5,469,574	D. T. Chang and Y. Wolfsthal	Method and System for Interfacing Interpreted Applications with Compiled Procedures Using Signature Files
5,469,575	H. H. Madduri	Determining a Winner of a Race in a Data Processing System
5,469,576	N. J. Dauerer and E. E. Kelley	Front End for File Access Controller
5,469,577	R. C. Eng, J. W. Galella, R. E. McCrary, M. G. McDonald, E. H. Stelzer, and F. C. Yentz	Providing Alternate Bus Master with Multiple Cycles of Bursting Access to Local Bus in a Dual Bus System Including a Processor Local Bus and a Device Communications Bus
5,469,981	K. V. Srikrishnan, J. F. White, and J.-M. Yang	Electrically Blowable Fuse Structure Manufacturing for Organic Insulators
5,470,661	F. D. Bailey, D. A. Buchanan, A. C. Callegari, H. M. Clearfield, F. E. Doany, D. G. Flagello, H. J. Hovel, D. C. Latulipe, Jr., N. E. Lustig, A. T. S. Pomerene, S. Purushothaman, C. M. Scherpereel, D. E. Seeger, and J. M. Shaw	Diamond-Like Carbon Films from a Hydrocarbon Helium Plasma
5,470,681	T. A. Brunner, D. B. Dove, and L. L. Hsu	Phase Shift Mask Using Liquid Phase Oxide Deposition
5,470,693	K. G. Sachdev, J. R. Whitaker, and U. M. Ahmad	Method of Forming Patterned Polyimide Films
5,470,781	D. Chidambarrao, L. L. Hsu, J. D. Mis, and J. P. Peng	Method to Reduce Stress from Trench Structure on SOI Wafer
5,470,788	G. A. Biery, D. M. Boyne, and H. M. Dalal	Method of Making Self-Aligned, Lateral Diffusion Barrier in Metal Lines to Eliminate Electromigration
5,470,796	Y. Tsukada, Y. Kobayakawa, Y. Maeda, and S. Tsuchita	Electronic Package with Lead Wire Connections and Method of Making Same
5,471,027	A. J. Call, S. H. Meisner, F. L. Pompeo, and J. A. Zitz	Method for Forming Chip Carrier with a Single Protective Encapsulant
5,471,033	N. DiPaolo and D. J. Ferguson	Process and Apparatus for Contamination-Free Processing of Semiconductor Parts
5,471,090	A. Deutsch, D. A. Lewis, C. Narayan, and A. L. Plachy	Electronic Structures Having a Joining Geometry Providing Reduced Capacitive Loading
5,471,092	C.-J. Chan, J. W. Chang, and L. T. Romankiw	Metallurgical Joint Including a Stress Release Layer
5,471,096	K. L. Papathomas, F. R. Christie, and D. W. Wang	Solder Interconnection from a Composition Containing a Mixture of Dicyanates
5,471,188	B. A. Chappell, T. L. Chappell, B. M. Fleischer, and S. E. Schuster	Fast Comparator Circuit
5,471,189	C. D. Dietz and K. J. Hoover	Comparator Circuitry and Method of Operation
5,471,368	A. P. Downie, P. Gallagher, J. J. Garrrity, and B. L. Robertson	Module Having Vertical Peripheral Edge Connection
5,471,397	E. P. Hsieh and R. V. Hudli	Identifying Subsets of Noise Violators and Contributors in Package Wiring
5,471,410	R. N. Bailey, S. M. Burchfiel, and M. P. Taborn	Method and Apparatus for Sticky and Leading One Detection
5,471,445	R. G. Emberty, F. J. Misewich, and E. J. Pawlowski	Apparatus and Method for Sensing and Holding an Item
5,471,488	C. A. Bender	Clock Fault Detection Circuit
5,471,561	R. B. Cowgill and D. C. Teale	Automated Storage Library with Rotatable Arm and Oblique Angle Effectors
5,471,570	D. P. Rackley and R. M. P. West	Hardware XOR Sprite for Computer Display Systems
5,471,576	R. L. Yee	Audio/Video Synchronization for Application Programs
5,471,581	J.-M. Munier, A. Pauporte, and C. Poiraud	Elastic Configurable Buffer for Buffering Asynchronous Data
5,471,585	E. H. Barakat, A. L. Chin, and E. B. Schorn	Personal Computer System with Input/Output Controller Having Serial/Parallel Ports and a Feedback Line Indicating Readiness of the Ports
5,471,599	T. B. Brodnax, B. K. Bullis, S. A. King, and D. A. Rickard	Partitioning of Virtual Addressing Memory
5,471,609	H. M. Yudenfriend, D. H. Surman, and B. C. Beardsley	Method for Identifying a System Holding a 'Reserve'
5,471,613	K. R. Banning, W. S. James, S.-G. Li, and A. Versteeg	Tree Structure Representation of an SQL Clause

5,471,615	K. Amatsu, Z. Butoh, H. Hirai, M. Massaki, H. Morita, T. Nakajima, N. Nomura, M. Sakaki, S. J. Schmandt, and K. Tanaka	Distributed Data Processing System Having Front-End and Back-End Computers with Different Operating Systems
5,471,616	W. J. Johnson, R. S. Keller, G. C. Manthuruthil, and M. L. Williams	Method of and Apparatus for Providing Existential Presence Acknowledgement
5,471,619	M. Messina	Method and System for Controlling Access to Objects in a Data Processing System
5,471,626	M. J. Carnevale, R. N. Kalla, G. P. McClannahan, and M. R. Trombley	Variable Stage Entry/Exit Instruction Pipeline
5,471,628	J. E. Phillips, B. Blaner, and S. Vassiliadis	Multi-Function Permutation Switch for Rotating and Manipulating an Order of Bits of an Input Data Byte in Either Cyclic or Non-Cyclic Mode
5,471,631	B. C. Beardsley, A. C. Brailey, and P. L. H. Leung	Using Time Stamps to Correlate Data Processing Event Times in Connected Data Processing Units
5,471,948	J. Burroughes, R. T. Hodgson, D. T. McInturff, M. R. Melloch, N. Otsuka, P. M. Solomon, A. C. Warren, and J. M. Woodall	Method of Making a Compound Semiconductor Having Metallic Inclusions
5,472,735	C. M. Boyko, R. W. Carpenter, R. T. Galasco, K. W. Semkow, and H. Wegener	Method for Forming Electrical Connection to the Inner Layers of a Multilayer Circuit Board
5,472,814	B. J. Lin	Orthogonally Separated Phase Shifted and Unphase Shifted Mask Patterns for Image Improvement
5,473,574	R. Clemen and K. Getzlaff	Multi-Port Static Random Access Memory with Fast Write-Thru Scheme
5,473,593	A. R. Wheeler	Compact Disk Transport Tray Moved by a Disk Reading Mechanism
5,473,669	Y. Kanada, Y. Terashita, M. Kasuya, and Y. Katoh	Method for Controlling a Mobile IVD System Having a Plurality of Personal Stations (PSS)
5,473,737	J. E. Harper	Method and Apparatus for Displaying a Composite Image Made Up of a Foreground Image and a Background Image
5,473,740	J. M. Kasson	Method and Apparatus for Interactively Indicating Image Boundaries in Digital Image Cropping
5,473,745	R. E. Berry and J. H. Werner	Exposing and Hiding a Title Bar Behind Its Window Using a Visual Cue
5,473,747	C. Bird and S. Chapman	Method and Apparatus for Identifying Features in a Multidimensional Data Set
5,473,772	H. Halliwell and M. K. Vaughton	Automatic Update of Static and Dynamic Files at a Remote Network Node in Response to Calls Issued by or for Application Programs
5,473,773	J. D. Aman, C. K. Eilert, G. M. King, B. R. Pierce, and P. B. Yocom	Apparatus and Method for Managing a Data Processing System Workload According to Two or More Distinct Processing Goals
5,473,813	I. L. Chobot, J. A. Covert, R. L. Haight, K. D. Mansfield, D. W. Miller, R. A. Neira, A. Petrovich, P. C. Sviedrys, L. A. Tiemann, G. A. Valenta, and T. B. Youngs, Jr.	Methods of Forming Electronic Multi-Layer Printed Circuit Boards and/or Cards and Electronic Packages Including Said Boards or Cards
5,473,814	L. H. White	Process for Surface Mounting Flip Chip Carrier Modules
5,475,401	G. F. Verrier and J. M. Zetts	Architecture and Method for Communication of Writing and Erasing Signals from a Remote Stylus to a Digitizing Display
5,475,542	S. R. Bentley, A. Chliwnyj, S. D. Johnson, S. M. Paranjape, and R. K. Rhodes	Method and Apparatus for Improving Inter-Block Gap Length Tolerance and Locate Accuracy for Write Appends
5,475,606	J. E. Muyshondt, G. A. Parker, and B. J. Wilkie	Faraday Cage for a Printed Circuit Card
5,475,607	J. Apte and R. Gupta	Method of Target Generation for Multilevel Hierarchical Circuit Designs
5,475,685	D. R. Garris and D. R. Staddon, Jr.	Multi-Media Flow Control Over FDDI Synchronous Networks
5,475,792	V. M. Stanford and N. F. Brickman	Telephony Channel Simulator for Speech Recognition Application
5,475,812	J. Corona, R. L. Henderson, and G. D. Laib	Method and System for Independent Control of Multiple Windows in a Graphics Display System
5,475,813	R. A. Cieslak, D. F. Ferguson, and J. Sairamesh	Routing Transactions in the Presence of Failing Servers
5,475,827	J. Y. Lee, J. H. Mirza, and R. J. Stanton, Jr.	Dynamic Look-Aside Table for Multiple Size Pages
5,475,833	N. J. Dauerer and E. E. Kelley	Database System for Facilitating Comparison of Related Information Stored in a Distributed Resource
5,475,834	M. J. Anglin, W. W. Chow, R. M. Nugent, J. M. Showalter, G. J. Tevis, and D. P. Warren, Jr.	Integration of Migration Level Two and Backup Tape Processing Using Multiple Inventory Entries
5,475,853	B. Blaner and S. Vassiliadis	Cache Store of Instruction Pairs with Tags to Indicate Parallel Execution

5,475,856	P. M. Kogge	Dynamic Multi-Mode Parallel Processing Array
5,476,575	D. J. Brophy, M. Datta, D. B. Harris, M. T. Kurdziel, and F. S. Ryan	Fabrication of Moly Masks by Electroetching
5,476,680	K. R. Coffey, J. K. Howard, T. L. Hylton, and M. A. Parker	Method for Manufacturing Granular Multilayer Magnetoresistive Sensor
5,476,691	K. Komvopoulos, I. G. Brown, B. Wei, S. Anders, A. Anders, and C. S. Bhatia	Surface Treatment of Magnetic Recording Heads
5,476,738	C.-M. Yuan	Photolithographic Dose Determination by Diffraction of Latent Image Grating
5,477,234	H. Suzuki and M. Suzuki	Liquid Crystal Display Apparatus
5,477,242	S. P. Thompson, D. P. Rackley, and S. Brannon	Display Adapter for Virtual VGA Support in XGA Native Mode
5,477,402	P. J. Elliott and A. R. Hearn	Disk File with Multiplexed Servo System
5,477,450	K. Takeda and H. Maruyama	Machine Translation Method and Apparatus
5,477,451	P. F. Brown, J. Cocke, S. A. Della Pietra, V. J. Della Pietra, F. Jelinek, J. C. Lai, and R. L. Mercer	Method and System for Natural Language Translation
5,477,460	C. K. Vakirtzis, G. A. Katopis, G. W. Mahoney, C. R. Selinger, B. D. McCredie, and W. D. Becker	Early High Level Net Based Analysis of Simultaneous Switching
5,477,530	H. Ahmadi, J. S. Chen, K. Arvind, and O. Yoram	Method and Apparatus for Managing Communications Between Multi-Node Quota-Based Communication Systems
5,478,009	M. J. Interrante and L. Economikos	Selective Removal of a Single Solder Ball from an Array of Solder Balls
5,478,270	J. L. Cuomo, R. T. Kerth, and J. C. Major	Ultrasonic Micro Machining Slider Air Bearings with Diamond Faced Patterned Die
5,478,420	J. P. Gauci and T. A. Kline	Process for Forming Open-Centered Multilayer Ceramic Substrates
5,478,700	M. A. Gaynes, G. D. Oxx, M. V. Pierson, and J. Zalesinski	Method for Applying Bonding Agents to Pad and/or Interconnection Sites in the Manufacture of Electrical Circuits Using a Bonding Agent Injection Head
5,478,781	C. L. Bertin, P. A. Farrar, Sr., W. J. Howell, C. P. Miller, and D. J. Perlman	Polyimide-Insulated Cube Package of Stacked Semiconductor Device Chips
5,479,027	Y. Kato	Semiconductor Device Having a Channel for a Zero-or One-Dimensional Carrier Gas
5,479,361	J. M. Kurtzberg and M. Levanoni	Evaluation and Ranking of Manufacturing Line Non-Numeric Information
5,479,404	P. Francois, E. Lebrun, and J. R. Warren	System for Network Wide Bandwidth Allocation
5,479,414	P. N. Keller and T. J. Koprowski	Look Ahead Pattern Generation and Simulation Including Support for Parallel Fault Simulation in LSSD/VLSI Logic Circuit Testing
5,479,415	D. E. Staiger	Method and Apparatus for Generating Test Pulses
5,479,514	J. L. Klonowski	Method and Apparatus for Encrypted Communication in Data Networks
5,479,536	L. D. Comerford	Stroke Syntax Input Device
5,479,598	D. G. Feitelson, B. G. Fitch, and M. E. Giampapa	Compact Graphical Parallel Program User Output Interface Controlled Directly by the Parallel Computer Program
5,479,599	S. L. Rockwell, K. N. Schroeder, and S. A. Sylvester	Computer Console with Group Icon Control
5,479,613	J. E. Geyer and J. K. Lee	Real-Time Ring Bandwidth Utilization Calculator, Calculating Bandwidth Utilization Based on Occurrences of First and Second Predetermined Bit Patterns
5,479,622	G. F. Grohoski and R. D. Groves	Single Cycle Dispatch Delay in a Multiple Instruction Dispatch Mechanism of a Data Processing System
5,479,629	H. F. Angjelo and J. R. Hatersley	Method and Apparatus for Translation Request Buffer and Requestor Table for Minimizing the Number of Accesses to the Same Address
5,479,631	D. C. Manners, E. S. Schulze, and D. R. Sutherland	System for Designating Real Main Storage Addresses in Instructions While Dynamic Address Translation Is On
5,479,640	F. P. Cartman, B. W. Curran, M. A. Krygowski, T.-C. Lo, S. N. Luu, S. B. Patel, and W. W. Shen	Memory Access System Including a Memory Controller with Memory Redrive Circuitry
5,479,703	K. V. Desai, H. Kohn, R. C. Senger, and D. P. Seraphim	Method of Making a Printed Circuit Board or Card
5,480,503	J. A. Casey, D. B. Goland, D. Gupta, L. W. Herron, J. N. Humenik, T. E. Lombardi, J. U. Knickerbocker, R. J. Sullivan, and J. R. Wylder	Process for Producing Circuitized Layers and Multilayer Ceramic Sub-Laminates and Composites Thereof
5,480,511	S. G. Barbee, T. F. Heinz, L. Li, and E. H. Ratzlaff	Method for Contactless Real-Time In-Situ Monitoring of a Chemical Etching Process
5,480,748	P. E. Bakeman, Jr., H. K. Lee, and S. E. Luce	Protection of Aluminum Metallization Against Chemical Attack During Photoresist Development

5,480,841	H. R. Bickford, P. W. Coteus, and L. C. Matthew	Process of Multilayer Conductor Chip Packaging
5,481,296	B. J. Cragun and P. R. Day	Apparatus and Method for Selectively Viewing Video Information
5,481,357	A. M. Ahsan, K. Beyzavi, and N. P. Rao	Apparatus and Method for High-Efficiency, In-Situ Particle Detection
5,481,472	V. M. Chung and J. E. Stuart	Method and Apparatus for Automatically Recognizing Repeated Shapes for Data Compaction
5,481,473	Y. O. Kim, P. J. Russell, and G. S. Weinert	System and Method for Building Interconnections in a Hierarchical Circuit Design
5,481,475	R. W. Young	Method of Semiconductor Device Representation for Fast and Inexpensive Simulations of Semiconductor Device Manufacturing Processes
5,481,495	W. H. Henkels, W. Hwang, and T. I. Chappell	Cells and Read-Circuits for High-Performance Register Files
5,481,500	W. R. Reohr, Y. H. Chan, and P.-F. Lu	Precharged Bit Decoder and Sense Amplifier with Integrated Latch Usable in Pipelined Memories
5,481,552	A. Aldereguia, D. C. Cromer, and K. K. Sendlein	Method and Structure for Providing Error Correction Code for 8-Byte Data Words on SIMM Cards
5,481,573	L. Jacobowitz and D. Stigliani, Jr.	Synchronous Clock Distribution System
5,481,658	N. Megiddo	Method and Apparatus for Displaying a Line Passing Through a Plurality of Boxes
5,481,675	C. T. Kapogiannis and J. F. Harmuth	Asynchronous Serial Communication System for Delaying with Software Dwell Time a Receiving Computer's Acknowledgement in Order for the Transmitting Computer to See the Acknowledgement
5,481,683	F. Karim	Super Scalar Computer Architecture Using Remand and Recycled General Purpose Register to Manage Out-of-Order Execution of Instructions
5,481,691	K. F. Day III and D. W. Dewey	Cache Page Replacement Using Sequential LIFO and Non-Sequential LRU Cast Out
5,481,706	J. S. Peek	System and Method for Creating Thread-Safe Shared Libraries
5,481,709	R. Bealkowski, D. E. Blaschke, M. M. Bolt, D. R. Geisler, R. G. Hillis, and F. J. Schroeder	Method and Apparatus for Providing a Modular BIOS Device Support Layer in a Computer System
5,481,710	P. J. Keane and K. P. Mitchell	Method of and System for Providing Application Programs with an Undo/Redo Function
5,481,714	D. Pipkin, E. Rogers, L. Jones-Scott, and K. T. Scott	Method and System for Installing an Operating System on a Data Processing System with Abort Capability and Voice Input Feature
5,481,719	D. F. Ackerman, H. H. Desai, R. K. Gupta, and R. R. Srinivasan	Exception Handling Method and Apparatus for a Microkernel Data Processing System
5,481,720	L. K. Loucks and T. A. Smith	Flexible Interface to Authentication Services in a Distributed Data Processing Environment
5,481,724	A. J. Heimsoth, E. N. Mandese, J. P. McGovern, and R. N. Mendelson	Peer to Peer Computer-Interrupt Handling
5,481,748	W. B. Cunto and J. H. Goncalves	Method and Apparatus for Reducing the Processing Time Required to Solve Numerical Problems
5,481,754	W. J. Piazza	Apparatus and Method for BIOS Interface to Features in Multiple Adapter Cards in One Operation Using Registers with Bits Distributed Across the Adapter Cards
5,481,755	J. D. Harper, P. W. Kalendra, W. J. Piazza, H. C. Tanner, and A. Vinh	Apparatus and Method for Addressing Multiple Adapter Cards in One Operation by Distributing Bits of Registers Across the Adapter Cards
5,482,113	D. Agonafer, T. M. Anderson, G. M. Chrysler, R. C. Chu, R. E. Simons, and D. T. Vader	Convertible Heat Exchanger for Air or Water Cooling of Electronic Circuit Components and the Like
5,482,198	H. Kohn	Solder Preform Pick-and-Place Machine and Operation
5,482,497	N. T. Gonnella and S. F. Starcke	Method and Apparatus for Texturing Zones of a Magnetic Disk
5,482,817	R. R. Dichiaro, C. F. Lyons, F. Sooriyakumaran, G. T. Spinillo, K. M. Welsh, and R. L. Wood	Mid and Deep-UV Antireflection Coatings and Methods for Use Thereof
5,482,883	T. V. Rajeevakumar	Method for Fabricating Low Leakage Substrate Plate Trench DRAM Cells and Devices Formed Thereby
5,482,903	P. R. Duncombe, S. L. Shinde, and T. Takamori	Aluminum Nitride Body Utilizing a Vitreous Sintering Additive
5,483,105	S. Kaja, E. D. Perfecto, W. H. Price, S. Purushothaman, S. N. Reddy, V. M. Sura, and G. E. White	Module Input-Output Pad Having Stepped Set-Back
5,483,179	S. H. Dhong, T. Kirihata, and R. Wordeman	Data Output Drivers with Pull-Up Devices
5,483,258	K. B. Cornett and E. F. Mark	Pick Correlation
5,483,398	Z.-E. Boutaghou	Compliant Vibration Isolation Housing Assembly for a Data Storage System
5,483,400	H. Tsujino	Disk Drive Apparatus

5,483,403	O. Voegeli	Thin Film Magnetic Head Structure Having Tapered Side Edges
5,483,421	R. W. Gedney and T. A. Sholtes	IC Chip Attachment
5,483,439	H. Ono and K. Ikarugi	Digital Servo Control System
5,483,468	J. N. Chen, N. Christiansen, J. C. Ross, and A. T. Rowan	System and Method for Concurrent Recording and Displaying of System Performance Data
5,483,515	D. C. Cheng, J. E. Hurst, Jr., R. W. Olson, and D. A. Thompson	Apparatus and Method for Cancelling Cross-Talk in Signals from Optical Data Storage Systems
5,483,522	J. H. Derby, J. E. Drake, Jr., J. G. Dudley, R. Guerin, M. A. Kaplan, G. A. Marin, M. L. Peters, and K. H. Potter, Jr.	Packet Switching Resource Management Within Nodes
5,483,618	W. J. Johnson and M. L. Williams	Method and System for Distinguishing Between Plural Audio Responses in a Multimedia Multitasking Environment
5,483,626	Y. Nakayama	Method and Apparatus for Transforming Graphics
5,483,633	W. J. Johnson	Method and Apparatus for Surfacing an Object Based Upon Forthcoming Criteria
5,483,637	A. Winokur, J. Shiloach, A. Ribak, and Y. Huang	Expert Based System and Method for Managing Error Events in a Local Area Network
5,484,055	S. L. Felde and K. R. Shelley	Machine and Human Readable Label for Data Cartridge
5,484,672	S. P. Bajuk, D. S. O'Grady, and E. T. Smith	Method of Making a Rim-Type Phase-Shift Mask
5,485,029	E. F. Crabbe, K. A. Jenkins, and J. L. Snare	On-Chip Ground Plane for Semiconductor Devices to Reduce Parasitic Signal Propagation
5,485,032	D. J. Schepis, K. V. Srikrishnan, S. Subbanna, and M. J. Tejwani	Antifuse Element with Electrical or Optical Programming
5,485,091	R. L. Verkuil	Contactless Electrical Thin Oxide Measurements
5,485,095	J. E. Bertsch, R. W. Mann, E. J. Nowak, and M. H. Tong	Fabrication Test Circuit and Method for Signalling Out-of-Spec Resistance in Integrated Circuit Structure
5,485,126	J. E. Gersbach and M. Hayashi	Ring Oscillator Circuit Having Output with Fifty Percent Duty Cycle
5,485,174	S. F. Henshaw and S. D. Redpath	Display Image Scroll Control and Method
5,485,322	T. J. Chainer, W. J. Sohn, and E. J. Yarmchuk	Method and System for Writing a Clock Track on a Storage Medium
5,485,323	T. B. Anderson, J. F. Boigenzahn, J. J. Stephenson, and A. L. Warner	Method and Apparatus for Moving a Disk Drive Actuator Away from a Magnetic Latch
5,485,409	S. Gupta and V. D. Gligor	Automated Penetration Analysis System and Method
5,485,472	L. Fredrickson	Trellis Codes with Algebraic Constraints for Input Restricted Partial Response Channels
5,485,476	S. M. Paranjape and F. Quintana	Method and System for Error Tolerant Synchronization Character Detection in a Data Storage System
5,485,562	C. Narayanaswami	System and Method for Clipping Pixels Drawn in One of Plurality of Windows in a Computer Graphics System
5,485,567	K. R. Banning, G. P. Erwin-Grotsky, S.-G. Li, A. Sanchez-Frank, and T. Shrader	Iconic Table of Database Information
5,485,570	L. J. H. Busboom, T. L. Clausen, S. T. Eagen, H. G. Kiel, R. J. Manulik, M. E. Plute, J. E. Remfert, and R. F. Romon	Display Station Controller
5,485,571	J. M. Menon	Method and Apparatus for Providing Distributed Sparing with Uniform Workload Distribution in Failures
5,485,575	D. M. Chess, J. O. Kephart, and G. B. Sorkin	Automatic Analysis of a Computer Virus Structure and Means of Attachment to Its Hosts
5,485,585	D. Q. Huynh, K. V. Lam, and L. T. Tran	Personal Computer with Alternative System Controller and Register for Identifying Active System Controller
5,485,588	S. Singh	Memory Array Based Data Reorganizer
5,485,594	D. J. Foster	Apparatus and Method Using an Atomic Fetch and Add for Establishing Temporary Ownership of a Common System Resource in a Multiprocessor Data Processing System
5,485,605	W. J. Johnson, R. S. Keller, G. C. Manthuruthil, and M. L. Williams	Method of and Apparatus for Providing a Group Query
5,485,616	M. G. Burke, P. R. Carini, and J.-D. Choi	Using Program Call Graphs to Determine the Maximum Fixed Point Solution of Interprocedural Bidirectional Data Flow Problems in a Compiler
5,485,619	M. Lai, J. L. Ng, and J.-F. Shaw	Array Variable Transformation System Employing Subscript Table Mapping to Scalar Loop Indices