

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

74,952	E. Yeomans	I/O Card and Memory Card Packaging Using Extruded Material For Covers
75,111	A. J. Kerklaan	PCMCIA III Package with Integral One Piece Top Cover

France

131,652	J. Boixadera Ferrera and J. E. Alcaniz Fornes	Integrated Multifunction Module
227,871	C. Jacquart, J. C. Czarniak, and M. Ferry	Parallel Algorithmic Digital to Analog Converter
351,479	C. Galand and M. Rosso	LPF/HPF Generalized Bandwidth Selection in RPE/LPT Speech
365,730	D. Omet and S. LeForestier	High-Gain, High-Speed BiCMOS Sensing for SRAM
365,731	B. Naudin, J. Calvignac, J. M. Munier, and J. P. Lips	Mechanism for Transferring Messages Between Source and Destination Users Through a Shared Memory
392,126	C. Galand and M. Rosso	Fast Pitch Tracking Process for LTP-based Speech Coders
394,599	P. Huon and R. Glaise	Circuit for Synchronizing Data Transfers Between Two Devices Operating at Different Speeds
405,042	A. Milewski, A. Vedrenne, G. Lebizay, and M. Demange	High-Speed Digital Packet Switching System
419,750	B. Naudin, E. Saint-Georges, J. Feraud, and J. P. Lips	Distribution Mechanism for Establishing Communications Between User Interfaces of a Communication System
469,212	G. Robert and H. Lochon	Method of Forming Metal Contact Pads and Terminals on Semiconductor Chips

Germany

4,024,973.5	D. Modjesch, G. Boehmer, J. Gentischer, R. Lehner, and W. Schmutz	Arrangement for Storing, Transporting, and Loading Substrates
4,126,909.8	J. Schnee, K. Staudenmaier, M. Schmidt, V. Weber, and W. Mannsdoerfer	Plate-Shaped Component Surrounded by a Sealing on its Lateral Faces
4,223,600.2	A. Blum, G. Goldrian, and W. Kumpf	Interprocessor Communication Coupler within Multiprocessor Computer System
4,244,275.3	G. Gervais and G. Zilles, H. Kohler, I. Holm, N. Schumacher, and T. G. Koehler	Checking Data Integrity in Buffered Data Transmission
68,916,784.9	E. Klink, H. Schettler, K. Kroell, O. Wagner, R. Stahl, T. Ludwig, and W. Haug	Integrated Circuit Package

68,919,683.0	H. Astheimer, H. Brumme, H. Schuy, K. Kissel, and W. Heck	Low Profile Suspension (LPS)
69,012,555.0	J. Greschner and T. Bayer	Method of Producing Micromechanical Sensors for the AFM/STM/MFM Profilometry and Micromechanical AFM/STM/MFM Sensor Head
3,852,543,708	D. Hagmann, H. J. Wildau, and W. Euen	Thin Dielectric Layer on a Substrate and Method for Forming such a Layer
Japan		
909,158	T. Takahashi	Industrial Design of Venice System Unit
909,163	H. Shima, R. Sapper, and Y. Manabe	Industrial Design A4 Tablet
911,609	K. Yamazaki	Ornamental Appearance of a Table Type Personal Computer
913,383	T. Takahashi	Industrial Design of Venice Expansion Box
914,877	T. Imai, T. Takahashi, and Y. Manabe	14" Mono Monitor
916,307	T. Imai	Color LDC Display
1,879,343	H. Fujikura and T. Watanabe	Bill Pick Mechanism Using Diamond Roll
1,884,781	T. Ueki	Color LCD
Switzerland		
344,352	C. A. Mueller and V. Graf	Method for Making Artificial Layered High-T _c Superconductors
363,550	C. W. Schneiker and D. W. Pohl	Distance-controlled Tunneling Transducer and Direct Access Storage Unit Employing the Transducer
442,536	D. Pohl, G. K. Binnig, H. Rohrer, J. K. Gimzewski, and U. T. Duerig	Atomic Force Sensor Head for Measuring the Properties of a Data Store
United Kingdom		
329,892	R. T. Wood	Display System Comprising a Windowing Mechanism
380,491	J. J. Richards and K. Williams	Online Help Architecture for Computers
389,697	A. C. Lee, C. N. Wallis, M. J. Palmer, P. J. Murfet, S. Furniss, and T. Winlow	Serial Data Receiver
422,295	A. C. Gay and N. D. Butler	Display System
422,297	R. B. Harrison and R. T. Wood	Display System
422,298	D. C. Haigh, R. B. Harrison, and R. T. Wood	Display System
422,299	A. C. Gay, M. D. Bates, R. M. West, and T. Williams	Memory with Page Mode
422,300	D. C. Haigh, H. R. Bonnor, and R. B. Harrison	Display System with Graphics Cursor
456,923	D. Sawdon	Display System
460,314	J. R. Woodward	Display Medium
462,333	C. C. Pietrzak and J. S. Beeteson	Display System
2,037,957	M. D. Oates	Workstation
2,040,320	D. J. Gray, J. Farran, and P. Hulme	Data Storage Device
2,248,145	A. N. Other, D. Sawdon, and D. J. Whittle	A Contact Device for Connecting Coax Cable to a Non-coaxial Connector
United States		
5,229,039	A. Ikeda, T. Koseki, T. Ueki	Color Liquid Crystal Display and Method of Manufacture
5,255,386	J. M. Prager	Method and Apparatus for Intelligent Help that Matches the Semantic Similarity of the Inferred Intent of Query or Command to a Best-Fit Predefined Command Intent
5,276,541	K. Terada and M. Shoji	Method for Forming a Liquid Crystal Display with a Sealing Member
5,278,983	S. Kawabe, H. Masuda, and K. Shimada	Boundary Representation Solid Modeling System
5,293,385	J. M. Hary	Method and Means for Using Sound to Indicate Flow of Control During Computer Program Execution
5,296,408	R. R. Wilbarg and C. Johnson, Jr.	Fabrication Method for Vacuum Microelectronic Devices
5,297,150	A. L. Clark	Rule-Based Method for Testing of Programming Segments
5,298,351	N. Bobroff and A. E. Rosenbluth	Ablation Mask and Use Thereof
5,303,145	S. F. Griffin, D. L. Griffin, and P. J. Keane	Method and Apparatus for Meeting Confirmation in a Data Processing System
5,305,340	A. Antreasyan, M. N. Boenke, G. Costrini, K. R. Grebe, C. Harder, and P. D. Hoh	Waveguide Ridge Laser Device with Improved Mounting and Ridge Protection
5,307,216	W. A. Cook and D. D. Reno	Sector Identification Method and Apparatus for a Direct Access Storage Device

5,307,357	M. E. Jost, R. W. Kwong, A. Behfar-Rad, and P. P. Kwan	Protection Means for Ridge Waveguide Laser Structures Using Thick Organic Films
5,307,457	B. J. Beitel and P. C. Yanker	Trigger Field Display Selection
5,313,609	S. J. Baylor, K. P. McAuliffe, and B. D. Rath	Optimum Write-Back Strategy for Directory-Based Cache Coherence Protocols
5,314,840	D. J. Schepis, K. V. Srikrishnan, S. Subbanna, and M. J. Tijwani	Method for Forming an Antifuse Element with Electrical or Optical Programming
5,316,970	J. S. Batchelder, V. P. Gross, R. A. Gruver, P. C. D. Hobbs, and K. D. Murray	Generation of Ionized Air for Semiconductor Chips
5,323,057	R. K. Cook and M. M. A. Pelella	Lateral Bipolar Transistor with Insulating Trenches
5,326,643	E. Adamopoulos, J. Kim, K.-W. Lee, T. S. Oh, T. R. O'Toole, S. Purushothaman, J. J. Ritsko, J. M. Shaw, A. Viehbeck, and G. F. Walker	Adhesive Layer in Multi-Level Packaging and Organic Material as a Metal Diffusion Barrier
5,327,570	D. J. Foster, A. Garcia, and R. B. Pearson	Multiprocessor System Having Local Write Cache Within Each Data Processor Node
5,333,248	A. H. J. Christensen	Method and System for the Smooth Contouring of Triangulated Surfaces
5,338,698	S. Subbanna	Method of Fabricating an Ultra-Short Channel Field Effect Transistor
5,338,702	E. Kobeda, J. P. Gambino, G. G. Gifford, and N. J. Mazzeo	Method for Fabricating Tungsten Local Interconnections in High Density CMOS
5,338,818	W. R. Brunsvold, P. Jagannathan, S. S. Miura, M. W. Montgomery, H. S. Sachdev, and R. Sooriyakumaran	Silicon Containing Positive Resist for DUV Lithography
5,338,900	M. S. Schneider, J. Acocella, L. W. Herron, M. R. Kordus, and L. H. Wirtz	Structures for Electrically Conductive Decals Filled with Inorganic Insulator Material
5,338,963	W. A. Klaasen and W.-Y. Wang	Soft Error Immune CMOS Static RAM Cell
5,339,204	D. T. James, A. W. Leonard, and P. J. Elliott	System and Method for Servowriting a Magnetic Disk Drive
5,339,212	R. M. Geffken and L. J. Dunlop	Sidewall Decoupling Capacitor
5,339,274	S. H. Dhong, T. Kiriata, H. J. Shin, T. Sunaga, Y. Taira, and L. M. Terman	Variable Bitline Precharge Voltage Sensing Technique for DRAM Structures
5,339,299	N. Kagami and H. Kubo	Optical Disk Drive Apparatus for Searching a Target Track
5,339,388	C. L. Bates and J. M. Ryan	Cursor Lock Region
5,339,389	C. L. Bates and J. M. Ryan	User Selectable Lock Regions
5,339,394	L. E. Johnson, Jr., D. J. Kokoszka, S. P. Larky, and P. Sidoli	I/O Register Protection Circuit
5,339,397	R. G. Eikill, S. B. Levenstein, L. A. McMahon, and J. P. Weigel	Hardware Primary Directory Lock
5,339,404	G. C. Vandling III	Asynchronous TMR Processing System
5,339,405	D. A. Elko, J. A. Frey, A. A. Helffrich, J. M. Nick, and M. D. Swanson	Command Quiesce Function
5,339,410	N. Kanai	Method of Bidirectional Recalculation
5,339,413	M. J. Koval, W. W. Lawton, J. G. Tyler, and S. L. Winters	Data Stream Protocol for Multimedia Data Streaming Data Processing System
5,339,414	J. S. Chana, P. J. Lupton, and D. L. Plum	Data Processing System Including Linked Functional Units
5,339,420	S. M. Hoxey	Partitioning Case Statements for Optimal Execution Performance
5,339,421	B. C. Housel III	General Data Stream Parser for Encoding and Decoding Data and Program Interface for Same
5,339,423	B. J. Beitel, C. L. Haug, and E. M. Sobrino	System for Accessing Objects External to an Application Using Tables Containing Path Definitions
5,339,427	D. A. Elko, J. F. Isenberg, Jr., B. B. Moore, J. P. Strickland, M. D. Swanson, and G. W. Wang	Method and Apparatus for Distributed Locking of Shared Data, Employing a Central Coupling Facility
5,339,438	M. H. Conner, A. R. Martin, and L. K. Raper	Version Independence for Object Oriented Programs
5,339,952	L. C. Hecht, M. P. Sulger, E. E. Thiele, M. V. Pierson, and L. E. Williams	Transfer Container for Transferring Flimsy Circuit Panels Under Clean Room Conditions
5,340,013	J. D. Poole and F. P. Zickefoose	Rework Process for Printed Circuit Cards and Solder Fountain Having Gas Blanket for Carrying Out the Process
5,340,451	L. P. Buchwalter, S. L. Buchwalter, C. R. Davis, R. D. Goldblatt, J. E. Heidenreich III, S. L. Nunes, J. M. Park, R. R. Thomas, D. Tortorella, and L. M. Ferreiro	Process for Producing a Metal Organic Polymer Combination

5,340,617	B. V. Fasano, V. J. Novotny, and L. B. Schein	Electrostatic Patterning of Multi-Layer Module Lamina
5,340,753	E. Bassous, G. L. Patton, and J. M. C. Stork	Method for Fabricating Self-Aligned Epitaxial Base Transistor
5,340,759	C.-M. Hsieh, L. L. C. Hsu, and S. Ogura	Method of Making a Vertical Gate Transistor with Low Temperature Epitaxial Channel
5,340,775	R. A. Carruthers, F. J. Dorleans, J. A. Fitzsimmons, R. Flitsch, J. A. Jubinsky, G. R. Larsen, G. C. Schwartz, P. J. Tsang, and R. W. Zielinski	Structure and Fabrication of SiCr Microfuses
5,340,914	J. J. Cuomo, J. D. Gelorme, M. Hatzakis, Jr., D. A. Lewis, J. M. Shaw, and S. J. Whitehair	Microwave Processing
5,341,023	C.-M. Hsieh, L. L. C. Hsu, S.-N. Mei, R. W. Knepper, and L. F. Wagner, Jr.	Novel Vertical-Gate CMOS Compatible Lateral Bipolar Transistor
5,341,042	C.-L. Chen	Low Voltage, Cascoded NTL Based BiCMOS Circuit
5,341,118	S. S. P. Parkin and K. P. Roche	Multilayer Magnetic Structure Wherein the Magnitude of the Structure Magnetoresistance is a Function of Nonmagnetic Layer Thickness
5,341,153	T. L. Benzschawel and W. E. Howard	Method of and Apparatus for Displaying a Multicolor Image
5,341,261	B. Dieny, B. A. Gurney, S. S. P. Parkin, I. L. Sanders, V. S. Speriosu, and D. R. Wilhoit	Magnetoresistive Sensor Having Multilayer Thin Film Structure
5,341,302	D. P. Connors and D. D. Yao	Job Configuration for Semiconductor Manufacturing
5,341,310	S. W. Gould, M. G. Marshall, and P. E. Perry	Wiring Layout Design Method and System for Integrated Circuits
5,341,351	S. W. Ng	Method and Means for Optimally Accessing Data Residing on Dual Actuator DASDs
5,341,475	P. Austruy, B. Brezzo, J.-P. Lips, B. Naudin, J. Calvignac, and R. H. Waller	Method for Exchanging Messages Between a Shared Memory and Communication Adapters Using an Efficient Logical Protocol
5,341,485	J. R. Hattersley, T. D. Kim, J. Y. Lee, and F. A. Reiley	Multiple Virtual Address Translation Per Computer Cycle
5,341,487	M. T. Derwin and W. A. Wall	Personal Computer Having Memory System with Write-Through Cache and Pipelined Snoop Cycles
5,341,503	L. A. Gladstein, C. D. Jones, K. Leontiades, P. D. Patel, and P. W. Petroskey	Battery Operated Computer Having Improved Battery Gauge and System for Measuring Battery Charge
5,342,123	P. D. Cole and D. E. Southworth	Detented Frame Door Assembly
5,343,092	S. H. Dhong and H. J. Shin	Self-Biased Feedback-Controlled Active Pull-Down Signal Switching
5,343,290	J. S. Batchelder, D. M. DeCain, P. C. D. Hobbs, and M. A. Taubenblatt	Surface Particle Detection Using Heterodyne Interferometer
5,343,340	Z.-E. Boutaghou, D. H. Brown, K. J. Erickson, and R. Greenberg	Digital Servo Signal Demodulation Method and Apparatus Utilizing a Partial-Response Maximum-Likelihood (PRML) Channel in a Disk File
5,343,366	T. M. Cipolla, P. W. Coteus, I. Damianakis, G. W. Johnson, P. G. Ledermann, L. C. Matthew, and L. S. Mok	Packages for Stacked Integrated Circuit Chip Cubes
5,343,385	L. Joskowicz and R. H. Taylor	Interference-Free Insertion of a Solid Body into a Cavity
5,343,422	K. T. Kung, D. D. Tang, and P.-K. Wang	Nonvolatile Magnetoresistive Storage Device Using Spin Valve Effect
5,343,473	I. Cidon, R. M. Doney, J. E. Drake, Jr., E. A. Hervatic, K. H. Potter, Jr., and T. E. Tedijanto	Method of Determining Whether to Use Preempt/Resume or Alternate Protocol for Data Transmission
5,343,527	J. W. Moore	Hybrid Encryption Method and System for Protecting Reusable Software Components
5,343,537	E. J. Bellegarda, J. R. Bellegarda, D. Nahamoo, and K. S. Nathan	Statistical Mixture Approach to Automatic Handwriting Recognition
5,343,542	J. A. Kash, B. Pezeshki, and F. F. Tong	Tapered Fabry-Perot Waveguide Optical Demultiplexer
5,343,548	S. A. Hall, R. Lane, and H. Wang	Method and Apparatus for Batch, Active Alignment of Laser Arrays to Fiber Arrays
5,343,557	H. G. Kiel, R. M. Paterson, and L. D. Whitley	Workstation Controller with Full Screen Write Mode and Partial Screen Write Mode
5,343,989	P. Y. Hu, D. R. Lyman, and D. K. Walston	Reduced Wear Cable Carrier and Pads
5,344,607	S. G. Gonya, J. K. Lake, R. C. Long, and R. N. Wild	Lead-Free, High Tin, Ternary Solder Alloy of Tin, Bismuth, and Indium

5,344,669	M.-M. Chen and C. H. Tsang	Method of Manufacture for an Improved Magnetoresistive Read Transducer
5,345,197	S. R. Riggio, Jr.	Battery Powered Magnetic Pen Having Oscillator with Improved Efficiency and Low Power Consumption
5,345,228	P. A. Franaszek and C. J. Georgiou	Very Large Scale Modular Switch
5,345,229	H. T. Olnowich, J. Bruck, M. Snir, and E. Upfal	Adaptive Switching Apparatus for Multi-Stage Networks
5,345,324	T. Koseki, T. Fukunaga, H. Takano, and H. Yamanaka	Active Matrix Liquid Crystal Display Device Having Two Light Shielding Layers
5,345,350	J. F. Ellis, M. D. Osborn, and D. J. Winarski	Dual-Purpose Picker for Automated Multimedia Library
5,345,353	M. C. Krantz, V. J. Novotny, T. C. O'Sullivan, and A. C. Tam	Step Projection Air Bearing Slider with Improved Stiction Performance and Wear Resistance
5,345,392	T. Mito, L. L. Suarez, S. Astarabadi, and M. G. Calescibetta	Battery Charge Monitor for a Personal Computer
5,345,507	A. Herzberg, H. M. Krawczyk, S. Kутten, and Y. Mansour	Secure Message Authentication for Binary Additive Stream Cipher Systems
5,345,516	S. K. Boyer, R. G. Casey, A. M. Miller, B. Oudot, and K. S. Zilles	Apparatus and Method for Parsing a Chemical String
5,345,549	A. Appel and M. A. O'Connor	Multimedia Based Security Systems
5,345,550	M. A. Bloomfield	User-Modifiable Popup Menus for Object Oriented Behavior
5,345,553	L. J. H. Busboom, S. T. Eagen, and H. G. Kiel	Method and Apparatus for Constructing Edit Mask Entry Fields with Multiple Nonprogrammable Terminals Attached to a Workstation Controller
5,345,567	C. O. Hayden, D. R. Snyder, S. B. Stillman, and C. F. Webb	System and Method for Modifying Program Status Word System Mask, System Access Key, and Address Space Code with Overlap Enabled
5,345,586	A. M. Hamala, W. A. Maron, and R. A. Reyburn	Method and System for Manipulation of Distributed Heterogeneous Data in a Data Processing System
5,345,590	D. F. Ault, D. B. Petersen, I. G. Redding, and S. J. Schmandt	Method and Apparatus for Cross-Partition Control in a Partitioned Process Environment
5,346,117	H. Kohn and D. J. Lazzarini	Method of Fabricating a Parallel Processor Package
5,346,518	R. J. Baseman, C. A. Brown, B. N. Eldridge, L. B. Rothman, H. R. Wendt, J. T. Yeh, and A. R. Zingher	Vapor Drain System
5,347,153	P. E. Bakeman, Jr.	Short Channel Transistors
5,347,234	J. E. Gersbach and I. I. Novof	Digital Voltage Controlled Oscillator
5,347,386	T. K. Gibbs, G. Luck, and D. Wares	Control Apparatus
5,347,460	G. G. Gifford and B. E. Osborn	Method and System Employing Optical Emission Spectroscopy for Monitoring and Controlling Semiconductor Fabrication
5,347,465	R. J. Ferreri, G. E. Holmes, and S. Magdo	Method of Integrated Circuit Chips Design
5,347,511	L. Gun	Traffic Management in Packet Communications Networks
5,347,514	G. T. Davis, D. J. Donaghy, L. V. Marks, and C. L. Purrington, Sr.	Processor-Based Smart Packet Memory Interface
5,347,518	A. Y. Lee	Method of Automating a Build Verification Process
5,347,580	R. Molva and G. Tsudik	Authentication Method and System with a Smartcard
5,347,619	D. J. Erb	Nonconvex Polygon Identifier
5,347,627	R. P. Hoffmann, J. W. Malcolm, J. D. Montgomery, and S. S. Stone	Graphical User Interface Including Dynamic Sizing and Spacing
5,347,628	S. C. Brewer, K. A. Brink, W. H. Krebs, Jr., and R. P. Welch	Method of Graphically Accessing Electronic Data
5,347,629	J. P. Barrett, R. P. Hoffmann, and J. D. Montgomery	Graphical User Interface Including Updating of Multiple Panels Using What You See Is What You Get (WYSIWYG) Editor
5,347,633	J. C. Ashfield, G. D. Baker, R. C. Nevins, J. A. Raynes, A. T. Sultan, J. K. Parks, M. D. Rhodin, and R. L. Sayre	System for Selectively Intercepting and Rerouting Data Network Traffic
5,347,639	R. N. Rechtschaffen and K. Ekanadham	Self-Parallelizing Computer System and Method
5,347,649	G. R. Alderson	System for Dynamically Generating, Correlating and Reading Multiprocessing Trace Data in a Shared Memory
5,347,651	W. T. Burke, L. W. Loen, and R. K. Rolfe	System for Allocating Worm Optical Medium File Storage in Groups of Fixed Size Addressable Areas While Tracking Unrecorded Areas and End of Volume
5,347,652	D. A. Epstein, G. G. Gilley, and K. P. McAuliffe	Method and Apparatus for Saving and Retrieving Functional Results
5,348,905	D. M. Kenney	Method of Making Diffused Buried Plate Trench DRAM Cell Array
5,349,139	G. F. Verrier, F. L. Stein, and R. L. Donaldson	Architecture for Communication of Remote Devices to a Digitizing Display
5,349,321	E. J. Selker	Powercord Transformer

5,349,348	K. L. Anderson, I. R. Finlay, J. L. Mitchell, and D. S. Thornton	Multi-Mode Data Stream Generator
5,349,543	S. Buliszyn, J. A. Landor, and R. Q. Zielinski	Constant Force Probe
5,349,582	C. Bisdikian and A. N. Tantawy	Scheme for Source Assisted Partial Destination Release of Slots in Slotted Networks
5,349,583	K. J. Christensen and F. E. Noel, Jr.	Multi-Channel Token Ring
5,349,643	J. O. Cox and J. M. Mott	System and Method for Secure Initial Program Load for Diskless Workstations
5,349,657	A. Y. Lee	Method of Automating Uploading of Help Panels
5,349,661	J. A. Brewer, P. R. Habermehl, J. M. Stafford, and E. R. Williams, Jr.	Installation of an Expanded Memory Manager
5,349,662	W. J. Johnson, R. S. Keller, G. C. Manthuruthil, and M. L. Williams	Method of and Apparatus for Providing Automatic Detection of User Activity
5,349,668	L. A. Gladstein, C. D. Jones, and J. C. Wulf	Battery Operated Computer Having Improved Battery Monitor and Cell Reversal Protection Circuit
5,349,674	N. Calvert, J. L. Koehler, E. D. Lindberg, M. A. McKelvey, S. P. Mervosh, J. A. Newton, G. B. Scarborough, R. A. Upchurch, and S. D. Westling	Automated Enrollment of a Computer System into a Service Network of Computer Systems
5,349,675	A. K. Fitzgerald, C. W. Gaaney, Jr., W. K. Kelley, and S. L. Wentz	System for Directly Displaying Remote Screen Information and Providing Simulated Keyboard Input by Exchanging High Level Commands
5,350,246	L. T. Sehringer	High Speed Continuous-Forms Printer
5,350,909	K. E. Powell and D. A. Mallory	Optical Scanner for Bar Code Scanning
5,350,997	M.-R. Ghotbi and T. R. Larson	Step-Up Voltage Converter with Overcurrent Protection
5,351,056	J. C. Sawyer	Target Tracking in Clutter Degraded Scenes Using Central Level Stereo Processing
5,351,067	L. Lumelsky, S. M. Choi, A. W. Peevers, and J. L. Pittas	Multi-Source Image Real Time Mixing and Anti-Aliasing
5,351,093	D. J. Brown, D. C. Conway-Jones, J.-H. Kim, and P. M. Smith	Phase Alternating Line Video Signal Processing Apparatus
5,351,156	T. A. Gregory, C. G. Keller, and J. M. Severson	Method and Apparatus for Cleaning Disks
5,351,202	J. M. Kurtzberg and M. Levanoni	Evaluation and Ranking of Manufacturing Line Non-Numeric Information
5,351,229	B. Brezoczky, G. M. McClelland, and H. Seki	Tribo-Attractive Contact Slider for an Optical Read/Write System
5,351,246	M. M. Blaum and R. M. Roth	Method and Means for Coding and Rebuilding That Data Contents of Unavailable DASDs or Rebuilding the Contents of DASDs in Error in the Presence of Reduced Number of Unavailable DASDs in a DASD Array
5,351,291	J. Menez, M. Rosso, and P. Scotton	Adaptive Echo Cancellation Method and Device for Implementing Said Method
5,351,310	A. Califano and R. Mohan	Generalized Shape Autocorrelation for Shape Acquisition and Recognition
5,351,412	T. Furuhashi and T. Hirano	Micro Positioning Device
5,352,326	D. Cywar, D. H. Hess, and C. Lalonde	Process for Manufacturing Metalized Ceramic Substrates
5,352,350	P. C. Andricacos, J. O. Dukovic, and L. T. Romankiw	Method for Controlling Chemical Species Concentration
5,352,927	D. R. Cote, D. Stanasolovich, and R. A. Warren	Self-Aligned Contact Studs for Semiconductor Structures
5,353,061	A. A. Rodriguez, M. A. Pietras, and A. J. Saenz	System and Method for Frame-Differencing Video Compression/Decompression Using Perceptually-Constant Information and Image Analysis
5,353,181	N. K. Frater and A. P. Giorgi	Etched Suspension System
5,353,202	D. F. Ansell, J. W. Benck, T. A. Bocchino, J. W. Deiso, J. E. Richards, M. L. Shipley, and R. D. Wysong	Personal Computer with Shielding of Input/Output Signals
5,353,273	J. M. Zavislan and B. N. Kurdi	Multi-Channel Optical Head and Data Storage System
5,353,280	G. Ungerböck	Full-Duplex Start-Up for Modems
5,353,345	C. Galand	Method and Apparatus for DTMF Detection
5,353,377	A. Kuroda, M. Nishimura, and K. Toshioka	Speech Recognition System Having an Interface to a Host Computer Bus for Direct Access to the Host Memory
5,353,410	J. F. Macon, Jr., S. Ong, and F.-H. Shih	Method and System for Deferred Read in Lazy-Write Disk Cache Systems
5,353,417	D. P. Fuoco, L. A. Hernandez, E. Mathisen, D. L. Moeller, J. H. Raymond, and E. Tashakori	Personal Computer with Bus Interface Controller Coupled Directly with Local Processor and Input/Output Data Buses and for Anticipating Memory Control Changes on Arbitration for Bus Access

5,353,421	P. G. Emma, J. W. Knight, J. H. Pomerene, T. R. Puzak, R. N. Rechtschaffen, and J. R. Robinson	Multi-Prediction Branch Prediction Mechanism
5,355,580	Y. Tsukada	Method for Replacing Semiconductor Chips
5,356,513	P. A. Burke and M. A. Leach	Polishstop Planarization Method and Structure
5,356,837	P. J. Geiss, T. J. Licata, H. L. Ho, and J. G. Ryan	Method of Making Epitaxial Cobalt Silicide Using a Thin Metal Underlayer
5,357,005	S. L. Buchwalter, C. R. Davis, R. D. Goldblatt, and R. R. Thomas	Reactive Surface Functionalization
5,357,150	G. A. Jaquette	Defect Tolerant Envelope Follower
5,357,250	P. D. Healey and J. J. Rissanen	Adaptive Computation of Symbol Probabilities in N-Ary Strings
5,357,363	C.-S. Li, K. Liu, H. S. Stone, and F. F. Tong	Interconnections Having Improved Signal-to-Noise Ratio
5,357,380	C. E. Bailey, S. R. Bentley, S. M. Paranjape, F. Quintana, and S. C. West	Method and Apparatus for Indicating When Each Track is in Burst in a Multitrack Tape Device
5,357,430	U. Rackow and U. Schwall	System for Text Translation
5,357,508	J.-Y. Le Boudec, H. L. Truong, and R. Oechsle	Connectionless ATM Network Support Using Partial Connections
5,357,520	P. C. Arnett, J. D. Coker, R. L. Galbraith, Y.-S. Tang, and R. W. Wood	Method and Apparatus for Precompensation Value Determination in a PRML Data Channel
5,357,521	C. Cheng and P. J. Roy	Address Sensitive Memory Testing
5,357,523	R. Bogholtz, Jr., L. J. Bosch, K. C. Gower, and T. Mitchell	Memory Testing System with Algorithmic Test Data Generation
5,357,528	N. Alon and J. Bruck	Depth-2 Threshold Logic Circuits for Logic and Arithmetic Functions
5,357,546	T. A. Meriwether and B. Sinha	Multimode and Multiple Character String Run Length Encoding Method and Apparatus
5,357,597	M. Aparicio IV and S. E. Otto	Convolutional Expert Neural System (CONEXNS)
5,357,599	W. L. Luken	Method and Apparatus for Rendering Polygons
5,357,608	N. G. Bartow, R. S. Capowski, L. T. Fasano, T. A. Gregg, G. Salyer, and D. W. Westcott	Configurable, Recoverable Parallel Bus
5,357,612	H. Alaiwan	Mechanism for Passing Messages Between Several Processors Coupled Through a Shared Intelligent Memory
5,357,617	G. T. Davis and S. Ventrone	Method and Apparatus for Substantially Concurrent Multiple Instruction Thread Processing by a Single Pipeline Processor
5,357,618	J. H. Mirza and S. W. White	Cache Prefetch and Bypass Using Stride Registers
5,357,623	I. Megory-Cohen	Dynamic Cache Partitioning by Modified Steepest Descent
5,357,625	G. J. Arends	System for Identifying Common Formats and Protocols and Allocating Respective Buffers Spaces for Simultaneous Communication
5,357,631	W. E. Howell, H. N. Reddy, and D. S. Wang	Method and System for Creating and Maintaining Multiple Document Versions in a Data Processing System Library
5,357,899	E. Bassous, B. S. Meyerson, and K. J. Uram	Epitaxial Silicon Membranes
5,358,325	D. C. Teale	Robotic System
5,358,899	A. J. Fleischman, N. E. Lustig, and R. G. Schad	Oxygen Assisted Ohmic Contact Formation to N-Type Gallium Arsenide
5,359,514	G. C. Manthuruthil, S. N. Parikh, and H. N. Reddy	Method and Apparatus for Facilitating Comprehension of On-Line Documents
5,359,552	S. H. Dhong, H. J. Shin, and W. Hwang	Power Supply Tracking Regulator for a Memory Array
5,359,557	A. G. Aipperspach and D. T. Cox	Dual-Port Array with Storage Redundancy Having a Cross-Write Operation
5,359,563	F. Bredin and T. Cantiant	Memory System with Adaptable Redundancy
5,359,593	J. H. Derby, J. E. Drake, Jr., C. Galand, L. Gun, G. A. Marin, A. L. Roginsky, and T. E. Tedijanto	Dynamic Bandwidth Estimation and Adaptation for Packet Communications Networks
5,359,617	S. Kano, K. Kumata, and S. Owa	Nonlinear Optical Device
5,359,660	D. K. Clark, W. J. Johnson, L. M. Lachman, and D. Flores	Local Area Network Peripheral Lock Method and System
5,359,704	J. R. Rossignac and M. J. van Emmerick	Method for Selecting Silhouette and Visible Edges in Wire Frame Images in a Computer Graphics Display System
5,359,708	H. A. Bloomer and K. A. Zaiken	Dynamic in a Document Processing System for Dynamically Locating Format Controls and Determining Formatting Information in Effect Before and After Each Format Control
5,359,709	A. Blanc, S. Gohl-Roux, and G. Ungerboeck	Apparatus and Method for Providing Multiple Operating Configurations in Data Circuit Terminating Equipment
5,359,718	J. E. Phillips and S. Vassiliadis	Early Scalable Instruction Set Machine ALU Status Prediction Apparatus

5,359,722	S. K. Chan, J. H. Datres, Jr., and T.-C. Lo	Method for Shortening Memory Fetch Time Relative to Memory Store Time and Controlling Recovery in a DRAM
5,359,730	A. Marron	Method of Operating a Data Processing System Having a Dynamic Software Update Facility
5,359,767	W. T. Chen, T. P. Gall, J. R. Wilcox, and T. Y. Wu	Method of Making Multilayered Circuit Board
5,360,445	M. Goldowsky	Blood Pump Actuator
5,360,651	D. W. Baxter, L. E. Gregg, and W. Jaaskelainen	Optical Disk Having R/W and WORM Area
5,360,758	G. B. Bronner, J. K. DeBrosse, and D. M. Kenney	Self-Aligned Buried Strap for Trench Type DRAM Cells
5,360,946	C. Feger, T. O. Graham, K. R. Grebe, A. P. Lanzetta, J. J. Liutkus, L. C. Matthew, M. J. Palmer, N. R. Tanner, H.-M. Tong, C. H. Wilson, and H. L. Yeh	Flex Tape Protective Coating
5,360,974	J. M. Hammond, M. A. Klos, Y. Martin, K. G. Roessler, and R. M. Stowell	Dual Quad Flexure Scanner
5,360,978	P. L. Gueret	Multiple-Tip Scanning Tunneling Microscopy
5,361,148	G. C. Bjorklund, D. M. Burland, M. C. J. M. Donckers, R. D. Miller, W. E. Moerner, S. M. Silence, R. J. Twieg, and C. A. Walsh	Apparatus for Photorefractive Two Beam Coupling
5,361,256	W. Doeringer, D. Dykeman, A. K. Edwards, D. P. Pozefsky, S. Sarkar, and R. D. Turner	Inter-Domain Multicast Routing
5,361,326	M. Aparicio IV, P. C. Miller, and W. A. Miller	Enhanced Interface for a Neural Network Engine
5,361,350	M. H. Conner, A. R. Martin, and L. K. Raper	Object Oriented Method Management System and Software for Managing Class Method Names in a Computer System
5,361,353	B. P. Carr, D. A. Glowny, C. A. Mastrangelo, P. M. Mayer, A. Seltzer, and P. D. Shier	System for Parsing Message Units From an Unstructured Message Stream of Interleaved Message Units to Form Structured Messages
5,361,356	C. E. Clark, J. A. Frey, K. E. Plambeck, C. A. Scalzi, and B. Sinha	Storage Isolation with Subspace-Group Facility
5,361,358	C. A. Cox, S. G. Nacamuli, L. B. Turner, and A. L. Wilson	System and Method for Installing Program Code for Operation from Multiple Bootable Operating Systems
5,361,368	R. D. Herzl, L. L. Quinn, and R. E. Schroter	Cross Interrogate Synchronization Mechanism Including Logic Means and Delay Register
5,361,376	D. A. Cummins and W. R. Hucaby	Keyboard and Controller with a Three Wire Half Duplex Asynchronous and Bidirectional Communications Architecture
5,361,390	J. G. Heyen, C. Kasiraj, and T. J. Wolf	Method for Displaying and Confirming the Status of Deferred System Requests
5,361,547	M. A. Church, A. M. Desouches, and R. E. Krebs	Ultimate Inductive Head Integrated Lapping System
5,362,192	C. H. Dang	Automated Data Storage Library with Single Input Double Gripper Mechanism
5,362,360	A. Bross and T. J. Walsh	Method and Product for Extruding Materials that Exhibit Anisotropic Properties by Means of Reciprocating Die Surfaces
5,362,584	P. J. Brock, J. A. Franklin, F. M. Schellenberg, and J. Tsay	Phase-Shifting Transparent Lithographic Mask for Writing Contiguous Structures from Noncontiguous Mask Areas
5,362,599	C. J. Knors, S. S. Miura, M. W. Montgomery, W. M. Moreau, and R. J. Smith	Fast Diazoquinone Positive Resists Comprising Mixed Esters of 4-Sulfonate and 5-Sulfonate Compounds
5,362,663	G. B. Bronner, S. H. Dhong, and W. Hwang	Method of Forming Double Well Substrate Plate Trench DRAM Cell Array
5,362,986	J. M. Angiulli, E. S. Kolankowsky, R. R. Konian, and L. L. Wu	Vertical Chip Mount Memory Package with Packaging Substrate and Memory Chip Pairs
5,363,100	J. A. Bailey and Y. B. Patel	Digital Peak-Threshold Tracking Method and Apparatus
5,363,252	S. R. Bentley, S. M. Paranjape, and R. W. Murray	Method and System for Track Skew Tolerant Acquisition Burst Sequence Validation in a Data Storage System
5,363,258	R. D. Coles and D. C. Teale	Robotic Manipulator with Ejector Tab
5,363,261	E. A. Eckberg, B. P. Kruger, R. E. Lagergren, B. E. Schultz, and A. L. Warner	Disk Drive Actuator Latch
5,363,275	J. A. Frankeny, R. F. Frankeny, K. Hermann, and R. L. Imken	Modular Component Computer System

5,363,280	I. I. Chobot, J. A. Covert, R. L. Haight, K. D. Mansfield, D. W. Miller, R. A. Neira, A. Petrovich, P. C. Sviedrys, L. A. Tiemann, G. A. Valenta, and T. B. Youngs, Jr.	Printed Circuit Board or Card Thermal Mass Design
5,363,309	W. H. Tong Loh	Normal Distance Construction for Machining Edges of Solid Models
5,363,310	J. Haj-Ali-Ahmadi and E. C. Hume III	System and Method for Partial Order Filling
5,363,321	S. D. Trong, K. Helwig, and M. Loch	Digital Circuit for Calculating a Logarithm of a Number
5,363,323	G. G. Lange	Power Supply with Plural Outputs Supplying Dynamic and Steady Loads
5,363,327	W. H. Henkels and W. Hwang	Buried-Sidewall-Strap Two Transistor One Capacitor Trench Cell
5,363,379	T. Eckenrode, D. R. Stauffer, and R. Stempski	FDDI Network Test Adaptor Error Injection Circuit
5,363,484	C. M. Desnoyers, D. L. Garmire, S. M. Genco, D. G. Grice, W. R. Milani, M. P. Muhlada, D. C. Myers, P. K. Szwed, V. M. Tsinker, A. E. Vallone, and C. A. Bender	Multiple Computer System with Combiner/Memory Interconnection System Employing Separate Direct Access Link for Transferring Information Packets
5,363,495	R. E. Fry and T. N. Hicks	Data Processing System with Multiple Execution Units Capable of Executing Instructions Out of Sequence
5,363,497	E. D. Baker, J. M. Dinwiddie, Jr., L. E. Grice, J. M. Loffredo, K. R. Sanderson, and G. A. Suarez	System for Removing Section of Memory from First System and Allocating to Second System in a Manner Indiscernable to Both Operating Systems
5,363,505	B. A. M. Maslak, J. M. Showalter, and T. J. Szczysielski	Local and Global Commit Scopes Tailored to Work Units
5,363,550	J. M. Aitken, S. Akbar, B. L. Crowder, A. Iqbal, and P. Nihal	Method of Fabricating a Micro-Coaxial Wiring Structure
5,363,553	R. D. Edwards, F. D. Egitto, T. P. Gall, P. S. Gursky, D. E. Houser, J. S. Kamperman, and W. R. Wrenner	Method of Drilling Vias and Through Holes
5,364,673	M. Schmidt, K. Staudenmaier, J. Schnee, W. Mannsdoerfer, and V. Weber	Transparent Plate-Shaped Component Having a Stepped Edge Totally Surrounded by a Seal
5,365,089	S. N. Mohammad	Double Heterojunction Bipolar Transistor and the Method of Manufacture Therefor
5,365,097	D. M. Kenney	Vertical Epitaxial SOI Transistor, Memory Cell and Fabrication Methods
5,365,117	R. C. Wong	Logic Gates Having Fast Logic Signal Paths Through Switchable Capacitors
5,365,204	J. M. Angiulli, A. K. Ghose, R. R. Konian, S. R. Levine, D. Meltzer, W.-Y. Wang, and L. L. Wu	CMOS Voltage Controlled Ring Oscillator
5,365,211	W. D. Carbaugh, Jr., D. C. Long, and K. F. Stroms	Wound Coil with Integral Cooling Passages
5,365,228	P. L. Childs, H. T. Olnowich, and J. F. Skovira	SYNC-NET—A Barrier Synchronization Apparatus for Multi-Stage Networks
5,365,360	R. J. Torres	Method and Apparatus for Presenting Information Contained Within a Data Icon by Assigning Attributes to Various Dimensions of the Data Icon
5,365,463	W. E. Donath, R. B. Hitchcock, and J. P. Soreff	Method for Evaluating the Timing of Digital Machines with Statistical Variability in Their Delays
5,365,469	B. R. Mercy	Fast Fourier Transform Using Balanced Coefficients
5,365,523	J. H. Derby, W. A. Doeringer, J. E. Drake, Jr., D. H. Dykeman, L. Li, M. L. Peters, H. J. Sandick, and K. Van Vu	Forming and Maintaining Access Groups at the LAN/WAN Interface
5,365,587	J. E. Campbell, M. J. Chan, S. F. Hajek, and P. L. Wiltgen	Self Modifying Access Code for Altering Capabilities
5,366,156	T. L. Bauer, W. A. Cavaliere, D. C. Linnell, and J. J. Wu	Nozzle Apparatus for Producing Aerosol
5,366,908	M. M. A. Pelella	Process for Fabricating a MOS Device Having Protection Against Electrostatic Discharge
5,366,923	K. D. Beyer, C.-M. Hsieh, L. L. Hsu, and T.-D. Yuan	Bonded Wafer Structure Having a Buried Insulation Layer
5,366,757	B. J. Lin	IN SITU Resist Control During Spray and Spin in Vapor
5,367,124	H. S. Hoffman and R. W. Noth	Compliant Lead for Surface Mounting a Chip Package to a Substrate
5,367,139	R. S. Bennett, A. R. Ellingboe, G. G. Gifford, K. L. Haller, J. S. McKillop, G. S. Selwyn, and J. Singh	Methods and Apparatus for Contamination Control in Plasma Processing
5,367,195	G. DiGiacomo, J.-I. Kim, C. Narayan, and S. Purushothaman	Structure and Method for a Superbarrier to Prevent Diffusion Between a Noble and a Non-Noble Metal
5,367,231	M. Kobayashi and R. Kataoka	Display System

5,367,247	W. S. Blocher, B. M. Cassidy, J. O. Groves, Jr., R. S. Malik, and S. F. Newton	Critically Continuous Boost Converter
5,367,254	L. H. Faure and T. W. Spoor	Test Probe Assembly Using Buckling Wire Probes Within Tubes Having Opposed Overlapping Slots
5,367,409	H. H. Ottesen and G. J. Smith	Even Harmonic Distortion Compensation for Digital Data Detection
5,367,416	L. A. Cossette and Z.-E. Boutaghou	Spindle Grounding Device Using Magnetic Fluid
5,367,417	C. A. Brown and H. W. Wendt	Disk Drive with Flow-By Chemical Breather Filter
5,367,435	F. E. Andros, C. G. Angulas, and J. M. Milewski	Electronic Package Structure and Method of Making Same
5,367,513	K. A. Bates, N. X. Bui, and D. A. Hansen	Focus and Tracking Servo Decoupling System
5,367,517	I. Cidon, I. S. Gopal, and R. A. Guerin	Method and System of Requesting Resources in a Packet-Switched Network with Minimal Latency
5,367,523	R.-F. Chang, J. E. Drake, Jr., L. Gün, and L. T. Huynh	Adaptive Rate-Based Congestion and Flow Control in Packet Communications Networks
5,367,541	C. Barbero	Method and Apparatus for Setting Asynchronous Transmission Characteristics in a Telecommunication Equipment Including a SCC
5,367,621	A. S. C. Cohen, C. F. Gleason, D. R. Hyatt, M. E. Moran, J. N. Stevens, and A. J. Wecker	Data Processing Method to Provide a Generalized Link from a Reference Point in an On-Line Book to an Arbitrary Multimedia Object Which Can be Dynamically Updated
5,367,632	A. D. Bowen and D. C. Tannenbaum	Flexible Memory Controller for Graphics Applications
5,367,637	S. N. Wei	Self-Tuning Virtual Storage Management for Dedicated Real-Time Computer System
5,367,643	P. Chang, G. S. Delp, H. E. Meleis, R. M. Montalvo, D. L. Seidman, A. N. Tantawy, and D. A. Zumbo	Generic High Bandwidth Adapter Having Data Packet Memory Configured in Three Level Hierarchy for Temporary Storage of Variable Length Data Packets
5,367,648	C.-M. Chuang and K. Ebcioğlu	General Purpose Memory Access Scheme Using Register-Indirect Mode
5,367,653	W. E. Coyle, D. W. Nuechterlein, K. E. O'Donnell, T. A. Sartorius, K. D. Schultz, and E. M. Wolters	Reconfigurable Multi-Way Associative Cache Memory
5,367,671	B. A. Feigenbaum and F. Miro	System for Accessing Extended Object Attribute (EA) Data Through File Name or EA Handle Linkages in Path Tables
5,367,674	J. E. Berk, J. R. Coon, P. R. Day, P. J. Heyrman, and D. L. Johnson	Data Stream Optimizer Utilizing Difference Coding Between a Current State Buffer and a Next State Buffer
5,367,680	G. A. Flurry and L. W. Henson	Rendering Context Manager for Display Adapters Supporting Multiple Domains
5,367,686	A. J. Fisher, A. Hlava, P. D. Koeller, M. C. Manges, M. K. Russell, R. H. Satin, G. G. Stewart, and P. A. Timms	Method for Automated Complex Multilevel Software Installation in a Data Processing System
5,367,703	D. S. Levitan	Method and System for Enhanced Branch History Prediction Accuracy in a Superscalar Processor System
5,368,054	R. G. Koretsky, D. R. Vigliotti, and R. J. von Gutfeld	Ultrasonic Jet Semiconductor Wafer Cleaning Apparatus
5,368,814	S. G. Gonya, J. K. Lake, R. C. Long, and R. N. Wild	Lead Free, Tin-Bismuth Solder Alloys
5,369,049	J. E. Acocella, L. L. Hsu, S. Ogura, N. Rovedo, and J. F. Shepard	DRAM Cell Having Raised Source, Drain and Isolation
5,369,358	R. Metzger, M. Schmidt, O. Torreiter, and D. Wendel	Method of Ensuring Electrical Contact Between Test Probes and Chip Pads Through the Use of Vibration and Nondestructive Deformation
5,369,533	H. H. Ottesen and G. G. Floryance	Data Storage Device Having Different Storage Densities for Differing Types of Data
5,369,535	S. R. Hetzler	Fixed Block Architecture Disk File with Improved Position Identification and Error Handling
5,369,594	C.-C. Huang and R. D. Kimmel	Conjugate Gradient Method in Computer-Aided Circuit Design
5,369,595	E. L. Gould, D. W. Kemerer, L. A. McAllister, R. A. Piro, G. R. Richardson, and D. A. Wellburn	Method of Combining Gate Array and Standard Cell Circuits on a Common Semiconductor Chip
5,369,652	C. E. Bailey, E. S. Gale, C. A. Hassell, S. J. Schaffer, S. M. Paranjape, and S. C. West	Error Detection and Correction Having One Data Format Recordable On Record Media Using a Diverse Number of Concurrently Recorded Tracks
5,369,705	R. F. Bird, A. Herzberg, P. A. Janson, S. Kutten, R. A. Molva, and M. M. Yung	Multi-Party Secure Session/Conference
5,369,749	E. D. Baker, J. M. Dinwiddie, Jr., L. E. Grice, J. M. Joyce, J. M. Loffredo, and K. R. Sanderson	Method and Apparatus for the Direct Transfer of Information Between Application Programs Running on Distinct Processors Without Utilizing the Services of One or Both Operating Systems
5,369,767	J. M. Dinwiddie, Jr., L. E. Grice, J. M. Loffredo, and K. R. Sanderson	Servicing Interrupt Requests in a Data Processing System Without Using the Services of an Operating System

5,370,178	D. Agonafer, T. M. Anderson, G. M. Chrysler, R. C.-F. Chu, R. E. Simons, and D. T. Vader	Convertible Cooling Module for Air or Water Cooling of Electronic Circuit Components
5,370,825	M. Angelopoulos, J. D. Gelorme, T. H. Newman, N. M. Patel, and D. E. Seeger	Water-Soluble Electrically Conducting Polymers, Their Synthesis and Use
5,370,974	P. A. Agostino, A. P. Giri, J. R. Lankard, Sr., and R. J. McDonald	Laser Exposure of Photosensitive Polyimide for Pattern Formation
5,371,022	C.-M. Hsieh, L. L. G. Hsu, S.-N. Mei, R. W. Knepper, and L. F. Wagner, Jr.	Method of Forming a Novel Vertical-Gate CMOS Compatible Lateral Bipolar Transistor
5,371,047	S. E. Greco and K. V. Srikrishnan	Chip Interconnection Having a Breathable Etch Stop Layer
5,371,328	B. L. Gutierrez and C.-Y. Yu	Component Rework
5,371,399	J. Burroughes, R. T. Hodgson, D. T. McInturf, M. R. Melloch, N. Otsuka, P. M. Solomon, A. C. Warren, and J. M. Woodall	Compound Semiconductor Having Metallic Inclusions and Devices Fabricated Therefrom
5,371,514	J. J. Lawless, D. W. Li, and D. C. Tannenbaum	Method and Apparatus for Determining the Drawing Primitives that are Visible in a Pick Aperture of a Graphics System
5,371,654	B. S. Beaman, F. E. Doany, K. E. Fogel, J. L. Hedrick, Jr., P. A. Lauro, M. H. Norcott, J. J. Ritsko, L. Shi, D.-Y. Shih, and G. F. Walker	Three Dimensional High Performance Interconnection Package
5,371,733	M. M. Denneau, B. D. Gavril, P. H. Hochschild, and C. B. Stunkel	Method and Apparatus for Centralized Determination of Virtual Transmission Delays in Networks of Counter-Synchronized Communication Devices
5,371,735	M. M. Denneau, P. H. Hochschild, and C. B. Stunkel	Communication Network with Non-Unique Device Identifiers and Method of Establishing Connection Paths in Such a Network
5,371,764	R. D. Gillingham and C. L. Johnson	Method and Apparatus for Providing an Uninterrupted Clock Signal in a Data Processing System
5,371,766	J. E. Gersbach, I. I. Novof, and J. K. Lee	Clock Extraction and Data Regeneration Logic for Multiple Speed Data Communications Systems
5,371,843	G. P. Pomichter, Jr.	Method and System for Filling Non-Complex Polygons Using Vertical Spans
5,371,844	C. Andrew and M. A. Bloomfield	Palette Manager in a Graphical User Interface Computer System
5,371,846	C. L. Bates	Non-Linear Scroll Bar
5,371,848	B. E. Casey, G. L. Dunlap, and S. L. Rockwell	Method for Operating and Displaying Status of a Computer System
5,371,852	C. R. Attanasio and S. E. Smith	Method and Apparatus for Making a Cluster of Computers Appear as a Single Host on a Network
5,371,861	D. S. Keener and G. J. Moore	Personal Computer with Small Computer System Interface (SCSI) Data Flow Storage Controller Capable of Storing and Processing Multiple Command Descriptions ("Threads")
5,371,864	C.-M. Chuang	Apparatus for Concurrent Multiple Instruction Decode in Variable Length Instruction Set Computer
5,371,867	J. George, R. E. Hough, M. J. Kim, A. H. Preston, D. E. Stucki, and C. F. Webb	Method of Using Small Addresses to Access and Guest Zone in a Large Memory
5,371,872	L. D. Larsen, D. W. Nuechterlein, K. E. O'Donnell, T. A. Sartorius, K. D. Schultz, and H. I. Linzer	Method and Apparatus for Controlling Operation of a Cache Memory During an Interrupt
5,371,875	R. G. Eikill and Q. G. Schmierer	Logic on Main Storage Memory Cards for Insertion and Extraction of Tag Bits
5,371,883	K. L. Gross and K. D. Sullivan	Method of Testing Programs in a Distrubuted Environment
5,371,886	K. H. Britton, A. P. Citron, and B. G. Lindsay	System for Managing Unit-of-Work Identifiers When a Chained, Distributed, Two Phase Commit Transaction System is Severed
5,371,888	G. K. Lehnertz, K. L. Milsted, B. J. Wright, and K. L. Zemanek	Using Specialized Output Device Job Spooler as Generic Buffer Manager
5,371,890	G. P. Bozman, J. A. Pershing, Jr., and J. Ruvalo-Chong	Problem State Cross-Memory Communication Using Communication Memory Domains
5,371,893	D. W. Price, F. A. Reiley, and W. K. Rodiger	Look-Ahead Priority Arbitration System and Method
5,371,894	M. T. DiBrino	Off-Chip Breakpoint System for a Pipelined Microprocessor
5,371,897	P. J. Brown, K. J. Fredericks, Sr., E. P. Hefferon, G. T. Moffitt, and A. S. Meritt	Method for Requesting Identification of a Neighbor Node in a Data Processing I/O System
5,372,296	S. J. Konecke and F. G. Weindelmayer	Graphite Lid Seal Fixture
5,372,652	K. V. Srikrishnan and J. J. Wu	Aerosol Cleaning Method
5,372,670	M. J. Cummings, J. M. Griffin, and C. L. Miller	System for Wet Application of a Dry Film to a Panel
5,372,685	M. Anschel	Process for Coating a Substrate with Metallic Layer
5,372,785	C. L. Johnson and S. L. Silverman	Solid-State Multi-Stage Gas Detector

5,372,848	K. J. Blackwell, L. J. Matienzo, and A. R. Knoll	Process for Creating Organic Polymeric Substrate with Copper
5,372,912	R. D. Allen, J. Conley, W. D. Hinsberg III, P. E. Jones, and K. M. Welsh	Radiation-Sensitive Resist Composition and Process for Its Use
5,373,109	S. N. Argyrakis, R. W. Oldrey, and E. E. Steele	Electrical Cable Having Flat, Flexible, Multiple Conductor Sections
5,373,147	M. Noda	Apparatus and Method for Detecting Line Segment Direction
5,373,166	N. I. Buchan, W. Heuberger, and P. Roentgen	Modulated Strain Heterostructure Light Emitting Device
5,373,238	T. R. McGuire and T. S. Plaskett	Four Layer Magnetoresistance Device and Method for Making a Four Layer Magnetoresistance Device
5,373,253	J. A. Bailey, J. E. Malmberg, and L. L. Tretter	Monolithic Current Mirror Circuit Employing Voltage Feedback for β -Independent Dynamic Range
5,373,300	R. V. Jenness and V. S. Moore	Mobile Data Terminal with External Antenna
5,373,388	R. Betts	AC Coupled Fiber Optic Receiver with DC Coupled Characteristics
5,373,401	S. R. Bentley, S. M. Paranjape, F. Quintana, and S. C. West	Data Storage System Having Multimodal Interblock Gap Detection Utilizing a Predicted Occurance Time Window
5,373,434	R. S. Malik	Pulse Width Modulated Power Supply
5,373,455	A. D. Edgar	Positive Feedback Error Diffusion Signal Processing
5,373,499	W. I. Imaino, H. J. Rosen, K. A. Rubin, and T. C. Strand	Multilayer Optical Disk and System Having Multiple Optical Paths Include Identical Total Disk Substrate Thickness
5,373,893	D. E. Eisenmann, P. M. Elenius, J. M. Leas, and W. M. Wazni	Method and Apparatus for Cooling Thermally Massive Parts in a Continuous Furnace
5,374,338	C. M. Boyko, R. W. Carpenter, R. T. Galasco, K. W. Semkow, and H. Wegener	Selective Electroetch of Copper and Other Metals
5,374,344	T. P. Gall and J. Wilcox	Method and Apparatus for Electrodeposition of a Metallurgically Bondable Circuitized Flexible Substrate
5,374,454	H. R. Bickford, P. J. Duke, E. Foster, M. J. Goldberg, V. R. Markovich, L. C. Matthew, D. G. McBride, T. R. O'Toole, S. L. Tisdale, and A. Viehbeck	Method for Conditioning Halogenated Polymeric Materials and Structures Fabricated Therewith
5,374,463	D. S. Bethune, M. Sjim'on de Vries, G. Meijer, V. J. Novotny, T. C. Reiley, and A. W. Wu	Magnetic Recording Disk Having a Contiguous Fullerene Film and a Protective Overcoat
5,374,481	S. J. Jeng, J. Kanicki, D. E. Kotecki, C. C. Parks, and Z.-J. Tien	Polyemitter Structure with Improved Interface Control
5,374,493	T. Fukunaga, T. Koseki, T. Ueki, H. Yamanaka, and K. Kobayashi	Color Filter and Method of Fabricating a Color Filter
5,374,500	B. J. Carpenter, Jr., M. G. McMaster, J. LaTorre, and L. L. Simpson	Positive Photoresist Composition Containing Photoacid Generator and Use Thereof
5,374,503	K. G. Sachdev, J. R. Whitaker, and U. M. Ahmad	Method of Forming Patterned Polyimide Films
5,374,590	K. L. Batdorf, R. A. Gilmour, and P. Tsang	Fabrication and Laser Deletion of Microfuses
5,374,788	S. Endoh and M. Suga	Printed Wiring Board and Manufacturing Method Therefor
5,374,924	F. A. McKiel, Jr.	Computer User Interface with Stereo and Filtered Sound Effects
5,375,070	P. C. Hershey, K. J. Barker, C. S. Lingafelt, Sr., and J. G. Wacławsky	Information Collection Architecture and Method for a Data Communications Network
5,375,078	D. A. Hrusecky and M. Putrino	Arithmetic Unit for Performing $XY + B$ Operation
5,375,084	R. M. Begun, C. M. Herring, and M. W. Kellogg	Selectable Interface Between Memory Controller and Memory SIMMS
5,375,091	R. W. Berry, Jr., B. K. F. Koenemann, W. J. Scarpero, Jr., P. G. Shephard III, K. D. Wagner, and G. Yasar	Method and Apparatus for Memory Dynamic Burn-In and Test
5,375,138	W. J. Kozlovsky and J. M. Zavislan	Optical Cavities for Lasers
5,375,166	S. Odaohara	Communication Apparatus with Cross-Talk Protection
5,375,200	P. A. Dugan, G. P. Fitzpatrick, W. J. Johnson, and M. L. Williams	Method and System for Graphic Interaction Between Data and Applications Within a Data Processing System
5,375,210	P. J. Monnes and J. G. Wilkinson	Display Mode Query and Set
5,375,214	J. H. Mirza and S. W. White	Single Translation Mechanism for Virtual Storage Dynamic Address Translation with Non-Uniform Page Sizes
5,375,223	S. D. Meyers, H. C. Ngo, and P. R. Schwartz	Single Register Arbiter Circuit
5,375,232	V. J. Legvold, W. F. Micka, K. M. Nagin, Y. Novick, P. A. Vortman, and E. Zeidner	Method and System for Asynchronous Pre-Staging of Backup Copies in a Data Processing Storage Subsystem
5,375,234	W. S. Davidson and K. W. Eggerling	Data Dictionary Manager for Maintaining an Active Data Dictionary

5,376,569	J. G. Bednorz, J. D. Mannhart, and C. A. Mueller	Superconducting Field-Effect Transistors with Inverted MISFET Structure and Method for Making the Same
5,376,578	L. L. Hsu, S. Ogura, and J. F. Shepard	Method of Fabricating a Semiconductor Device with Raised Diffusions and Isolation
5,376,582	A. Behfar-Rad, C. S. Harder, and H. P. Meier	Planar, Topology-Free Single-Mode, High-Power Semiconductor Quantum-Well Laser with Non-Absorbing Mirrors and Current Confinement
5,376,584	B. N. Agarwala	Process of Making Pad Structure for Solder Ball Limiting Metallurgy Having Reduced Edge Stress
5,376,587	P. Buchmann, P. Unger, P. Vettiger, and O. Voegeli	Method for Making Cooling Structures for Directly Cooling An Active Layer of a Semiconductor Chip
5,376,830	D. J. Ashley, M. J. Johnson, and D. R. Thomas	High Frequency Slope Compensation Circuit for Current Programmed Converter
5,376,831	Z. X. Chen	Power Switch Circuit Providing Linear Voltage Rise
5,376,849	J. E. Dickol, D. L. Do, and A. J. Gruodis	High Resolution Programmable Pulse Generator Employing Controllable Delay
5,376,949	D. C. Haigh, R. B. Harrison, and H. R. Bonnor	Display System with Graphics Cursor
5,377,052	A. M. Guzman, P. Y. Hu, A. I. Panasiuk, and L. K. Whitted	Actuator Assembly for Servo-Controlled Magnetic Tape Head
5,377,058	D. J. Good, J. E. Mason, and H. H. Ottesen	Fly Height Servo Control of Read/Write Head Suspension
5,377,096	H. Ono	Digital Actuator Controller Using Low-Pass Filter
5,377,121	K. E. Dimitri and D. L. Wilson	Automated Storage Library Having Inventory at Picker Level
5,377,131	H. H. Ottesen and G. J. Smith	Digital Amplitude Estimator
5,377,134	R. R. Faget	Leading Constant Eliminator for Extended Precision in Pipelined Division
5,377,184	P. D. Beal, K. Dutke, B. W. Noordyke, and M. L. Shupert	Method of Controlling TWA Link in a Communications Adapter by Monitoring Buffer Fill Levels
5,377,282	T. C. Fellows, N. E. Rittenhouse, and P. J. Yablonsky	Optical Inspection System Utilizing Dynamic Analog-to-Digital Thresholding
5,377,313	F. J. Scheibl	Computer Graphics Display Method and System with Shadow Generation
5,377,314	C. L. Bates, J. M. Ryan, and B. T. Watts	Method and System for Selective Display of Overlapping Graphic Objects in a Data Processing System
5,377,317	C. L. Bates and J. M. Ryan	Method and Apparatus for Distinctively Displaying Windows on a Computer Display Screen
5,377,331	B. C. Drerup and J. V. Peterson	Converting a Central Arbiter to a Slave Arbiter for Interconnected Systems
5,377,336	R. J. Eickemeyer and S. Vassiliadis	Improved Method to Prefetch Load Instruction Data
5,377,337	J. Antognini, G. A. Brent, T. E. Cook, T. J. Dewkett, J. C. Elliot, F. E. Johnson, C. A. Scalzi, K. R. Veraska, J. A. Williams, and H. M. Yudenfriend	Method and Means for Enabling Virtual Addressing Control by Software Users Over a Hardware Page Transfer Control Entity
5,377,348	Y. C. Lau, E. J. Mozdy, and J. A. Oleszkiewicz	System for Searching a Data Base by Creating a Marking Matrix in Which Two Dimensional Patterns Control the Search and Selection
5,377,355	D. J. Hager and C. G. Rose	Method and Apparatus for Automated Procedure Initiation in a Data Processing System Including Soliciting an Evaluation Vote from Users Automatically Determined in Response to Identification of a Functional Area Associated with a Document
5,377,476	G. Böhmer, J. Gentischer, R. Lehner, D. Modjesch, and W. Schmutz	Arrangement for Storing, Transporting and Loading Substrates
5,377,911	T. L. Bauer, W. A. Cavaliere, C. R. Dart III, T. H. Freebern, D. C. Linnell, J. M. Miller, and J. J. Wu	Apparatus for Producing Cryogenic Aerosol
5,377,961	T. M. Smith and R. E. Winstead	Electrodynamic Pump for Dispensing Molten Solder
5,378,306	M. J. Cibulsky, K. I. Papathomas, W. J. Summa, D. W. Wang, and P. R. Zippetelli	Composite for Providing a Rigid-Flexible Circuit Board Construction and Method for Fabrication Thereof
5,378,307	S. L. Bard, G. A. Bendz, M. J. Canestaro, J. R. Chapura, E. J. Frankoski, M. S. Horan, J. D. Jones, J. S. Kamperman, J. R. Kjelgaard, Jr., and J. M. McCreary	Fluid Treatment Apparatus
5,378,312	G. G. Gifford, Y.-J. T. Lii, and J. J. Wu	Process for Fabricating a Semiconductor Structure Having Sidewalls
5,378,511	T. J. Cardinali and B. J. Lin	Material-Saving Resist Spinner and Process
5,378,651	P. D. Agnello, D. A. Gruetzmacher, T.-S. Kuan, and T. O. Sedgwick	Comprehensive Process for Low Temperature Epitaxial Growth
5,378,927	M. F. McAllister, J. A. McDonald, K. Prasad, G. J. Robbins, and M. Swaminathan	Thin-Film Wiring Layout for a Non-Planar Thin Film Structure

5,378,943	R. H. Dennard	Low Power Interface Circuit
5,379,000	R. G. Brewer, R. G. DeVoe, K. L. Foster, J. A. Hoffnagle, and R. Kallenbach	Atomic Clock Employing Ion Trap of Mono- or Multi-Planar Geometry
5,379,037	C. Harrison, M. D. Marik, and R. L. Posthumus	Apparatus for Decoding Degraded Data Signals
5,379,071	S. N. Parikh and H. N. Reddy	Method of Encoding a Location of Changes in a Sequence of Video Images
5,379,161	F. Quintana	Method and System for Synchronization Character Location and Prediction in a Data Storage System
5,379,162	E. A. Cunningham and B. E. Hanson	Customized Data Recovery Procedures Selected Responsive to Readback Errors and Transducer Head and Disk Parameters
5,379,167	G. S. M. Robles and M. L. Sendelweck	Magnetic Tape Cartridge with Second Generation Leader Block and Leader Block Pin
5,379,193	T. P. Gall and J. R. Loomis	Parallel Processor Structure and Package
5,379,291	L. P. Herzberg, A. Ruiz, and M. H. Willebeek-LeMair	Apparatus for Fiber Distributed Data Interface Dynamic Station Bypass Via Skipping and Hopping
5,379,304	T. J. Dell and S. Washabaugh	Method and Structure for Providing Error Correction Code and Parity for Each Byte on SIMM's
5,379,370	J. E. Allain and B. D. Lucas	Method and Apparatus for Drawing Lines, Curves, and Points Coincident with a Surface
5,379,375	V. D. Dao and S. Ostojic	Automatic Location of Screen Objects Through the Use of Relational Position Data
5,379,376	A. L. Bednowitz	Bi-Directional Graphics Attribute Conversion Profile
5,379,385	R. W. Shomler	Method and Means for Effectuating Rule Based I/O Data Transfer Address Control Via Address Control Words
5,379,386	J. L. Swarts, J. S. Fields, Jr., G. L. Guthrie, and D. A. Smetana, Jr.	Micro Channel Interface Controller
5,379,398	O. Cohn, M. H. Hartung, J. N. McCauley, Jr., W. F. Micka, C. W. Mikkelsen, K. M. Nagin, Y. Novick, and A. Winokur	Method and System for Concurrent Access During Backup Copying of Data
5,379,399	D. C. Conway-Jones and P. M. Smith	FIFO Memory Controller for a Digital Video Communications Channel Having a Detector, Comparator, and Threshold Select Logic Circuit
5,379,400	E. H. Barakat and R. B. Haig	Method and System for Determining Memory Refresh Rate
5,379,407	S. J. P. Todd	Error Handling in a State-Free System
5,379,412	L. E. Eastridge and J. M. Ratliff	Method and System for Dynamic Allocation of Buffer Storage Space During Backup Copying
5,379,434	M. T. DiBrino	Apparatus and Method for Managing Interrupts in a Multiprocessor System
5,379,437	J. Celi, Jr. and G. D. Webster	Reset of Peripheral Printing Devices After a Hot Plug State
5,379,448	R. N. Ames, T. W. Kludt, and V. S. Moore	Load Balancing in a Digital Communications Network Using Radio Links
5,379,601	J. B. Gillett	Temperature Actuated Switch for Cryo-Coolers
5,380,548	T. Lin, J. K. Howard, C. Hwang, D. Mauri, and N. Staud	Method for Fabricating a Magnetoresistive Sensor Having Antiferromagnetic Layer
5,380,560	S. Kaja, S. P. Mukherjee, E. J. O'Sullivan, and M. Paunovic	Palladium Sulfate Solution for the Selective Seeding of the Metal Interconnections on Polyimide Dielectrics for Electroless Metal Deposition
5,380,621	R. R. Dichiaro, C. F. Lyons, R. Sooriyakumaran, G. T. Spinillo, K. M. Welsh, and R. L. Wood	Mid and Deep-UV Antireflection Coatings and Methods for Use Thereof
5,380,955	S. N. Argyrakakis, W. S. Harris, R. W. Oldrey, and E. J. Ossolinski	Device for Passing a Member Through a Sealed Chamber Wall
5,380,998	D. C. Bossen, C.-L. Chen, M.-Y. Hsiao, and J. M. Mulligan	Single Width Bar Code with End Code with End Code Providing Bidirectionality
5,380,999	R. Prakash and G. D. Rohrer	Linear Scanning Using a Single Illumination and Imaging Optic Stations with a First Pair of Parallel Mirrors and a Second Pair of Mirrors Extending Into Paths of Light Beams
5,381,046	C. Cederbaum, R. Chanclo, M. Combes, and P. Mone	Stacked Conductive Resistive Polysilicon Lands in Multilevel Semiconductor Chips
5,381,060	H. A. Ainspan and J. F. Ewen	Differential Current Switch to Super Buffer Logic Level Translator
5,381,234	S. G. Barbee, T. F. Heinz, R. J. Lebel, L. Li, and V. J. Silvestri	Method and Apparatus for Real-Time Film Surface Detection for Large Area Wafers
5,381,282	K. Arai, H. H. Ottesen, A. Sharma, M. Sri-Jayantha, and M. C. Stich	Inter-Sample Switching of Servo Control in Direct Access Storage Devices
5,381,284	T. A. Gregory and C. G. Keller	Spacer Ring Reservoir for Magnetic Recording Disk Lubricant
5,381,401	M. E. Best, H. J. Rosen, K. A. Rubin, and T. C. Strand	Multiple Data Surface Optical Data Storage System
5,381,418	R. K. Montoye	Testable Latch Self Checker

5,381,421	J. E. Dickol, A. J. Gruodis, and D. E. Hoffman	Per Pin Circuit Test System Having n-Bit Pin Interface Providing Speed Improvement with Frequency Multiplexing
5,381,480	A. S. Butter, B. S. Finkel, C.-Y. Kao, S. K. Kodukula, and J. P. Kuruts	System for Translating Encrypted Data
5,381,527	H. A. Inniss, R. S. Keller, W. J. Johnson, and M. L. Williams	System for Efficient Message Distribution by Successively Selecting and Converting to an Alternate Distribution Media Indicated in a Priority Table Upon Preferred Media Failure
5,381,535	P. H. Gum, R. E. Hough, and R. E. Murray	Data Processing Control of Second-Level Quest Virtual Machines Without Host Intervention
5,381,537	R. I. Baum, K. E. Plambeck, C. A. Scalzzi, R. J. Schmalz, and B. Sinha	Large Logical Addressing Method and Means
5,381,538	N. Amini, B. F. Boury, and T. J. Lohman	DMA Controller Including a FIFO Register and a Residual Register for Data Buffering and Having Different Operating Modes
5,381,541	R. M. Begun and M. R. Turner	Computer System Having Planar Board with Single Interrupt Controller and Processor Card with Plural Processors and Interrupt Director
5,381,545	S. M. Baker and P. J. Lupton	Data Backup and Recovery in a Data Processing System
5,382,127	G. Garric and A. Lafond	Pressurized Interface Apparatus for Transferring a Semiconductor Wafer Between a Pressurized Sealable Transportable Container and a Processing Equipment
5,382,447	S. Kaja, E. J. O'Sullivan, and A. G. Schrott	Process for Fabricating Improved Multilayer Interconnect Systems
5,382,483	M. A. Young	Self-Aligned Phase-Shifting Mask
5,382,491	A. F. Diaz, J. M. Guay, and M. T. Nguyen	Toner Composition
5,382,565	J. G. Bednorz, J. D. Mannhart, and C. A. Mueller	Superconducting Filed-Effect Transistors
5,382,637	M. Angelopoulos, C. Feger, J. D. Gelorme, and J. M. Shaw	Solid State Chain Extension Polymerization Between Lewis Acid Oligomers and Deblocked Lewis Bases
5,382,815	S. N. Mohammad, R. B. Renbeck, and K. M. Walter	Carrier Conduction Conductor-Insulator Semiconductor (CIS) Transistor
5,382,832	T. N. Buti, L. L. Hsu, R. V. Joshi, and J. F. Shepard	Semiconductor Device and Wafer Structure Having a Planar Buried Interconnect by Wafer Bonding
5,382,911	T. J. Cotler, J. C. Forster, L. A. Kropp, and J. Singh	Reaction Chamber Interelectrode Gap Monitoring by Capacitance Measurement
5,382,922	J. E. Gersbach and M. Hayashi	Calibration Systems and Methods for Setting PLL Gain Characteristics and Center Frequency
5,383,088	J. D. Chapple-Sokol, R. A. Conti, and J. P. Gambino	Storage Capacitor with a Conducting Oxide Electrode for Metal-Oxide Dielectrics
5,383,155	T. K. Ta	Data Output Latch Control Circuit and Process for Semiconductor Memory System
5,383,175	B. I. Finkelstein and D. E. Call	Laser Power Control with Defocusing Offset During Data Recording
5,383,191	R. B. Hobgood, J. L. Smith, R. D. Verne, and A. D. Walker	Dual Ring Reconfiguration Switching Unit
5,384,152	J. C. Chu, L. L.-C. Hsu, T. Mii, J. F. Shepard, S. R. Stiffler, M. J. Tejwani, and E. J. Vishnesky	Method for Forming Capacitors with Roughened Single Crystal Plates
5,384,277	L. L. Hsu, T. Mii, J. F. Shepard, and S. Ogura	Method for Forming a DRAM Trench Cell Capacitor Having a Strap Connection
5,384,283	R. E. Gegenwarth and A. F. Arnold	Resist Protection of Ball Limiting Metal During Etch Process
5,384,474	J. W. Park and S. H. Voldman	Double Grid Substrate Plate Trench
5,384,546	J. E. Gersbach	Time Domain Component Multiplexor
5,384,567	M. A. Hassner, E. D. Karnin, U. Schwiagelshohn, and T. Tamura	Combination Parallel/Serial Execution of Sequential Algorithm for Data Compression/Decompression
5,384,598	A. A. Rodriguez, C. T. Rutherford, S. M. Hancock, R. F. Kantner, Jr., and M. A. Pietras	System and Method for Frame Differencing Video Compression and Decompression with Frame Rate Scalability
5,384,639	H. K. Wickramasinghe	Depth Measurement of High Aspect Ratio Structures
5,384,669	E. C. Dunn, S. M. Fry, S. A. Jackson, N. H. MacLean, Jr., R. P. Reynolds, and R. A. Ripberger	Combining Small Records Into a Single Record Block for Recording on a Record Media
5,384,690	C. R. Davis, T. P. Duffy, S. L. Hanakovic, H. L. Heck, J. T. Kolias, J. S. Kresge, D. N. Light, and A. K. Trivedi	Flex Laminate Package for a Parallel Processor
5,384,700	C. S. Lim, K. M. McBride, and T. Wakejima	Method and System for Storing Multiple, Modifiable Yomi and Kanji Strings in a Structured Document
5,384,723	F. O. Karim and C. H. Olson	Method and Apparatus for Floating Point Normalization
5,384,762	N. Kagami, H. Kubo, and K. Okada	Focusing Servo in an Optical Disk Drive
5,384,773	H. T. Olnwich, J. D. Jabusch, R. F. Lusch, and A. Maniguet	Multi-Media Analog/Digital/Optical Switching Apparatus

5,384,777	H. Ahmadi, D. F. Bantz, F. J. Bauchot, A. Krishna, R. O. La Maire, and K. S. Natarajan	Adaptive Medium Access Control Scheme for Wireless LAN
5,384,894	S. Vassiliadis, W. Kobrosly, and G. Triantafyllos	Fuzzy Reasoning Database Question Answering System
5,384,903	J. A. Herrod	Graphics Processing Method and Apparatus
5,384,909	J. R. Brown	Precision Automatic Scrolling for an Image Display System
5,384,910	R. J. Torres	Method and Apparatus for Facilitating Operator Reconfiguration of a Graphical User Interface in a Data Processing System
5,384,911	M. A. Bloomfield	Method of Transferring Programs from Action Oriented GUI Paradigm to Object Oriented GUI Paradigm
5,384,953	L. Economikos and R. P. Surprenant	Structure and a Method for Repairing Electrical Lines
5,384,955	R. B. Booth, R. H. Gephart, B. S. Gremban, J. E. Poetzinger, and D. T. M. Shen	Method for Replacing IC Chip Package Interposer
5,385,661	P. C. Andricacos, I.-C. Chang, H. Deligianni, and W. J. Horkans	Acid Electrolyte Solution and Process for the Electrodeposition of Copper-Rich Alloys Exploiting the Phenomenon of Underpotential Deposition
5,385,850	J. O. Chu, C.-M. Hsieh, V. R. Nastasi, M. Revitz, and P. A. Ronsheim	Method of Forming a Doped Region in a Semiconductor Substrate Utilizing a Sacrificial Epitaxial Silicon Layer
5,385,853	S. N. Mohammad	Method of Fabricating a Metal Oxide Semiconductor Heterojunction Field Effect Transistor (MOSHFET)
5,385,866	T. A. Bartush	Polish Planarizing Using Oxidized Boron Nitride as a Polish Stop
5,386,121	S. G. Barbee, T. F. Heinz, L. Li, and V. J. Silvestri	In-Situ, Non-Destructive CVD Surface Monitor
5,386,219	E. C. Greanias, G. F. Verrier, and R. L. Donaldson	Touch Overlay for Improved Touch Sensitivity
5,386,324	S. M. Fry, P. R. Nylander-Hill, and S. M. Paranjape	Apparatus and Method for Anticipated Error Recovery Using Debris Profiles
5,386,344	B. S. Beaman, F. E. Doany, T. J. Dudek, A. P. Lanzetta, D.-Y. Shih, W. J. Tkazyik, and G. F. Walker	Flex Circuit Card Elastomeric Cable Connector Assembly
5,386,392	T. Cantiant, B. Gabillard, J.-P. Mifsud, and S. Rapoport	Programmable High Speed Array Clock Generator Circuit for Array Built-In Self Test Memory Chips
5,386,393	E. C. Hallee, S.-L. Huang, K. H. Hwang, and B. U. Messina	Fencing Circuit and Method for Isolating Spurious Noise at System Interface
5,386,420	M. M. Blaum and J. Bruck	Coding Method for Correction and Detection of Skewed Transitions in Parallel Asynchronous Communication Systems
5,386,423	C. C. Koo, B. U. Messina, and J. Saia	Ordering Shift Register Latches in a Scan Ring to Facilitate Diagnosis, Testing and Isolation
5,386,505	I. Beattie, N. M. Desai, M. T. Vanover, and J. A. Voltin	Selective Control of Window Related Overlays and Underlays
5,386,511	H. Murata and S. Shimizu	Multiprocessor System and Data Transmission Apparatus Thereof
5,386,512	M. B. Crisman, J. C. Daly, A. M. Day, C. W. Gainey, Jr., P. G. Greenstein, D. C. Hughes, J. T. Rodell, and K. M. Walsh	System for Deriving and Testing Mutual Capability Set After Receiving Updated Capability from Other Processors and Before Requesting Service Information
5,386,516	C. J. Monahan, M. L. Monohan, and D. L. Willson	Virtual Drives in an Automated Storage Library
5,386,520	M. J. L. Gillet	Prolog Addressing
5,386,522	D. H. Evans	Dynamic Physical Address Aliasing During Program Debugging
5,386,531	B. Blaner, R. J. Eberhard, T. L. Jeremiah, and M. J. Mack	Computer System Accelerator for Multi-Word Cross-Boundary Storage Access
5,386,542	J. J. Brann and T. C. Ralya	System for Generating a Time Reference Value in the MAC layer of an ISO/OSI Communications Model Among a Plurality of Nodes
5,386,556	E. B. Hedin, G. I. Jonsson, L. E. Olsson, M. A. Sanamrad, and S. O. G. Westling	Natural Language Analyzing Apparatus and Method
5,386,557	J. R. Boykin, L. A. Geer, G. R. Horn, and T. R. Malkemus	Enforcement of Referential Constraints in a Database System
5,386,559	N. R. Eisenberg, R. L. Huddleston, G. R. Karasiuk, M. C. Lehner, and C. S. Tribolet	Variant Domains and Variant Maps in a Versioned Database Management System
5,386,560	D. W. McCauley, R. J. Schmalz, R. M. Smith, Sr., and S. B. Stillman	Execution of Page Data Transfer by PT Processors and Issuing of Split Start and Test Instructions by CPUs Coordinated by Queued Tokens
5,386,561	K. D. Huynh, C. M. Norcutt, Jr., and T. C. Waldron III	Method of Integrated System Load Control Through Dynamic Time-Slicing in a Virtual Storage Environment
5,386,567	Y.-C. Lien, H. Sone, K. Sekiya, and Y. Kanada	Hot Removable and Insertion of Attachments on Fully Initialized Computer Systems
5,386,571	W. Kurz	Computer System and Method for Storing and Displaying of a Semantically Structured Entity Relationship Diagram

5,386,583	J. E. Hendricks	High Performance Sort Hardware Accelerator in a Data Processing System Including a Plurality of Comparator Modules
5,386,627	R. B. Booth, R. H. Gephard, B. S. Gremban, J. E. Poetzinger, and D. T. Shen	Method of Fabricating a Multi-Layer Integrated Circuit Chip Interposer
5,387,110	E. A. Kantner and K. D. Schultz	Reversible Dual Media Adapter Cable
5,387,484	F. E. Doany and D. S. Goodman	Two-Sided Mask for Patterning of Materials with Electromagnetic Radiation
5,387,553	T. W. Moksvoeld, J. Alteri, C.-T. Tarn, and C. M. Snavely	Method for Forming a Lateral Bipolar Transistor with Dual Collector, Circular Symmetry and Composite Structure
5,387,777	R. S. Bennett, A. R. Ellingboe, G. G. Gifford, K. L. Haller, J. S. McKillop, G. S. Selwyn, and J. Singh	Method and Apparatus for Contamination Control in Plasma Processing
5,387,910	Y. Medan and U. Shvadron	Signal Processor
5,387,919	T. H. Lam, M. J. Milicic, Jr., and D. M. Pritchett	Dipole Antenna Having Co-Axial Radiators and Feed
5,388,017	L. P. Franco, A. M. Homola, and L. Viswanathan	Disk File with Air-Bearing Slider Having Skids
5,388,019	B. E. Argyle, T. C. Arnoldussen, T. J. Beulieu, D. A. Herman, Jr., S. Krangelb, H. P. E. Lee, D. A. Nepela, B. Petek, L. T. Romankiw, and J. C. Slonczewski	Magnetic Thin Film Structures Fabricated with Edge Closure Layers
5,388,072	R. E. Matick and S. E. Schuster	Bit Line Switch Array for Electronic Computer Memory
5,388,085	G. A. Jaquette	Apparatus and Method for Accessing Sectors of a Rotating Disk
5,388,097	M. J. Baugher, D. D. Heimsoth, and I. B. Van Horn	System and Method for Bandwidth Reservation for Multimedia Traffic in Communication Networks
5,388,136	D. K. Halliday, C. D. McCall, A. S. McKinnon, and C. D. Smith	X-ray Inspection Apparatus for Electronic Circuits
5,388,156	J. W. Blackedge, Jr., G. L. Clarke, Jr., R. A. Dayan, K. D. Le, P. E. McCourt, M. T. Mittelstedt, D. L. Moeller, P. E. Newman, D. L. Randall, and J. B. Yoder	Personal Computer System with Security Features and Method
5,388,215	E. D. Baker, J. M. Dinwiddie, Jr., L. E. Grice, J. M. Joyce, J. M. Loffredo, and K. R. Sanderson	Uncoupling a Central Processing Unit from its Associated Hardware for Interaction with Data Handling Apparatus Alien to the Operating System Controlling Said Unit and Hardware
5,388,219	S.-W. Chan, D. L. Dosch, K. Dutke, and W. R. Longcore	Efficient Channel and Control Unit for Host Computer
5,388,223	G. Guthrie and J. L. Swarts	1-Bit Token Ring Arbitration Architecture
5,388,228	C. A. Heath, J. O. Nicholson, J. D. Reid, and F. E. Strietelmeier	Computer System Having Dynamically Programmable Linear/Fairness Priority Arbitration Scheme
5,388,240	U. Olderdissen and M. Walz	DRAM Chip and Decoding Arrangement and Method for Cache Fills
5,388,244	G. A. Brent and C. A. Scalzi	Controls for Initial Diversion of Page-Frame Logical Content as Part of Dynamic Virtual-To-Real Translation of a Virtual Page Address
5,388,250	T. F. Lewis and S. P. Thompson	Apparatus and Method for Guaranteeing Strobe Separation Timing
5,388,253	M. Geneste, F. Jacob, and C. Poiraud	Processing System Having Device for Testing the Correct Execution of Instructions
5,388,254	J. F. Betz, A. S. Meritt, L. R. Perry, W. C. Shepard, and H. M. Yudenfriend	Method and Means for Limiting Duration of Input/Output (I/O) Requests
5,388,260	C. J. Monahan, M. L. Monohan, D. L. Willson, and L. D. Willson	Transparent Library Management
5,388,266	J. A. Frey, R. F. Hill, J. M. Nick, M. D. Swanson, and A. A. Verdibello, Jr.	Management of Data Objects Used Intain State Information for Shared Data at a Local Complex
5,388,635	P. A. Gruber and A. R. Zingher	Compliant Fluidic Coolant Hat
5,388,945	G. Garric and A. Lafond	Fully Automated and Computerized Conveyor Based Manufacturing Line Architectures Adapted to Pressurized Sealable Transportable Containers
5,388,993	F. A. McKiel, G. M. Trowbridge, C. J. Cavendish, and R. J. Catino	Method of and System for Demonstrating A Computer Program
5,389,559	C.-M. Hsieh, L. L. Hsu, T. Mii, S. Ogura, and J. F. Shepard	Method of Forming Integrated Interconnect for Very High Density DRAMs
5,389,562	S. N. Mohammad	Double Heterojunction Bipolar Transistor and the Method of Manufacture Therefor
5,389,567	A. Acovic, C.-H. Hsu, M. R. Wordeman, and B. S. Wu	Method of Forming a Non-Volatile DRAM Cell
5,389,803	S. N. Mohammad	High-Gain Si/SiGe MIS Heterojunction Bipolar Transistors
5,389,814	K. V. Srikrishnan, J. F. White, and J.-M. Yang	Electrically Blowable Fuse Structure for Organic Insulators
5,389,832	D. W. Boerstler, E. B. Eichelberger, G. T. Hendrickson, and C. B. Winn	Capacitively Cross-Coupled DCS Emitter-Follower Output Stage

5,389,836	A. R. Bertolet, A. M. Chu, W. R. Griffin, J. G. Petrovick, Jr., and L. Wissel	Branch Isolation Circuit for Cascode Voltage Switch Logic
5,389,838	G. Orenge	Device and Method of Managing Asynchronous Events in a Finite State Machine
5,389,858	G. O. Langner and P. F. Petric	Variable Axis Stigmator
5,389,860	M. Kobayashi, R. Kataoka, and A. Funakoshi	Focus Control Circuit for CRT
5,389,951	R. G. Baker	Liquid Crystal Display Integrated with Hall Effect Pointing Device
5,390,035	J. M. Kasson and W. E. Plouffe, Jr.	Method and Means for Tetrahedron/Octahedron Packing and Tetrahedron Extraction for Function Approximation
5,390,075	J. J. English, R. D. Loy, O. A. Mantilla, W. Melnychenko, and R. J. Stockholm	Electronic Apparatus with Flexible Cable Interconnect Having Independently Movable Member Supported on Tab Portions
5,390,082	A. W. Chase and J. W. Wilson	Chip Carrier with Protective Coating for Circuitized Surface
5,390,234	A. Bar-Noy and I. Kessler	Dynamic Tracking of Mobile Stations in Wireless Networks
5,390,260	J. D. Bezek	Method and Apparatus for On-Line, Real Time Recognition of Stroked Hand-Drawn Characters
5,390,289	B. J. Beitel, R. D. Gordon, and J. B. Witherspoon III	Anti-Alias Font Generation
5,390,295	C. L. Bates and J. M. Ryan	Method and Apparatus for Proportionally Displaying Windows on a Computer Display Screen
5,390,312	K. A. Chiarot, R. M. Dinkjian, and T. J. Schmitt	Access Look-Aside Facility
5,390,315	G. O. Blandy and C. M. Rossi	Allocation of Uniform Contiguous Blocks of DASD Storage by Maintaining Both a Bit and a Bit Map Record of Available Storage
5,390,316	L. Cramer, S. A. Fagen, J. T. Gates, Jr., J. K. Johnson, J. P. S. Kong, R. Mohan, and C. P. Vignola	Multicomputer Complex Having a Distributed Shared Memory System for Providing a Single System View From Multiple Consoles
5,390,328	J. A. Frey, A. A. Helffrich, J. M. Nick, and M. D. Swanson	Data Processing System and Method for Providing Notification in a Central Processor of State Changes for Shared Data Structure on External Storage
5,390,334	C. G. Harrison	Workstation Power Management by Page Placement Control
5,390,359	F. J. Damerau	Storing and Retrieving Records in a Computer System
5,390,785	G. Garric and A. Lafond	Pressurized Sealable Transportable Containers for Storing a Semiconductor Wafer in a Protective Gaseous Environment
5,391,036	T. L. Bauer, W. A. Cavaliere, D. K. Green, J. P. Karidis, and D. C. Linnell	Magnetic Transfer Device
5,391,464	A. Afzali-Ardankani, P. J. Brock, D. J. Dawson, and J. D. Gelorme	Thioxanthone Sensitizer for Radiation Sensitive Polyimides
5,391,510	L. L. Hsu, G. S. Mathad, and R. V. Joshi	Formation of Self-Aligned Metal Gate FETs Using a Benign Removable Gate Material During High Temperature Steps
5,391,514	T. P. Gall and A. P. Ingraham	Low Temperature Ternary C4 Flip Chip Bonding Method
5,391,911	K. D. Beyer and A. S. Yapsir	Reach-Through Isolation Silicon-on-Insulator Device
5,391,917	R. J. Gilmour and G. Schrottke	Multiprocessor Module Packaging
5,391,947	J. O. Groves, Jr., J. J. Hurd, and S. F. Newton	Voltage Ratio to Current Circuit
5,392,033	P. W. Oman and A. J. Rindos III	Priority Generator for Providing Controllable Guaranteed Fairness in Accessing a Shared Bus
5,392,072	A. A. Rodriguez, S. M. Hancock, and M. A. Pietras	Hybrid Video Compression System and Method Capable of Software-Only Decompression in Selected Multimedia Systems
5,392,116	G. Makosch	Interferometric Phase Measurement
5,392,118	H. K. Wickramasinghe	Method for Measuring a Trench Depth Parameter of a Material
5,392,124	S. G. Barbee, T. F. Heinz, U. Höfer, L. Li, and V. J. Silvestri	Method and Apparatus for Real-Time, In-Situ Endpoint Detection and Closed Loop Etch Process Control
5,392,169	B. E. Argyle, A. P. Praino, M. E. Re, R. Schäfer, S. Takayama, and P. L. Trouilloud	Electrical Means to Diminish Read-Back Signal Waveform Distortion in Recording Heads
5,392,177	T. J. Chainer, E. I. Cooper, S. C. Hegde, and C. Narayan	Sealed DASD Having Humidity Control and Method of Making Same
5,392,223	J. C. Caci	Audio/Video Communications Processor
5,392,224	A. R. Badia, D. P. Pagnani, and E. D. Shockley	Midi Through Port Hardware Emulator
5,392,241	E. Butler, R. A. Sasaki, R. Tamlyn, and E. P. Thoma	Semiconductor Memory Circuit with Block Overwrite
5,392,262	B. I. Finkelstein, A. A. Gaudet, T. W. McDaniel, H. A. Rosen, K. A. Rubin, and T. C. Strand	Two-Sided Optical Disks Having Recording Layers Disposed Close to Outer Surfaces and Spaced Axially Apart a Greater Distance and Devices for Using the Disks
5,392,294	L. J. Bosch and R. K. Smith	Diagnostic Tool and Method for Locating the Origin of Parasitic Bit Faults in a Memory Array

5,392,360	F. G. Weindelmayer and M. A. Sipe	Method and Apparatus for Inspection of Matched Substrate Heatsink and Hat Assemblies
5,392,385	C. J. Evangelisti, L. Lumelsky, and M. J. Pavicic	Parallel Rendering of Smoothly Shaped Color Triangles with Anti-Aliased Edges for a Three Dimensional Color Display
5,392,387	G. P. Fitzpatrick and T. R. Haynes	Method and System for Enhanced Data Access Efficiency in an Electronic Book
5,392,388	K. P. Gibson	Method and System for Viewing Graphic Images in a Data Processing System
5,392,396	A. G. MacInnis	Method and Apparatus for Gradually Degrading Video Data
5,392,397	D. A. Elko, J. A. Frey, A. A. Helffrich, J. M. Nick, and M. D. Swanson	Command Execution System for Using First and Second Commands to Reserve and Store Second Command Related Status Information in Memory Portion Respectively
5,392,398	C. Meyer	System and Method for Deallocation a First Message Independently of the Deallocation of a Second Message in a Bundle of Messages
5,392,400	D. B. Berkowitz, M. C. Hao, H. C. Lieu, and F. D. Snow	Collaborative Computing System Using Pseudo Server Process to Allow Input from Different Server Processes Individually and Sequence Number Map for Maintaining Received Data Sequence
5,392,401	G. Barucchi, J. Calvignac, D. Orsatti, and A. Tracol	Switching System for Simultaneously Transferring Data Between Data Processing Units
5,392,410	L. Liu	History Table for Prediction of Virtual Address Translation for Cache Access
5,392,415	P. R. Badovinatz, L. Brenner, J. R. Hedglin, B. P. Lubart, P. O'Rourke, and A. Pruscino	System for Grouping Non-Contiguous Pages Belonging to a Storage Object for Page Out
5,392,425	J. C. Elliot, K. R. Lynch, M. W. Sachs, and J. H. Sorg, Jr.	Channel-Initiated Retry and Unit Check for Peripheral Devices
5,392,430	M.-S. Chen, J. J. E. Turek, J. L. Wolf, and P. S. Yu	Hierarchical Scheduling Method for Processing Tasks Having Precedence Constraints on a Parallel Processing System
5,392,433	S. D. Hammersley, A. D. Smet, and P. M. Wottreng	Method and Apparatus for Intraprocess Locking of a Shared Resource in a Computer System
5,393,376	M.-M. Chen, K. Ju, and P.-K. Wang	Method of Making Pole Tip Structure for Thin Film Magnetic Heads
5,393,489	S. G. Gonya, R. C. Long, and R. N. Wild	High Temperature, Lead-Free, Tin Based Solder Composition
5,394,069	C. T. Danielson, G. T. Kishi, M. P. McInticosh, H. E. Mery, and S. M. Rockwell	Mechanical Brake Hold Circuit for an Electric Motor
5,394,121	T. M. Cipolla, P. W. Coteus, and W. R. Hardell, Jr.	Wiring Topology for Transfer of Electrical Signals
5,394,226	G. S. Beardsley and L. B. Schein	Method for Reducing High Quality Electrophotographic Images
5,394,280	A. Chliwnyj, D. C. Graves, S. M. Fry, P. R. Nylander-Hill, H. M. Torab, and W. A. Wright	Detecting a Servo Error on a Magnetic Tape and Identifying Extent and Location of the Detected Servo Error by Linked Control Marks
5,394,284	G. A. Benson, P. M. Herman, and R. W. Luoma	Direct Access Storage Device Having a Stable Ball Bearing Cage Design
5,394,294	S.-N. Mei, D. J. Schepis, and A. S. Yapsir	Self Protective Decoupling Capacitor Structure
5,394,299	R. C. Chu, M. J. Ellsworth, Jr., R. E. Simons, and D. T. Vader	Topology Matched Conduction Cooling Module
5,394,390	D. R. Stauffer, R. S. McMahon, and T. J. Eckenrode	FDDI Network Test Adapter History Store Circuit (HSC)
5,394,405	J. Savir	Universal Weight Generator
5,394,410	K.-C. Chen	Differentially Coded and Guard Pulse Position Modulation for Communication Networks
5,394,414	W. J. Kozlovsky and W. P. Risk	Laser System and Method Having a Nonlinear Crystal Resonator
5,394,433	D. F. Bantz, F. J. Bauchot, C.-C. Huang, F. P. Lanne, K. S. Natarajan, and M. M. Wetterwald	Frequency Hopping Pattern Assignment and Control in Multiple Autonomous Collocated Radio Networks
5,394,484	R. G. Casey, Y. Nakamura, and K. Toyokawa	Image Recognition Apparatus
5,394,487	M. E. Burger and H. H. Sun	Forms Recognition Management System and Method
5,394,519	W. K. Bodin	Data Processing Apparatus for High Resolution Display in Multiple Virtual DOS Applications
5,394,522	A. Sanchez-Frank	Selecting and Locating Graphical Icon Objects to Define and Configure the Workstations in Data Processing Networks
5,394,524	P. D. DiNicola, J. C. Kantz, O. M. Rahim, D. A. Rice, and E. M. Ruddick	Method and Apparatus for Processing Two Graphic Data Streams in Parallel
5,394,531	K. F. Smith	Dynamic Storage Allocation System for A. Prioritized Cache
5,394,534	J. E. Kulakowski and R. J. Means	Data Compression/Decompression and Storage of Compressed and Uncompressed Data on a Same Removable Data Storage Medium
5,394,539	D. E. Neuhard, J. M. Nick, and D. R. Sutherland	Method and Apparatus for Rapid Data Copying Using Reassigned Backing Pages

5,394,542	J. A. Frey, J. M. Nick, and M. D. Swanson	Clearing Data Objects Used to Maintain State Information for Shared Data at a Local Complex When at Least One Message Path to the Local Complex Cannot be Recovered
5,394,545	S. E. Emrick, J. E. DeWitt, and J. H. Summers	System for Determination and Display of Memory Used, Dedicated, and Shared by a Process at a Particular Time
5,394,547	J. A. Correnti, R. M. Pipitone, and M. W. Thomas	Data Processing System and Method Having Selectable Scheduler
5,394,549	B. Stringfellow and M. Mitchell	Task Spawning Responsive to Relational Database Conditions and Operations
5,394,554	D. A. Elko, J. F. Isenberg, Jr., A. S. Meritt, B. B. Moore, J. M. Nick, W. C. Shepard, D. H. Surman, and M. D. Swanson	Interdicting I/O and Messaging Operations from Sending Central Processing Complex to Other Central Processing Complexes and to I/O Device in Multi-System Complex
5,394,609	M. E. Ferguson, K. J. Guskie, L. S. Nguyen, J. D. Poole, S. L. Young, and S. Yu	Method and Apparatus for Manufacture of Printed Circuit Cards
5,394,675	K. Yonehara	Tab Tape, Method of Bonding Tab Tape and Tab Tape Package
5,395,038	S. A. F. Olson and B. Ventimiglia	High Placement Accuracy Wire Bonder for Joining Small Closely Spaced Wiring Features
5,395,198	T. P. Duffy, L. C. Hecht, M. P. Sulger, E. E. Thiele, M. V. Pierson, and L. E. Williams	Vacuum Loading Chuck and Fixture for Flexible Printed Circuit Panels
5,395,199	K. F. Day III and T. Makansi	Automated Storage Library with Horizontal Array of Storage Cells
5,395,261	D. H. Addison, J. M. Carnes, R. L. Cowardin, A. N. Gore III, R. C. Lynch, C. M. McCray, S. Narisawa, W. L. Talley, Y. Toyosato, and C. W. Turner	Serviceable Data Terminal Structure
5,395,643	J. Brown, K. Murray, J. McNee, and B. Robertson	Method of and Apparatus for Depositing Solder on a Printed Circuit Board
5,395,786	L. L. Hsu, S. Ogura, and J. F. Shepard	Method of Making a DRAM Cell with Trench Capacitor
5,396,130	R. L. Galbraith, C. J. Goetschel, R. A. Kertis, R. A. Philpott, R. A. Richetta, T. J. Schmerbeck, D. J. Schulte, and D. P. Swart	Method and Apparatus for Adaptive Chip Trim Adjustment
5,396,182	D. W. Boerstler, E. B. Eichelberger, G. T. Hendrickson, and Charles B. Winn	Low Signal Margin Detect Circuit
5,396,280	V. Caiozza, D. H. Canfield, T. C. Fellows, N. E. Rittenhouse, and P. J. Yablonsky	Analog Video Processing Apparatus and Method for Eliminating Background Levels in the Analog Signal
5,396,386	S. A. Bolasna and D. S. Chhabra	Roll Insensitive Air Bearing Slider
5,396,394	T. W. Gee	ISDN Device Protection Circuit
5,396,476	P. Asthana	Sensing Contamination Level in an Optical Storage Device
5,396,478	M. C. Krantz	Optical Data Storage System with Aberration Compensation for Reduced Servo Crosstalk
5,396,564	G. Fitzpatrick, W. J. Johnson, and R. S. Keller	Method of and Apparatus for Recognizing Predominate and Non-Predominate Color Code Characters for Optical Character Recognition
5,396,573	M. E. Ecker, L. Jacobowitz, and C. M. DeCusatis	Pluggable Connectors for Connecting Large Numbers of Electrical And/Or Optical Cables to a Module Through a Seal
5,396,593	M. Mori and Y. Morimoto	Data Processing Apparatus
5,396,597	W. K. Bodin, D. M. Hyde, T. P. Lay, J. Wilkinson, and S. Yee	System for Transferring Data Between Processors via Dual Buffers Within System Memory with First and Second Processors Accessing System Memory Directly and Indirectly
5,396,619	S. D. Walton	System and Method for Testing and Remapping Base Memory for Memory Diagnostics
5,396,622	P. K. Lee and Z. Y. Yehudai	Efficient Radix Sorting System Employing a Dynamic Branch Table
5,396,630	V. P. Banda, R. F. Selby, S. E. Snyder, and J. T. Tuatini	Method and System for Object Management Across Process Boundries in a Data Processing System
5,396,632	M. J. L. Gillet	Prolog Interrupt Processing
5,396,636	R. J. Gallagher, K. H. Hoppe, A. J. Perri, M. S. Styduhar, J. M. Taylor, and B. W. Weidle	Remote Power Control via Data Link
5,420,455	R. A. Gilmour, T. J. Hartswick, D. C. Thomas, R. R. Uttecht, and E. G. Walton	Array Fuse Damage Protection Devices and Fabrication Method
5,420,456	D. E. Galbi, W. H. Guthrie, O. Kiehl, J. A. Mandelman, and J. S. Watts	Zag Fuse for Reduced Blow-Current Application
5,420,467	W. V. Huott and T. G. McNamara	Programmable Delay Clock Chopper/Stretcher with Fast Recovery
5,420,484	A. J. Morrish	Deflection Apparatus for Raster Scanned CRT Displays