

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

France

294,533	C. Galand, M. Rosso, and P. Elie	Error Detection in Encoded Data by Bit Stealing
318,624	A. Dansky, A. Zuckerman, A. G. Aipperspach, B. Pluchino, G. Boudon, I. Ong, N. V. Phan, P. Mollier, and S. J. Zier	Multi-emitter BICMOS Circuit
332,793	A. Pantani and C. L. Lasmayoux	Machine Pluggable Electronic Package Having a Mechanical Interlock
346,556	Y. Mourier	Mail Follow-up
365,733	C. Akrouit, P. Coppens, P. Y. Urena, and Y. Gautier	Programmable Logic Fuse Based on a 6-Device SRAM Cell for Logic Arrays

Germany

4,233,137.4	P. Sander and R. Steinmetz	Method and Apparatus for Controlling a Camera
9,308,049.2	A. Schaal, D. Sonnentag, and G. Klepser	Contact Layout for Chip Cards
68,910,825.7	J. Sykora	Method of and Apparatus for Drying Articles
3,886,203,408	D. W. Bock, H. Schulze-Schoelling, P. Rudolph, and W. Kumpf	Processor-Processor Synchronization
3,886,529,708	D. W. Bock, H. Schulze-Schoelling, P. Mannherz, and P. Rudolph	Arrangement in Data Processing System for System Initialization and Reset
3,886,754,008	D. Kern, J. Greschner, J. W. Bartha, R. R. Stoeck, T. Bayer, and V. Mattern	Apparatus for Plasma or Reactive Ion Etching and Method of Etching Substrates Having a Low Thermal Conductivity

Japan

1,798,288	H. Iida	Method and System for Displaying Images in Adjacent Display Areas
1,798,309	H. Takahashi and N. Itoh	A Selection Method of Candidate Words for Automatic Spell

United Kingdom

283,927	P. W. Norris	Display Adapter
310,743	P. A. Gardner	Current-Controlling Circuit
365,728	G. P. Loten	Multiprocessing Computer System
450,206	A. C. Lowe	Disk File
452,575	D. J. Gray, F. E. Castle, I. Gollidge, and R. Oregan	Modular Electrical Apparatus
2,032,576	M. D. Oates	MM-1 Multimedia Audio Accessory (9529)
2,033,150	T. J. Rodd	Flat Panel Display
2,033,562	G. Grant and M. D. Oates	Visual Display Unit

United States

- 5,229,257 J. E. Cronin, C. W. Kaanta, P.-I. P. Lee, R. A. Previti-Kelly, J. G. Ryan, and J. H. Yoon
- 5,230,632 J. G. Baumberger, J. J. Kershaw, and J. R. Petrozello
- 5,230,782 F. J. Downes, Jr., O. A. Moreno, C. M. Reidsema, and J. E. Varsik
- 5,231,299 T. H. Ning and C.-H. Hsu
- 5,231,484 C. A. Gonzales and E. Viscito
- 5,231,581 D. Garneau and W.-H. Sears
- 5,231,625 K. Hokozone, T. Shinohara, A. Tokizono, and H. Ueno
- 5,231,669 C. Galand and M. Rosso
- 5,231,751 K. G. Sachdev, B. M. J. Kellner, K. M. McGuire, and P. J. Sorce
- 5,232,140 F. A. Gregory, J. T. Holton, J. Maliwacki, and L. A. Stone
- 5,232,548 S. G. Ehrenberg, L. W. Herron, E. F. Miersch, J. Park, and J. L. Poetzinger
- 5,232,566 D. A. Edmonson, K. E. Johnson, J. J. Mayerle, and A. C. Wall
- 5,232,866 K. D. Beyer, V. J. Silvestri, and A. S. Yapsir
- 5,233,175 M. R. Latta and T. S. Gardner
- 5,233,221 M. F. Bregman, R. R. Horton, A. P. Lanzetta, I. C. Noyan, M. J. Palmer, and H.-M. Tong
- 5,233,263 J. E. Cronin, K. E. Morrett, M. D. Potter, and T. D. Sullivan
- 5,233,302 B. A. Beatty and G. D. Wisgo
- 5,233,327 T. A. Bartush and J. J. Curtin
- 5,233,331 L. D. Comerford and L. I. Levy
- 5,233,453 K. N. Sivarajan, D. G. Steinberg, and F. F. Tong
- 5,233,482 R. L. Galbraith, G. J. Kerwin, and J. M. Poss
- 5,233,487 T. C. Christensen, M. W. Rooke, and M. L. Workman
- 5,233,509 R. Ghotbi
- 5,233,584 J. E. Kulakowski, J. A. McDowell, and R. J. Means
- 5,233,585 J. J. Dente and A. A. Fennema
- 5,233,604 H. Ahmadi, J. S. Chen, C.-S. Chow, R. Guérin, L. Gün, A. M. Lee, and T. E. Tedijanto
- 5,233,611 G. Triantafyllos and W. H. Shield
- 5,233,614 S. Singh
- 5,233,624 M. J. LaPlante, H. A. Bender III, W. D. Carbaugh, Jr., D. C. Long, and C. D. Setzer
- 5,233,681 L. R. Bahl, P. V. De Souza, P. S. Gopalakrishnan, and M. A. Picheny
- 5,233,702 P. G. Emma, J. W. Knight, J. H. Pomerene, T. R. Puzak, and R. N. Rechtschaffen
- 5,233,895 A. F. Coneski and M. Waian
- 5,234,535 K. D. Beyer, L. L. Hsu, V. J. Silvestri, and A. S. Yapsir
- 5,234,846 S.-F. S. Chu, K.-M. Kim, M. Shaw-Ning, V. R. Nastasi, and S. Ratanaphanyarat
- 5,234,868 W. J. Cote
- 5,235,154 L. Economikos
- 5,235,206 B. H. Desilets, C.-M. Hsieh, and L. L. Hsu
- Process for Forming Multi-level Coplanar Conductor/Insulator Films Employing Photosensitive Polyimide Polymer Compositions
- Dual Element Electrical Contact and Connector Assembly Utilizing Same
- Electrolytic Process for Reducing the Organic Content of an Aqueous Composition and Apparatus Therefore
- Structure and Fabrication Method for EEPROM Memory Cell with Selective Channel Implants
- Motion Video Compression System with Adaptive Bit Allocation and Quantization
- Method for Culturally Predictable Key Sort within a National Language Support (NLS) Data Processing System
- Optical Disk Drive Wherein a Preset Reading Laser Power is Used to Generate a Desired White Laser Power
- Low Bit Rate Voice Coding Method and Device
- Process for Thin Film Interconnect
- Mechanism for Positioning Tractors
- Discrete Fabrication of Multi-layer Thin Film, Wiring Structures
- Underlayer Doping in Thin Film Magnetic Recording Media
- Isolated Films Using an Air Dielectric
- Laser Power Control Independent of Beamsplitter Transmissivity
- Electronic Substrate Multiple Location Conductor Attachment Technology
- Lateral Field Emission Devices
- Removable and Reversible Display Device for Portable Computer
- Active Resistor Trimming by Differential Annealing
- Inking Buffer for Flat-Panel Display Controllers
- Space-Division Switched Waveguide Array Filter and Method Using Same
- Thermal Asperity Compensation for PRML Data Detection
- Functional Measurement of Data Head Misregistration
- Switch-Mode AC-to-DC Converter
- Optical Disk Device Using High and Low Data-sensing Criteria Plus Device Recalibration for Error Control
- Acquiring Focus in an Optical Disk System Using a Time and Shape Modified Focus Error Signal
- Methods and Apparatus for Optimum Path Selection in Packet Transmission Networks
- Automated Function Testing of Application Programs
- Fault Mapping Apparatus for Memory
- Method and Apparatus for an Increased Output for a Pumped Laser Using a Moving Aperture
- Context-Dependent Speech Recognizer Using Estimated Next Word Context
- Cache Miss Facility with Stored Sequences for Data Fetching
- Magnetic Plate Punch Actuator
- Method of Producing a Thin Silicon-on-Insulator Layer
- Method of Making Bipolar Transistor with Reduced Topography
- Method for Determining Planarization Endpoint During Chemical-Mechanical Polishing
- Laser Removal of Metal Interconnects
- Vertical Bipolar Transistor with Recessed Epitaxially Grown Intrinsic Base Region

5,235,216	R. K. Cook and B. H. Yun	Bipolar Transistor Using Emitter-Base Reverse Bias Carrier Generation
5,235,433	C. J. Clarkson and J. E. St. Lawrence, Jr.	System and Method for Automatically Indexing Facsimile Transmissions Received in a Computerized Image Management System
5,235,448	H. Suzuki and H. Yamaguchi	Liquid Crystal Display Having Proportional TFT Channel Width
5,235,521	C. L. Johnson, R. F. Lembach, B. G. Rudolph, and R. R. Williams	Reducing Clock Skew in Large-Scale Integrated Circuits
5,235,574	H. E. Aviles, E. R. Childers, J. L. Overacker, and R. P. Stahl	Optical Disk Drive Relative Position Sensor
5,235,585	J. E. Bish, D. W. Fok, B. G. Goodman, and A. A. Mojica	Reassigning Defective Sectors on a Disk
5,235,592	T. D. Cheng, P. A. Franaszek, C. J. Georgiou, G. M. Nordstrom, T. K. Philips, M. W. Sachs, A. M. Varma, and T. M. Walker	Dynamic Switch Protocols on a Shared Medium Network
5,235,597	H. A. Himwich, J. R. Mason, T. P. McSweeney, R. A. Vrabel, and L. S. Wolhar	Synchronizing Asynchronous Protocol Interactions between Peer Layers in Different Nodes of a Layered Communication Network
5,235,602	P. J. Klim	Synchronous/Asynchronous I/O Channel Check and Parity Check Detector
5,235,626	A. L. Flamholz, R. P. Rippstein, Y. Vladimirovsky, and C. A. Wasik	Segmented Mask and Exposure System for X-ray Lithography
5,235,639	D. Chevalier, J. Gallian, M. Geneste, P. Massi, and G. Platel	Programmable Communication Controller Line Adapter and Method for Programming Line Adapter
5,235,654	G. B. Anderson, J. H. Bamford, T. S. Betts, V. M. Carras, M. C. Concagh, M. E. Daley, J. M. Hawkins, P. M. Jakab, L. B. Knecht, F. W. Kratochvil, S.-Y. Liu, T. L. Paulson, D. Rajagopal, and M. K. Tiwari	Advanced Data Capture Architecture Data Processing System and Method for Scanned Images of Document Forms
5,235,673	S. M. Austvold, J. P. Bigus, J. D. Henckel, and P. A. Hospers	Enhanced Neural Network Shell for Application Programs
5,235,690	B. C. Beardsley, M. T. Benhase, G. A. Spear, and W. D. Williams	Method for Operating a Cached Peripheral Data Storage Subsystem Including a Step of Subsetting the Data Transfer into Subsets of Data Records
5,235,692	K. J. Ayres, B. C. Beardsley, K. A. Bello, M. T. Benhase, D. M. Nordahl, and R. E. Wilsey	Disk Rotational Position Controls for Channel Operations in a Cached Peripheral Subsystem
5,235,695	J. W. Pence	Apparatus for Efficient Utilization of Removable Data Recording Media
5,235,700	H. Alaiwan, J. Calvignac, J.-L. Combes, A. Pauporte, C. Basso, and F. Kermarec	Checkpointing Mechanism for Fault-Tolerant Systems
5,235,740	E. L. B. Kroeker, T. R. Kroeker, and R. N. Sage	Press with Pin Detection for Interference Connectors
5,237,224	F. A. DeLisle and A. M. Jacoutot	Variable Self-correcting Digital Delay Circuit
5,237,262	D. J. Ashley, M. K. DeMoor, and P. W. Graf	Temperature Compensated Circuit for Controlling Load Current
5,237,269	B. R. Aimi, R. F. Antonucci, F. D. Dimaria, and H. R. Torabi	Connections between Circuit Chips and a Temporary Carrier for Use in Burn-In Tests
5,237,312	A. A. Akiyama, L. J. H. Busboom, and W. J. Maitland, Jr.	Display with Enhanced Scrolling Capabilities
5,237,466	T. W. Glaser, R. Greenberg, and H. H. Ottesen	Method and Apparatus for Programmably Controlling Spindle Synchronization and Phase among Disk Drives in a Storage Subsystem
5,237,468	J. F. Ellis	Camera and Gripper Assembly for an Automated Storage Library
5,237,493	U. T. Durig and B. Michel	Current-to-Voltage Converter with Low Noise, Wide Bandwidth and High Dynamic Range
5,237,502	B. F. White, I. P. Bretan, and M. A. Sanamrad	Method and Apparatus for Paraphrasing Information Contained in Logical Forms
5,237,503	C. Bedecarrax, P. Parisot, and I. Warnesson	Method and System for Automatically Disambiguating the Synonymic Links in a Dictionary for a Natural Language Processing System
5,237,514	J. J. Curtin	Minimizing Path Delay in a Machine by Compensation of Timing through Selective Placement and Partitioning
5,237,572	M. Badaoui, J. Calvignac, G. Carle, C. Garcia, and P. Vachee	Active Remote Module for the Attachment of User Equipments to a Communication Processing Unit
5,237,616	D. G. Abraham and S. G. Aden	Secure Computer System Having Privileged and Unprivileged Memories
5,237,626	D. C. Forslund and H.-J. Muenster	Universal Image Processing Module
5,237,654	F. W. Shackelford and R. E. Moore	Hierarchical Inter-Panel Process Flow Control

5,237,668	G. O. Blandy, D. B. Emmes, R. F. Hill, D. B. Lindquist, K. E. Plambeck, C. A. Scalzi, and R. J. Schmalz	Process Using Virtual Addressing in a Non-privileged Instruction to Control the Copying of a Page of Data in or between Multiple Media
5,237,676	R. K. Arimilli, S. Dhawan, G. A. Lerom, J. O. Nicholson, and D. W. Siegel	High Speed Data Transfer System Which Adjusts Data Transfer Speed in Response to Indicated Transfer Speed Capability of Connected Device
5,237,679	D. S. Wang and M. L. Williams	Method and System for Automatic Deletion of a Folder Having Temporary Document Relationships within a Data Processing System
5,237,682	E. J. Bendert and R. B. Bennett	File Management System for a Computer
5,237,684	S. E. Record, A. M. Shepherd, and S. S. Shultz	Customized and Versatile Event Monitor within Event Management Services of a Computer System
5,237,685	E. V. Toney	Linear Recurrence Dispersal Structure and Method for Parallel Processors
5,237,688	N. Clavert, J. S. Effle, D. L. Johnston, J. L. Naylor, H. M. Olson-Williams, R. H. Satin, D. L. Shaffer, and G. A. Turk	Software Packaging Structure Having Hierarchical Replaceable Units
5,237,690	R. Bealkowski, R. Davila, and K. M. Zyvoloski	System for Testing Adaptor Card Upon Power Up and Having Disablement, Enablement, and Reconfiguration Options
5,237,743	R. A. Busacco, F. W. Chapin, D. W. Dranchak, J. A. Molla, G. J. Saxenmeyer, Jr., and R. D. Topa	Method of Forming a Conductive End Portion on a Flexible Circuit Member
5,238,176	H. Nishimura	Method and Apparatus for Forming Bump
5,238,503	R. B. Phenix and W. T. Tandy	Device for Decontaminating a Semiconductor Wafer Container
5,238,773	E. D. Babich, D. G. Flagello, M. Hatzakis, J. R. Paraszczak, J. M. Shaw, and D. F. Witman	Alkaline Developable Photoresist Composition Containing Radiation Sensitive Organosilicon Compound with Quinone Diazide Terminal Groups
5,239,262	M. Grutzner and C. W. Starke	Integrated Circuit Chip with Built-in Self-test for Logic Fault Detection
5,239,287	I. Siio and S. Ishikawa	Table Editor
5,239,289	F. D. Ferraiolo, D. P. Pagnani, and P. R. Tomaszewski	Tunable Inductor
5,239,443	A. J. Fahey, G. P. Messina, J. B. Pavelka, and R. A. Sherif	Blind Hole Cold Plate Cooling System
5,239,447	P. W. Cotues, P. A. Moskowitz, P. Murphy, M. B. Ritter, and G. F. Walker	Stepped Electronic Device Package
5,239,448	C. T. Perkins and G. Schrottke	Formulation of Multichip Modules
5,239,481	T. W. Brooks, P. T. Gutwin, C. G. Melrose, F. A. Nemec, Jr., and J. J. Tomczak	Method for Measuring Pulse Distortion
5,239,487	S. A. Horejsi, J. R. Landon, G. C. Marquardt, and J. C. Stokes	Computer Integrated Manufacturing Rework Apparatus and Method
5,239,489	G. F. Russell	Pen Position and Tilt Estimators for a Digitizer Tablet
5,239,504	M. J. Brady, S. S. Dana, and R. J. Gambino	Magnetostriuctive/Electrostrictive Thin Film Memory
5,239,506	W. R. Dachtera, L. C. Ritchie, and A. D. Tuminaro	Latch and Data Out Driver for Memory Arrays
5,239,580	A. Bruno, J. Fieschi, J. Martin, and R. Vautier	Modem-Telephone Coupler
5,239,617	D. L. Gardner, D. M. Lamberti, and J. M. Prager	Method and Apparatus Providing an Intelligent Help Explanation Paradigm Paralleling Computer User Activity
5,239,627	J. L. Beck, J. R. Booth, J. C. Buchanan, M. E. Claffey-Cohen, C. P. Cole, T. J. Louie, A. F. Neel II, L. M. Oliver, J. P. Ward, and J. F. Webb	Bi-Directional Parallel Printer Interface
5,239,631	B. F. Boury, T. J. Lohman, and L. D. Nguyen	CPU Bus Allocation Control
5,239,640	J. W. Froemke and D. A. Styczinski	Data Storage System and Method Including Data and Checksum Write Staging Storage
5,239,643	M. L. Blount, S. P. Morgan, and K. A. V. Rader	Method for Reducing Disk I/O Accesses in a Multi-processor Clustered Type Data Processing System
5,239,647	M. J. Anglin, G. J. Tevis, and D. P. Warren	Data Storage Hierarchy with Shared Storage Level
5,239,649	G. E. McBride, J. W. Pence, and D. G. Van Hise	Channel Path Load Balancing Through Selection of Storage Volumes to be Processed, for Long Running Applications
5,239,650	J. C. Hartung, C. J. Monahan, J. L. Thrall, D. L. Willson, and D. J. Winarski	Preemptive Demount in an Automated Storage Library
5,239,673	K. S. Natarajan	Scheduling Methods for Efficient Frequency Reuse in a Multi-cell Wireless Network Served by a Wired Local Area Network

5,240,166	R. E. Fontana, Jr., L. H. Lane, and C. E. Yeack-Scranton	Device for Thermally Enhanced Ultrasonic Bonding with Localized Heat Pulses
5,240,738	M. G. Quirk	Method of Applying Solder to a Flexible Circuit
5,240,812	W. E. Conley, R. W. Kwong, R. J. Kvittek, R. N. Lang, C. F. Lyons, S. S. Miura, W. M. Moreau, and H. S. Sachdev	Top Coat for Acid Catalyzed Resists
5,240,878	J. A. Fitzsimmons, J. Havas, M. J. Lawson, E. J. Leonard, and B. N. Rhoads	Method for Forming Patterned Films on a Substrate
5,240,906	J. G. Bednorz, J. D. Mannhart, C. A. Mueller, and D. Schlom	Enhanced Superconducting Field-Effect Transistor
5,241,040	J. J. Cuomo, J. D. Gerlorme, M. Hatzakis, Jr., J. M. Shaw, and S. J. Whitehair	Microwave Processing
5,241,139	K. P. Gungl and J. L. Levine	Method and Apparatus for Determining the Position of a Member Contacting a Touch Screen
5,241,185	A. Z. Meiri and D. Ramm	Proximity Correction Method for E-Beam Lithography
5,241,223	D. C. Banker, J. A. Dorler, P. D. Hendricks, F. M. Masci, and S. J. Tytran	NOR ₂ Circuit/Bias Generator Combination Compatible with CSEF Circuits
5,241,245	M. S. Barnes, D. K. Coultas, J. G. Forster, and J. H. Keller	Optimized Helical Resonator for Plasma Processing
5,241,309	R. D. Cideciyan and F. B. Dolivo	Data Coding for Fast Start-up of PRML Receivers
5,241,423	G. L. Chiu, R. N. Singh, and J. S. Wilczynski	High Resolution Reduction Catadioptric Relay Lens
5,241,433	T. B. Anderson, M. E. Bofferding, M. R. Green, M. D. Hagen, J. M. Humel, W. J. Hunt, L. P. Segar, J. J. Stephenson, and M. C. Stich	Disk Drive Servo Control
5,241,454	J. G. Ameen, J. Funari, and D. W. Sissenstein, Jr.	Multilayered Flexible Circuit Package
5,241,493	T. V. Chu, F. O. Karim, and C. H. Olson	Floating Point Arithmetic Unit with Size Efficient Pipelined Multiply-Add Architecture
5,241,500	J. E. Barth, Jr. and H. L. Kalter	Method for Setting Test Voltages in a Flash Write Mode
5,241,540	K. B. Ocheltree	Reverse Ordered Control Information Transmission
5,241,541	J. K. Farrell, J. S. Gordon, D. C. Kuhl, T. V. Lee, and T. E. Parker	Burst Time Division Multiplex Interface for Integrated Data Link Controller
5,241,542	K. S. Natarajan and C.-C. Huang	Battery Efficient Operation of Scheduled Access Protocol
5,241,614	M. E. Ecker and L. Jacobowitz	Apparatus and a Method for an Optical Fiber Interface
5,241,624	R. J. Torres	Method for Determining a User Selected Group of Data Objects for the Propagation of Attribute Values
5,241,648	J. M. Cheng, D. J. Haderle, R. W. Hedges, B. R. Iyer, C. Mohan, and Y. Wang	Hybrid Technique for Joining Tables
5,241,656	L. K. Loucks and R. O. Simpson	Depth Buffer Clipping for Window Management
5,241,661	I. A. Concilio, J. A. Hawthorne, C. A. Heath, J. F. Lenta, and L. D. Ngyuen	DMA Access Arbitration Device in Which CPU Can Arbitrate on Behalf of Attachment Having No Arbiter
5,241,664	N. Ohba and S. Shimizu	Multiprocessor System
5,241,668	L. E. Eastridge, R. F. Kern, W. F. Micka, C. W. Mikkelsen, and J. M. Ratliff	Method and System for Automated Termination and Resumption in a Time Zero Backup Copy Process
5,241,669	O. Cohn, M. H. Hartung, W. F. Micka, J. N. McCauley, Jr., C. W. Mikkelsen, and K. M. Nagin	Method and System for Sidefile Status Polling in a Time Zero Backup Copy Process
5,241,670	L. E. Eastridge, R. F. Kern, C. W. Mikkelsen, and J. M. Ratliff	Method and System for Automated Backup Copy Ordering in a Time Zero Copy Session
5,241,682	D. B. Bryant, M. A. Cossack, D. J. Frett, H. A. Himwich, L. T. Huynh, and J. E. McGinn	Border Node Having Routing and Functional Capability in a First Network and Only Local Address Capability in a Second Network
5,242,524	M. A. Leach, B. J. Machesney, and E. J. Nowak	Device for Detecting an End Point in Polishing Operations
5,242,541	T. Bayer, J. Greschner, Y. Martin, H. Weiss, H. K. Wickramasinghe, and O. Wolter	Method of Producing Ultrafine Silicon Tips for the AFM/STM Profilometry
5,242,551	E. R. Frank, T. R. O'Toole, and A. Viehbeck	Electron Induced Transformation of an Isoimide to an N-imide and Uses Thereof
5,242,569	S. K. Kang, M. J. Palmer, T. C. Reiley, and R. D. Topa	Thermocompression Bonding in Integrated Circuit Packaging

5,242,713	A. Viehbeck, S. L. Buchwalter, W. A. Donson, J. J. Glenning, M. J. Goldberg, K. R. Grebe, C. A. Kovac, L. C. Matthew, W. P. Pawlowski, M. J. Schadt, M. R. Scheuermann, and S. L. Tisdale	Method for Conditioning an Organic Polymeric Material
5,242,784	M. Y. Chen and K. A. Rubin	System and Method for Optical Phase Change Recording
5,242,859	J. F. Degelormo, P. M. Fahey, T. N. Jackson, C. M. Ransom, and D. K. Sadana	Highly Doped Semiconductor Material and Method of Fabrication Thereof
5,243,133	S. R. Engle, S. P. Moore, and M. K. Saraiya	Ceramic Chip Carrier with Lead Frame or Edge Clip
5,243,140	H. S. Bhatia, M. J. Interrante, S. D. Kadakia, S. D. Malaviya, M. H. McLeod, S. K. Ray, and H. I. Stoller	Direct Distribution Repair and Engineering Change System
5,243,141	K. Yonehara	Tab Tape, Method of Bonding Tab Tape and Tab Tape Package
5,243,149	L. D. Comerford, H. A. Ellozy, F. Jelinek, S. E. Levy, and D. Nahamoo	Method and Apparatus for Improving the Paper Interface to Computing Systems
5,243,222	J. M. E. Harper, K. L. Holloway, and T. Y. Kwok	Copper Alloy Metallurgies for VLSI Interconnection Structures
5,243,302	W. O. Camp, Jr., D. E. Del Nero, C. N. Herbert, and J. A. Marozas	Voltage Controlled Oscillator with Correction of Tuning Curve Non-linearities
5,243,519	G. P. Andrews, W. C. Rapp, P. C. Schloss, D. D. Schulz, and S. Shao	Method and System for Language Translation Within an Interactive Software Application
5,243,599	S. B. Barrett and C. R. Ogilvie	Tree-Type Multiplexers and Methods for Configuring the Same
5,243,688	G. G. Pechanek, S. Vassiliadis, and J. G. Delgado-Frias	Virtual Neurocomputer Architectures for Neural Networks
5,244,143	T. G. Ference, P. A. Gruber, B. Hernandez, M. J. Palmer, and A. R. Zingher	Apparatus and Method for Injection Molding Solder and Applications Thereof
5,244,378	A. Bross and T. Walsh	Apparatus for Dynamic Gating of Polymers for Producing Molded Articles with Isotropic Properties
5,244,402	H. Pasterchick, Jr. and A. C. Ruocchio	Socket for Multiple Types of Telephone Plugs
5,244,730	T. N. Nguyen, G. S. Oehrlein, and Z. A. Weinberg	Plasma Deposition of Fluorocarbon
5,244,833	P. Gansauge, V. Kreuter, and H. Schettler	Method for Manufacturing an Integrated Circuit Chip Bump
5,245,136	D. A. Chance, D. B. Goland, and H.-M. Tong	Electrode Using a Polymer Layer and a Photoresist Layer
5,245,206	J. O. Chu, L. L. Hsu, T. Mii, J. F. Shepard, S. R. Stiffler, M. J. Tejwani, and E. J. Vishnesky	Hermetic Package for an Electronic Device
5,245,225	D. C. Banker, J. A. Dorler, and F. M. Masci	Capacitors with Roughened Single Crystal Plates
5,245,261	D. J. Ashley and M. K. DeMoor	High Performance Bifet Complementary Emitter Follower Logic Circuit
5,245,322	J. M. Dinwiddie, Jr., B. J. Freeman, G. A. Suarez, and B. J. Wilkie	Temperature Compensated Overcurrent and Undercurrent Detector
5,245,326	W. N. Zalph	Bus Architecture for a Multimedia System
5,245,339	R. D. Cideciyan	Calibration Apparatus for Brightness Controls of Digitally Operated Liquid Crystal Display System
5,245,429	R. Virginio and R. Emilio	Flexible Encoding Method and Architecture for High Speed Data Transmission and Storage
5,245,485	E. C. Dunn, O. B. Jeppson, and S. B. Wilson	Selective Data Broadcasting Receiver Adapter Apparatus and Method for Personal Computers
5,245,508	J. V. Mizzi	Multiple Tape Thickness, Multiple Recording Format Tape Drive Systems
5,245,521	J. H. Spreen	Close Card Cooling Method
5,245,532	Y. P. Mourier	Suppression of Transformer Capacitive Current
5,245,605	Y. Ofek	Electronic Mail Follow-up System
5,245,608	G. A. Deaton, Jr., V. G. Horton, Y. J. Huchet, J. J. Lynch, C. R. Pond, R. J. Potok II, J. H. Ragsdale, A. J. Stagg, R. M. Teague, and C. E. Williford	Integration of Synchronous and Asynchronous Traffic on Rings
5,245,609	Y. Ofek and M. M. Yung	Logical Grouping of Layer Entities in a Layered Communication Architecture
5,245,615	A. R. Treu	Communication Network and a Method of Regulating the Transmission of Data Packets in a Communication Network
5,245,637	J. E. Gersbach and I. I. Novof	Diagnostic System and Interface for a Personal Computer
5,245,700	G. C. Fossum	Phase and Frequency Adjustable Digital Phase Lock Logic System
5,245,921	E. F. Helinski and L. T. Sehringer	Adjustment of Z-Buffer Values for Lines on the Surface of a Polygon
		Interposer Device for Impact Printers

5,246,745	T. H. Baum, P. B. Comita, J. R. Lankard, Sr., T. F. Redmond, T. A. Wassick, and R. L. Jackson	Laser-Induced Chemical Vapor Deposition of Thin-Film Conductors
5,246,884	M. A. Jaso, P. B. Jones, B. S. Meyerson, and V. V. Patel	CVD Diamond or Diamond-like Carbon for Chemical-Mechanical Polish Etch Stop
5,246,885	B. E. Braren, K. H. Brown, K. A. Perry, R. Srinivasan, and A. Sugerman	Deposition Method for High Aspect Ratio Features Using Photoablation
5,247,167	R. A. Bargerhuff, L. D. Dickson, J. Groot, M. E. Rabedeau, and J. M. Zavislan	Multiple Beam Power Monitoring System and Method with Radiation Detection and Focusing Means of Overlapping Beams
5,247,284	S. S. Fleming	Graphical Method of Inputting Time Values
5,247,424	W. S. Harris, M. A. Hutchinson, R. R. Konian, E. J. Ossolinski, and V. C. Vasile	Low Temperature Conduction Module with Gasket to Provide a Vacuum Seal and Electrical Connections
5,247,471	S. A. Hilker and G. H. Handlogten	Radix Aligner for Floating Point Addition and Subtraction
5,247,498	M. Takekoshi and L. D. Tipton	Optical Disk Drive Apparatus and a Seek Method Spiral Track
5,247,520	D. M. Geise, T. F. Pebbles, and R. K. Richter	Communications Architecture Interface
5,247,546	J.-C. Abbiate and L. Quenel	Method and Apparatus for Automatic Functional Speed Setting of a Data Circuit Terminating Equipment
5,247,597	A. Blacha, F. Gfeller, and P. Vettiger	Optical Fiber Alignment
5,247,614	S. T. Eagen, H. G. Kiel, N. A. Martel, Jr., W. C. Rapp, and S. M. Shao	Method and Apparatus for Distributed Processing of Display Panel Information
5,247,616	C. E. Berggren and F. T. Kozuh	Computer System Having Different Communications Facilities and Data Transfer Processes between Different Computers
5,247,628	G. F. Grohoski	Parallel Processor Instruction Dispatch Apparatus with Interrupt Handler
5,247,636	J. A. Minnick and W. J. Spina	Digital Processor Clock Circuit
5,247,645	J. H. Mirza and S. W. White	Dynamic Memory Mapper Which Supports Interleaving Across $2^N + 1$, 2^N , and $2^N - 1$ Number of Banks for Reducing Contention during Nonunit Stride Accesses
5,247,647	D. D. Brown, W. J. Morschhauser, R. F. Reinheimer, and M. D. Swanson	Detection of Deletion of Stored Data by Concurrently Executing Processes in a Multiprocessing Data Processing System
5,247,661	D. J. Hager and C. G. Rose	Method and Apparatus for Automated Document Distribution in a Data Processing System
5,247,663	L. J. H. Busboom, S. T. Eagen, H. G. Kiel, R. F. Romon, and D. G. Wenz	Method and Apparatus for Constructing Continued Entry Fields with Multiple Terminals
5,247,669	R. L. Abraham, R. E. Moore, W. L. Rich, F. W. Shackelford, J. R. Tiller, Jr., and R. S. Briggs, Jr.	Persistent Data Interface for an Object Oriented Programming System
5,247,671	J. T. Adkins, J. S. Pogorzelski, and J. H. Wilson	Scalable Schedules for Serial Communications Controller in Data Processing Systems
5,247,672	C. Mohan	Transaction Processing System and Method with Reduced Locking
5,247,675	J. A. Farrell, S. E. Record, and B. K. Wade	Preemptive and Non-preemptive Scheduling and Execution of Program Threads in a Multitasking Operating System
5,247,683	K. Holmes, C. G. Cunningham, and F. Gleeson	System and Method for Installing Software and Updating Configuration Files
5,247,687	C. K. Eilert and B. R. Pierce	Method and Apparatus for Determining and Using Program Paging Characteristics to Optimize System Productive CPU Time
5,247,735	D. A. Parry and R. J. Reis	Electrical Wire Deletion
5,248,069	R. J. Consaga, R. A. Heath, C. W. Hunter, Jr., D. Reid, and R. E. Turk	Viscous Fluid Pressurizing Apparatus
5,248,262	R. A. Busacco, C. S. Chang, F. W. Chapin, D. W. Dranchak, T. G. Macek, J. R. Petrozello, G. J. Saxenmeyer, Jr., and R. A. Smith	High Density Connector
5,248,633	J. F. Morar and R. M. Tromp	Methods for Forming Epitaxial Self-aligned Calcium Silicide Contacts and Structures
5,248,876	P. J. Kerstens, J. R. Mandeville, and F. Y. Wu	Tandem Linear Scanning Confocal Imaging System with Focal Volumes at Different Heights
5,248,883	R. G. Brewer, R. G. Devoe, K. L. Foster, J. A. Hoffnagle, and R. Kallenbach	Ion Traps of Mono- or Multi-planar Geometry and Planar Ion Trap Devices
5,249,070	H. Takano	Liquid Crystal Display
5,249,092	J. H. Russell-Smith and I. S. Warn	Head Lead Termination
5,249,101	B. D. Frey, C. A. Joseph, F. J. Olshefski, and J. W. Wilson	Chip Carrier with Protective Coating for Circuitized Surface
5,249,149	D. Cocanougher and P. W. Markstein	Method and Apparatus for Performing Floating Point Division
5,249,168	N. Kagami, H. Kubo, and H. Ueno	Method and Apparatus for Initializing Optical Disk Drive Apparatus
5,249,206	L. H. Appelbaum, T. Van Dang, W. A. Moorman, and T. B. Smith III	Fault-Tolerant Clock for Multicomputer Complex

5,249,261	K. S. Natarajan	Failure History Table for Optimizing Backtrack Searches
5,249,263	P. C. Yanker	Color Palette Display Interface for a Computer-Based Image Editor
5,249,289	H. C. Thamm and M. D. Wilkes	System and Method for Rebuilding Edited Digital Audio Files
5,249,291	M. L. Williams	Method and Apparatus for Consensual Delegation of Software Command Operations in a Data Processing System
5,249,343	J. A. Grosso, R. G. Malangone, J. J. Masten, Jr., R. M. Schroedl, R. Stokrocki, and D. G. Will	Apparatus for Alignment of Workpieces
5,249,520	G. S. Andris and J. P. Gauci	Mask Shock Absorber Pad
5,249,687	W. S. Rosenbaum, B. T. Carris, and A. Ankerstjerne	Barcode Translation for Deferred Optical Character Recognition Mail Processing
5,249,741	W. R. Bistline, W. C. Johnson, and J. M. Peterson	Automatic Fan Speed Control
5,249,978	D. J. Gazda, R. L. Stark, and L. R. Strnatka	High Power Connector
5,250,347	A. A. Mehta, T. M. Niu, L. P. Springer, W. J. Summa, and L. P. Wilding	Mechanically Enhanced, High Impact Strength Triazine Resins, and Method of Preparing Same
5,250,395	R. D. Allen, S. A. MacDonald, D. R. McKean, H. Schlosser, R. J. Twieg, G. M. Wallraff, and C. G. Willson	Process for Imaging of Photoresist Including Treatment of the Photoresist with an Organometallic Compound
5,250,829	G. B. Bronner, S. H. Dhong, and W. Hwang	Double Well Substrate Plate Trench DRAM Cell Array
5,250,834	E. J. Nowak	Silicide Interconnection with Schottky Barrier Diode Isolation
5,250,848	F. R. Christie, K. I. Papathomas, and D. W. Wang	Solder Interconnection Structure
5,250,860	W. M.-S. Chu and C. K.-L. Wong	Three-Level Cascode Differential Current Switch
5,250,943	P. L. Childs, H. T. Olnowich, and J. F. Skovira	GVT-NET—A Global Virtual Time Calculation Apparatus for Multi-stage Networks
5,251,081	L. A. Cossette, C. G. Keller, and B. E. Schultz	Spindle Grounding Device
5,251,095	W. C. Miller, R. N. Smith, and R. J. Stutzman	Low Temperature Conduction Module for a Cryogenically-Cooled Processor
5,251,130	G. P. Andrews and P. C. Schloss	Method and Apparatus for Facilitating Contextual Language Translation within an Interactive Software Application
5,251,140	V. M. Chung, J. B. Frei, and J. E. Stuart	E-Beam Control Data Compaction System and Method
5,251,173	J. Y. Wong	High-speed, Low DC Power, PNP-Loaded Word Line Decoder/Driver Circuit
5,251,206	J. Calvignac, E. Saint Georges, D. Orsatti, G. Toubol, F. Verplanken, and F. Nicolas	Hybrid Switching System for a Communication Node
5,251,207	D. S. Abensour, J. E. Fox, M. J. Joshi, F. Lai, and K.-B. K. Sy	Combined Terminal Adapter for SMDS and Frame Relay High Speed Data Services
5,251,210	A. B. Mann, A. J. Ra, J. W. Reedy, and S. J. Teleoglou	Method and Apparatus for Transforming Low Bandwidth Telecommunications Channels into a High Bandwidth Telecommunication Channel
5,251,265	L. Döhle, V. Rudolph, and W. Ruppert	Automatic Signature Verification
5,251,273	T. S. Betts, V. M. Carras, L. B. Knecht, G. B. Anderson, and T. L. Paulson	Data Processing System and Method for Sequentially Repairing Character Recognition Errors for Scanned Images of Document Forms
5,251,287	S. Vassiliadis and G. G. Pechanek	Apparatus and Method for Neural Processing
5,251,291	J. W. Malcolm	Method of Selectively Transferring Video Displayed Information
5,251,300	H. Halliwell, D. J. Vines, and H. W. Prior	Data Processing Network
5,251,303	R. G. Fogg, Jr., J. R. Mathis, and J. O. Nicholson	System for DMA Block Data Transfer Based on Linked Control Blocks
5,251,314	M. L. Williams	System for Converting from One Document Type to a Plurality of Document Types Allowing Accurate Reversal Therefrom Using Tables Containing Indications Regarding Non-transformable Elements
5,251,320	M. J. Kuzawinski and E. J. Zielinski	Power Controller for Permitting Multiple Processors to Power Up Shared Input/Output Devices and Inhibit Power Down Until All Processors Have Ceased Service with the I/O Devices
5,251,806	B. N. Agarwala, A. M. Ahsan, A. Bross, M. F. Chadurjian, N. G. Koopman, L.-C. Lee, K. J. Puttlitz, S. K. Ray, J. G. Ryan, J. G. Schaefer, K. K. Srivastava, P. A. Totta, E. G. Walton, and A. E. Wirsing	Method of Forming Dual Height Solder Interconnections
5,251,844	T. R. Albrecht, J. K. Spong, and J. H. Eaton	Gimbaled Compliant Guide for Tape Drivers

5,252,062	R. F. Groves, L. L. Eaton, D. L. Gardell, and P. H. Boileau	Thermal Processing Furnace
5,252,179	J. V. Ellerson, L. J. Konrad III, R. J. Moore, and J. A. Varcoe	Apparatus and Method for Selectively Etching a Plastic Encapsulating Material
5,252,516	D. B. Nguyen and H. S. Rathore	Method for Producing Interlevel Stud Vias
5,252,781	M. Shirai and K. Yamanaka	Substrate Member Having Electric Lines and Apertured Insulating Film
5,252,857	M. H. Kane, J. G. Roby, and G. Schrottke	Stacked DCA Memory Chips
5,252,951	A. R. Tannenbaum, J. M. Zetts, Y. L. An, G. W. Arbeitman, E. C. Greanias, and G. F. Verrier	Graphical User Interface with Gesture Recognition in a Multiapplication Environment
5,253,091	S. Kimura, H. Suzuki, and H. Yamaguchi	Liquid Crystal Display Having Reduced Flicker
5,253,195	H. J. Broker, R. S. Cook, J. O'Connor, and N. S. Xu	High Speed Multiplier
5,253,202	G. B. Bronner, S. H. Dhong, and W. Hwang	Word Line Driver Circuit for Dynamic Random Access Memories
5,253,236	M. R. Latta, T. C. Strand, and J. M. Zavislan	Optical Beam Focusing and Tracking System for an Optical Disk Information Storage Device
5,253,245	M. E. Rabedeau	Optical Data Storage Tracking System
5,253,274	F. J. Janniello, R. Ramaswami, and D. G. Steinberg	Means to Differentiate Between Commands and Data on a Communications Link
5,253,342	M. L. Blount, S. P. Morgan, K. A. V. Rader, R. K. Rader, and A. A. Shaheen-Gouda	Intermachine Communication Services
5,253,344	J. E. Bostick, R. E. Hough, S. M. John, J. P. Kubala, K. M. Noonan, N. E. Shafa, and I. G. Siegel	Method and Apparatus for Dynamically Changing the Configuration of a Logically Partitioned Data Processing System
5,253,349	S. S. Kreitzer	Decreasing Processing Time for Type 1 Dyadic Instructions
5,254,202	L. H. Kaplan	Fabrication of Laser Ablation Masks by Wet Etching
5,254,488	I. Haller	Easily Manufacturable Thin Film Transistor Structures
5,254,891	J. A. Dorler and F. M. Masci	BICMOS ECL Circuit Suitable for Delay Regulation
5,254,982	R. I. Feigenblatt and C. G. Powell	Error Propagated Image Halftoning with Time-varying Phase Shift
5,255,089	D. E. Dybas, K. L. Haller, E. F. Patterson, and G. S. Selwyn	Portable Particle Detector Assembly
5,255,131	J. D. Coker and R. L. Galbraith	Asynchronous Servo Identification/Address Mark Detection for PRML Disk Drive Ssystem
5,255,216	W.-E. Blanz and C. E. Cox	Reduced Hardware Look Up Table Multiplier
5,255,224	D. E. Galbi, R. J. Houghton, and R. M. Parent	Boosted Drive System for Master/Local Word Line Memory Architecture
5,255,240	R. C. Wong	One Stage Word Line Decoder/Driver with Speed-up Darlington Drive and Adjustable Pull Down
5,255,253	N. Kagami, H. Kubo, K. Okada, and M. Takekoshi	Apparatus and Method for Controlling the Velocity of a Disk Storage Head
5,255,256	E. M. Engler, S. L. Heesacker, D. A. Holmstrom, and B. S. Lee	Optical Data Storage System with Reduced Particle Contamination
5,255,262	M. E. Best, H. J. Rosen, K. A. Rubin, and T. C. Strand	Multiple Data Surface Optical Data Storage System with Transmissive Data Surfaces
5,255,340	C. Arnaud and M. Rosso	Method for Detecting Voice Presence on a Communication Line
5,255,359	T. J. Ebberts, D. G. Gibbons, D. W. Li, B. C. Liang, and D. C. Tannenbaum	Picking Function for a Pipeline Graphics System Using Hierarchical Graphics Structures
5,255,361	J. R. Callaway, J. M. McConaughy, S. T. Pancoast, and J. M. Thompson	Method of and System for Updating a Display Unit
5,255,372	A. S. Butter and H. E. Parsons	Apparatus for Efficiently Interconnecting Channels of a Multiprocessor System Multiplexed Via Channel Adapters
5,255,374	A. Aldereguia, N. Amini, R. L. Horne, T. J. Lohman, and C. N. Tran	Bus Interface Logic for Computer System Having Dual Bus Architecture
5,255,387	M. E. Arnold and G. P. Bate	Method and Apparatus for Concurrency Control of Shared Data Updates and Queries
5,255,389	D. S. Wang	Document Interchange Replace Option Via a Copy Command
5,256,597	J. P. Gambino	Self-aligned Conducting Etch Stop for Interconnect Patterning
5,256,896	T. H. Ning	Polysilicon-Collector-on-Insulator Polysilicon-Emitter Bipolar Transistor
5,256,964	I. K. Ahmed and N. B. Gupta	Tester Calibration Verification Device
5,257,113	M.-S. Chen and Z.-Y. Shae	Video Mixing Technique Using JPEG Compressed Data
5,257,136	J. M. Cobb and F. Topolovec	Reverse Telephoto Agron Objective Lens
5,257,201	C. L. Berman and L. H. Trevillyan	Method to Efficiently Reduce the Number of Connections in a Circuit
5,257,227	V. Klimanis and F. A. Montegari	Bipolar FET Read-Write Circuit for Memory

5,257,232	S. H. Dhong, K. Kitamura, T. Kirihaata, and T. Sunaga	Sensing Circuit for Semiconductor Memory with Limited Bitline Voltage Swing
5,257,237	M. A. Aranda, A. D. Bowen, T. J. Ebbers, R. L. Henderson, N. R. Hildebeitel, and R. Tamlyn	SAM Data Selection on Dual-Ported DRAM Devices
5,257,251	W. W.-C. Chow and A. A. Fennema	Single Loop Servo-Positioning Systems Having Means for Changing the Dynamic Range of a Position-Error Signal with Speed of the Relatively Movable Members
5,257,258	A. Birman, H. R. Gail, Jr., and S. L. Hantler	"Least Time to Reach Bound" Service Policy for Buffer Systems
5,257,272	L. J. Fredrickson	Time-varying Modulo N Trellis Codes for Input Restricted Partial Response Channels
5,257,325	M. A. Casparian and D. C. Forslund	Electronic Parallel Raster Dual Image Registration Device
5,257,345	R. L. Malm	Computer System and Method for Displaying Functional Information with Parallax Shift
5,257,346	W. A. Hanson	Wire-Mesh Generation from Image Data
5,257,351	A. G. Leonard and R. L. Verburg	Selective Processing of a Data Stream Based on Font Format
5,257,354	S. T. Comfort, J. S. Liptay, and C. F. Webb	System for Monitoring and Undoing Execution of Instructions Beyond a Serialization Point Upon Occurrence of In-correct Results
5,257,362	J. M. Menon	Method and Means for Ensuring Single Pass Small Read/Write Access to Variable Length Records Stored on Selected DASDs in a DASD Array
5,257,366	J. G. Adair, R. A. Demers, D. Ecimovic, R. J. Grafe, R. D. Jackson, B. G. Lindsay, M. E. Murphy, R. A. Reinsch, R. P. Resch, R. R. Sanders, P. G. Selinger, R. L. Sunday, and M. R. Zimowski	Query Language Execution on Heterogeneous Database Servers Using a Bind-File Bridge Between Application and Database Languages
5,257,368	S. M. Benson, R. Cwiakala, M. J. Fantacone, J. D. Haggar, D. D. Koren, A. S. Merritt, M. Schulte, and H. M. Yudenfriend	System for Dynamically Changing a System I/O Configuration by Determining Differences between Current and Future Configurations and Describing Differences to Software and Hardware Control Blocks
5,257,374	W. E. Hammer, W. H. Schwane, and F. J. Ziecina	Bus Flow Control Mechanism
5,257,375	C. E. Clark and J. A. Frey	Method and Apparatus for Dispatching Tasks Requiring Short-Duration Processor Affinity
5,257,376	G. S. Beck, W. K. Jones, and D. R. Musselwhite	Method and Apparatus for Configuring a Control Program Nucleus with a Minimum Impact on System Availability
5,257,378	R. A. Sideserf and D. W. H. Heintze	Network Computer Workstation with Initial Program Load Control
5,257,379	R. Cwiakala, J. D. Haggar, C. E. Shapley, T. J. Spewak, D. E. Stucki, and H. M. Yudenfriend	Establishing Synchronization of Hardware and Software I/O Configuration Definitions
5,257,395	H. Li	Methods and Circuit for Implementing and Arbitrary Graph on a Polymorphic Mesh
5,257,714	G. E. Beers, F. W. Bogaczyk, M. A. Rubsam, and H. E. Wattenbarger	Method and Apparatus for Electronic Component Lead Inspection
5,258,236	G. Arjavalasingam, A. Deutsch, F. E. Doany, B. K. Furman, D. J. Hunt, C. Narayan, M. M. Oprysko, S. Purushothaman, V. Ranieri, S. Renick, J. M. Shaw, J. S. Wilczynski, and D. F. Witman	Multi-layer Thin Film Structure and Parallel Processing Method for Fabricating Same
5,258,264	G. S. Mathad, D. Stanasolovich, and G. G. Via	Process of Forming a Dual Overhang Collimated Lift-off Stencil with Subsequent Metal Deposition
5,258,318	T. N. Buti, L. L. Hsu, M. E. Jost, S. Ogura, and R. N. Schulz	Method of Forming a BiCMOS SOI Wafer Having Thin and Thick SOI Regions of Silicon
5,258,640	C.-M. Hsieh, L. L. Hsu, P. T. Nguyen, and L. F. Wagner	Gate Controlled Schottky Barrier Diode
5,258,661	D. C. Banker, J. A. Dorler, W. S. Klara, and F. M. Masci	High Noise Tolerance Receiver
5,258,884	J. K. Howard and C. Hwang	Magnetoresistive Read Transducer Containing a Titanium and Tungsten Alloy Spacer Layer
5,258,909	F. J. Damerau, E. K. Mays, and R. L. Mercer	Method and Apparatus for "Wrong Word" Spelling Error Detection and Correction
5,258,940	J. D. Coker, R. L. Galbraith, and P. A. Ziperovich	Distributed Arithmetic Digital Filter in a Partial-Response Maximum-Likelihood Disk Drive System
5,258,982	K. H. Britton, A. P. Citron, C. Mohan, and G. M. Samaras	Method of Excluding Inactive Nodes from Two-Phase Commit Operations in a Distributed Transaction Processing System
5,258,984	J. M. Menon, R. L. Mattson, and S. W. Ng	Method and Means for Distributed Sparing in DASD Arrays
5,259,049	G.-L. Bona, F. Gfeller, H. Jaeckel, and D. J. Webb	Self-aligned Optical Waveguide to Laser Structure and Method for Making the Same