

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

France

280,827	C. Galand, E. Lancon, H. Crepy, M. Rosso, P. Elie, and T. Liethoudt	Determination of Long Term Prediction Lags for Predictive Pitch Detection Process and Speech Coder Using Said Process
287,742	D. Godard, E. Morlec, J. Belloc, and M. Quintin	Echo Cancelling Device for Data Transmission over Two-Wire Lines
308,565	A. Cretegny, G. Marmigere, G. Platel, L. Bustini, and P. Secondo	Voice-packet Consecutive Discard Avoidance (VPCDA)
312,671	A. Blanc, E. Lallemand, J. C. Abbiate, and P. Jeanniot	Predictive Clock Recovery Circuit

Germany

68,904,426.7	D. Wagner and M. Kallmeyer	Method and Apparatus for Determining Line Centers in a Microminiature Element
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Japan

1,949,071	H. Fujisawa	Cursor Recognition Apparatus for Scanner
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Switzerland

306,552	H. Fink	Atto-amperemeter
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United Kingdom

268,038	A. C. Gay	Graphics Processing System
2,025,640	T. J. Rodd	A Display
2,025,641	T. J. Rodd	A Display Unit

United States

5,132,760	C. P. Umbach, M. Heiblum, and U. Sivan	Electron Wave Deflection in Modulation Doped and Other Doped Semiconductor Structures
5,132,765	B. S. Meyerson, I. Fulton, J. L. Blouse, K. A. Nummy, M. Revitz, R. Rosenberg, and R. C. Lange	Narrow Base Transistor and Method of Fabricating Same
5,144,556	D. S. Wang and M. Williams	Method for Retaining Access to Detected Documents in a Data Processing System
5,156,710	P. C. Chen, T. E. Kindl, P. G. Rickerl, M. J. Schadt, and J. G. Stephanie	Method of Laminating Polyimide to Thin Sheet Metal
5,163,162	R. L. Berry, B. C. Centerwall, S. G. Luning, and F. L. Wade	System and Method for Data Recovery in Multiple Head Assembly Storage Devices
5,168,803	B. H. Kunz and L. A. Stone	Band Line Printer with Grooved Platen
5,169,132	C. C. Myers and J. E. Spence	Workpiece Handler

5,169,310	D. A. Chance, D. B. Goland, and H.-M. Tong	Hermetic Package for an Electronic Device and Method of Manufacturing Same
5,169,332	K. K. Cooke, R. V. Davidge, J. R. DeWitt, P. J. Galinis, J. H. Neer, and T. H. Newsom	Means for Locking Cables and Connector Ports
5,169,488	G. J. Giuffre, M. A. Sturans, J. F. White, and R. R. Wilbarg	Method of Forming Planarized, Reusable Calibration Grids
5,169,805	L. S. Mok, R. E. Schwall, and H.-M. Tong	Method of Resiliently Mounting an Integrated Circuit Chip to Enable Conformal Heat Dissipation
5,170,008	R. T. Evans, R. E. Trogisch, and R. E. Wagner	External Cable Grommet for Cable Entry of EMI Protected Cabinets
5,170,058	P. H. Berasi and L. R. Bolde	Apparatus and a Method for Alignment Verification Having an Opaque Work Piece between Two Artwork Masters
5,170,226	T. Fukuzawa and H. Munekata	Fabrication of Quantum Devices in Compound Semiconductor Layers and Resulting Structures
5,170,243	S. H. Dhong and W. Hwang	Bit Line Configuration for Semiconductor Memory
5,170,245	J. Kim, J. C. Logue, J. J. Ritsko, R. R. Shaw, and G. F. Walker	Semiconductor Device Having Metallic Interconnects Formed by Grit Blasting
5,170,318	G. Catala, C. Lasmayoux, M. Ferry, P. Vachee, A. Paci, and H. Braquet	Flexible Capacitor
5,170,319	R. C.-F. Chu, M. J. Ellsworth, Jr., G. F. Goth, R. E. Simons, and M. L. Zumbrennen	Enhanced Multichip Module Cooling with Thermally Optimized Pistons and Closely Coupled Convective Cooling Channels
5,170,443	S. J. P. Todd	Image Processor for Processing Source Pixel Intensity Values
5,170,464	R. R. Hayes and L. Hsieh	Method for Rolling Back an Expert System
5,170,471	F. M. Bonevento, D. R. Chrisholm, S. D. Dodds, D. M. Desai, E. N. Mandese, A. B. McNeill, and R. N. Mendelson	Command Delivery for a Computing System for Transferring Data between a Host and Subsystems with Busy and Reset Indication
5,170,472	R. Cwiakala, E. P. Hefferon, K. J. Oakes, A. H. Preston, D. E. Stucki, L. W. Wyman, and H. M. Yudenfriend	Dynamically Changing a System I/O Configuration Definition
5,170,477	D. W. Potter and R. J. Scaccia	Odd Boundary Address Aligned Direct Memory Access Device and Method
5,170,480	C. Mohan, R. L. Obermarck, and R. K. Treiber	Concurrently Applying Redo Records to Backup Database in a Log Sequence Using Single Queue Server Per Queue at a Time
5,170,481	R. M. Begun, P. M. Bland, and M. E. Dean	Microprocessor Hold and Lock Circuitry
5,170,929	D. C. Long and K. Seshan	Ultrasonic Adhesion/Dehesion Monitoring Apparatus with Acoustic Transducer Means
5,170,931	K. V. Desai, N. P. Franchak, R. H. Katyl, H. Kohn, T. A. Sholtes, V. G. Veeraraghavan, and C. G. Woychik	Method and Apparatus for Mounting a Flexible Film Semiconductor Chip Carrier on a Circuitized Substrate
5,171,093	N. Iwamoto, T. Nagano, S. Sugita, and S. Yamashita	Control System for a Dot Matrix Printer
5,171,642	P. W. DeHaven, J. D. Mis, K. P. Rodbell, P. A. Totta, and J. F. White	Multilayered Intermetallic Connection for Semiconductor Devices
5,171,709	J. J. Donelon, J. P. Doyle, J. E. Hurst, Jr., and S. M. Rossnagel	Laser Methods for Circuit Repair on Integrated Circuits and Substrates
5,171,717	R. F. Broom, M. Gasser, C. S. Harder, E. E. Latta, A. Oosenbrug, H. Richard, and P. Vettiger	Method for Batch Cleaving Semiconductor Wafers and Coating Cleaved Facets
5,171,964	M. A. Booke, M. H. W. Kallmeyer, N. A. Rizk, and Y.-W. Yau	High Accuracy, High Flexibility, Energy Beam Machining System
5,171,992	J. G. Clabes, M. Hatzakis, K. L. Lee, B. Petek, and J. C. Slonczewski	Nanometer Scale Probe for an Atomic Force Microscope, and Method for Making Same
5,172,010	F. A. Montegari	Clock Chopper/Stretchers for High End Machines
5,172,204	A. M. Hartstein	Artificial Ionic Synapse
5,172,253	K. J. Lynne	Neural Network Model for Reaching a Goal State
5,172,340	S. Leforestier and D. Omet	Double Stage Bipolar Sense Amplifier for BiCMOS SRAMS with a Common Base Amplifier in the Final Stage
5,172,365	D. E. Call, B. I. Finkelstein, and C. S. Singer	System for Predicting Laser End-of-Life from the Power vs. Current Curve of the Diode
5,172,381	J. M. Karp, S. W. Roach, R. C. Schneider, and S. C. West	Enhanced Data Formats and Machine Operations for Enabling Error Correction
5,172,388	D. C. Long, H. A. Bender, W. D. Carbaugh, Jr., M. J. La Plante, and C. D. Setzer	Method and Apparatus for an Increased Pulse Repetition Rate for a CW Pumped Laser
5,173,256	R. B. Booth, G. W. Grube, P. A. Gruber, I. Y. Khandros, and A. R. Zingher	Liquid Metal Matrix Thermal Paste

5,173,331	R. W. Adams, Jr., D. R. Clarke, S. H. Knickerbocker, L. L. Rapp, and B. Schwartz	Zirconia Toughening of Glass-Ceramic Materials
5,173,392	E. F. Miersch and J. M. Park	Forming a Pattern on a Substrate
5,173,439	S. Dash, M. L. Kerbaugh, C. W. Koburger III, B. J. Machesney, and N. B. Parekh	Forming Wide Dielectric-Filled Isolation Trenches in Semi-Conductors
5,173,619	G. J. Gaudenzi, K. G. Kramer, and S. L. Tempest	Bidirectional Buffer with Latch and Parity Capability
5,173,624	B. Gabillard, P. Girard, and M. Granguillot	Level-Shifters Circuit for High-Speed Low-Power BiCMOS ECL to CMOS Input Buffers
5,173,668	L. Jacobowitz and M. E. Ecker	Apparatus and a Method for an Electrical Transmission-Line Interface
5,173,694	R. T. Lynch, Jr., D. Rugar, and T. C. Weigandt	Binary Data Encoding and Decoding Using a Rate 2/5 (2,18,2) Code
5,173,756	H.-S. P. Wong and Y. L. Yao	Trench Charge-Coupled Device
5,173,763	T. M. Cipolla, P. W. Coteus, R. H. Katyl, R. J. Kelleher, and P. A. Moskowitz	Electronic Packaging with Varying Height Connectors
5,173,842	D. T. Depew	Electrical Assembly with Deformable Bridge Printed Circuit Board
5,173,861	A. Inselberg, J. S. Eickemeyer, and A. A. Hurwitz	Motion Constraints Using Particles
5,173,885	H. Iiyori and S. Takayama	Magneto-Optical Recording Medium and System for Use with Short Wavelength Light
5,173,898	P. Heinzmann, J. R. Mueller, and M. M. Nassehi	Multiple-Access Control for a Communication System with Order Pad Passing
5,174,201	G. S. Andris, J. P. Gauci, and J. F. Harmuth	Thick Film Mask Separation Detection System
5,175,421	R. H. Harris	Dual Depth of Field Deflector for Bar Code Scanners
5,175,449	D. Ballatore	Circuit for Testing the Frequency of a Clock in an Electronic System
5,175,609	G. DiGiacomo, A. S. Cammarano, and N. DiPaolo	Structure and Method for Corrosion and Stress-Resistant Interconnecting Metallurgy
5,175,641	D. W. Boerstler and B. W. Weidle	Dual-Mode Laser Diode Transmitter
5,175,658	H. C. Chang, M.-M. Chen, C. T. Horng, and R. O. Schwenker	Thin Film Magnetic Head Having a Protective Coating and Method for Making Same
5,175,702	J.-P. Beraud and C. Galand	Digital Signal Processor Architecture with Plural Multiply/Accumulate Devices
5,175,731	G. A. Suarez	Arbitration Circuit for a Multimedia System
5,175,813	M. M. Golding, L. R. Kalmin, and R. I. Seidner	Window Display System and Method for Creating Multiple Scrollable and Non-Scrollable Display Regions on a Non-Programmable Computer Terminal
5,175,822	J. D. Dixon, D. S. Keener, H. J. Locker, G. A. Marazas, A. B. McNeill, T. H. Newsom, and N. A. Osborn	Apparatus and Method for Assigning Addresses to SCSI Supported Peripheral Devices
5,175,826	R. M. Begun, P. M. Bland, and M. E. Dean	Delayed Cache Write Enable Circuit for a Dual Bus Microcomputer System with an 80386 and 82385
5,175,830	A. M. Sherman and L. S. Walling	Method for Executing Overlays in an Expanded Memory Data Processing System
5,175,851	D. W. Johnson, S. P. Morgan, and T. A. Smith	System and Method for Controlling Client Machine Access to a Portion of a File with a Variable Length
5,175,852	D. W. Johnson, A. A. Shaheen-Gouda, and T. A. Smith	Distributed File Access Structure Lock
5,175,863	G. D. Jones, Jr.	Signal Data Processing System Having Independently, Simultaneously Operable ALU and MACU
5,176,289	D. J. Klossner and M. J. Yurko	Direct Vertical Pin Feeder
5,176,811	L. G. Keim, R. S. Paonessa, and D. C. Van Hart	Gold Plating Bath Additives for Copper Circuitization on Polyimide Printed Circuit Boards
5,176,947	A. Afzali-Ardakani, M. K. Desai, J. P. Hoekstra, K. S. Pennington, and E. E. Simonyi	Electroerosion Printing Plates
5,177,031	P. L. Buchmann, D. J. Webb, and P. Vettiger	Method of Passivating Etched Mirror Facets of Semiconductor Laser Diodes
5,177,374	G. D. Carpenter and M. B. Lundberg	Current Mode Gate Drive for Power MOS Transistors
5,177,482	R. D. Cideciyan and E. A. Zurfluh	RLL Encoder and Decoder with Pipelined Plural Byte Processing
5,177,559	J. S. Batchelder and M. A. Taubenblatt	Dark Field Imaging Defect Inspection System for Repetitive Pattern Integrated Circuits
5,177,594	D. A. Chance, E. E. Davidson, T. R. Dinger, D. B. Goland, and D. P. Lapotin	Semiconductor Chip Interposer Module with Engineering Change Wiring and Distributed Decoupling Capacitance
5,177,667	N. H. Graham and K. P. Moran	Thermal Conduction Module with Integral Impingement Cooling

5,177,744	B. K. Cesare, T. J. Slegel, and D. S. Whitaker	Method and Apparatus for Error Recovery in Arrays
5,177,747	L. B. Capps, Jr., J. G. Foster, W. E. Price, R. W. Rupe, and K. A. Uplinger	Personal Computer Memory Bank Parity Error Indicator
5,177,791	P. C. Yeh, D. G. Abraham, D. B. Johnson, A. Van Le, S. M. Matyas, R. Prymak, R. M. Smith, Sr., and J. D. Wilkins	Secure Translation of Usage-Control Values for Cryptographic Keys
5,177,796	E. Feig, V. S. Miller, and J. H. Morgan	Image Data Processing of Correlated Images
5,178,739	M. S. Barnes, J. C. Forster, and J. H. Keller	Apparatus for Depositing Material into High Aspect Ratio Holes
5,178,914	R. D. Goldblatt, C. A. Kovac, and D. Tortorella	Means of Seeding and Metallizing Polymide
5,178,975	K. N. Chiong and D. E. Seeger	High Resolution X-ray Mask Having High Aspect Ratio Absorber Patterns
5,179,264	J. J. Cuomo, C. R. Guarnieri, and S. Whitehair	Solid State Microwave Powered Material and Plasma Processing Systems
5,179,294	R. A. Béchade and B. A. Kaffmann	Process Independent Digital Clock Signal Shaping Network
5,179,322	T. K. Gibbs and G. Luck	Linearity Correction Apparatus
5,179,337	J. L. Staarman and T. B. Ziemkowski	Over-Discharge Protection for Rechargeable Batteries
5,179,345	L. C. Jenkins and R. L. Wisnief	Method and Apparatus for Analog Testing
5,179,372	R. M. P. West and T. Williams	Video Random Access Memory Serial Port Access
5,179,379	D. Allen and D. Hill	Analog to Digital Converter with Drift Compensation
5,179,467	S. L. Buchwalter, M. J. Goldberg, R. Iyengar, T. R. O'Toole, and A. Viehbeck	Charge Transfer Salts and Uses Thereof
5,179,483	A. C. Lowe	Method for Controlling Airflow Through a Disc File Spindle
5,179,508	G. G. Lange and M. M. Walters	Standby Boost Converter
5,179,654	J. J. C. Richards and K. Williams	Help Provision in a Data Processing System
5,179,656	R. J. Lisle	Three Dimensional Directional Pointer Icon
5,179,657	D. W. Dykstal, E. A. Fishel, M. A. Temple, D. D. Snyder, G. D. Timms, Jr., and S. R. Shi	Panel Definition Language for Computer Display
5,179,660	E. P. Devany, J. R. Garrison, D. C. Jacobs, and L. E. Jordan II	System for Reducing Communications Overhead in Distributed Database Transactions by Serializing in Order Related Requests into Single Transmission Message and Receiving Transmission Response
5,179,662	M. J. Corrigan, G. R. Ricard, R. M. Rocheleau, and L. W. Youngren	Optimized I/O Buffers Having the Ability to Increase or Decrease in Size to Meet System Requirements
5,179,672	T. B. Genduso and S. Ong	Apparatus and Method for Modeling Parallel Processing of Instructions Using Sequential Execution Hardware
5,179,695	A. G. Derr and R. J. Poliquin	Problem Analysis of a Node Computer with Assistance from a Central Site
5,179,699	B. R. Iyer, G. R. Ricard, and P. J. Varman	Partitioning of Sorted Lists for Multiprocessors Sort and Merge
5,179,700	T. Aihara, M. Ogata, T. Kurosawa, and Y.-C. L. Lien	User Interface Customization Apparatus
5,179,703	W. O. Evans	Dynamically Adaptive Environment for Computer Programs
5,179,709	R. N. Bailey, R. L. Mansfield, and A. K. Spencer	Look Ahead Bus Transfer Request
5,179,711	A. P. Vreeland	Minimum Identical Consecutive Run Length Data Units Compression Method by Searching Consecutive Data Pair Comparison Results Stored in a String
5,179,718	M. G. MacPhail	Method of Filing Having a Directed Relationship Through Defining a Staple Relationship within the Context of a Folder Document
5,179,725	W. O. Camp, Jr., D. E. Del Nero, C. N. Herbert, and J. A. Marozas	Voltage Controlled Oscillator with Correction of Tuning Curve Non-Linearities
5,180,891	B. A. Trumbo	Digitizer Tablet with Internally Stored Wireless Stylus
5,180,955	J. P. Karidis, G. McVicker, and J. P. Pawletko	Positioning Apparatus
5,181,017	A. H. Frey, Jr., J. M. Gould, and C. M. Higgins, Jr.	Adaptive Routing in a Parallel Computing System
5,181,200	C. G. Harrison	Handoff Method and Apparatus for Mobile Wireless Workstation
5,181,465	P. J. Fitzgibbons, H. C. Lee, L. A. Stone, and P. C. Van Note	Ribbon Guide System for a Line Printer
5,181,853	K. B. Van Brunt, Jr. and J. R. Petrozello	Fluid Pressure Actuated Electrical Connector
5,182,029	A. J. Erb	Aqueous Photoresist Waste Treatment by Acid/Cupric Chloride Precipitation

5,182,230	J. J. Donelon, J. P. Doyle, J. E. Hurst, Jr., M. M. Oprysko, S. M. Rossnagel, and R. J. von Gutfeld	Laser Methods for Circuit Repair on Integrated Circuits and Substrates
5,182,468	C. K. Erdelyi, M. G. Marshall, J. W. Mathews, and P. E. Perry	Current Limiting Clamp Circuit
5,182,554	J. M. Kaiser and J. C. St. Clair	Third Party Eavesdropping for Bus Control
5,182,656	D. S. Chevion, E. D. Karnin, and E. Walach	Method for Compressing and Decompressing Forms by Means of Very Large Symbol Matching
5,182,747	J. F. Frenzel III and B. A. Youngman	Method for Controlling the Insertion of Stations into FDDI Network
5,182,684	V. P. Thomas and P. Baker	Estimator Positioning System and Method
5,182,773	L. R. Bahl, M. A. Picheny, D. Nahamoo, and P. V. de Souza	Speaker-Independent Label Coding Apparatus
5,182,800	J. K. Farrell, J. S. Gordon, D. C. Kuhl, and T. V. Lee	Direct Memory Access Controller with Adaptive Pipelining and Bus Control Features
5,182,809	R. M. Begun, P. M. Bland, and P. E. Milling	Dual Bus Microcomputer System with Programmable Control of Lock Function
5,183,999	P. A. Hakenewerth, G. D. Rohrer, and N. G. Stalberg	Self-Service Transaction Apparatus and Method Using a Robot for Article Transport and Repair of Internal Article Handling Devices
5,184,126	M. E. Nagy	Method of Decompressing Compressed Data
5,184,284	B. T. Ashelin, M. A. Bergquist, and D. E. Noltee, Sr.	Method and Apparatus for Implementing Engineering Changes for Integrated Circuit Module
5,185,289	H. P. Meier, E. A. Van Gieson, and W. Walter	Process for the Selective Growth of GaAs
5,185,294	C. H. Lam, J. B. Lasky, C. M. Hill, J. S. Nakos, S. J. Holmes, S. F. Geissler, and D. K. Lord	Boron Out-Diffused Surface Strap Process
5,185,638	V. C. Conzola, N. E. Rittenhouse, J. M. Solomon, T. J. Toomey, and P. J. Yablonsky	Computer Controlled, Multiple Angle Illumination System
5,185,698	E. M. Hesse, M. Kozol, and C. Lim	Technique for Contracting Element Marks in a Structured Document
5,185,733	B. I. Finkelstein, M. Tayefeh, and H. Ueno	Calibrating Lasers for Optical Recording Using a Maximal Readback Signal Amplitude as a Criterion
5,185,734	D. E. Call and J. Lewkowicz	Calibrating and Power-Protecting Laser Drive Circuits
5,185,737	M. M. Nassehi and E. A. Zurfluh	Method and Apparatus for Cyclic Reservation Multiple Access in a Communications System
5,185,768	F. D. Ferraiolo, J. E. Gersbach, and I. I. Novof	Digital Integrating Clock Extractor
5,185,811	G. E. Beers, M. D. Flickner, W. L. Kelly-Mahaffey, D. R. Polk, J. M. Stafford, and H. E. Wattenbarger	Automated Visual Inspection of Electronic Component Leads Prior to Placement
5,185,833	R. Betts	Modular Active Fiber Optic Coupler System
5,185,871	B. G. Frey and R. J. Pedersen	Coordination of Out-of-Sequence Fetching between Multiple Processors Using Re-execution of Instructions
5,185,884	D. R. Martin, M. J. McDermott, S. D. Plumlee, R. H. Satin, and R. L. Wenger	Computer Controlled Optimized Pairing of Disk Units
5,186,041	D. Nyyssonen	Microprobe-Based CD Measurement Tool
5,186,238	S. E. del Puerto and P. M. Gaschke	Liquid Film Interface Cooling Chuck for Semiconductor Wafer Processing
5,186,335	L. B. Fahey, M. D. Garten, and D. G. Lyke	Adjustment Document Storage Device for a Document Sorter
5,186,629	J. E. Rohen	Virtual Graphics Display Capable of Presenting Icons and Windows to the Blind Computer User and Method
5,187,109	R. K. Cook and M. M. A. Pelella	Lateral Bipolar Transistor and Method of Making the Same
5,187,121	D. R. Cote, D. Stanasolovich, and R. A. Warren	Process for Fabrication of a Semiconductor Structure and Contact Stud
5,187,679	S. Vassiliadis and E. M. Schwarz	Generalized 7/3 Counters
5,187,776	P. C. Yanker	Image Editor Zoom Function
5,187,781	C. A. Heath	Shared Hardware Interrupt Circuit for Personal Computers
5,187,792	R. A. Dayan, S. H. Lam, and J. P. Zimmerman	Method and Apparatus for Selectively Reclaiming a Portion of RAM in a Personal Computer System