

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,682,675,808	G. Makosch	Mask-to-Wafer Registration with Optical Gratings
3,683,039,908	K. Malin, L. Biro, and O. Schmid	Method of Forming Silicon and Oxygen Containing Layers
3,683,499,808	H. Maier, T. Tandjung, J. B. Landrock, and M. Perske	I/O Slip-On Concept MCM/Card
3,683,763,608	G. Elsner, H. Hinkel, and J. Greschner	Method of Forming Identically Positioned Alignment Marks on Opposite Side of a Semiconductor Wafer
3,683,943,408	J. Gould, J. Perchik, and K. Dutke	Control Interface for Transferring Data between a Data Processing Unit and Input/Output Devices
3,773,904,208	H. Schmid, J. Greschner, J. Wittlinger, M. Elsaesser, O. Wolter, R. R. Stoehr, and T. Bayer	Contact Probe Arrangement for Electrically Connecting a Test System to the Circular Contact Pads of a Device
3,776,325,308	E. Hoerner, H. J. Trumpp, and R. Muehl	Process of Making Via Holes in a Double-Layer Insulation

France

221,221	C. Galand and G. Platel	A Process for Determining an Echo Path Delay and Echo Canceler Using Said Process
243,562	C. Galand and J. Menez	Improve Voice Coding Process and Device for Implementing Said Process
263,222	A. Bitailou and M. Grandguillot	Method of Forming Solder Terminals for a Pinless Ceramic Module
265,571	J. Calvignac, J. M. Munier, and M. Bouillot	Bypass Booster Mechanisms for the Line Scanners of a Communication Controller
265,575	G. Montanari, P. P. Louis, and R. L. Leotard	Data Processing System Having Automatic Address Allocation Arrangements for Addressing Interface Cards
271,626	A. Billian	Bypass Mechanism for Daisy Chain Connected Units
273,082	F. Wallart, G. Boudon, J. C. Le Garrec, M. Boneau, and P. Mollier	A New Latch Cell Family in CMOS Technology Gate Array

Japan

829,244	T. Takahashi and Y. Manabe	Portable Computer
1,620,725	M. Kanoh, S. Tezuka, and Y. Matsuda	Monosyllable Speech Recognition System
1,623,258	T. Suzuki	Hangeul Character Input Apparatus
1,833,903	S. Watanabe and Y. Aoki	Digital Calculation Circuit
1,869,196	S. Watanabe and Y. Gohda	Window Priority Circuit
1,872,634	M. Sakai	Pedestal for CRT Display

Sweden

244,625 B. G. Dahlen, H. E. Sandin, P. A. Dahlin, and R. R. Redziejowski
 286,780 M. Al-Jaroudi

Data Base Processor and Its Use In a Data Processing System
 Drive Device

Switzerland

86,993 D. Chevion, E. Karnin, and E. Walach
 238,690 B. J. Van Zeghbroeck, P. Vettiger, and P. L. Buchmann
 290,648 D. Pohl, G. K. Binnig, H. Rohrer, J. K. Gimzewski, and U. T. Duerig

High Efficiency Multiplication Free Approximation of Mult
 Process for Forming Sidewalls
 Atomic Force Sensor Head

United Kingdom

172,312 P. W. Johnson
 186,981 P. R. Smith
 205,692 I. M. McCallion
 261,289 A. R. Cowburn, D. S. Mansbridge, F. T. Moth, P. Bowman, and W. M. Morgan
 287,720 P. R. Smith and S. G. Lee
 2,012,433 J. A. Wiseman
 2,012,434 J. A. Wiseman
 2,013,772 J. A. Wiseman and M. D. Oates
 2,016,106 M. H. Sharp

Data Display Systems
 Security Module for an Electronic Funds Transfer System
 Improvements in Microprocessors
 A Semiconductor Integrated Display
 Management of Cryptographic Keys
 Keyboard
 Keyboard
 Communications Apparatus
 Data Storage Unit

United States

5,022,077 R. Bealkowski, J. W. Blackledge, Jr., D. S. Cronk, R. A. Dayan, J. D. Dixon, S. G. Kinnear, G. D. Kovach, A. B. McNeill, M. S. Palka, Jr., R. Sachsenmaier, E. I. Wachtel, and K. M. Zyvoloski
 5,023,813 J. K. Brown III
 5,032,979 M. S. Hecht, A. Johri, T. T. Wei, and D. H. Steves
 5,039,939 C. J. Dick, B. J. Ditmyer, T. L. Jeremiah, L. Jones, and G. S. Still
 5,040,131 R. J. Torres
 5,040,169 R. A. Guerin and Y.-C. Lien
 5,040,227 D. G. Lyke and M. Ominsky
 5,043,920 R. L. Malm and C. L. Meiley
 5,043,989 P. Dalmas, D. Esteban, J.-F. Le Pennec, P. Michel, and P. Therias
 5,044,615 R. L. Newman, M. C. Weller, and F. P. Zickefoose
 5,044,872 R. E. Hunt and V. E. Whitehead
 5,044,875 R. E. Hunt and V. E. Whitehead
 5,045,159 M. J. Goldberg, D. P. Morris, and A. Viehbeck
 5,045,402 R. W. Adams, Jr., D. R. Clarke, S. H. Knickerbocker, L. L. Rapp, and B. Schwartz
 5,045,431 R. D. Allen, G. M. Wallraff, L. L. Simpson, and W. D. Hinsberg III
 5,045,608 J. L. Hedrick, Jr., D. C. Hofer, J. W. Labadie, S. A. Swanson, and W. Volksen
 5,045,680 B. Fan and R. A. Rand
 5,045,723 R. S. Mao
 5,045,852 J. L. Mitchell, W. B. Pennebaker, and J. J. Rissanen
 5,045,870 P. Lamey and R. Kachmarik
 5,045,911 P. A. Habitz, C.-M. Hsieh, and Y.-S. Huang
 5,045,913 R. P. Masleid and P. T. Patel

Apparatus and Method for Preventing Unauthorized Access to BIOS in a Personal Computer System
 Non-Volatile Memory Usage
 Distributed Security Auditing Subsystem for an Operating System
 Calculating AC Chip Performance Using the LSSD Scan Path
 Graphical Processing
 Wavelength Division Photonic Switch
 Image Balancing System and Method
 Multi-Dimension Visual Analysis
 Terminal Adapter Having a Multiple HDLC Communication Channels Receiver for Processing Control Network Management Frames
 Dual Purpose Work Board Holder
 System and Method for Chip Orientation
 Method and Apparatus for Positioning Components
 Derivatives of Compounds Containing a Carbonyl Group Conjugated to an Aromatic Moiety and Electrophilic Methods of Fabrication Thereof
 Zirconia Toughening of Glass-Ceramic Materials
 Dry Film, Aqueous Processable Photoresist Compositions
 Polyimide-Poly(phenylquinoxaline) Block Copolymers
 Integrated Circuit Optoelectronic Toggle F/F
 Multiple Input CMOS Logic Circuits
 Dynamic Model Selection During Data Compression
 Thermal Ink Drop on Demand Devices on a Single Chip with Vertical Integration of Driver Device
 Lateral PNP Transistor and Method for Forming Same
 Bit Stack Compatible Input/Output Circuits

5,045,998	R. M. Begun, P. M. Bland, and M. E. Dean	Method and Apparatus for Selectively Posting Write Cycles Using the 82385 Cache Controller
5,046,000	Y. Hsu	Single-FIFO High Speed Combining Switch
5,046,001	B. A. Barker, T. R. Edel, and J. A. Stark	Method for Accessing Selected Windows in a Multi-Tasking System
5,046,036	S. Tezuka	Pseudorandom Number Generator
5,046,060	W. W. Chow, A. A. Fennema, and R. J. Kadlec	Servo Error Controls
5,046,413	J. S. Gregor, C. C. Hubbell, and S. K. Lichtensteiger	Method and Apparatus for Band Printing with Automatic Home Compensation
5,046,790	R. Onomoto, M. Sato, R. Onomoto, and H. Fujikura	Apparatus for Immobilizing Movable Cabinet
5,046,819	E. R. Childers	Objective Lens Support Having Rotary and Axial Motions for Use in an Optical Disk Apparatus
5,046,872	P. S. H. Chang, and H. C. Wang	Printer Actuated by Piezoelectrically Generated Shock Wave
5,046,966	K. A. Snyder and P. E. Winkler	Coaxial Cable Connector Assembly
5,047,568	R. W. Angelo, J. D. Gelorme, J. P. Kuczynski, W. H. Lawrence, S. P. Pappas, and L. L. Simpson	Sulfonium Salts and Use and Preparation Thereof
5,047,649	R. T. Hodgson, J. E. Stanland, and O. C. Wells	Method and Apparatus for Writing or Etching Narrow Linewidth Patterns on Insulating Materials
5,047,834	C. A. Kovac and I. C. Noyan	High Strength Low Stress Encapsulation of Interconnected Semiconductor Devices
5,047,898	K. K. Cooke, J. R. Dewitt, and P. J. Galinis	Deflectable Contact for Providing Positive Surface Contact for Shielding Electromagnetic Interference
5,047,921	H. B. Kinter and G. R. Westcott	Asynchronous Microprocessor Random Access Memory Arbitration Controller
5,047,954	J. Corona and T. W. Lindgren	Graphics Vector Generator Setup Technique
5,047,977	R. R. Hill and R. J. Hrabik	Methods of Generating and Retrieving Error and Task Message Records within a Multitasking Computer System
5,048,018	D. Bernstein and K. So	Debugging Parallel Programs by Serialization
5,048,047	W. J. Kozlovsky and W. P. Risk	Passive Absorptive Resonator Laser System and Method
5,048,055	J. L. Creig, J. Dagher, and J. W. Sylvant	Multi-Data Rate Selectable Equalizer
5,048,062	T. A. Gregg and L. Skarshinski	Transmitting Commands Over a Serial Link
5,048,085	D. G. Abraham, S. G. Aden, T. W. Arnold, S. W. Neckyfarow, and W. S. Rohland	Transaction System Security Method and Apparatus
5,048,178	P. S. Bindra, P. J. Lueck, and E. H. Naegele	Alignment-Registration Tool for Fabricating Multi-Layer Electronic Packages
5,048,744	C.-A. Chang, N. G. Koopman, J. M. Roldan, S. Strickman, K. K. Srivastava, and H. L. Yeh	Palladium Enhanced Fluxless Soldering and Bonding of Semiconductor Device Contacts
5,049,201	S. Cheng, C. J. Araps, A. J. Arnold, J. T. Coffin, and L. T. Nguyen	Method of Inhibiting Corrosion in an Electronic Package
5,049,410	A. Johary, B. E. Kennedy, J. J. Mayerle, and J. C. S. Shen	Lubricant Film for a Thin-Film Disk
5,049,461	P. C. Arnett, A. Bryant, J. S. Foster, J. E. Frommer, and J. A. C. Iwata	Method of Making and Using a High Resolution Lithographic Mask
5,049,805	N. Celenza, R. M. Clemo, R. S. Jungling, and B. D. Miller	Voltage Sensitive Switch
5,049,955	J. L. Freeouf, T. N. Jackson, P. D. Kirchner, J. Y. Tang, and J. M. Woodall	Semiconductor Ballistic Electron Velocity Control Structure
5,050,077	J. P. Vincent	Meeting Scheduler with Alternative Listing
5,050,104	J. G. Heyen, C. Kasiraj, L. A. Melkus, and T. J. Wolf	Method for Notifying a Terminal User of an Asynchronous Event Occurrence
5,050,105	A. M. Peters	Direct Cursor-Controlled Access to Multiple Application Programs and Data
5,050,144	I. E. Henderson and M. Tayefeh	Recording Intensity Modulated Signals and Polarization-Modulated Optical Signals in the Same Area of a Storage Member
5,050,215	M. Nishimura	Speech Recognition Method
5,050,225	M. Itoh	Image Processing Apparatus and Method
5,050,296	A. J. Emerick, E. L. Marsh, T. L. Miller, and J. M. Zalesinski	Affixing Pluggable Pins to a Ceramic Substrate
5,051,008	Y. Honda and T. Suzuki	Automatic Gap Adjusting Mechanism
5,051,096	K. K. Cooke, J. R. Dewitt, P. J. Galinis, and W. B. Koteff	Planar Board Support Structure
5,051,288	R. H. Ahlert, J. K. Howard, S. E. Lambert, and I. L. Sanders	Thin Film Magnetic Recording Disk Comprising Alternating Layers of a CoNi or CoPt Alloy and a Non-magnetic Spacer Layer

5,051,366	D. H. Anderson, D. K. Walston, and G. G. Zamora	Electrical Connector
5,051,377	W. Euen, D. Hagmann, and H.-J. Wildau	Method for Forming a Thin Dielectric Layer on a Substrate
5,051,379	T. Bayer, J. Greschner, G. Kraus, H. Weiss, and O. Wolter	Method of Producing Micromechanical Sensors for the AFM/STM Profilometry and Micromechanical AFM/STM Sensor Head
5,051,598	C. J. Ashton, P. D. Gerber, D. P. Kern, W. W. Molzen, Jr., S. A. Rishton, M. G. Rosenfield, and R. G. Viswanathan	Method for Correcting Proximity Effects in Electron Beam Lithography
5,051,736	W. E. Bennett, S. J. Boies, A. R. Davies, K.-F. Etzold, and T. K. Rodgers	Optical Stylus and Passive Digitizing Tablet Data Input System
5,051,887	B. H. Berger and B. P. Smith	Maintaining Duplex-Paired Storage Devices During Gap Processing Using of a Dual Copy Function
5,051,891	M. G. MacPhail	Method to Manage Transfer of Ownership of Electronic Documents Stored in an Interactive Information Handling System
5,051,892	M. E. Ferree, J. P. Gray, M. Pozefsky, and J. F. Wilder	Full Duplex Conversation Between Transaction Programs
5,051,917	E. L. Gould, D. W. Kemerer, L. A. McAllister, R. A. Piro, G. R. Richardson, and D. A. Wellburn	Method of Combining Gate Array and Standard Cell Circuits on a Common Semiconductor Chip
5,051,926	J. D. Stevens, B. M. Walk, and J. S. Wunder	System Wide Local Copy Management of Screen Copy Printing
5,051,940	S. Vassiliadis, J. E. Phillips, and B. Blaner	Data Dependency Collapsing Hardware Apparatus
5,051,985	I. Cidon and I. S. Gopal	Contention Resolution in a Communications Ring
5,051,994	R. G. Bluethman and D. J. Willson	Computer Memory Module
5,052,481	J. L. Horvath, R. G. Biskeborn, and J. M. Harvilchuck	High Conduction Cooling Module Having Internal Fins and Compliant Interfaces for VLSI Chip Technology
5,052,606	T. M. Cipolla, R. R. Horton, A. P. Lanzetta, M. J. Palmer, and M. B. Ritter	Tape Automated Bonding Feeder
5,052,730	B. Frank and U. Hengstler	Locking Means for a Gate in an Automatic Teller Machine
5,052,763	R. N. Singh and J. S. Wilczynski	Optical System with Two Subsystems Separately Correcting Odd Aberrations and Together Correcting Even Aberrations
5,052,834	C. H. Feistel and S. R. Vohra	System and Method of Printing Sideways
5,052,844	R. A. Kendall	Ball Joint with Dynamic Preload Adjustment
5,052,888	I. W. Bolton, D. S. Gaunt, and R. K. Russell	DC Motor Driven Centrifugal Fan
5,053,104	S. V. Babu, J. G. Hoffarth, A. R. Knoll, W. E. Mlynko, J. F. Rembetski, and K. D. Mack	Method of Plasma Etching a Substrate with a Gaseous Organohalide Compound
5,053,250	R. J. Baseman and C. V. Jahnes	Thin Film Magnetic Medium with Controlled Grain Morphology and Topology for Improved Performance
5,053,272	K. Y. Ahn, J. Kim, P. Lauro, and G. F. Walker	Optical Storage Device
5,053,361	L. W. Herron, S. H. Knickerbocker, G. Natarajan, and S. S. N. Reddy	Setter Tile for Use in Sintering of Ceramic Substrate Laminates
5,053,761	J. W. Webster III	Method for Smooth Bitmap Scrolling
5,053,851	E. Berndmaier, G. Das, and T. L. Viau	Metal Bump for a Thermal Compression Bond and Method for Making Same
5,053,853	J. Haj-Ali-Ahmadi, R. F. Frankeny, and K. Hermann	Modular Electronic Packaging System
5,053,887	G. R. Thompson	Technique for Producing a Fine Grained Dithered Halftone Image Having an Increased Number of Gray Levels
5,054,019	J. R. Mathis and G. L. Rouse	Transfer Direction Turnaround in Network Data Communications
5,054,025	C. Garland, P. Elie, and M. Rosso	Method for Eliminating Errors in Block Parameters
5,054,054	D. Pessia and R. Gliemer	Voice Applications Generator
5,054,074	R. Bakis	Optimized Speech Recognition System and Method
5,054,088	S. C. Gunderson and A. B. Wadia	Signature Verification Data Compression for Storage on an Identification Card
5,054,360	R. J. Lisle, B. S. McDonald, and M. D. Wilkes	Method and Apparatus for Simultaneous Output of Digital Audio and MIDI Synthesized Music
5,054,872	B. Fan, D. G. Flagello, J. D. Gelorme, M. M. Oprysko, A. Speth, and J. M. Trehwella	Polymeric Optical Waveguides and Methods of Forming the Same
5,055,158	W. J. Gallagher, C.-K. Hu, M. A. Jaso, M. B. Ketchen, A. W. Kleinsasser, and D. J. Pearson	Planarization of Josephson Integrated Circuit

5,055,342	V. Markovich, A. Mehta, J. M. Park, E. Skarvinko, and D. W. Wang	Fluorinated Polymeric Composition, Fabrication Thereof and Use Thereof
5,055,383	O. Koblinger, K. Meissner, R. Mühl, H.-J. Trumpp, and W. Zapka	Process for Making Masks with Structures in the Submicron Range
5,055,439	R. D. Allen, W. D. Hinsberg III, L. L. Simpson, R. J. Twieg, G. M. Wallraff, and C. G. Willson	Photoacid Generating Composition and Sensitizer Therefor
5,055,839	J. C. Davis and J. C. Irwin	Breakaway Keyboard
5,055,898	K. E. Beilstein, Jr., C. L. Bertin, J. R. Pessetto, and F. R. White	DRAM Memory Cell Having a Horizontal SOI Transfer Device Disposed Over a Buried Storage Node and Fabrication Methods Therefor
5,055,957	G. A. Daughenbaugh and E. B. Rigby	Method of Making Low Wear Glass for Magnetic Heads
5,055,964	J. S. Logan, R. R. Ruckel, R. E. Tompkins, and R. P. Westerfield, Jr.	Electrostatic Chuck Having Tapered Electrodes
5,055,989	L. B. Carroll, K. H. Kidder, Jr., and T. Visentin	Three Phase to Single Phase Converter
5,056,003	W. E. Hammer, W. H. Schwane, and F. J. Ziecina	Distributed Data Management Mechanism
5,056,111	I. N. Duling III, D. R. Grischkowsky, J.-M. Halbout, and M. B. Ketchen	Integrated Terahertz Electromagnetic Wave System
5,057,184	A. Gupta, B. Haba, B. W. Hussey, and L. T. Romankiw	Laser Etching of Materials in Liquids
5,057,450	G. B. Bronner, P. M. Fahey, B. S. Meyerson, and W. D. Pricer	Method for Fabricating Silicon-on-Insulator Structures
5,057,773	S. D. Golladay, F. J. Hohn, D. J. Hutson, W. D. Meisburger, and J. Rasch	Method for Opens/Shorts Testing of Capacitively Coupled Networks in Substrates Using Electron Beams
5,057,785	P. W. Chung, M. O. Jenkins, S. A. Jove, K. B. Klaassen, P. Saber, and J. C. L. van Peppen	Method and Circuitry to Suppress Additive Disturbances in Data Channels
5,057,909	L. S. Mok, R. E. Schwall, and H.-M. Tong	Electronic Device and Heat Sink Assembly
5,057,935	M. L. Williams	Method for Confirmation of Document Recipient in a Data Processing System
5,057,969	J. G. Ameen, J. Funari, and J. A. Goldfuss, Jr.	Thin Film Electronic Device
5,057,997	T.-L. Chang, P. W. Hunter, D. J. Lang, and S. G. Luning	Interruption Systems for Externally Changing a Context of Program Execution of a Programmed Processor
5,058,001	H. Li	Two-Dimensional Array of Processing Elements for Emulating a Multi-Dimensional Network
5,058,003	S. W. White	Virtual Storage Dynamic Address Translator Mechanism for Multiple-Sized Pages
5,058,024	A. Inselberg	Conflict Detection and Resolution Between Moving Objects
5,058,053	J. B. Gillett	High Performance Computer System with Unidirectional Information Flow
5,058,056	W. E. Hammer and H. F. Kossman	Workstation Takeover Control
5,058,115	R. M. Blake, D. C. Bossen, C.-L. Chen, J. A. Fifield, H. L. Kalter, and T.-C. Lo	Fault Tolerant Computer Memory Systems and Components Employing Dual Level Error Correction and Detection with Lock-Up Feature
5,058,116	H. H. Chao and J.-H. Chang	Pipelined Error Checking and Correction for Cache Memories
5,058,134	P. R. Chevillat, E. Eleftheriou, D. G. U. Maidwald, and M. Quintin	Process of Synchronizing a Receiving Modem After a Training on Data
5,058,140	M. A. Johnson	Self-Correcting Serial Baud/Bit Alignment
5,058,185	H. M. Morris, C. A. Parks, D. Rajagopal, and G. L. Young	Object Management and Delivery System Having Multiple Object-Resolution Capability
5,058,997	J. A. Dickerson and N. M. Poley	TFT LCD with Optical Interference Color Filters
5,059,090	N. Bobroff and R. A. Kendall	Two-Dimensional Positioning Apparatus
5,059,129	W. L. Brodsky, T. G. Macek, and J. J. Squires	Connector Assembly Including Bilayered Elastomeric Member
5,059,243	R. Jagannathan, R. F. Knarr, M. Krishnan, and G. P. Wandy	Tetra Aza Ligand Systems as Complexing Agents for Electroless Deposition of Copper
5,059,512	E. D. Babich, J. D. Gelorme, M. Hatzakis, J. M. Shaw, K. J. Stewart, and D. F. Witman	Ultraviolet Light Sensitive Photoinitiator Compositions, Use Thereof and Radiation Sensitive Compositions
5,059,544	J. N. Burghartz, B. J. Ginsberg, and S. Mader	Method of Forming Bipolar Transistor Having Self-Aligned Emitter-Base Using Selective and Non-Selective Epitaxy
5,059,552	C. S. Harder, W. Heuberger, P. D. Hoh, and D. J. Webb	Process for Forming the Ridge Structure of a Self-Aligned Semiconductor Laser
5,059,553	E. Berndlmaier, G. Das, and T. L. Viau	Metal Bump for a Thermal Compression Bond and Method for Making Same
5,059,789	S. E. Salcudean	Optical Position and Orientation Sensor

5,059,804	H.-W. Fink, H. Schmid, and B. Stocker	Delta-Phi Microlens for Low-Energy Particle Beams
5,059,962	K. Sekiya and T. Shiraishi	Display System
5,060,088	F. B. Dolivo, S. Oelcer, A. Rüngeler, and W. H. Schott	Adaptive Equalization for Recording Systems Using Partial-Response Signaling
5,060,095	J. M. Rigotti, L. R. Tufty, and M. N. Zell	Head-Disk Enclosure Assembly for a Magnetic Disk Storage Device
5,060,136	R. W. Furney, G. C. Hurlbut, and M. P. Vachon	Four-Way Associative Cache with DLAT and Separately Addressable Arrays Used for Updating Certain Bits Without Reading Them Out First
5,060,146	P. Y. T. Chang and M. R. Cross	Multilingual Indexing System for Alphabetically Sorting by Comparing Character Weights and ASCII Codes
5,060,149	G. A. Flurry and L. A. Wagner	Method for Controlling Multiple Terminals from a Single Logically Partitioned Input Device
5,060,170	N. E. Bourgeois, S. L. Hause, and A. B. Lindquist	Space Allocation and Positioning Method for Screen Display Regions in a Variable Windowing System
5,060,233	C. S. Harder, W. Lenth, H. P. Meier, and W. P. Risk	Miniature Blue-Green Laser Source Using Second-Harmonic Generation
5,060,844	J. R. Behun, A. J. Call, F. F. Cappel, M. S. Cole, K. G. Hoebener, B. T. Klingel, and J. C. Milliken	Interconnection Structure and Test Method
5,061,070	J. S. Batchelder, D. M. DeCain, M. A. Taubenblatt, H. K. Wickramasinghe, and C. C. Williams	Particulate Inspection of Fluids Using Interferometric Light Measurements
5,061,192	F. W. Chapin, D. W. Dranchak, D. E. Engle, R. R. Hall, and T. G. Macek	High Density Connector
5,061,359	S. V. Babu, N.-H. Lu, and C.-O. Nilsen	Plasma Processing Apparatus Including Three Bus Structures
5,061,652	R. E. Bendernagel, K.-M. Kim, V. J. Silvestri, P. Smetana, T. H. Strudwick, and W. H. White	Method of Manufacturing a Semiconductor Device Structure Employing a Multi-Level Epitaxial Structure
5,061,779	D. W. Wang	Liquid Epoxy Polymer Composition and Use Thereof Based on Cycloaliphatic Amine Cured Difunctional/Polyfunctional Resin Blends
5,061,902	G. D. Carpenter	Linear Amplifier
5,062,037	D. M. Shorter and J. S. Kapernick	Method to Provide Concurrent Execution of Distributed Application Programs by a Host Computer and an Intelligent Work Station on an SNA Network
5,062,039	J. A. Brown and M. T. Wheatley	Sharing of Workspaces in Interactive Processing Using Workspace Name Tables for Linking of Workspaces
5,062,045	F. L. Janis, M. L. Williams, and D. S. Wang	System for Maintaining a Document and Activity Selectable Alterable Document History Log in a Data Processing System
5,062,671	F. E. Goetz, M. S. Miller, and H. Shelton, Jr.	Door Latch for a Computer Housing
5,062,896	W.-S. Huang, I. Y. Khandros, R. Saraf, and L. Shi	Solder/Polymer Composite Paste and Method
5,062,898	W. T. McDermott, R. C. Ockovic, J. J. Wu, D. W. Cooper, A. Schwarz, and H. L. Wolfe	Surface Cleaning Using a Cryogenic Aerosol
5,062,938	J. K. Howard	High Coercivity Low Noise Cobalt Alloy Magnetic Recording Medium and its Manufacturing Process
5,063,120	D. A. Edmonson, P. R. Ivett, K. E. Johnson, S. M. T. Mirzamaani, and J. F. Ward, Jr.	Thin Film Magnetic Media
5,063,173	M. Gasser and E. E. Latta	Method for Mirror Passivation of Semiconductor Laser Diodes
5,063,300	M. Kallmeyer and D. Wagner	Method and Apparatus for Determining Line Centers in a Microminiature Element
5,063,362	P. A. Herman and W. F. McCarthy	Suppression of Electrical Interferences from an Electronic Circuit
5,063,464	H. Astheimer, H. Brumme, W. Heck, K. Kissel, and H. Schuy	Low Profile Suspension
5,063,475	A. L. Balan	Multileveled Electronic Assembly with Cooling Means
5,063,495	M. G. MacPhail	Method of Associating Document Processing States with an Action Market
5,063,496	R. A. Dayan, D. R. Geisler, S. C. Kinnear, J. F. Macon, Jr., and W. H. Schwartz	Signaling Attempted Transfer to Protected Entry Point BIOS Routine
5,063,500	D. U. Shorter	System for Executing Segments of Application Program Concurrently/Serially on Different/Same Virtual Machine
5,063,506	J. C. Brockwell and E. J. Collins	Cost Optimization System for Supplying Parts
5,063,537	C. Akrou, P. Coppens, Y. Gautier, and P.-Y. Urena	Reprogrammable Logic Based on a 6-Device SRAM Cell for Logic Arrays

5,063,562	T. P. Barzilai, M.-S. Chen, B. K. Kadaba, and M. A. Kaplan	Flow Control for High Speed Networks
5,063,655	P. Lamey and R. Kachmarik	Method to Integrate Drive/Control Devices and Ink Jet on Demand Devices in a Single Printhead Chip
5,063,951	S. L. Bard, D. N. Christensen, J. J. Glenning, J. A. Nicoletti, and M. W. Urdanick	Fluid Treatment Device
5,064,063	A. Manca and H. Mayron	Tube Assembly for Pin Grid Array Modules
5,064,264	S. P. Ducharme, W. E. Moerner, J. C. Scott, and R. J. Twieg	Photorefractive Materials
5,064,681	C. J. Berry, J. J. Cuomo, C. R. Guarnieri, and D. S. Yee	Selective Deposition Process for Physical Vapor Deposition
5,064,772	C. G. Jambotkar	Bipolar Transistor Integrated Circuit Technology
5,064,777	S. H. Dhong and W. Hwang	Fabrication Method for a Double Trench Memory Cell Device
5,065,103	J. A. Slinkman, H. K. Wickramasinghe, and C. C. Williams	Scanning Capacitance-Voltage Microscopy
5,065,146	M. J. Garrett	Manually-Operated Control Device
5,065,227	J. A. Frankeny, R. F. Frankeny, K. Hermann, and R. L. Imken	Integrated Circuit Packaging Using Flexible Substrate
5,065,273	T. V. Rajeevakumar	High Capacity DRAM Trench Capacitor and Methods of Fabricating Same
5,065,279	K. Lazenby and A. R. Cox	Integral Protective Enclosure for Tab or Similar Delicate Assembly Mounted Locally on a Flexible Printed Circuit Board
5,065,310	H. S. Stone	Reducing Cache-Reload Transient at a Context Swap
5,065,339	S. Vassiliadis and G. G. Pechanek	Orthogonal Row-Column Neural Processor
5,065,368	S. Gupta, R. L. Henderson, N. R. Hildebeitel, R. Tamlyn, S. W. Tomashot, and T. Williams	Video RAM Double Buffer Select Control
5,065,483	R. P. Zammit	Method of Lapping Magnetic Recording Heads
5,065,858	N. J. Akam, D. M. Cameron, D. K. Halliday, and R. Paterson	Gas-Fed Transfer Track
5,065,932	T. F. Hayden, C. A. Hicks, P. G. Ledermann, A. D. Nguyne, S. C. Steinbach, and S. K. Yu	Solder Placement Nozzle with Inert Cover Gas and Inert Gas Bleed
5,066,360	L. R. Daley and G. G. Leslie II	Pad Printing of Resist Over Via Holes
5,066,370	J. C. Andreshak, M. Datta, L. T. Romankiw, and L. F. Vega	Apparatus, Electrochemical Process, and Electrolyte for Microfinishing Stainless Steel Print Bands
5,066,512	M. P. Goldowsky and W. B. Pennebaker	Electrostatic Deposition of LCD Color Filters
5,066,552	J. K. Howard, R. G. Simmons, and T. Yogi	Low Noise Thin Film Metal Alloy Magnetic Recording Disk
5,067,041	K. K. Cooke, J. R. Dewitt, and J. E. McCloskey	Apparatus for Reducing Electromagnetic Radiation from a Computer Device
5,067,073	P. N. Andrews	Embedded Tracing Method for Identifying Program Executed Paths
5,067,105	J. M. Borkenhagen, Q. G. Schmierer, and C. P. Geer	System and Method for Automatically Configuring Translation of Logical Addresses to a Physical Memory Address in a Computer Memory System
5,067,129	T. G. Evans, M. W. Gould, and T. J. McNamara	Service Processor Tester
5,067,166	N. Ito	Method and Apparatus for DP Matching Using Multiple Templates
5,068,054	K. M. Betterton, W. D. Hinsberg, H. T. Nguyen, W. T. Tang, and R. J. Twieg	Ferroelectric Liquid Crystals
5,068,124	J. Batey and E. Tierney	Method for Depositing High Quality Silicon Dioxide by PECVD
5,058,776	W. M. Polivka	Switched-Mode DC-DC Power Converter for Reducing Effects of Magnetization Current
5,068,819	M. Misra, R. J. Urquhart, M. T. Vanover, and J. A. Voltin	Floating Point Apparatus with Concurrent Input/Output Operations
5,068,858	M. M. Blaum and H. T. Hao	Error Correction Capability Varied with Track Location on a Magnetic or Optical Disk
5,068,875	M. Quintin	Adaptive Loop Gain Phase Filter
5,068,916	C. G. Harrison and P. D. Hortensius	Coordination of Wireless Medium Among a Plurality of Base Stations
5,069,522	T. R. Block, M. B. Ebler, L. W. Freitag, G. M. Heiling, S. C. Holter, D. L. Karst, D. W. Siljensberg, R. L. Soderstrom, and J. T. Trnka	Optical Fiber Link Card
5,070,448	D. R. Crandall	Method for Testing a Microprogrammed Input/Output Interface Using Steal Techniques
5,070,456	D. Garneau and W.-H. Sears	Method for Facilitating the Sorting of National Language Keys in a Data Processing System

5,070,470	K. J. Scully and H. S. Singh	Methods for Handling Calendar Information in a Data Processing System
5,070,471	S. Dao-Trong, K. J. Getzlaff, and K. Helwig	High Speed Multiplier which Divides Multiplying Factor into Parts and Adds Partial End Products
5,070,495	W. L. Bletscher, Jr., J. Lewkowicz, and M. Tayefeh	Write Calibration for Magneto-optic Disk Recorders
5,070,504	D. D. Bossen, C.-L. Chen, and M.-Y. Hsiao	Method for Apparatus for Providing Error Correction to Symbol Level Codes
5,070,534	M. C. Lascelles and M. Wong	Simplified CAD Parametric Macroinstruction Capability Including Variational Geometrics Feature
5,071,357	K. B. Van Brunt, Jr. and J. R. Petrozello	Fluid Pressure Actuated Electrical Connector
5,071,508	A. Scheithauer, W. Alberth, G. Pohl, and H. Ziegler	Method of and Arrangement for Etching an Etch Material Consisting at Least Partly of Metal
5,071,714	K. P. Rodbell, P. A. Totta, and J. F. White	Multilayered Intermetallic Connection for Semiconductor Devices
5,071,730	R. D. Allen, W. D. Hinsberg III, L. L. Simpson, and G. M. Wallraff	Liquid Apply, Aqueous Processable Photoresist Compositions
5,071,788	R. V. Joshi	Method for Depositing Tungsten on Silicon in a Non-Self-Limiting CVD Process and Semiconductor Device Manufactured Thereby
5,072,076	W. O. Camp, Jr.	Tablet Digitizer with Untethered Stylus
5,072,126	C. J. Proglar	Proximity Alignment Using Polarized Illumination and Double Conjugate Projection Lens
5,072,217	C. J. Georgiou and A. M. Varma	One-Sided Crosspoint Switch with Distributed Control
5,072,318	M. M. Yu	Disk File with Adaptive Cancellation of Nonrepeatable Disk Runout
5,072,367	N. H. Clayton, J. L. Rivero, and K.-C. Sun	System Using Two Passes Searching to Locate Record Having Only Parameters and Corresponding Values of an Input Record
5,072,370	P. A. Durdik	System and Method for Monitoring Electronic Data Processing Equipment
5,072,386	D. Garneau and W.-H. Sears	Method for Culturally Predictable Keysort Within a National Language Support (NLS) Data Processing System
5,072,452	P. F. Brown, P. V. DeSouza, D. Nahomoo, and M. A. Picheny	Automatic Determination of Labels and Markov Word Models in a Speech Recognition System
5,073,180	S. Farooq, G. Natarajan, S. S. N. Reddy, R. A. Shelleman, N. C. Stoffel, and R. V. Vallabhaneni	Method for Forming Sealed Co-Fired Glass Ceramic Structures
5,073,730	C. R. Hoffman	Current Transient Reduction for VLSI Chips
5,073,834	J. S. Best, S. R. Hetzler, W. J. Kabelac, and D. A. Thompson	Disk File or Tape Drive with High Servo Sample Rate Embedding Additional Servo Sectors Within Individual Data Sectors
5,073,934	S. M. Matyas, D. B. Johnson, A. V. Le, W. C. Martin, R. Prymak, W. S. Rohland, and J. D. Wilkins	Method and Apparatus for Controlling the Use of a Public Key, Based on the Level of Import Integrity for the Key
5,073,938	C. Galand	Process for Varying Speech Speed and Device for Implementing Said Process
5,073,941	M. E. Locke	Multifrequency Detection
5,073,962	A. Califano	Generalized Neighborhoods Parameter Transform for Image Features Extraction
5,074,029	W. W. Brooks, Jr., J. B. Brown, J. T. Coffey, R. H. Estry, M. P. Graves, G. L. Heitkamp, L. H. Lengerman, T. J. Myhre, Sr., T. L. Schaefer, P. D. Teig, A. C. Tougas, D. J. Wanek, J. H. Wirz, and W. E. Zahn	Method for Stringing Wire on an Actuator Arm
5,074,969	W. D. Brewer, K. R. Grebe, R. R. Horton, L. C. Matthew, I. C. Noyan, M. J. Palmer, S. Purushothaman, and D. L. Rath	Composite and Coating to Prevent Current Induced Electrochemical Dendrite Formation Between Conductors on Dielectric Substrate
5,075,566	C.-T. K. Chuang and H. J. Shin	Bipolar Emitter-Coupled Logic Multiplexer
5,075,571	S. H. Dhon, W. Hwang, and Y. Taira	PMOS Wordline Boost Circuit for DRAM
5,075,574	G. Boudon	Differential Cascode Current Switch (DCCS) Logic Circuit Family with Input Diodes
5,075,621	E. S. Hoyt	Capacitor Power Probe
5,075,673	P. C. Yanker	Variable Speed, Image Pan Method and Apparatus
5,075,675	B. A. Barker, T. R. Edel, and J. A. Stark	Method and Apparatus for Dynamic Promotion of Background Window Displays in Multi-Tasking Computer Systems
5,075,840	G. F. Grohoski, J. A. Kahle, M. Nguyenphu, and D. S. Ray	Tightly Coupled Multiprocessor Instruction Synchronization
5,075,965	C. F. Carey, K. M. Fallon, R. Ginsburg, and C. G. Woychik	Low Temperature Controlled Collapse Chip Attach Process
5,077,267	E. M. Engler, T. T. Kodas, and V. Y. Lee	Process for Making Composite High Temperature Superconductor Copper Wires

5,077,658	E. J. Bendert and R. B. Bennett	Data Access System for a File Access Processor
5,077,669	D. Garneau and W.-H. Sears	Method for Quasi-Key Search Within a National Language Support (NLS) Data Processing System
5,077,676	C. L. Johnson, R. F. Lembach, B. G. Rudolph, and R. R. Williams	Reducing Clock Skew in Large-Scale Integrated Circuits
5,077,681	A. Miyazawa	System for Source Image Transformation
5,077,748	W. J. Kozlovsky and W. Lenth	Laser System and Method
5,077,763	A. Gagnoud and P. Pignal	Mechanism for Measuring the Service Times of Software and Hardware Components in Complex Systems
5,077,826	G. F. Grohoski, J. F. Kearns, and S. G. Ludwig	Cache Performance in an Information Handling System Employing Page Searching
5,079,983	P. H. Bruhn	Replaceable Head for Magnetic Repulsion Punch
5,080,825	R. L. Bradshaw	Tape Drive Cleaning Composition
5,081,439	W. C. Natzle, H. B. Pogge, and K. L. Batdorf	Thin Film Resistor and Method for Producing Same
5,081,450	B. D. Lucas, D. P. Rackley, and A. Saenz	Apparatus and Method for Compressing and Expanding Multibit Digital Pixel Data
5,081,477	B. D. Gibson	Method and Apparatus for Synchronizing the PEL Clock of a Laser Printer
5,081,552	T. W. Glaser, R. Greenberg, N. F. Misso, and R. E. Schopp	Rigid Magnetic Disk Drive Head Disk Assembly and Enclosure Structure
5,081,563	B.-C. Feng, G. C. Feng, and R. H. McMaster	Multi-layer Package Incorporating a Recessed Cavity for a Semiconductor Chip
5,081,574	L. D. Larsen and D. J. Esteban	Branch Control in a Three Phase Pipelined Signal Processor
5,081,607	M. D. Bates, N. D. Butler, A. C. Gay, J. H. Kim, and R. M. West	Arithmetic Logic Unit
5,081,622	M. M. Nassehi, J. R. Muller, E. A. Zurfluh, P. A. Zafropulo, and W. K. Bux	Method and Apparatus for Distributed Queue Multiple Access in a Communication System
5,081,623	F. Ainscow	Communication Network
5,081,624	B. L. Beukema	Fault-Tolerant Serial Attachment of Remote High-Speed I/O Busses
5,081,677	L. L. Green, M. J. Kelly, R. M. Smith, J. Thomas, and P. C. Yeh	Cryptographic Key Version Control Facility
5,083,229	A. H. Frey, Jr.	Disk Drive with Minimized Head-Arm Excursions
5,083,262	L. E. Haff, Jr.	Language Bindings for Graphics Functions to Enable One Application Program to be Used in Different Processing Environments
5,083,927	J. D. Herard and S. R. Herard	Solderless Compliant Socket
5,083,931	R. V. Davidge and J. R. Dewitt	Device Grounding Spring
5,084,071	A. Nenadic and R. W. Pasco	Method of Chemical-Mechanical Polishing an Electronic Component Substrate and Polishing Slurry Therefor
5,084,331	A. Afzali-Ardakani	Electroerosion Recording Medium of Improved Corrosion Resistance
5,084,417	R. V. Joshi, C.-S. Oh, and D. Moy	Method for Selective Deposition of Refractory Metals on Silicon Substrates and Device Formed Thereby
5,084,610	H.-G. Markgraf, J. Moll, and K. Schroth	Alignment and Transport Roll Made of Flexible Material
5,084,637	R. P. Gregor	Bidirectional Level Shifting Interface Circuit
5,084,666	J. P. Bolsh	Switchable Output Voltage Converter
5,084,783	A. G. Dewey, W. Lenth, and D. Rugar	Focus-Error Detection Using Prism-Enhanced Spot Size Monitoring
5,084,840	W. J. Kozlovsky and W. P. Risk	Passive Absorptive Resonator Laser System and Method
5,086,016	S. B. Brodsky, R. V. Joshi, J. S. Lechaton, J. G. Ryan, and D. J. Schepis	Method of Making Semiconductor Device Contact Including Transition Metal-Compound Dopant Source
5,086,018	H. W. Conru, G. H. Irish, F. J. Pakulski, W. J. Slaterry, S. G. Starr, and W. C. Ward	Method of Making a Planarized Thin Film Covered Wire Bonded Semiconductor Package
5,086,321	J. Batey, S. Tiwari, and S. L. Wright	Unpinned Oxide-Compound Semiconductor Structures and Method of Forming Same
5,086,387	R. X. Arroyo and J. T. Hanna	Multi-Frequency Clock Generation With Low State Coincidence Upon Latching
5,086,393	R. H. Kerr and R. M. Mesnard	System for Testing Human Factors and Performance of a System Program