

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

1,272,312	A. G. Ryman	Virtual Memory for Image Processing
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Federal Republic of Germany

3,763,835,108	C. Trollmann, D. Dierich, H. Wilken, and W. Rotter	Drilling Unit
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Sweden

118,037	B. E. Norstedt	Computer Network System and its Use for Information
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United Kingdom

244,525	J. S. Heath and M. R. Hatchett	Disk File with Air Filtration System
2,198,019	A. C. Gay and C. J. Easton	Graphics Processing System
2,191,918	J. A. Herrod, M. W. Ricketts, and T. Anthias	Data Display System

United States

4,911,598	G. J. A. Sarvary and C. D. Wilson	Robotic Assembly Apparatus with Robot Tool for Placing a Plurality of Component Parts on a Workpiece
4,911,786	T. E. Kindl, P. G. Rickerl, and D. J. Russell	Method of Etching Polyimides and Resulting Passivation Structure
4,912,339	R. A. Béchade and C. R. Ogilvie	Pass Gate Multiplexer
4,912,521	F. A. Almquist, R. Gjone, T. A. Larsen, and R. Hawryluk	Electro-optical Transducer Assembly
4,912,547	J. A. Bilowith, E. J. Dombroski, W. H. Guthrie, and R. W. Noth	Tape Bonded Semiconductor Device
4,912,585	K. A. Belser, T. Makansi, and I. L. Sanders	Discrete Track Thin Film Magnetic Recording Disk with Embedded Servo Information
4,912,628	A. A. Briggs	Suspending and Resuming Processing of Tasks Running in a Virtual Machine Data Processing System
4,912,632	A. Gach, Y. Hartmann, and M. Peyronnenc	Memory Control Subsystem
4,912,648	I. Tyler	Expert System Inference Engine
4,912,659	B. C. Liang	Parallel Surface Processing System for Graphics Display
4,912,707	P. M. Kogge, K. T. Truong, D. A. Richard, and R. L. Schoenike	Checkpoint Retry Mechanism
4,912,758	Y. Arbel	Full-Duplex Digital Speakerphone
4,912,763	C. Galand and M. Rosso	Process for Multirate Encoding Signals and Device for Implementing Said Process
4,912,767	R. W. Chang	Distributed Noise Cancellation System

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4,912,772	J. S. Beaudry, Jr., P. A. Engel, J. R. Petrozello, and S. R. Shinnars	Connector and Circuit Package Apparatus for Pin Array Circuit Module and Circuit Board
4,912,883	M. Y. Chang, M. A. Church, and M. P. Salo	Lapping Control System for Magnetic Transducers
4,913,571	K. F. Bulson, C. L. Decoste, Jr., and J. W. Morris	Re-Inking Roller and Transfer Roller Assembly
4,913,600	D. Dierich, W. Rotter, and C. Trollmann	Drilling Unit
4,914,398	S. A. Jove, K. B. Klaassen, and J. C. L. van Peppen	Method and Circuitry to Suppress Additive Disturbance in Data Channels Containing MR Sensors
4,914,538	J. K. Howard, H.-C. W. Huang, and C. Hwang	Magnetoresistive Read Transducer
4,914,551	M. Anschel and B. G. Sammakia	Electronic Package with Heat Spreader Member
4,914,569	F. E. Levine and C. Mohan	Method for Concurrent Record Access, Insertion, Deletion and Alteration Using an Index Tree
4,914,571	A. E. Baratz, I. S. Gopal, J. P. Gray, G. A. Grover, J. M. Jaffe, J. A. Lorrain, M. R. Pollard, D. P. Pozefsky, M. Pozefsky, and L. M. Rafalow	Locating Resources in Computer Networks
4,914,579	M. Putrino, S. Vassiliadis, A. E. Huffman, and A. G. Ngai	Apparatus for Branch Prediction for Computer Instructions
4,914,612	D. K. Beece, M. M. Denneau, P. H. Hochschild, A. Rappaport, and C. A. Trempel	Massively Distributed Simulation Engine
4,914,617	M. Putrino, S. Vassiliadis, and E. M. Schwartz	High Performance Parallel Binary Byte Adder
4,914,619	J. F. Harris, F. A. Schwartz, and R. C. Will	Apparatus and Method for Interconnecting an Application of a Transparent Services Access Facility to Remote Source
4,914,625	A. F. J. Billian	Bypass Mechanism for Daisy Chain Connected Units
4,914,634	C. Akrou, P. Coppens, B. Denis, and P.-Y. Urena	Reference Voltage Generator for CMOS Memories
4,914,644	J. C. Chen, S.-M. Shih, and M. M. Yu	Disk File Digital Servo Control System with Coil Current Modeling
4,914,704	A. G. Cole and R. H. Riekert	Text Editor for Speech Input
4,914,725	K. A. Belser, P. J. Dounn, I. E. Henderson, R. J. Kedlec, and S. D. Roberts	Transducer Positioning Servo Mechanisms Employing Digital and Analog Circuits
4,914,740	D. M. Kenney	Charge Amplifying Trench Memory Cell
4,914,814	J. R. Behun, W. R. Miller, B. H. Newman, and E. L. Yankowski	Process of Fabricating a Circuit Package
4,914,868	M. A. Church, A. P. Deshpande, A. M. Desouches, G. S. Pal, M. P. Salo, and M. I. Ullah	Lapping Control System for Magnetic Transducers
4,915,280	J. K. Howes and J. H. Peterra	Forms Feed Tractors
4,915,482	R. T. Collins, J. R. Kirtley, and T. N. Theis	Optical Modulator
4,915,519	A. Afzali-Ardakani, M. Desai, and K. S. Pennington	Direct Negative from Resistive Ribbon
4,916,083	M. D. Monkowski and J. F. Shepard	High Performance Sidewall Emitter Transistor
4,916,259	R. S. Charsky, L. T. Olson, and D. P. Pagnani	Composite Dielectric Structure for Optimizing Electrical Performance in High Performance Chip Support Packages
4,916,260	C. J. Broadus, J. R. Jephson, E. M. Scales, C. C. Sissenstein, and J. P. Wiley	Circuit Member for Use in Multilayered Printed Circuit Board Assembly and Method of Making Same
4,916,301	R. L. Mansfield, A. K. Spencer, and J. C. St. Clair	Graphics Function Controller for a High Performance Video Display System
4,916,519	W. C. Ward	Semiconductor Package
4,916,605	B. C. Beardsley, M. D. Canon, M. C. Easton, M. H. Hartung, J. H. Howard, and R. H. Vosacek	Fast Write Operations
4,916,608	S. S. Shultz	Provision of Virtual Storage Resources to an Operating System Control Program
4,916,627	D. J. Hathaway	Logic Path Length Reduction Using Boolean Minimization
4,916,637	L. M. P. Allen, R. G. Elshaug, C. L. Harney, W. L. Lemmon, I. Miller, I. L. Miller, G. D. Murray, M. L. Nordstrom, L. F. Peterson, G. E. Rollings, J. A. Schablitsky, J. D. Shanklin, A. V. Steinman, T. W. Suther III, D. C. Taylor, D. C. Walberg, and E. P. Wojtczak	Customized Instruction Generator
4,916,652	E. M. Schwarz and S. Vassiliadis	Dynamic Multiple Instruction Stream Multiple Data Multiple Pipeline Apparatus for Floating-Point Single Instruction Stream Single Data Architectures

4,916,654	A. M. Sherman and P. C. Yanker	Method for Transfer of Data Via a Window Buffer from a Bit-Planar Memory to a Selected Position in a Target Memory
4,916,658	C.-C. Lee and C. R. Moore	Dynamic Buffer Control
4,916,680	D. M. Oldham	Magneto-optic Recording Member Having Selectively-Reversed Erasure Directions in Predetermined Recording Areas of the Record Member
4,916,688	J. S. Foster, K. A. Rubin, and D. Rugar	Data Storage Method Using State Transformable Materials
4,916,697	T. J. Roche and G. S. Still	Apparatus for Partitioned Clock Stopping in Response to Classified Processor Errors
4,916,701	J. S. Eggenberger, P. Hodges, and A. M. Patel	Method and System for Correcting Long Bursts of Consecutive Errors
4,916,703	N. T. Christensen, S. T. Comfort, R. J. Hurban, B. L. McGilvray, A. J. Sutton, J. R. Urquhart, and D. R. Willoughby	Handling Errors in the C Bit of a Storage Key
4,916,738	A. N. Chandra, L. D. Comerford, and S. R. White	Remote Access Terminal Security
4,917,453	T. R. Block, G. M. Heiling, D. L. Karst, D. C. Kobliska, T. Lu, and R. L. Soderstrom	Optical Communication Signal Source
4,918,033	J. W. Bartha, T. Bayer, J. Greschner, G. Kraus, and G. Schmid	PECVD (Plasma Enhanced Chemical Vapor Deposition) Method for Depositing of Tungsten or Layers Containing Tungsten by In Situ Formation of Tungsten Fluorides
4,918,262	D. R. Flowers, K. P. Goffinet, A. W. Miles, J. T. Rowe, Jr., and K. H. Vorhees	Touch Sensing Display Screen Signal Processing Apparatus and Method
• 4,918,309	J. G. Beha, A. U. Blacha, R. Clauberg, R. B. Moeller, and W. D. Pohl	Method for Investigating Surfaces at Nanometer and Picosecond Resolution and Laser-Sampled Scanning Tunneling Microscope for Performing Said Method
4,918,334	A. Correale, Jr. and G. C. Luckett	Bias Voltage Generator for Static CMOS Circuits
4,918,429	D. A. Clarke	Display System with Symbol Font Memory
4,918,448	P. Hauviller and M. Verhaeghe	Device for Extending the Resolution of a N-Bit Resistive Digital-to-Analog Converter to a (N+P)-Bit Digital-to-Analog Converter
4,918,545	J. N. Scheffel	Disk Clamp for Rigid Disk File Without Radial Load
4,918,554	C. H. Bajorek, C. T. Horng, and E. T. Yen	Process for Making a Shielded Magnetoresistive Sensor
4,918,574	J. G. Hoffarth, D. J. Lazzarini, J. A. Welsh, and J. P. Wiley	Multilayer Circuit Board with Reduced Susceptibility to Shorts Caused by Trapped Impurities
4,918,595	K. A. Kahn, R. M. Martinez, and J. P. Vainkainen	Subsystem Input Service for Dynamically Scheduling Work for a Computer System
4,918,639	E. M. Schwarz and S. Vassiliadis	Overlapped Multiple-Bit Scanning Multiplication System with Banded Partial Product Matrix
4,918,653	A. Johri and G. L. Luckenbaugh	Trusted Path Mechanism for an Operating System
4,918,687	L. Bustini, A. Cretegny, G. Marmigere, G. Platel, and P. Secondo	Digital Packet Switching Networks
4,918,728	S. M. Matyas, D. G. Abraham, D. B. Johnson, R. K. Karne, A. V. Le, R. Prymak, J. Thomas, J. D. Wilkins, and P. C. Yeh	Data Cryptography Operations Using Control Vectors
4,918,812	R. E. Ruane	Processing of Cores for Circuit Boards or Cards
4,919,076	D. L. Lutz, K. W. Meranda, and R. B. Minter	Reusable Evaporation Fixture
4,919,729	P. J. Elmgren, A. J. Emerick, D. L. Rivenburg, M. K. Saraiya, and D. W. Sissenstein, Jr.	Solder Paste for Use in a Reducing Atmosphere
4,919,750	R. C. Bausmith, W. J. Cote, J. E. Cronin, K. L. Holland, C. W. Kaanta, P.-I. P. Lee, and T. M. Wright	Etching Metal Films with Complexing Chloride Plasma
4,919,780	H.-W. Fink, R. Morin, H. Schmid, and W. Stocker	Method of Making Self-Aligned Apertures
4,919,970	K. G. Hoebener and J. J. Stankus	Solder Deposition Control
4,919,971	C. J. Chen	Self-Induced Repairing of Conductor Lines
4,920,065	D. Chin and S. H. Dhong	Method of Making Ultra Dense DRAM Cells
4,920,069	E. R. Fossum, P. D. Kirchner, G. D. Pettit, A. C. Warren, and J. M. Woodall	Submicron Dimension Compound Semiconductor Fabrication Using Thermal Etching
4,920,091	D. R. Grischkowsky	Direct Generation of Ultrafast Electrical Pulses
4,920,237	R. A. Eardley, B. J. Keenan, I. A. Lawson, and J. McAllister	Membrane Keyboards
4,920,383	W. P. Cook	Paper Handling for Repetitive Movement of Variable Length Media Through an Image Transfer Station
4,920,429	R. S. Jaffe, M. A. Lavin, R. A. Rand, and P. Schreiner	Exposure Compensation for a Line Scan Camera

4,920,538	J. N. Chan and A. Moy	Method of Checking the Execution of Microcode Sequences
4,922,125	W. J. Canasova, R. F. Dimmick, W. A. Hall, L. C. Homan, F. J. Lukes, B. L. Martin, C. J. Mosley, A. P. Reckinger, Jr., P. W. Schaefer, Z. D. Squillace, G. W. Westphal, and S. E. Wheeler	System Cable Assembly and Component Packaging
4,922,128	S. H. Dhong, W. Hwang, and N. C. Lu	Boost Clock Circuit for Driving Redundant Wordlines and Sample Wordlines
4,922,135	P. Mollier and P. Tannhof	GaAs MESFET Logic Circuits Including Push Pull Output Buffers
4,922,238	T. Aoki, H. Iida, and K. Nojima	Method and System for Smooth Scrolling of a Displayed Image on a Display Screen
4,922,347	L. V. Galetto and C. O. Ross	Apparatus for Recording Displayed Video Data
4,922,451	Y.-C. Lo, D. L. Moeller, and J. J. Szarek	Memory Re-Mapping in a Microcomputer System
4,922,455	W. B. Chin, R. D. Dussault, R. W. Knepper, F. C. Wernicke, and R. C. Wong	Memory Cell With Active Device for Saturation Capacitance Discharge Prior to Writing
4,922,491	J. L. Coale	Input/Output Device Service Alert Function
4,924,284	K. D. Beyer and V. J. Silvestri	Method of Trench Filling
4,924,384	M. C. Hao, R. L. Obermarck, G. E. Trivett, and L. Trivett	Method for Controlling the Peer-to-Peer Processing of a Distributed Application Across a Synchronous Request/Response Interface Using Push-Down Stack Automata
4,924,408	F. D. Highland	Technique for Compilation of Knowledge Bases
4,924,423	S. Vassiliadis and E. M. Schwarz	High Speed Parity Prediction for Binary Adders Using Irregular Grouping Scheme
4,924,424	S. Vassiliadis, E. M. Schwarz, M. Putrino, and B. J. Feal	Parity Prediction for Binary Adders with Selection
4,924,434	P. J. Christenson, R. J. Hart, D. C. Naatz, E. J. Rowland, and D. G. Wenz	Sharing Word-Processing Functions Among Multiple Processors
4,924,466	S. L. Gregor and V. S. Lee	Direct Hardware Error Identification Method and Apparatus for Error Recovery in Pipelined Processing Areas of a Computer System
4,924,484	L. J. Grasso, D. E. Hoffman, C. E. Morgan, C. A. Puntar, and D. K. Young	High Speed Digital Counter
4,924,493	M.-H. Dang and J. R. Taborga	Method for Monitoring Call Setup Communications
4,924,508	H. Crepy, P. Elie, C. Galand, E. Lancon, T. Liethoudt, and M. Rosso	Pitch Detection for Use in a Predictive Speech Coder
4,924,514	S. M. Matyas, D. G. Abraham, D. B. Johnson, R. K. Karne, A. V. Lee, A. Prymak, J. Thomas, J. D. Wilkins, P. C. Yeh, and R. M. Smith	Personal Identification Number Processing Using Control Vectors
4,924,515	S. M. Matyas, D. G. Abraham, W. C. Arnold, D. B. Johnson, R. K. Karne, A. V. Lee, R. Prymak, S. R. White, and J. D. Wilkins	Secure Management of Keys Using Extended Control Vectors
4,924,521	R. F. Dinan, J. F. Dubil, J. R. Hillery, R. R. Rodite, and J. M. White	Image Processing System and Method Employing Combined Black and White and Gray Scale Image Data
4,925,139	M. A. McCord	Mechanical Stage Support for a Scanning Tunneling Microscope
4,925,523	B. E. Braren and R. Srinivasan	Enhancement of Ultraviolet Laser Ablation and Etching Organic Solids
4,925,700	B. Brezoczky, J. J. Cuomo, C. R. Guarnieri, K. V. Ramanathan, S. A. Shivashankar, D. A. Smith, and D. S. Yee	Process for Fabricating High Density Disc Storage Device
4,926,349	L. R. Thor	Method for Display Panel Processing
4,926,369	E. Hokenek and R. K. Montoye	Leading 0/1 Anticipator (LZA)
4,926,370	J. D. Brown, D. L. Freerksen, S. A. Hilker, and D. L. Stasiak	Method and Apparatus for Processing Postnormalization and Rounding in Parallel
4,926,371	S. Vassiliadis, E. M. Schwarz, and B. M. Sung	Two's Complement Multiplication with a Sign Magnitude Multiplier
4,926,374	M. M. Schaffer	Residue Checking Apparatus for Detecting Errors in Add, Subtract, Multiply, Divide and Square Root Operations
4,926,414	A. E. Baratz and G. A. Grover	Control Point Session Synchronization in a Network
4,926,418	I. Cidon and Y. Ofek	Fairness Algorithm for Full-Duplex Buffer Insertion Ring
4,926,442	E. R. Bukowski and C. R. Hoffman	CMOS Signal Threshold Detector
4,926,488	A. J. Nadas and D. Nahamoo	Normalization of Speech by Adaptive Labelling