

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Switzerland

196,346	D. W. Pohl, J. K. Gimzewski, U. T. Duerig, and W. D. Pohl	Apparatus for Manufacturing Surface Structures in the Nanometer Range
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United Kingdom

140,013	A. N. Other, B. Brachtel, C. Holloway, C. H. Meyer, C. J. Holloway, J. Oseas, M. Matyas, R. E. Lennon, and S. M. Matyas	Improvements in Point of Sale and Electronic Funds Transfer Systems
211,119	D. A. Clarke	Digital Data Processing Arbitration System

United States

4,795,981	F. Ertingshausen and J. D. Mehrens	Method for Monitoring the Performance of the Head-Disk-Interface and Device for Preventing Data Losses Due to Magnetic Head-Disk-Interferences
4,796,222	F. J. Aichelmann, Jr. and V. F. Sollitto, Jr.	Memory Structure for Nonsequential Storage of Block Bytes in Multi-bit Chips
4,801,869	E. J. Sprogis	Semiconductor Defect Monitor for Diagnosing Processing-Induced Defects
4,801,988	D. M. Kenney	Semiconductor Trench Capacitor Cell with Merged Isolation and Node Trench Construction
4,803,100	J. G. Ameen, T. F. Carden, L. R. Cutting, S. R. Engle, and R. J. Moore	Suspension and Use Thereof
4,805,683	I. E. Magdo and D. W. Ormond, Jr.	Method for Producing a Plurality of Layers of Metallurgy
4,806,755	U. T. Duerig, J. K. Gimzewski, J. Greschner, W. D. Pohl, and O. Wolter	Microchemical Atomic Force Sensor Head
4,807,006	D. L. Rogers, J. M. Woodall, G. D. Pettit, and D. T. McInturff	Heterojunction Interdigitated Schottky Barrier Photodetector
4,807,970	R. R. Dube and S. Yao	Holograms in Photorefractive Materials
4,808,365	B. M. J. Kellner and S. J. Scilla	Method for Reducing Edge Curl on Green Sheets
4,808,431	R. G. Rickert	Method for Controlling Plating on Seeded Surfaces
4,808,435	M. E. Cropp, P. De Pardo, and K. S. Desai	Screen Printing Method for Producing Lines of Uniform Width and Height
4,808,511	S. J. Holmes	Vapor Phase Photoresist Silylation Process
4,808,545	K. Balasubramanyam, R. R. Joseph, and R. B. Renbeck	High Speed GaAs MESFET Having Refractory Contacts and a Self-Aligned Cold Gate Fabrication Process
4,808,840	P. W. Chung and N. N. Wang	Dynamic Edge-Triggered Latch
4,808,986	R. L. Mansfield, M. Segre, A. K. Spencer, and J. C. St. Clair	Graphics Display System with Memory Array Access
4,809,102	M. R. Hatchett and J. S. Heath	Disk File with Air Filtration System
4,809,109	J. K. Howard, M. F. Toney, and C. H. Tsang	Magnetoresistive Read Transducer and Method for Making the Improved Transducer
4,809,155	M. L. Costes, G. Dalboussi�re, and K. J. Barker	Very High Speed Line Adapter for a Communication Controller

4,809,168	J. P. Hennessy, D. L. Osisek, and J. W. Seigh II	Passive Serialization in a Multitasking Environment
4,809,196	D. A. Miller	Method for Designating/Sorting Semiconductor Wafers According to Predicted Oxygen Precipitation Behavior
4,809,219	T. J. Ashford, N. A. Burns, R. L. Flagg, C. T. Iwaskiw, and R. P. Starbird	Method for Processing an Expert System Rulebase on a System Having Limited Memory
4,809,257	D. Gantenbein, F. R. Gfeller, and E. P. Mumprecht	Hierarchical Distributed Infrared Communication System
4,809,275	S. P. Jackowski and R. B. Jenkins	Device for Verifying Operation of a Checking Code Generator
4,810,119	A. Afzali-Ardakani, R. T. Cannavaro, W. Crooks, M. Desai, K. S. Pennington, J.-P. Hoekstra, and E. E. Simonyi	Resistive Ribbon for High Resolution Printing
4,810,322	G. M. Gut and S. E. Monahan	Anode Plate for a Parallel-Plate Reactive Ion Etching Reactor
4,810,326	S. V. Babu, V. Q. Bui, J. G. Hoffarth, and J. A. Welsh	Interlaminar Adhesion Between Polymeric Materials and Electrolytic Copper Surfaces
4,810,601	R. D. Allen, K. N. Chiong, M.-F. Chow, S. A. MacDonald, J.-M. Yang, and C. G. Willson	Top Imaged Resists
4,810,914	J. P. Karidis and J. P. Pawletko	Linear Actuator with Multiple Closed Loop Flux Paths Essentially Orthogonal to Its Axis
4,810,962	G. J. Gaudenzi and S. L. Tempest	Voltage Regulator Capable of Sinking Current
4,811,067	B. F. Fitzgerald, K. Y. T. Nguyen, and S. V. Nguyen	High Density Vertically Structured Memory
4,811,077	A. B. Fowler, J. L. Freeouf, P. D. Kirchner, A. C. Warren, and J. M. Woodall	Compound Semiconductor Surface Termination
4,811,082	S. L. Jacobs, P. Nihal, B. Ozmat, and H. D. Schnurmann	High Performance Integrated Circuit Packaging Structure
4,811,205	G. Normington, R. C. B. Speed, and G. H. Tuttle	Pipeline Display Control Apparatus with Logic for Blocking Graphics Processor Accesses to Shared Memory During Selected Main Processor Graphics Operations
4,811,206	W. M. Johnson	Data Processing System with Overlapped Address Translation and Address Computation
4,811,240	R. Ballou, S. M. Belyeu, J. A. Boscoe, H. L. Kurtz, P. Langer, A. B. McNeill, B. M. Reid, and H. Rodriguez	System for Creating and Controlling Interactive Graphic Display Screens
4,811,241	B. C. Liang	Clipping of Fixed Character Strings in a Graphics System
4,811,284	D. W. Adler, D. A. Kirtland, and R. S. Manka	Computer Terminal System with Memory Shared Between Remote Devices
4,811,298	K. Helwig, W. Lohlein, and M. H. Tong	Decoding Circuit Arrangement for Redundant Semiconductor Storage Systems
4,811,339	M. Bouillot, J. L. Calvignac, and J.-M. L. Munier	Non-Coded Information and Companion Data Switching Mechanism
4,811,343	G. T. Johansson and M. B. Johansson	On-Chip On-Line AC and DC Clock Tree Error Detection System
4,811,411	H. Hashihara and Y. Kitayama	Image Processing Method and System
4,811,413	M. J. Kimmel	System of Reconfigurable Pipelines of Generalized Neighborhood Function Morphic Image Processors
4,812,687	T. R. Larson and L. L. Tretter	Dual Direction Integrating Delay Circuit
4,812,756	H. W. Curtis, M.-S. Fung, and R. L. Verkuil	Contactless Technique for Semiconductor Wafer Testing
4,812,784	P. W. Chung and P. Saber	Temperature Stable Voltage Controlled Oscillator with Super Linear Wide Frequency Range
4,812,886	T. P. Smith III	Multilayer Contact Apparatus and Method
4,812,959	C. D. Driscoll, E. M. Najm, and M. Waechter	Synchronization Circuit for a Blocking Oscillator
4,812,968	F. Poole	Method for Controlling Processor Access to Input/Output Devices
4,813,011	J. E. Kulakowski and R. J. Means	Data Processing Peripheral Subsystems Having a Controller and a Plurality of Peripheral Devices
4,813,017	R. C. Wong	Semiconductor Memory Device and Array
4,813,033	H. J. Baasch and F. S. Luecke	Two Axis Electromagnetic Actuator with Axis Balanced Suspension
4,813,044	M. Kumar, A. Goyal, and B. D. Rath	Method and Apparatus for Detecting Transient Errors
4,817,093	S. L. Jacobs, M. T. McMahon, Jr., P. Nihal, B. Ozmat, H. D. Schnurmann, and A. R. Zingher	Method of Partitioning, Testing and Diagnosing a VLSI Multichip Package and Associated Structure
4,817,094	G. Lebizay, Y.-C. Lien, and M. M. Taso	Fault Tolerant Switch with Selectable Operating Modes
4,817,140	A. N. Chandra, L. D. Comerford, and S. R. White	Software Protection System Using a Single-Key Cryptosystem, a Hardware-Based Authorization System and a Secure Coprocessor
4,817,156	L. R. Bahl, R. L. Mercer, and D. Nahamoo	Rapidly Training a Speech Recognizer to a Subsequent Speaker Given Training Data of a Reference Speaker
4,817,158	M. A. Picheny	Normalization of Speech Signals
4,817,161	H. Kaneko	Variable Speed Speech Synthesis by Interpolation Between Fast and Slow Speech Data

4,817,186	M. A. Goolsbey, E. R. Jones, W. F. Micka, and T. S. Robinson	Locating Individual Images in a Field for Recognition or the Like
4,817,842	J. T. Wilson III	Door and Spring Assembly for a Paper Feed Mechanism
4,818,240	F. A. Karner	Power Rails for Edge Mounting Modules
4,818,286	R. Jagannathan, M. Krishnan, and G. P. Wandy	Electroless Copper Plating Bath
4,818,359	F. Jones and J. S. Logan	Low Contamination RF Sputter Deposition Apparatus
4,818,812	J. Economy, J. R. Susko, W. Volksen, and R. A. Wheeler	Sealant for Integrated Circuit Modules, Polyester Suitable Therefor and Preparation of Polyester
4,818,885	D. E. Davis, S. K. Doran, M. H. Perkins, and H. C. Pfeiffer	Electron Beam Writing Method and System Using Large Range Deflection in Combination with a Continuously Moving Table
4,819,038	P. M. Alt	TFT Array for Liquid Crystal Displays Allowing In-Process Testing
4,819,091	B. Brezoczky and E. Hopner	High Speed Magnetic Disk Contact Recording System
4,819,156	D. S. DeLorme, M. L. Holm, W. D. Lee, P. B. Passe, G. R. Ricard, G. D. Timms, Jr., and L. W. Youngren	Database Index Journaling for Enhanced Recovery
4,819,185	J. Corona and Y. Iida	Method and Apparatus for Drawing Wide Lines in a Raster Graphics Display System
4,819,191	K. J. Scully and H. S. Singh	Electronic Calendaring Method to Establish Calendar Floating Triggers for Calendared Events and Processes
4,819,230	J. Calvignac and P. Secondo	Optimized Bandwidth Allocation Mechanism Between Circuit Slots and Packet Bit Stream in a Communication Network
4,819,271	L. R. Bahl, P. V. DeSouza, R. L. Mercer, and M. A. Picheny	Constructing Markov Model Word Baseforms from Multiple Utterances by Concatenating Model Sequences for Word Segments
4,820,643	W. J. Amelio, K. R. Czarnecki, G. K. Lemon, V. Markovich, C. J. Sambucetti, and R. S. Zarr	Process for Determining the Activity of a Palladium-Tin Catalyst
4,821,125	T. C. Christensen, J. D. Coker, E. A. Cunningham, R. C. Jaworski, G. J. Kerwin, D. C. Palmer, and J. R. Roepke	Compensation of Write Current and Delta-V for Recording Component and Radial Position
4,821,145	W. D. Corfits, C. J. Mosley, J. R. Rasmussen, and S. E. Wheeler	Pluggable Assembly for Printed Circuit Cards
4,821,146	L. M. Behrens, N. A. Blohm, R. L. Cimijotti, W. D. Corfits, and J. R. Rasmussen	Pluggable Interposer and Printed Circuit Card Carrier
4,821,178	A. L. Levin, D. W. Rain, and D. J. Thomas	Internal Performance Monitoring by Event Sampling
4,821,183	J. F. Hauris	A Microsequencer Circuit with Plural Microprogram Instruction Counters
4,821,196	T. K. Collopy and D. F. Haire	High Resolution Automatic Focus Correction Electronic Subsystem for E-Beam Lithography
4,821,209	B. C. Hempel and B. C. Liang	Data Transformation and Clipping in a Graphics Display System
4,821,211	R. J. Torres	Method of Navigating Among Program Menus Using a Graphical Menu Tree
4,821,614	R. W. Fleet, D. C. Long, K. F. Stroms, and G. Weiss	Programmable Magnetic Repulsion Punching Apparatus
4,821,639	J. E. Carrington and G. R. Westcott	Control for Enabling Flight Timing of Hammers During Printing
4,822,442	G. R. Ashcraft, R. E. Gorman, E. J. Kozol, and R. J. Paul	Flexible Article Application Apparatus
4,823,095	F. I. Atallah, P. W. Bond, and D. E. Frey	Remote Connection of Termination Network
4,823,205	D. M. Hannon and D. E. Horne	Capacitive Sensor for Head Positioning in Magnetic Recording Disk Files
4,823,220	T. D. Milster and S. Yao	Detector for Magnetooptic Recorders
4,823,259	F. J. Aichelmann, Jr., R. H. Blumberg, D. Meltzer, J. H. Pomerene, T. R. Puzak, R. N. Rechtschaffen, and F. J. Sparacio	High Speed Buffer Store Arrangement for Quick Wide Transfer of Data
4,823,261	J. H. Bank, H. G. Familetti, and C. W. Lickel	Multiprocessor System for Updating Status Information Through Flip-Flopping Read Version and Write Version of Checkpoint Data
4,823,272	A. Inselberg	N-Dimensional Information Display Method for Air Traffic Control
4,823,281	C. J. Evangelisti and L. Lumelsky	Color Graphic Processor for Performing Logical Operations
4,823,286	L. Lumelsky, J. C. St. Clair, R. L. Mansfield, M. Segre, and A. K. Spencer	Pixel Data Path for High Performance Raster Displays with All-Point-Addressable Frame Buffer
4,823,304	B. J. Frantz and S. E. Hellstrom	Method of Providing Synchronous Message Exchange in an Asynchronous Operating Environment
4,823,306	F. Barbic and D. M. Choy	Text Search System
4,823,330	N. K. Arter, M. G. Call, and M. L. Rhoades	Focus Servo Loop Off-Set Compensation
4,823,339	F. Bigo and F. LeMaut	Method and Device for Protecting Modems Against Echoes
4,823,345	A. A. Daniel, D. R. Irvin, and R. E. Moore	Method and Apparatus for Communication Network Alert Recording Identification
4,823,347	V. H. Chin, C. Chihara, and A. S. Sha	Deferred Parity Checking of Control Signals Across a Bidirectional Data Transmission Interface

4,823,869	A. J. Arnold and K. L. Sutton	Heat Sink
4,824,009	R. N. Master, M. S. Pittler, P. A. Totta, N. G. Ainslie, and P. H. Palmateer	Process for Braze Attachment of Electronic Package Members
4,824,544	D. J. Mikalesen and S. R. Rossnagel	Large Area Cathode Lift-Off Sputter Deposition Device
4,824,797	G. R. Goth	Self-Aligned Channel Stop
4,825,178	E. F. Culican, J. D. Davis, M. E. Powell, and P. E. Pritzlaff, Jr.	Oscillator with Noise Rejection and Square Wave Output
4,825,206	F. W. Brice, Jr. and R. A. Weingarten	Automatic Feedback of Network Topology Data
4,825,325	J. K. Howard	Magnetoresistive Read Transducer Assembly
4,825,410	H.-S. Lee	Sense Amplifier Control Circuit
4,826,564	B. H. Desilets, R. D. Kaplan, H. S. Sachdev, K. G. Sachdev, and S. A. Sanchez	Method of Selective Reactive Ion Etching of Substrates
4,827,401	J. Hrustich and E. W. Jackson, Jr.	Method and Apparatus for Synchronizing Clocks Prior to the Execution of a Flush Operation
4,827,406	G. Bischoff, D. R. Blokkum, A. de Leon Penalzoa III, and D. L. Peterson	Memory Allocation for Multiple Processors
4,827,411	A. H. Arrowood, A. E. Baratz, P. F. Chimento, Jr., J. E. Drake, Jr., J. L. Eisenbies, J. P. Gray, K. J. Norsworthy, and D. P. Pozefsky	Method of Maintaining a Topology Database
4,827,447	R. E. Croes, A. V. Gentry, M. L. Gentry, Jr., and M. R. Kendrick	Method for Selectively Returning to the Beginning or the Previous Revision Point in Document Edition
4,827,462	W. J. Flannagan, R. M. Kern, J. E. Kulakowski, and R. E. Wagner	Modular Data Storage Directories for Large-Capacity Data Storage Units
4,827,521	L. R. Bahl, P. F. Brown, P. V. deSouza, and R. L. Mercer	Training of Markov Models Used in a Speech Recognition System
4,830,723	J. Galvagni and R. A. Miller	Method of Encapsulating Conductors
4,831,725	B. Dunham, J. B. Hickson, Jr., and H. Lewitan	Global Wiring by Removal of Redundant Paths
4,832,255	H. R. Bickford, K. R. Grebe, C. A. Kovac, and M. J. Palmer	Precision Solder Transfer Method and Means
4,832,787	J. A. Bondur and F. D. Martinet	Gas Mixture and Method for Anisotropic Selective Etch of Nitride
4,832,808	S. L. Buchwalter	Polyimides Reaction Products and Use in Electrophoretic Deposition
4,833,094	D. M. Kenney	Method of Making a Dynamic RAM Cell Having Shared Trench Storage Capacitor with Sidewall-Defined Bridge Contacts and Gate Electrodes
4,833,346	W. P. Marple	Switched Threshold Comparator for a Fiber-Optic Receiver
4,833,421	C. W. Hanson	Fast One Out of Many Differential Multiplexer
4,833,425	E. F. Culican, Sr., J. D. Davis, J. F. Ewen, S. A. McCabe, J. M. Mosley, A. L. Mullgrav, Jr., P. F. Noto, C. O. Peterson, Jr., and P. E. Pritzlaff, Jr.	Analog Macro Embedded in a Digital Gate Array
4,833,516	W. Hwang and N. C. Lu	High Density Memory Cell Structure Having a Vertical Trench Transistor Self-Aligned with a Vertical Trench Capacitor and Fabrication Methods Therefor
4,833,517	M. Heiblum, C. M. Knoedler, and D. C. Thomas	Theta Device with Improved Base Contact
4,833,594	H. G. Familetti, C. W. Lickel, R. A. Mauri, M. E. Swallow, and J. L. Coltin	Method of Tailoring an Operating System
4,833,596	P. A. Buckland, J. W. Irwin, and J. E. Reeder	Logical Arrangement for Controlling Use of Different System Displays by Main Processor and Co-Processor
4,833,602	J. R. Levy and S. T. Ventrone	Signal Generator Using Modulo Means
4,833,604	J. M. Cheng, N. V. Nomm, and J. A. Yothers	Method for the Relocation of Linked Control Blocks
4,833,610	A. Zamora and E. M. Zamora	Morphological/Phonetic Method for Ranking Word Similarities
4,833,626	J. W. Malcolm	Optimizing Printer Throughput
4,833,670	G. Lebizay, Y.-C. Lien, K. Maruyama, and S. E. Schuster	Cross-Point Bit-Switch for Communication
4,833,679	R. W. Anderson, R. L. Gee, J. A. Indelicato, and A. M. Patel	Method and Apparatus with Improved Error Correction and Error Information Availability
4,833,712	L. B. Bahl, P. V. DeSouza, R. L. Mercer, and M. A. Picheny	Automatic Generation of Simple Markov Model Stunted Baseforms for Words in a Vocabulary
4,834,506	K. R. Demke and N. D. Lubart	Perceived Contrast of Liquid Crystal Displays
4,834,835	W. Cziep, U. Kuenzel, and W.-D. Ruh	Producing Viaholes in Plastic Sheets and Application of the Method
4,835,419	B. A. Chappell, T. I. Chappell, and S. E. Schuster	Source-Follower Emitter-Coupled-Logic Receiver Circuit
4,835,573	C. E. Rohrer and R. D. Shepherd	Machine Control System Utilizing Paper Parameter Measurements
4,835,593	A. F. Arnold, J. A. Corso, S. Kapur, W. F. Lange, and D.-Y. Shih	Multilayer Thin Film Metallurgy for Pin Brazing

4,835,632	S.-M. Shih, R. J. Spanel, K. G. Yamamoto, and M. M. Yu	Disk File Digital Servo Control System with Multiple Sampling Rate
4,835,633	S. F. Edel, M. L. Workman, K. G. Yamamoto, and M. H. Yu	Disk File Digital Servo Control System with Compensation for Variation in Actuator Acceleration Factor
4,835,637	R. E. Mach and P. F. Merrell	Disk File with In-Hub Motor
4,835,722	D. A. Clarke and R. W. E. Farr	Curve Generation in a Display System
4,835,847	J. S. Kamperman	Method and Apparatus for Mounting a Flexible Film Electronic Device Carrier on a Substrate
4,836,105	J.-P. Hoekstra, K. S. Pennington, and B. J. Factor	Direct Negative and Offset Master Production Using Thermal Liftoff
4,836,106	A. Afzali-Ardakani and K. S. Pennington	Direct Offset Master by Resistive Thermal Printing
4,836,435	D. T. Napp and K. J. Roche	Component Self Alignment
4,836,885	C. P. Breiten, D. Stanasolovich, and J. F. Theisen	Planarization Process for Wide Trench Isolation
4,836,886	T. H. Daubenspeck	Binary Chlorofluorocarbon Chemistry for Plasma Etching
4,836,887	T. H. Daubenspeck and F. S. Ischishita	Chlorofluorocarbon Additives for Enhancing Etch Rates in Fluorinated Halocarbon/Oxidant Plasmas
4,837,459	E. R. Bukowski and C. R. Hoffman	CMOS Reference Voltage Generation
4,837,563	R. L. Mansfield, A. K. Spencer, and J. C. St. Clair	Graphics Display System Function Circuit
4,837,677	G. S. Burrus, Jr., R. J. Cooper, M. R. Marr, M. A. Marsico, J. C. Pescatore, and P. D. Sullivan	Multiple Port Service Expansion Adapter for a Communications Controller
4,837,680	R. N. Crockett, R. F. Kern, A. G. Miller, R. E. Norris, M. W. Pousson, Jr., and R. E. Wagner	Controlling Asynchronously Operating Peripherals
4,837,688	T. D. Cronauer and G. P. Plunkett	Multi-Channel Shared Resource Processor
4,837,704	W. L. Lengefeld	Computer Controlled Material Handling
4,837,845	D. R. Pruett, G. Goertzel, and R. G. Tompson	Method for Rotating a Binary Image
4,840,302	D. A. Gardner, J. G. Ryan, J. G. Schaefer, and E. G. Walton	Chromium-Titanium Alloy
4,840,503	S. D. Roberts, L. B. Samuels, and D. T. Shadwick	Stuffed Chamber Ribbon Cartridge
4,840,923	D. G. Flagello, J. S. Wilczynski, and D. F. Witman	Simultaneous Multiple Level Interconnection Process
4,841,232	P. K. Graham and R. R. Williams	Method and Apparatus for Testing Three State Drivers
4,841,254	E. R. Bukowski and C. R. Hoffman	CMOS Precision Gain Amplifier
4,841,291	S. R. Swix and L. A. Krueger	Interactive Animation of Graphics Objects
4,841,310	A. R. Hoffman	High Performance Ink Jet Print Head for Use in a High Speed Printer
4,841,389	R. F. Hoyt, C. M. Jefferson, and S. E. Millman	Magnetic Transducer Crash Anticipation and Response Method and Apparatus
4,841,453	D. E. Finlay, S. D. Hanna, D. C. Stevenson, and J. T. Varga	Multidirectional Scan and Print Capability
4,841,476	J. A. Mitchelli and J. F. Bechdel	Extended Floating Point Operations Supporting Emulation of Source Instruction Execution
4,841,485	R. J. Prilik and J. R. Varner	Read/Write Memory Device with an Embedded Read-Only Pattern and Method for Providing Same
4,841,574	X. N. Pham and J. H. Wilson	Voice Buffer Management
4,843,711	H. Rager	Device for Placing Components in Particular on Circuit Boards
4,844,314	J. R. Wilson III	Pulley for Forms Feed Tractor
4,844,616	M. V. Kulkarni and W. H. Lancaster, Jr.	Interferometric Dimensional Measurement and Defect Detection Method
4,844,635	C. D. Malkemes and B. R. Cavill	Wire Fire Control Mechanism for a Wire Matrix Printer
4,845,354	A. Gupta and B. W. Hussey	Process Control for Laser Wire Bonding
4,845,379	R. W. Carlsten	Sense Circuit for Detecting Absence of a Pulse Train
4,845,425	J. G. Beha, R. W. Dreyfus, J. A. Kash, and G. W. Rubloff	Full Chip Integrated Circuit Tester
4,845,477	I. Shibata and S. Watanabe	Color Blinking System
4,845,482	W. E. Howard and P. M. Alt	Method for Eliminating Crosstalk in a Thin Film Transistor/Liquid Crystal Display
4,845,624	N. H. Clayton, J. L. Rivero, and K.-C. Sun	Relational Data Base Lock Avoidance System
4,845,644	T. Anthias, J. A. Herrod, and M. W. Ricketts	Data Display System
4,845,659	D. A. Hrusecky	Accelerated Validity Response Permitting Early Issue of Instructions Dependent Upon Outcome of Floating Point Operations
4,845,665	D. C. Heath, A. C. Lind, and C. A. Schneier	Simulation of Computer Program External Interfaces
4,845,676	W.-D. Lohlein, H. Schettler, and O. Wagner	Non-Clocked Static Memory Cell

4,845,677	B. A. Chappell, T. I. Chappell, and S. E. Schuster	Pipelined Memory Chip Structure Having Improved Cycle Time
4,845,684	R. L. Garwin, J. L. Levine, and M. A. Schappert	Acoustic Contact Sensor for Handwritten Computer Input
4,845,704	C. J. Georgiou and G. Lebizay	Switching System for Integrated Voice/Data Communications
4,845,752	A. Blanc, P. Jeannot, and S. Spalmacin-Roma	Multi-Signal Processor Synchronized System
4,846,589	T. K. Chikuma, R. O. Jones, W. H. Nelson, and W. F. Price, Jr.	Scanning Carriage System Useful for Optical Scanners
4,846,920	J. H. Keller, G. S. Selwyn, and J. Singh	Plasma Amplified Photoelectron Process Endpoint Detection Apparatus
4,846,929	S. L. Bard, C. Feger, J. J. Glenning, G. G. Hougham, S. E. Molis, W. P. Pawlowski, J. J. Ritsko, P. Slota, Jr., and R. W. Snyder	Wet Etching of Thermally or Chemically Cured Polyimide
4,847,605	R. M. Callahan, A. M. Fiore, E. D. Flickinger, B. C. Hempel, and B. E. Whistance	Picking in a Graphics System
4,847,636	R. C. Durbeck, J. M. Eldridge, F. C. Lee, and G. Olive	Thermal Drop-on-Demand Ink Jet Print Head
4,847,670	M. D. Monkowski and J. F. Shepard	High Performance Sidewall Emitter Transistor
4,847,749	R. W. Collins, W. S. Davidson, S. M. Dickes, J. S. Effle, C. J. Larson, R. J. Weinschenk, and P. M. Wottreng	Job Interrupt at Predetermined Boundary for Enhanced Recovery
4,847,759	V. G. Oklobdzija	Register Selection Mechanism and Organization of an Instruction Prefetch Buffer
4,847,761	K. M. Ferriter and R. B. Mathis	Automated Bill of Material
4,847,785	L. K. Stephens	Interactive Display for Trend or Bar Graph
4,847,789	R. E. Kelly and P. D. Welch	Method for Hidden Line Removal
4,847,824	N. R. Davie	Readback Circuits for Magneto optic Players
4,847,877	J. A. Besseyre	Method and Apparatus for Detecting a Predetermined Bit Pattern Within a Serial Bit Stream
4,847,893	M.-H. Dang, N. V. James, E. A. Jreij, and J. R. Taborga	Method for Monitoring Telephone Status Changes
4,847,984	R. V. Rossi, S. F. Starcke, and D. A. Swagel	Method of Assembling a Flexible Circuit Magnetic Core Winding onto a Core Member
4,848,131	M. A. Haller and D. J. Klossner	Crimping Apparatus
4,849,259	L. Biro, K. Malin, and O. Schmid	Method of Forming Silicon and Oxygen Containing Layers
4,849,501	R. D. Diller, A. F. Arnold, Y. Y. Cheng, P. M. Cotts, D. C. Hofer, M. Khojasteh, E. H. Macy, P. R. Shah, and W. Volksen	Polyimide Coating Compositions Based on Meta-Dialkyldihydrogen Pyromellitate and Aromatic Diamines
4,849,706	G. T. Davis and B. D. Mandalia	Differential Phase Modulation Demodulator
4,849,802	T. N. Jackson, M. Murakami, W. H. Price, S. Tiwari, J. M. Woodall, and S. L. Wright	Thermally Stable Low Resistance Contact
4,849,856	J. Funari, M. C. Green, S. D. Reynolds, and B. G. Sammakia	Electronic Package with Improved Heat Sink
4,849,883	M. O. Mitchell and K. P. McGlynn	Professional Office System Printer Support for Personal Computers
4,849,904	A. G. Aipperspach, D. M. Dewanz, and J. M. Fitzgerald	Macro Structural Arrangement and Method for Generating Macros for VLSI Semiconductor Circuit Devices
4,849,905	D. J. Loeb and K. R. Milliken	Method for Optimized RETE Pattern Matching in Pattern-Directed, Rule-Based Artificial Intelligence Production Systems
4,849,975	A. M. Patel	Error Correction Method and Apparatus
4,850,017	S. M. Martyas, Jr., C. H. W. Meyer, and B. O. Brachtl	Controlled Use of Cryptographic Keys Via Generating Station Established Control Values
4,850,027	M. J. Kimmel	Configurable Parallel Pipeline Image Processing System
4,850,044	T. R. Block, R. L. Soderstrom, G. M. Heiling, and C. J. Rocca	Serial Optical Interconnect Bus for Logic Cards and the Like
4,850,473	D. MacWilliams, D. A. Paton, A. C. Larcombe, and S. G. Baker	Conveyor System Including a Buffer Store