

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,571,161,208 G. Pfeiffer and H. Moritz Production of a Lift-Off Mask and Its Application

France

89,441 J. P. Nuez, M. Grandguill, and P. Mollier True/Complement Generator Employing Feedback Circuit Means for Controlling the Switching of the Outputs
 87,541 G. Orenco Floating Backup Power Supply
 166,839 D. Godard Method and Device for Measuring Phase Jitter on a Transmission Channel
 186,720 J. M. Blachere, M. Bonneau, and R. Hornung High Performance Unit Cell for CMOS LSI Gate

Switzerland

182,925 C. A. Mueller, K. A. Mueller, and R. Jaggi Insulated Gate Field Effect Transistor
 189,498 B. Reihl, G. Bednorz, J. G. Bednorz, and J. K. Gimzewski Field-Emission Scanning Microscope
 196,346 D. W. Pohl, J. K. Gimzewski, U. T. Duerig, and W. D. Pohl Apparatus for Manufacturing Surface Structure

United States

4,809,168 P. Hennessy, D. L. Osisek, and W. Seigh II Passive Serialization in a Multitasking Environment
 4,809,196 D. A. Miller Method for Designating/Sorting Semiconductor Wafers According to Predicted Oxygen Precipitation Behavior
 4,809,219 T. J. Ashford, N. A. Burns, R. L. Flagg, C. T. Iwaskiw, and C. T. Starbird Method for Processing an Expert System Rulebase on a System Having Limited Memory
 4,809,257 D. Gantenbein, F. R. Gfeller, and E. P. Mumprecht Hierarchical Distributed Infrared Communication System
 4,809,273 S. P. Jackowski and R. B. Jenkins Device for Verifying Operation of a Checking Code Generator
 4,814,041 B. Auda Method of Forming a Via-Hole Having a Desired Slope in a Photoresist Masked Composite Insulating Layer
 4,814,290 J. R. Barber, H. N. Kotecha, D. D. Meyer, and D. Stanasolovich Method for Providing Increased Dopant Concentration in Selected Regions of Semiconductor Devices
 4,814,634 P. Abramson, D. E. Conner, and L. C. Haas Ternary Signal Multiplexor Circuit
 4,814,746 V. S. Miller and M. N. Wegman Data Compression Method
 4,814,755 P. W. Johnson and P. Quarendon Interactive Display System
 4,814,855 R. T. Hodgson, H. J. Jones, P. G. Ledermann, T. C. Reilly, and P. C. Moskowitz Balltape Structure for Tape Automated Bonding, Multilayer Packaging, Universal Chip Interconnection and Energy Beam Processes for Manufacturing Balltape
 4,814,857 G. G. Werbizky Circuit Module with Separate Signal and Power Connectors
 4,814,903 J. E. Kulakowski and R. J. Means Alternate Storage Areas in Magneto-optical Media
 4,814,940 R. E. Horstmann and J. M. Oberschmidt Low Inductance Capacitor

4,815,029	B. A. Barker, I. H. Hernandez, and B. H. Machart	In-Line Dynamic Editor for Mixed Object Documents
4,815,983	J. W. Erickson, M. W. Jeché, R. D. O'Groske, and G. L. Richter	Customizable Plugs for A.C. Power Cords
4,816,112	G. A. Brooks and N. A. Greco	Planarization Process Through Silylation
4,816,115	E. Hörner, R. Mühl, and H.-J. Trumpp	Process of Making Via Holes in a Double-Layer Insulation
4,816,692	J. R. Rudert, Jr.	Pattern Splicing System and Method for Scanning of Electron Beam System
4,816,706	S. H. Dhong and N. C. C. Lu	Sense Amplifier with Improved Bitline Precharging for Dynamic Random Access Memory
4,816,754	A. W. Buechele, T. J. Cochran, and P. B. Hodge	Contactors and Probe Assembly for Electrical Test Apparatus
4,816,773	W. D. Pricer	Non-Inverting Repeater Circuit for Use in Semiconductor Circuit Interconnections
4,816,812	H. Iida	Method and System for Displaying Images in Adjacent Display Areas
4,816,884	W. Hwang and N. C. Lu	High Density Vertical Trench Transistor and Capacitor Memory Cell Structure and Fabrication Method Therefor
4,816,941	S. F. Edel and I. Van Pham	Disk File Digital Servo Control System With Optimized Sampling Rate
4,816,999	C. L. Berman, D. Brand, and L. H. Trevillyan	Method of Detecting Constants and Redundant Connections in a Logic Network
4,817,018	C. M. N. Cree, G. J. Landry, K. J. Scully, and H. S. Singh	Electronic Calendaring Method Which Provides for Automatic Assignment of Alternates in Requested Events
4,817,037	H. Hoffman and C. G. Wright	Data Processing System with Overlap Bus Cycle Operations
4,817,093	S. L. Jacobs, M. T. McMahon, Jr., P. Nihal, B. Ozmat, H. D. Schnurmann, and A. R. Zingher	Method of Partitioning, Testing and Diagnosing a VLSI Multichip Package and Associated Structure
4,817,094	G. Lebizay, Y.-C. Lien, and M. M. Taso	Fault Tolerant Switch with Selectable Operating Modes
4,817,140	A. N. Chandra, L. D. Comerford, and S. R. White	Software Protection System Using a Single-Key Cryptosystem, a Hardware-Based Authorization System and a Secure Coprocessor
4,817,156	L. R. Bahl, R. L. Mercer, and D. Nahamoo	Rapidly Training a Speech Recognizer to a Subsequent Speaker Given Training Data of a Reference Speaker
4,817,158	M. A. Picheny	Normalization of Speech Signals
4,817,161	H. Kaneko	Variable Speed Speech Synthesis by Interpolation Between Fast and Slow Speech Data
4,817,186	M. A. Goolsbey, E. R. Jones, W. F. Micka, and T. S. Robinson	Locating Individual Images in a Field for Recognition or the Like
4,818,286	R. Jagannathan, M. Krishnan, and G. P. Wandy	Electroless Copper Plating Bath
4,818,359	F. Jones and J. S. Logan	Low Contamination RF Sputter Deposition Apparatus
4,828,964	W. R. Brunsvold, W. E. Conley, S. L. Jacobs, G. L. Mack, D. P. Merritt, and A. M. Uptmor	Polyimide Formulation for Forming a Patterned Film on a Substrate
4,829,198	G. A. Maley, J. M. Mosley, and S. D. Weitzel	Fault Tolerant Logical Circuitry
4,829,336	J. R. Champion and D. E. Sartin	Toner Concentration Control Method and Apparatus
4,829,405	K. A. Snyder	Tape Automated Bonding Package
4,829,462	W. A. Freeman, D. E. Hurlimann, E. L. Miller, and D. W. Solie	Communication Bit Pattern Detection Circuit
4,829,470	J. S. Wang	Text Flow Around Irregular Shaped Graphic Objects
4,829,511	C. J. Georgiou	Switched Network of Optical Buses
4,829,577	A. Kuroda, M. Nishimura, and K. Sugawara	Speech Recognition Method
4,830,264	A. Bitailou and M. Grandguillot	Method of Forming Solder Terminal for a Pinless Ceramic Module
4,830,706	R. S. Horwath, J. R. Susko, and R. A. Susko	Method of Making Sloped Vias
4,830,909	M. S. Cohen, K. G. Sachdev, J. C. S. Shen, and K. S. Pennington	Scratch Resistant Recording Materials for Electroerosion Printing Comprising Cross-Linked Polymer Base Layer
4,831,284	C. J. Anderson and J. F. Ewen	Two Level Differential Current Switch MESFET Logic
4,831,421	W. J. Gallagher and S. I. Raider	Superconducting Device
4,831,469	C. C. Hanson and M. J. Ross	Disk Drive Head Retract and Motor Braking Method and Apparatus
4,831,494	A. J. Arnold, M. E. Bariether, S.-W. Chiang, H. M. Dalal, R. A. Miller, F. A. Montegari, J. M. Oberschmidt, and D. T. Shen	Multilayer Capacitor
4,831,517	R. S. Crouse, R. P. Boudreaux, and J. J. Cazzolla, Jr.	Branch and Return on Address Instruction and Methods and Apparatus for Implementing Same in a Digital Data Processing System
4,831,541	M. M. Eshel	System for Editing Real and Virtual Storage and Secondary Storage Media
4,831,550	S. M. Katz	Apparatus and Method for Estimating, from Sparse Data, the Probability that a Particular One of a Set of Events Is the Next Event in a String of Events

4,831,552	K. J. Scully and H. S. Singh	Method for Concurrently Displaying Entries from a Plurality of Different Electronic Calendars Based on Interactively Entered Non-Temporal Criteria
4,831,583	R. A. Pascoe	System for Saving Previous Format Parameters and Using the Saved Format Parameters and Changed Format Parameters to Format Data Output
4,831,614	U. T. Duerig, J. K. Gimzewski, and W. D. Pohl	Direct Access Storage Unit Using Tunneling Current Techniques
4,831,657	R. G. Casey and K. M. Mohiuddin	Method and Apparatus for Establishing Pixel Color Probabilities for Use in OCR Logic