

Recent IBM patents

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United Kingdom

154,062	R. D. Commander, B. P. Fenton, and J. G. Ramage	Power Switching Circuit
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United States

4,711,552	B. A. Nilsson and R. D. Shepherd	Synchronizing Electrostatic Copy Formation
4,711,858	C. H. Harder, H. Jaeckel, and H. P. Wolf	Method of Fabricating a Self-Aligned Metal-Semiconductor FET Having an Insulator Spacer
4,712,027	J. P. Karidis	Radial Pole Linear Reluctance Motor
4,712,102	D. Troupes and R. W. Alexander	Method and Apparatus for Displaying Enlarged or Enhanced Dot Matrix Characters
4,712,125	H. S. Bhatia, S. S. Bhatia, J. Riseman, and E. A. Valsamakis	Structure for Contacting a Narrow Width pn Junction Region
4,712,140	F. C. Mintzer and K. L. Anderson	Image Reduction Method
4,712,144	K. B. Klaassen	Method and Apparatus for Reading Recorded Data by a Magnetoresistive Head
4,712,176	K. J. Fredericks, T. W. Guerriero, G. H. Miracle, and M. R. Wiegand	Serial Channel Interface with Method and Apparatus for Handling Data Streaming and Data Interlocked Modes of Data Transfer
4,712,214	C. B. Meltzer, K. Kannan, T. G. Burket, D. J. Kruesi, and G. W. Braudaway	Protocol for Handling Transmission Errors over Asynchronous Communi- cation Lines
4,712,221	H. B. Pearce and L. P. West	Carrier Recovery of Modulated Signals
4,712,235	G. D. Jones, Jr.	Method and Apparatus for Improved Control and Time Sharing of an Echo Canceller
4,754,647	D. A. Clarke	Semiconductor Integrated Pressure Transducer
4,755,400	L. P. Dempsey, D. S. Ehn, R. W. Gunnell, B. P. Reinhardt, and L. A. Walker	Hot Roll Fuser and Method of Making a Fuser Roll
4,755,478	J. R. Abernathy, J. E. Cronin, and J. B. Lasky	Method of Forming Metal-Strapped Polysilicon Gate Electrode for FET Device
4,755,631	R. W. Churchwell, P. L. Flaitz, and J. N. Humenik	Apparatus for Providing an Electrical Connection to a Metallic Pad Situated on a Brittle Dielectric Substrate
4,755,808	G. R. Bullock, A. W. Heath, and R. K. Sheppard	Automatic Capture of Pointing Device Actions in a Keystroke Program
4,755,897	J. K. Howard	Magnetoresistive Sensor with Improved Antiferromagnetic Film
4,755,932	R. A. Diedrich	Interpreter Program for Providing Commands to a Computer Application Program
4,755,940	B. Brachtl, C. J. Holloway, R. E. Lennon, S. M. Matyas, C. H. Meyer, and J. Oseas	Transaction Security System
4,756,006	D. A. Rickard	Bus Transceiver
4,756,246	T. J. Kotasek, H. C. Lee, J. Pinter, and A. T. Shalkey	Lubrication System for Print Hammer Pivot Pin
4,756,687	C. Cabrera, R. W. Gunnell, G. E. Siemer, and E. G. Stahlberg	Roll Fuser Jam Clearance Mechanism

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4,756,795	P. Bakos, L. M. Baldauf, and D. T. Napp	Raw Card Fabrication Process with Nickel Overplate
4,756,923	M. A. Day, R. A. Kaschak, and S. A. Schubert	Method of Controlling Resistivity of Plated Metal and Product Formed Thereby
4,757,207	R. E. Chappelow and L. P. Hayes	Measurement of Registration of Overlaid Test Patterns by the Use of Reflected Light
4,757,309	R. J. Bowater, M. I. Davis, R. W. E. Farr, and C. V. Powell	Graphics Display Terminal and Method of Storing Alphanumeric Data Therein
4,757,358	H. J. Hovel and J. M. Woodall	MESFET Semiconductor Device Fabrication with Same Metal Contacting Source, Drain and Gate Regions
4,757,361	M. H. Brodsky and F. F. Fang	Amorphous Thin Film Transistor Device
4,757,369	T. N. Jackson, P. D. Kirchner, G. D. Pettit, R. F. Rutz, and J. M. Woodall	Group III-V Semiconductor Electrical Contact
4,757,370	D. Agonafer, R. C. Chu, and R. E. Simons	Circuit Package Cooling Technique with Liquid Film Spreading Downward Across Package Surface Without Separation
4,757,441	P. A. Buckland, J. W. Irwin, and J. E. Reeder	Logical Arrangement for Controlling Use of Different System Displays by Main Processor and Coprocessor
4,763,180	W. Hwang, S. E. Schuster, and L. M. Terman	Method and Structure for a High Density VMOS Dynamic RAM Array
4,763,245	P. G. Emma, J. H. Pomerene, G. S. Rao, R. N. Rechtschaffen, H. E. Sachar, and F. J. Sparacio	Branch Prediction Mechanism in Which a Branch History Table Is Updated Using an Operand Sensitive Branch Table
4,763,251	A. A. Kauffman, Jr. and J. S. Muhich	Merge and Copy Bit Block Transfer Implementation
4,763,255	M. E. Hopkins and H. S. Warren, Jr.	Method for Generating Short Form Instructions in an Optimizing Compiler
4,763,277	T. J. Ashford, N. A. Burns, R. L. Flagg, C. T. Iwaskiw, and R. P. Starbird	Method for Obtaining Information in an Expert System
4,763,279	K. K. Kellam and R. C. Tong	Method and Apparatus for Converting Dot Matrix Display Data of One Resolution to a Format for Displaying on a Display Device Having a Different Resolution
4,763,289	Z. Barzilai, V. S. Iyengar, B. K. Rosen, and G. M. Silberman	Method for the Modeling and Fault Simulation of Complementary Metal Oxide Semiconductor Circuits
4,763,321	J. Calvignac and P. Secondo	Dynamic Bandwidth Allocation Mechanism Between Circuit Slots and Packet Bit Stream in a Communication Network
4,764,014	G. Makosch and F. J. Schedewie	Interferometric Measuring Methods for Surfaces
4,764,039	H. R. Rublitz	Method for Controlling Line Printers Comprising a Revolving Type Band and a Print Hammer Bank
4,764,341	P. L. Flaitz, R. N. Master, P. H. Palmateer, and S. S. N. Reddy	Bonding of Pure Metal Films to Ceramics
4,764,722	C. P. Coughlin, R. H. Cadwallader, and R. J. Celestino	Coaxial Probe
4,764,799	S. D. Malaviya	Stud-Defined Integrated Circuit Structure
4,764,848	J. P. Simpson	Surface Mounted Array Strain Relief Device
4,764,893	C. Karabatsos	Noise-Immune Interrupt Level Sharing
4,764,955	C. Galand	Process for Determining an Echo Path Flat Delay and Echo Canceler Using Said Process
4,764,966	M. A. Einkauf and L. P. West	Method and Apparatus for Voice Detection Having Adaptive Sensitivity
4,764,984	H. Franke and F. R. Gfeller	Fluorescent Sensors for Infrared Free-Space Links in Data Communication Systems
4,765,397	G. M. Chrysler, R. C.-F. Chu, and R. E. Simmons	Immersion Cooled Circuit Module with Improved Fins
4,765,400	R. C. Chu, J. C. Eid, and M. L. Zumbunnen	Circuit Module with Pins Conducting Heat from Floating Plate Contacting Heat Producing Device
4,765,605	P. E. Abbott	Paper Cassette Tray with Front Edge Positioning Cams
4,766,093	H. J. Hovel and T. F. Kuech	Chemically Formed Self-Aligned Structure and Wave Guide
4,766,364	R. A. Biamonte, J. W. Bogdanski, K. R. Covi, H. P. Meyer, and D. I. Raffkind	Parallel Power Systems
4,766,399	J. D. Davis and A. L. Mullgrav, Jr.	Oscillator Circuit
4,766,532	K. A. Pearson and L. R. Zucker	Sequential Array Logic
4,766,535	D. J. Auerbach, T. C. Chen, and W. J. Paul	High-Performance Multiple Port Memory
4,766,564	R. D. DeGroot	Dual Putaway/Bypass Busses for Multiple Arithmetic Units
4,766,565	R. A. Béchade and M. S. Schmookler	Arithmetic Logic Circuit Having a Carry Generator
4,766,566	C.-M. Chuang	Performance Enhancement Scheme for a RISC Type VLSI Processor Using Dual Execution Units for Parallel Instruction Processing
4,766,605	L. Tin	Telephone Tie Trunk Driver Circuit
4,766,670	C. E. Gazdik, D. G. McBride, D. P. Seraphim, and P. A. Toole	Full Panel Electronic Packaging Structure and Method of Making Same
4,767,192	I. F. Chang and E. I. Gordon	Light Activated Light Valve with a Silicon Control Element
4,767,723	W. E. Hinsberg III, W. E. Howard, and C. G. Willson	Process for Making Self-Aligning Thin Film Transistors