

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,278,524,008	M. Zuerheide, W. Goessler, and A. Strube	Gate Insulation Layer and Method of Producing Such a Structure
3,376,502,208	G. Makosch and B. Drollinger	Method and Arrangement for Measuring Surface Profiles
3,376,912,508	A. Bohg, H. Matthaui, and K. Hartmann	Electromagnetic RAM Actuator in an Electroerosion Printer
3,377,078,608	G. Goldrian and V. Rudolph	Method of Driving Electrodes
3,471,368,908	W. Jaerisch and G. Makosch	Interferometric Thickness Analyzer and Measuring Method
3,471,901,608	H. Stock	Drive Mechanism for a Magnetic Head Carriage Assembly

France

112,430	C. de La Salle and G. D'Hervilly	Printer Hammer Resetting Control System
130,263	A. Desblache, C. Galand, and R. Vermot-Gauchy	Method of Initializing a Filter in an Echo Canceller and Communication System Using Said Method
139,804	A. Brunin, H. Beranger, and J. P. Rousseau	Latch for Storing a Data Bit and a Store Incorporating Said Latch
166,837	F. Baudelot, G. Montanari, and M. Costes	Device for Performing Wrap Tests on a Multiplex

United Kingdom

105,060	M. I. Davis, M. J. Garrett, and J. A. Wiseman	Electronic Assembly with Forced Convection Cooling
145, 821	A. J. Hawes	Area Filling Hardware for a Colour Graphics Frame Buffer

United States

4,741,000	M. V. Pierson	Pin Retention Method and Apparatus
4,741,799	L. Chen and G. S. Mathad	Anisotropic Silicon Etching in Fluorinated Plasma
4,741,964	I. Haller	Structure Containing Hydrogenated Amorphous Silicon and Process
4,742,019	R. A. Béchade	Method for Forming Aligned Interconnections Between Logic States
4,742,292	C. R. Hoffman	CMOS Precision Voltage Reference Generator
4,742,344	B. Nakagawa and K. Nojima	Digital Display System with Refresh Memory for Storing Character and Field Attribute Data
4,742,347	A. Arismendi	Refreshing Circuit for Multi-Panel Display
4,742,350	M. A. Ko and J. S. Muhich	Software Managed Video Synchronization Generation
4,742,383	J. M. Fitzgerald	Multi-Function FET Masterslice Cell
4,742,447	K. E. Duvall and A. D. Hooten	Method to Control I/O Accesses in a Multi-Tasking Virtual Memory Virtual Machine Type Data Processing System
4,742,450	K. E. Duvall, A. D. Hooten, and L. K. Loucks	Method to Share Copy on Write Segment for Mapped Files
4,742,471	E. L. Yoffa and P. S. Hauge	Method for Improving Wirability of Master-Image DCVS Chips
4,742,487	K. Bernstein	Inhibit and Transfer Circuitry for Memory Cell Being Read from Multiple Ports
4,744,615	B. Fan, R. E. Tibbetts, J. S. Wilczynski, and D. F. Witman	Laser Beam Homogenizer

4,744,683	L. Fischer, G. Goldrian, and V. Zimmermann	Revolving Endless Bands Comprising Means for Compensating for a Displacement Perpendicular to Their Direction of Movement
4,744,833	D. W. Cooper, H. L. Wolfe, and H. T. C. Yeh	Electrostatic Removal of Contaminants
4,745,044	L. V. Gregor	Multilayer Resists with Improved Sensitivity and Reduced Proximity Effect
4,745,045	E. C. Fredericks and G. G. Via	Method for Improving Resolution in Microelectronic Circuits Using Photoresist Overlayer by Using Thermally Processed Polyimide Underlayer Formed from Positive Photoresist and Polyamic Acid
4,745,081	K. D. Beyer and V. J. Silvestri	Method of Trench Filling
4,745,204	J. J. Cuomo, P. A. Leary, and J. M. Woodall	Process for Producing Aluminum Alkoxide or Aluminum Aryloxide
4,745,215	R. J. Cox, W. J. Summa, and D. W. Wang	Fluorine Containing Dicyanate Resins
4,745,355	E. B. Eichelberger, R. N. Langmaid, E. Lindbloom, F. Motika, J. L. Sinchak, and J. A. Waicukauski	Weighted Random Pattern Testing Apparatus and Method
4,745,405	C. S. Himmelstein and J. S. Wang	Object Movement Feedback
4,745,547	W. Buchholz, R. M. Smith, and D. S. Wehrly	Vector Processing
4,745,560	J. W. Decker, J. C. King, R. A. Lerner, and J. B. Lotspiech	Method of Controlling a Bit-Image Printer
4,745,565	R. L. Garwin and J. L. Levine	Calibration of a Force Sensing Type of Data Input Device
4,745,570	R. A. Diedrich and J. A. Lawrence	Binary Multibit Multiplier
4,745,575	A. J. Hawes	Area Filling Hardware for a Color Graphics Frame Buffer
4,745,604	A. M. Patel, D. T. Wang, and W. C. Yu	Method and Apparatus for Transferring Data Between a Host Processor and a Data Storage Device
4,745,681	R. L. Hollis, Jr., and M. A. O'Connor	Controlled Pin Insertion Using Airflow Sensing and Active Feedback
4,745,580	R. W. Bishop and A. E. Moxley	Read-Only Magnetic Recording Media
4,746,087	L. G. Beaton	Tilt Mechanism for Display Terminal
4,746,815	H. S. Bhatia, M. E. Ecker, H. J. Jones, and S. D. Malaviya	Electronic EC for Minimizing EC Pads
4,746,817	D. C. Banker, A. H. Dansky, J. A. Dorler, W. S. Klara, F. M. Masci, S. J. Zier, and A. Zuckerman	BIFET Logic Circuit
4,746,903	J.-C. Czarniak, M. F. Ferry, and C. Jacquart	Parallel Algorithmic Digital to Analog Converter
4,746,966	J. M. Fitzgerald, P. H. Nguyen, and R. R. Williams	Logic-Circuit Layout for Large-Scale Integrated Circuits
4,747,000	R. J. Godsoe	Multiple Pivoted Cartridge Receiver
4,747,050	B. Brachtel, C. J. Holloway, R. E. Lennon, S. M. Matyas, C. H. Meyer, and J. Oseas	Transaction Security System Using Time Variant Parameter
4,747,065	L. P. West	Automatic Gain Control in a Digital Signal Processor
4,747,156	F. M. Wahl	Image Preprocessing Procedure for Noise Removal
4,747,533	L. D. Lipschutz	Bonding Method and Apparatus
4,747,698	H. K. Wickramasinghe and C. C. Williams	Scanning Thermal Profiler
4,747,783	P. D. Bellamy, R. A. Fritz, W. V. Myers, G. J. Robinson, R. L. Simonetty, and J. M. Taylor	Resistive Pin for Printed Circuit Card Connector
4,747,907	J. Acocella and L. D. David	Metal Etching Process with Etch Rate Enhancement
4,748,316	L. D. Dickson	Optical Scanner for Reading Bar Codes Detected Within a Large Depth of Field
4,748,532	R. D. Commander, B. P. Fenton, and J. G. Ramage	Transformer Coupled Power Switching Circuit
4,748,587	J. Combes, J.-C. Robbe, and P. Viallon	Device for Improving Detection of Unoperational States in Non-Attended Drive Processor
4,748,588	V. R. Norman, S. B. Schrum, Jr., and C. R. Wicker	Fast Data Synchronizer
4,748,631	G. C. Bjorklund	Modulated Laser Source for Optical Storage
4,748,670	L. R. Bahl and F. Jelinek	Apparatus and Method for Determining a Likely Word Sequence from Labels Generated by an Acoustic Processor
4,749,614	G. L. Andrews, A. C. Bhatt, D. E. Doran, and L. E. Hunsinger	Process for Coating Fibers, Use Thereof, and Product
4,749,621	C. J. Araps, S. M. Kandetzke, and M. A. Takacs	Electronic Components Comprising Polyimide-Filled Isolation Structures
4,749,628	R. H. Ahlert, J. K. Howard, and G. S. Lim	Multilayered Vertical Magnetic Recording Medium
4,749,872	S. Asada and M. Hasegawa	Document Scan Apparatus with Reflective Reference Position Marker
4,749,983	G. G. Langdon, Jr.	Compression of Multilevel Signals
4,750,058	W. Hirt and W. H. Schott	Gain Control Circuitry for Readback Apparatus in a PRML Magnetic Recording System
4,750,106	J. A. Aiken, Jr.	Disk Volume Data Storage and Recovery Method
4,750,116	X. N. Pham and J. H. Wilson	Hardware Resource Management

4,750,193	J. A. Bailey	Phase-Locked Data Detector
4,750,202	P. N. Feldman, J. Friedman, and N. J. Pippenger	Non-Blocking Concentrator
4,750,262	I. S. Mahmoud and G. Schrottke	Method of Fabricating a Printed Circuitry Substrate
4,751,101	R. V. Joshi	Low Stress Tungsten Films by Silicon Reduction of WF ₆
4,751,169	U. Behringer, K. Dätwyler, and P. Vettinger	Method for Repairing Lithographic Transmission Masks
4,751,349	J. Kim, W. F. Lange, D.-Y. Shih, and S. H. Wen	Zirconium as an Adhesion Material in a Multi-Layer Metallic Structure
4,751,507	H. Hama and Y. Isobe	Method for Simultaneously Displaying an Image and an Enlarged View of a Selectable Portion of the Image with Different Levels of Dot Detail Resolution
4,751,510	J. de Saint Michel, J. Belloc, J. Cholat-Namy, M. Choquet, S. Huon, D. Pilost, and V. Spagnol	Method and System for Controlling a Network of Modems
4,751,558	D. M. Kenney	High Density Memory with Field Shield
4,751,563	R. B. Laibowitz and C. P. Umbach	Microminiaturized Electrical Interconnection Device and Its Method of Fabrication
4,751,634	G. S. Burrus, Jr., R. J. Cooper, M. R. Marr, M. A. Marsico, J. C. Pescatore, and P. D. Sullivan	Multiple Port Communications Adapter Apparatus
4,751,656	D. R. Conti, D. R. Dewar, R. Fonseca, and R. R. Wood	Method for Choosing Replacement Lines in a Two-Dimensionally Redundant Array
4,751,665	P. R. Cappello and W. L. Miranker	Systolic Super Summation Device
4,751,698	M. Bouillot, J.-L. Calvignac, and J.-M. L. Munier	Serial Link Adapter for a Communication Controller
4,751,702	H. A. Beier, T. Fukumoto, H. Scofield, and V. L. Watts	Improving Availability of a Restartable Staged Storage Data Base System That Uses Logging Facilities
4,751,703	J. Picon, C. Y. G. Poiraud, and D. Sazbon-Natansohn	Method for Storing the Control Code of a Processor Allowing Effective Code Modification and Addressing Circuit Therefor
4,751,708	T. N. Jackson and J. M. Woodall	Semiconductor Injection Lasers
4,751,730	C. Galand and G. Platel	Process and System for Improving Echo Cancellation Within a Transmission Network
4,752,267	D. R. Reynolds and M. Wong	Fast Point/Line Correlations in Graphics Entities
4,752,344	N. J. Jubb and T. M. Reith	Magnetic Layer and Method of Manufacture
4,752,699	H. C. Cranford, Jr., S. J. Garvin, W. K. Hodgins, and J. M. Mullen	On Chip Multiple Voltage Generation Using a Charge Pump and Plural Feedback Sense Circuits
4,752,765	L. E. Larson	Apparatus, Method and Algorithm for Encoding and Decoding Characters in a Message to be Transmitted Between a First System and a Second System
4,752,812	M. Arienzo and D. J. DiMaria	Permeable-Base Transistor
4,752,813	H. S. Bhatia, S. P. Bhatia, C. P. de Vries, and D. A. Grose	Schottky Diode and Ohmic Contact Metallurgy
4,752,817	J. S. Lechaton, P. M. Pitner, and G. R. Srinivasan	High Performance Integrated Circuit Having Modified Extrinsic Base
4,752,777	P. A. Franaszek	Delta Network of a Cross-Point Switch
4,752,872	D. Ballatore, S. Huon, and J.-M. Munier	Arbitration Device for Latching Only the Highest Priority Request When the Common Resource Is Busy
4,752,876	W. R. Couch and R. G. Burgess	Self Service Terminal for Lodging Industry Including Room Key Dispenser
4,752,890	K. S. Natarajan and H. S. Stone	Adaptive Mechanisms for Execution of Sequential Decisions
4,752,904	D. F. Paul	Efficient Structure for Computing Mixed-Radix Projections from Residue Number Systems
4,752,908	M. Bouillot	Visualizable and Descriptive Data Handling System
4,752,913	Y. H. Chan and J. R. Struk	Random Access Memory Employing Complementary Transistor Switch (CTS) Memory Cells
4,753,694	L. W. Herron, R. O. Lussow, R. W. Nufer, B. Schwartz, J. Acocella, and S. N. Reddy	Process for Forming Multilayered Ceramic Substrate Having Solid Metal Conductors
4,754,390	B. C. Felton and W. B. McCoy	Conductively Cooled Switching Regulator
4,754,394	W. C. Brantley, Jr., K. P. McAuliffe, V. A. Norton, G. F. Pfister, and J. Weiss	Multiprocessing System Having Dynamically Allocated Local/Global Storage and Including Interleaving Transformation Circuit for Transforming Real Addresses to Corresponding Absolute Address of the Storage
4,754,408	K. D. Carpenter, R. K. Jackson, and K. W. Lallier	Progressive Insertion Placement of Elements on an Integrated Circuit
4,754,409	T. J. Ashford, N. A. Burns, R. L. Flagg, C. T. Iwaskiw, M. E. McBride, J. T. Padden, and R. P. Starbird	Method for Dynamically Collecting Current Data from Specified External Processes and Procedures for Use in an Expert System
4,754,433	D. Chin and W. Hwang	Dynamic RAM Having Multiplexed Twin I/O Line Pairs
4,754,488	C. J. Lyke	Method for Ascertaining and Filling of Bounded Areas of a Colored Raster Display