

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,916,854	R. Brosch, H. Schettler, R. Remshardt, and R. Zuehlke	Method and Circuit Arrangement for Reducing the Manufacturing Tolerances in the Production of Monolithically Integrated Semiconductor Circuits
3,277,343,908	F. Frasc, W. Augstein, B. Jvancic, and M. Zuegel	Method of Adjusting the Edge Angle in Polysilicon
3,373,341,408	H. E. Korth	White Light Interferometer
3,373,729,008	A. Blum	Testing and Diagnostic Device for Digital Computers
3,373,763,008	G. Goldrian, V. Rudolph, W. Ruppert, and M. Wohnsdorf	Method of Reproducing Images with Gray Values
3,374,152,208	G. Kraus, M. Glatzel, E. Max, U. Kuenzel, A. Moennich, H. Hinkel, and G. Kaus	Apparatus Comprising a Lubricant-Coated Magnetic Disc and a Magnetic Head, and Method of Making Said Apparatus
3,467,080,708	M. Kallmeyer, H. Baier, M. Schneiderhan, P. Koepp, and E. Pfeffer	Circuit for a Control Device for Focussing a Lens System
3,467,118,808	F. W. Hilpert	Ribbon Correction Unit for Printers

France

99,956	H. Carsalade, F. Delaporte, and J. P. Pantani	Signal Receiver which Indicates the Status of the Device Connected Thereto
121,030	D. Giroir and G. Lacoste	Arbitration Method and Device for Allocating a Shared Resource in a Data Processing System
130,262	A. Brunin, G. Boudon, B. Denis, P. Stoppa, and P. Mollier	Logic Circuits for Forming VLSI Logic Networks
132,481	V. Spagnol and S. Huon	Elastic Buffer and Data Transmission System Incorporating the Same
139,803	P. Callens, C. Galand, G. Platel, and R. Vermot-Gauchy	Method of Reconstructing Lost Data in a Digital Voice Transmission System
151,653	G. Duforestel, M. Lechaczynski, C. Poiraud, and P. Viallon	Device for Serializing/Deserializing Bit Configuration of Variable Length

Sweden

166,847	D. B. Larson	Mean for Application of Self-Adhering Labels
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United Kingdom

97,209	A. J. Betts and P. J. Elliott	Magnetizable Recording Disk and Disk File Employing Servo
130,248	C. N. Wallis	Track Following Servo System for a Disk File
131,658	D. M. Taub	A Synchronisation Mechanism for a Multiprocessing System
146,656	J. S. Heath, S. A. Cutts, D. A. Evans, P. D. Carey, and D. E. White	A Disk Cartridge Having a Flexible Disk Storage Medium and a Disk File Including Such a Cartridge
151,846	M. F. Cowlishaw	Colour Display Apparatus and Method of Coding a Colour

United States

- 4,601,424 A. S. Adwalpalker, J. M. Harvilchuck, J. R. Ranalli, and D. W. Rich
- 4,693,908 E.-M. Rossi and A. R. Tietze
- 4,693,960 E. D. Babich, M. Hatzakis, J. J. Liutkis, J. R. Parasczak, and J. M. Shaw
- 4,694,261 J. F. Ewen and J. M. Mosley
- 4,694,433 S. K. Wiedmann
- 4,694,447 D. K. Cohen and R. Yardy
- 4,695,838 J. J. Ellis, Jr.
- 4,695,884 D. Anastassiou and W. B. Pennebaker
- 4,695,945 J. W. Irwin
- 4,695,948 B. J. Blevins, W. G. Kulpa, J. R. Mathis, and J. W. McCullough
- 4,695,950 H. R. Brandt, P. M. Gannon, W. L. Leung, and T. R. Marchini
- 4,695,997 G. M. Montanari, F. E. Baudelot, and M. L. Costes
- 4,695,999 G. Lebizay
- 4,696,003 R. H. Kerr and R. M. Mesnard
- 4,696,005 E. H. Millham, J. J. Moser, J. J. Shushereba, and G. P. Visco
- 4,696,426 D. D. Decker and M. E. Stanhope
- 4,696,578 M. S. Mansuria, R. G. Meinert, S. Oktay, and C. D. Ostergren
- 4,696,717 W. D. Bissinger
- 4,696,729 H. A. A. Santini
- 4,697,058 W. F. Mueller
- 4,697,099 A. R. Bonaccio
- 4,697,108 B. A. Chappell and S. E. Schuster
- 4,697,127 M. C. Stich, J. B. Resman, H. H. Ottesen, and T. B. Anderson
- 4,697,142 J. E. Frushour
- 4,698,502 J. G. Bednorz, J. K. Gimzewski, and B. Reihl
- 4,698,528 W. P. Marple and H. A. Miller
- 4,698,599 R. L. Hedman
- 4,698,600 D. B. Chapman
- 4,698,624 B. A. Barker, I. H. Hernandez, and R. A. McCaskill
- 4,698,625 R. A. McCaskill and J. S. Wang
- 4,698,644 G. A. Drago, W. F. Baier, F. C. Lee, R. N. Mills, and H. G. Mossbrugger
- 4,698,711 A. W. Vinal
- 4,698,757 D. M. Dill and H. F. Redmond
- 4,698,760 R. F. Lembach, S. D. Lewis, and R. R. Williams
- 4,698,779 I. Holden and M. J. Malings
- 4,698,800 J. R. Cavaliere, A. Y. Chang, and R. J. Robortaccio
- 4,698,808 T. Ishii
- 4,698,830 Z. Barzilai, V. S. Iyengar, and G. M. Silberman
- 4,699,469 T. A. Larsen
- 4,699,533 J. R. Crowe, D. B. Dove, and K. K. Shih
- 4,699,803 C. J. Araps, S. M. Kandetzke, and M. A. Takacs
- 4,700,016 R. B. Hitchcock, E. Kellerman, and J. P. Koons
- 4,700,086 D. T. Ling, V. G. Oklobdzija, and N. Raver
- 4,700,199 F. J. Horlander
- Stripped Gold Plating Process
- Magnetic Recording Disk with Improved Lubricant Retention
- Photolithographic Etching Process Using Organosilicon Polymer Composition
- Integrated High Gain Voltage Controlled Oscillator
- Semiconductor Memory Having Subarrays and Partial Word Lines
- Optical Signal Recorders Employing Two Lasers and Methods Therefor
- Plasma Panel Display Selectively Updatable on Pel Line Basis
- Correction of Shading Effects in Video Images
- Processor I/O and Interrupt Filters Allowing a Co-processor to Run Software Unknown to the Main Processor
- Bus to Bus Converter Using a RAM for Multiple Address Mapping
- Fast Two-Level Dynamic Address Translation Method and Means
- Device for Performing Wrap Tests on a Multiplex Link in a Data Communication System
- Cross-Point Switch of Multiple Autonomous Planes
- System for Testing Interactive Software
- Apparatus for Reducing Test Data Storage Requirements for High Speed VLSI Circuit Testing
- Document Reading Envelope Depository
- Single Chip Thermal Tester
- Process for Automatically Regenerating Copper Chloride Etch Solutions
- Electroplating Cell
- Microweld Apparatus with an Improved Electrode Tip Design and Tip Support
- Open Line Detector Circuit
- Complementary Input Circuit with Nonlinear Front End and Partially Coupled Latch
- Adaptive Control Technique for a Dynamic System
- Printed Circuit Conductor Test System
- Field-Emission Scanning Auger Electron Microscope
- Edge Detection Using Dual Trans-Impedance Amplifier
- Differential Summing Amplifier for Inputs Having Large Common Mode Signals
- Clock Phase Discriminator
- Definition of Line Unit Size
- Graphic Highlight Adjacent a Pointing Cursor
- Drop-on-Demand Ink Jet Print Head
- Simplified, Shielded Twin-Track Read/Write Head Structure
- Terminal Procedure for Utilizing Host Processor Log On and Log Off Prompts
- Method of Optimizing Signal Timing Delays and Power Consumption in LSI Circuits
- Graphic Display with Determination of Coincidence of Subject and Clip Areas
- Bi-directional Transceiver Circuit
- Method for Detecting Intermittent Error in Volatile Memory
- Shift Register Latch Arrangement for Enhanced Testability in Differential Cascode Voltage Switch Circuit
- Liquid Crystal Display
- Surface Layer to Reduce Contact Resistance in Resistive Printing Ribbon
- Method for Forming Electrical Components Comprising Cured Vinyl and/or Acetylene Terminated Copolymers
- Printed Circuit Board with Vias at Fixed and Selectable Locations
- Consistent Precharge Circuit for Cascode Voltage Switch Logic
- Print Quality Controller for a Thermal Printer

4,700,246	R. W. Luoma, J. R. Reidenbach, and C. R. Schwieters	Disk Drive with Isolated Voice Coil Motor Magnet
4,700,316	R. K. Nair	Automated Book Layout in Static CMOS
4,701,314	L. D. David	Method of Producing Microsized Amorphous Particles of Metal Phosphate
4,701,351	R. L. Jackson	Seeding Process for Electroless Metal Deposition
4,701,630	E. J. Annunziata, J. M. Faress II, and T. E. Stammely	Local Area Network Station Connector
4,701,637	R. A. Piro	Substrate Bias Generators
4,701,642	W. D. Pricer	BICMOS Binary Logic Circuits
4,701,752	J. S. Wang	Mirror Inverse Function in an Interactive Graphics System
4,701,836	F. J. Horlander	Method and Apparatus for Controlling Print Quality of a Thermal Printer
4,701,842	H. T. Olnowich	Method and Apparatus for Avoiding Excessive Delay in a Pipelined Processor During the Execution of a Microbranch Instruction
4,701,851	V. R. Bass, V. A. Bonebrake, D. A. Garrison, J. K. Landis, M. S. Neff, R. J. Urquhart, and S. C. Williams	Compound Word Spelling Verification
4,704,791	L. N. Chellis and T. L. Ellis	Process for Providing a Landless Through-Hole Connection
4,705,591	J.-F. Carle and P. Phillippot	Pulling Processes and Equipment for Growing Silicon Crystals Having High and Controlled Carbon Content
4,705,592	D. Bahrle, F. Schwerdt, and J. H. Stehling	Process for Producing Printed Circuits
4,706,008	D. D. Cronch, R. T. Fisher, P. A. Graham, and J. E. Passafiume	Self-Adjusting Homing for a Stepping Motor
4,706,018	J. G. Beha, R. W. Dreyfuss, and G. W. Rubloff	Noncontact Dynamic Tester for Integrated Circuits
4,706,074	J. S. Muhich and J. S. Thornley	Cursor Circuit for a Dual Port Memory
4,706,079	D. A. Kummer, J. A. Saenz, and S. W. Trynosky	Raster Scan Digital Display System with Digital Comparator Means
4,706,138	S. A. Jove and K. B. Klaassen	Amplification of Signals Produced by a Magnetic Sensor
4,706,150	G. Lebizay, Y.-C. L. Lien, and P. S. Yu	Switching Protocol for Multiple Autonomous Switching Planes
4,706,205	T. Akai and J. Yamaguchi	Imaging Processing Apparatus with Reduced Handling and Storage
4,706,250	A. M. Patel	Method and Apparatus for Correcting Multibyte Errors Having Improved Two-Level Code Structure
4,706,577	P. K. Jones	Safe Door Latch Deformation Actuated Interlock
4,707,155	H. A. Burkhead and R. E. Hunt	Re-inkable Ribbon Transport System
4,707,218	N. J. Giammarco, A. Gimpelson, G. A. Kaplita, A. D. Lopata, A. F. Scaduto, and J. F. Shepard	Lithographic Image Size Reduction
4,707,377	R. J. Capwell, R. A. Kaschak, D. G. McBride, R. G. Rickert, and D. P. Seraphim	Copper Plating
4,707,378	D. G. McBride and R. G. Rickert	Method and Apparatus for Controlling the Organic Contamination Level in an Electroless Plating Bath
4,707,667	J. E. Bertsch	Offset Corrected Amplifier
4,707,681	J. S. Eggenberger and A. M. Patel	Method and Apparatus for Implementing Optimum PRML Codes
4,707,753	W. J. Freihauf, D. M. Janssen, A. J. Kirton, M. E. Nard, and L. Rose	Disk Drive Head Access Mechanism
4,707,801	J. G. Barnes, J. N. Chen, and G. E. Hayes	Word Processing System Based on a Data Stream Having Integrated Alphanumeric and Graphic Data
4,707,803	B. O. Anthony, Jr., T. M. Heise, and F. P. Sheppard	Emulator for Computer System Input-Output Adapters
4,708,383	T. J. Deringer	Manipulator Gripper Tool
4,708,883	H. J. Hovel and T. F. Kuech	Annealing Process
4,709,162	G. M. Braceras and J. H. Dreibelbis	Off-Chip Driver Circuits
4,709,166	D. C. Banker, J. A. Dorler, and J. N. Hryckowian	Complementary Cascoded Logic Circuit
4,709,169	G. J. Ashton, J. R. Cavaliere, and M. T. Cheng	Logic Level Control for Current Switch Emitter Follower Logic
4,709,326	J. T. Robinson	General Locking/Synchronization Facility with Canonical States and Mapping of Processors
4,709,328	B. O. Anthony, Jr., G. D. Batalden, G. R. Mitchell, P. C. Schloss, and D. K. Wells	Composite Data-Processing System Using Multiple Standalone Processing Systems
4,709,348	G. R. Horn and R. C. Mikelait	Interactive Page Length Adjustment with Automatic Pagination
4,709,367	R. J. Grafe and S. T. Pancoast	Method and Apparatus for Distinguishing Between Diskettes in a Diskette Drive
4,709,754	R. C. Chu and R. E. Simons	Heat Transfer Element with Nucleate Boiling Surface and Bimetallic Fin Formed from Element
4,710,424	U. Bandara, H. Hinkel, W. Steiner, and G. Trippel	Magnetic Disk and Method of Making Same

4,710,769	L. G. Friedman, B. T. Hailpern, L. W. Hoevel, and Y. J. Thefaine	Transmit-Secure Non-Blocking Circuit-Switched Local Area Network
4,710,801	C. A. Kunzinger	System for Decomposition of NTSC Color Video Signals Using Two Frame Buffers
4,710,806	S. Iwai and S. Uno	Digital Display System with Color Lookup Table
4,710,868	J. Cocke and B. T. Hailpern	Interconnect Scheme for Shared Memory Local Networks
4,710,872	R. G. Scarborough	Method for Vectorizing and Executing on an SIMD Machine Outer Loops in the Presence of Recurrent Inner Loops
4,710,885	M. A. Litteken	Generating Figures in a Document Formatter Directly from a Declarative Tag
4,710,886	A. W. Heath	Table Driven Print Formatting
4,714,348	G. MaKosch	Method and Arrangement for Optically Determining Surface Profiles
4,714,400	J. A. Barnett, J. M. Gomez, W. F. Green, V. M. Lisica, and A. B. Rosenthal	Plural Robotic Drive
4,714,504	M. J. Cummings, D. E. Hanford, and R. M. Japp	Process of Laminating a Photosensitive Layer of a Substrate
4,714,646	D. L. Fletcher and A. E. Graham	Electrophoretic Insulation of Metal Circuit Board Core
4,714,831	G. J. Clark, P. Chaudhari, J. J. Cuomo, M. A. Frisch, and J. L. Speidell	Spherical Retarding Grid Analyzer
4,714,918	B. A. Barker and I. H. Hernandez	Window View Control
4,714,946	T. A. Bajgert, N. H. Bunker, J. L. Cochran, W. J. Duane, and A. Mojica	Continuous Form Feeder for a Reproducing Machine and Process
4,714,953	M. L. Buller, D. L. Lumbra, D. W. Phelps, S. J. Samuelsen, and W. C. Ward	Welded Wire Cooling
4,714,982	C. Prasad and A. F. Szewczyk	Substrate for a Semiconductor Package Having Improved I/O Pin Bonding
4,714,992	H. M. Gladney, D. J. Lorch, and R. L. Mattson	Communication for Version Management in a Distributed Information Service
4,714,993	D. L. Livingston, D. J. Sucher, and B. M. Walk	Apparatus and Method for Effecting Dynamic Address Translation in a Microprocessor Implemented Data Processing System
4,714,994	V. G. Oklobdzija and D. T. Ling	Instruction Prefetch Buffer Control
4,714,996	H. M. Gladney, D. J. Lorch, and R. L. Mattson	Impact Calculation for Version Management in a Distributed Information Service
4,717,448	R. D. Cox, A. B. Israel, and E. H. Payne	Reactive Ion Etch Chemistry for Providing Deep Vertical Trenches in Semiconductor Substrates
4,717,591	R. E. Acosta, W. J. Horkans, R. Mukherjee, and J. D. Olsen	Prevention of Mechanical and Electronic Failures in Heat-Treated Structures
4,717,596	S. G. Barbee, G. P. Devine, W. J. Patrick, and G. Seeley	Method for Vacuum Vapor Deposition with Improved Mass Flow Control
4,717,644	F. Jones and H. R. Voelker	Hybrid Electron Beam and Optical Lithography Method
4,717,678	G. R. Goth	Method for Forming Self-Aligned P Contact
4,717,818	E. C. Broockman and O. L. Stokes, Jr.	Bar Code Label Identification Circuit
4,717,943	H. P. Wolf and D. R. Young	Charge Storage Structure for Nonvolatile Memories
4,718,005	B. A. Feigenbaum, D. D. Gibbs, and R. Sachsenmaier	Distributed Control of Alias Name Usage in Networks
4,718,008	A. Chang, M. F. Mergen, J. T. O'Quin II, J. C. O'Quin III, and M. D. Rogers	Method to Control Paging Subsystem Processing in a Virtual Memory Data Processing System During Execution of Critical Code Sections
4,718,039	F. J. Aichelmann Jr., W. F. Shutler, and V. F. Sollitto, Jr.	Intermediate Memory Array with a Parallel Port and a Buffered Serial Port
4,718,094	L. R. Bahl, P. V. deSouza, S. V. DeGennaro, and R. L. Mercer	Speech Recognition System