

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,277,130,408	D. J. Bahr, K. H. Burckardt, V. Rudolph, G. Goldrian, and W. G. Spruth	Method of Recording Information on an Electrosensitive Record Carrier
3,465,931,508	H. D. Matthaei	Electromagnetic RAM Actuator, in Particular for Impact Printers

France

53,214	J. C. Froment, M. Verhaeghe, J. L. Marijon, and G. Orengo	Digital Signal Distribution System
64,120	J. Cholat Namy	A Process for Determining Active Channel Configurations in a Multiplex Communications System for Implementing Said Process
112,425	M. Boisseau, J. Borie, A. Croisier, M. Demange, G. Lebizay, and J. P. Rossi	Time Space Time Switching Network Using a Closed-Loop Link
112,428	D. Pilost	Analog-to-Digital Converter
124,674	D. Pilost	Method and Device for Synchronizing the Clocks in a Data Transmission System

United Kingdom

112,414	R. A. Cottrell and A. S. Murphy	Interactive Cursor Motion and Image Transformations
112,943	C. J. Holloway	Data Communication Network
117,930	D. C. Solly, T. A. Vines, and L. R. Walker	Interactive Work Station with Auxiliary Microprocessor for Storage Protection
123,745	T. G. Cole and R. M. West	A Step Waveform Generator and CRT Vertical Timebase Incorporating Such a Generator
130,246	J. W. Jones and V. P. Thomas	Content Addressable Memory Structures for Display Control
130,249	D. H. Martin and R. A. Spanner	Electrochromic Display Employing Potentiostatic Erasure
135,629	B. L. Holloway, D. A. Canton, R. J. Llewelyn, and N. B. Sargeant	Data Display Apparatus with Character Refresh Buffer and Row Buffers

United States

4,686,206	J. G. Bednorz, W. Denk, M. Lanz, and W. D. Pohl	Light Waveguide with a Submicron Aperture, Method for Manufacturing the Waveguide and Application of the Waveguide in an Optical Memory
4,684,219	A. R. Cox, A. C. Lowe, and J. C. Wood	Display Cell with Self-Sealing Collapsing Plug
4,684,339	R. J. Bezama and C. H. Perry	Gas-Loaded Pressure Diaphragm
4,684,436	F. C. Burns and G. R. Carden	Method of Simultaneously Etching Personality and Select
4,684,437	J. J. Donelon, Y. Tomkiewicz, T. A. Wassick, and J. T. Yeh	Selective Metal Etching in Metal/Polymer Structures
4,684,542	J. M. Jasinski, B. S. Meyerson, and B. A. Scott	Low Pressure Chemical Vapor Deposition of Tungsten Silicide
4,684,545	E. O. Fey, P. Haselbauer, D. Y. Jung, R. A. Kaschak, H.-D. Kiltbau, R. H. Magnuson, and R. J. Wagner	Electroless Plating with Bi-Level Control of Dissolved Oxygen

4,684,547	T. H. DiStefano, R. L. Hollis, Jr., M. Johnson, and S. S. Wang	Format Patterning Method for Magnetic Recording Media
4,684,802	P. A. Hakenewerth, A. B. Wadia, J. R. Walker, and J. M. White	Elliptical Finger Press Scanner with Rotating Light Source
4,684,921	W. W. Fok and J. P. Moussouris	RLL (1, 7) Encoder with Single State Bit
4,684,936	M. W. Brown and R. E. Dubke	Displays Having Different Resolutions for Alphanumeric and Graphics Data
4,685,005	D. S. Fields, Jr.	Two-Module-Read, Read-After-Write, Bi-Directional Tape Drive
4,685,020	C. D. Driscoll, E. M. Najm, and M. Waechter	Shutdown Circuit for Blocking Oscillator Power Supply
4,685,078	A. Torres	Dual Incrementor
4,685,080	J. C. Rhodes, Jr., V. S. Moore, W. R. Kraft, and J. W. Bans	Microword Generation Mechanism Utilizing Separate Programmable Logic Arrays for First and Second Microwords
4,685,088	R. A. Iannucci	High Performance Memory System Utilizing Pipelining Techniques
4,689,111	K. K. Chan and V. J. Malueg	Process for Promoting the Interlaminar Adhesion of Polymeric Materials to Metal Surfaces
4,689,113	K. Balasubramanyam, A. J. Dally, J. Riseman, and S. Ogura	Process for Forming Planar Chip-Level Wiring
4,689,260	M. Briska, G. Elsner, and H. Hinkel	Abrasion-Resistant Magnetic Recording Disk
4,689,441	C. J. Dick, R. Linsker, R. S. Rutter, and D. L. Thomas	Routing Method and Pattern for Reducing Cross Talk Noise Problems on Printed Interconnection Boards
4,689,608	F. J. Bolda, J. P. McKnight, and R. J. Sturm	Magnetically Snap Actuated Contact Keyboard Apparatus
4,689,617	M. C. Costa and J. B. Shapiro	Concentric Via Plasma Panel
4,689,648	A. W. Vinal	Magnetically Sensitive Metal Semiconductor Devices
4,689,656	V. J. Silvestri and D. D.-L. Tang	Method for Forming a Void Free Isolation Pattern and Resulting Structure
4,689,707	R. E. Schwall	Superconductive Magnet Having Shim Coils and Quench Protection Circuits
4,689,764	P. R. Daniels	Method and Apparatus for Formatting a Line of Text Containing a Complex Character Prior to Text Justification
4,689,768	R. S. Heard and J. C. Tremoulet	Spelling Verification System with Immediate Operator Alerts to Non-Matches Between Inputted Words and Words Stored in Plural Dictionary Memories
4,689,772	G. J. Jordy, D. B. Mooney, and J. M. Mosley	Read Complete Test Technique for Memory Arrays
4,689,824	J. L. Mitchell, K. L. Anderson, and F. C. Mintzer	Image Rotation Method
4,689,869	C. G. Jambotkar and R. B. Renbeck	Fabrication of Insulated Gate Gallium Arsenide FET with Self-Aligned Source/Drain and Submicron Channel Length
4,689,877	M. A. Church	Method and Apparatus for Controlling the Throat Height of Batch Fabricated Thin Film Magnetic Transducers
4,690,833	W. D. Donson, J. V. Ellerson, R. B. Hammer, W. L. Lafer, and K. A. Snyder	Providing Circuit Lines on a Substrate
4,690,836	T. C. Clarke, N. H. Lu, and Y. Tomkiewicz	Process for the Production of Void-Free PrePreg Sheets
4,690,838	H. Hiraoka, J. W. Labadie, J. H. Lee, S. A. MacDonald, and C. G. Willson	Process for Enhancing the Resistance of a Resist Image to Reactive Ion Etching and to Thermal Flow
4,690,962	R. J. Clark, J. Economy, M. A. Flandera, J. R. Susko, and R. A. Wheeler	Composition, Coated Article and Method of Coating
4,691,152	T. E. Ell and J. Stephenson	Data Disk Drive Velocity Estimator
4,691,170	M. W. Riley	Frequency Multiplier Circuit
4,691,219	G. R. Goth	Self-Aligned Polysilicon Base Contact Structure
4,691,258	M. Kobayashi, K. Takahashi, and K. Yoneyama	Temperature Compensating Mechanism for Positioning of a Magnetic Head of a Magnetic Disk Drive
4,691,277	E. P. Kronstadt, T. R. Gheewala, and S. P. Gandhi	Small Instruction Cache Using Branch Target Table to Effect Instruction Prefetch
4,691,426	J. A. Roucek and J. R. Tessier	Repair of Strip Conductor on Electrical Circuit Board
4,691,435	N. G. Anatha, H. S. Bhatia, S. P. Gaur, and J. L. Mauer IV	Method for Making Schottky Diode Having Limited Area Self-Aligned Guard Ring
4,692,044	A. Aviram and K. K. Shih	Interface Resistance and Knee Voltage Enhancement in Resistive Ribbon Printing
4,692,205	K. G. Sachdev, R. W. Kwong, M. R. Gupta, and H. S. Sachdev	Silicon-Containing Polyimides as Oxygen Etch Stop and Dual Dielectric Coatings
4,692,346	D. G. McBride and R. G. Rickert	Method and Apparatus for Controlling the Surface Chemistry on Object Plated in an Electroless Plating Bath
4,692,348	G. W. Rubloff and M. F. Wittmer	Low Temperature Shallow Doping Technique
4,692,395	H. T. Findlay	Transfer Element and Process for Radiation Defined Images
4,692,633	C. H. Ngai and G. J. Watkins	Edge Sensitive Single Clock Latch Apparatus with a Skew Compensated Scan Function
4,692,691	Y. Sueta	Test System for Keyboard Interface Circuit
4,692,758	B. W. Fawcett and L. A. Sendelbach	Legibility Enhancement for Alphanumeric Displays

4,692,832	U. Bandara and H. Hinkel	Method for Lowering the Frictional Coefficient of the Magnetic Head-Disk Interface in a Lubricated Magnetic Disk Storage System
4,692,877	K. A. Byerly, M. E. Feldhacker, and D. E. Grant	Digital Timing Control System with Memory Look Ahead
4,692,893	D. F. Casper	Buffer System Using Parity Checking of Address Counter Bit for Detection of Read/Write Failures