

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

1,207,905 M. F. Metwaly Method and System for the Generation of Arabic Script

Federal Republic of Germany

2,758,146 A. Blum, H. R. Geng, J. Hajdu, S. Richter, H. Chilinski, U. Wille, B. D. Rust, K. J. Getzlaff, and S. E. Neuber Control Circuit in the Instruction Unit of an Electronic Digital Computer

3,175,055,308 J. Greschner, K. Asch, W. Kulcke, and M. Kallmeyer Probe Head Arrangement for Conductor Line Testing with At Least One Probe Head Comprising a Plurality of Resilient Contacts

3,271,507,208 K. Hartmann and A. Bohg Electromagnetic RAM Actuator

3,271,524,208 G. Goldrian and V. Rudolph Transistor Circuit for Reducing Current After Ignition in a Metal Paper Printer

3,272,083,108 C. Brandies, J. Kempf, G. Kraus, and U. Kuenzel Reactor for Reactive Ion Etching and Etching Method

Switzerland

126,272 A. H. Lycke and C. R. Soderquist Magasin for Palett Byraladstyp

United Kingdom

62,121 R. F. Maddock Text Processing Apparatus with Two-Stage Formatting of Text

83,668 D. J. Barclay and C. L. Bird Electrolytic Display Device Employing Metal Ion Catarsis

85,209 V. G. Golding, C. J. Barnett, M. P. Dyche, and Y. H. Ng Audio Response Terminal for Use with Data Processing System

90,107 B. D. Chase Convergence Unit for In-Line Colour Cathode Ray Tube

90,108 B. D. Chase Convergence Unit for In-Line Colour Cathode Ray Tube

United States

4,598,470 W. E. Dougherty, Jr., S. E. Greer, and R. W. Sargent Method for Providing Improved Electrical and Mechanical Connection Between I/O Pin and Transverse Via Substrate

4,598,966 L. J. Boland Non-Shorting Pin System

4,598,974 R. D. Munn and G. R. Stillwell, Jr. Optical Fiber Connector Having Integral Electrodes for Use in Fusion Splicing

4,599,134 S. V. Babu, J. G. Hoffarth, and J. A. Welsh Plasma Etching with Tracer

4,599,136 C. J. Araps, S. M. Kandetzke, E. L. Kutner, and M. A. Takacs Method for Preparation of Semiconductor Structures and Devices Which Utilize Polymeric Dielectric Materials

4,599,243 H. S. Sachdev and K. G. Sachdev Use of Plasma Polymerized Organosilicon Films in Fabrication of Lift-off Masks

4,599,268 L. N. Chellis Product Containing an Epoxy Composition

4,599,277 J. M. Brownlow and R. Rosenberg Control of the Sintering of Powdered Metals

4,599,520 J. A. Gabric and E. F. O'Neil Boosted Phase Driver

4,599,558 A. J. Castellano, Jr., T. H. DiStefano, and R. S. Olyha, Jr. Photovoltaic Imaging for Large Area Semiconductors

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| 4,599,656 | J. M. Bellinghausen | Processing of Gray Scale Image Pels to Facilitate Data Compression |
| 4,599,670 | I. W. Bolton | Control of Relative Humidity in Machine Enclosures |
| 4,599,708 | S. E. Schuster | Method and Structure for Machine Data Storage with Simultaneous Write and Read |
| 4,599,720 | C. A. Kunzinger | Satellite Communications System |
| 4,599,792 | P. E. Cade, B. El-Kareh, and I. W. Kim | Buried Field Shield for an Integrated Circuit |
| 4,600,445 | R. A. Horr and R. L. Mohler | Process for Making Self Aligned Field Isolation Regions in a Semiconductor Substrate |
| 4,600,464 | B. H. Desilets, T. A. Gunther, C. J. Hendricks, and J. H. Keller | Plasma Etching Reactor with Reduced Plasma Potential |
| 4,600,624 | R. R. Joseph and M.-C. Wong | Composite Insulator Structure |
| 4,600,683 | S. E. Greco and D. C. Green | Cross-Linked Polyalkenyl Phenol Based Photoresist Compositions |
| 4,600,837 | T. H. DiStefano and M. Johnson | Optical Scanning Apparatus with Dynamic Scan Path Control |
| 4,600,854 | J. G. Bednorz, M. A. Lanz, H. Nievergelt, and W. D. Pohl | Piezoelectric Stepping Rotator |
| 4,600,936 | H. A. Khoury and B. E. Tompkins | Chip Registration Mechanism |
| 4,601,012 | J. A. Aiken, Jr. | Zone Partitioning in Volume Recovery System |
| 4,601,030 | A. Croisier | Multipoint System for Interconnecting Work Station |
| 4,601,424 | A. S. Adwalpalker, J. M. Harvilchuck, J. R. Ranalli, and D. W. Rich | Stripped Gold Plating Process |
| 4,601,637 | H. E. Aviles and J. A. Gregory | Manipulator Gripper Tool Changing Apparatus |
| 4,601,779 | J. R. Abernathey, J. B. Lasky, L. A. Nesbit, T. O. Sedgwick, and S. Stiffler | Method of Producing a Thin Silicon-on-Insulator Layer |
| 4,601,782 | J. K. Bianchi, R. A. Gdula, and D. J. Lange | Reactive Ion Etching Process |
| 4,601,807 | J. C. Lo and N.-H. Lu | Reactor for Plasma Desmear of High Aspect Ratio Hole |
| 4,601,913 | P. N. Chaloux, Jr., and J. Havas | Underlay Surface Modification to Control Resin Glass Polymerization |
| 4,601,939 | G. S. Gati, A. P. Lee, G. C. Schwartz, and C. L. Standley | Composite Insulator Structure |
| 4,601,969 | N. J. Clecak, B. D. Grant, R. D. Miller, T. C. Tompkins, and C. G. Willson | High Contrast, High Resolution Deep Ultraviolet Lithographic Resist Composition with Diazo Carbonyl Compound Having Alpha Phosphoryl Substitution |
| 4,602,164 | A. N. Gore III, R. C. Lynch, R. P. McAdams, and G. A. Shekita | Radiation Shield System |
| 4,602,170 | C. L. Bertin | Resistive Gate Field Effect Transistor Logic Family |
| 4,602,175 | J. T. Castagna | Damped Coil Construction for VCM |
| 4,602,271 | W. E. Dougherty, Jr., S. E. Greer, W. J. Nestork, and W. T. Norris | Personalizable Masterslice Substrate for Semiconductor Chips |
| 4,602,733 | J. R. Slack and W. D. von Voss | Desoldering Apparatus and Method |
| 4,602,981 | L. Chen and G. S. Mathad | Monitoring Technique for Plasma Etching |
| 4,603,023 | A. Mack and M. F. McAllister | Method of Making a Hybrid Dielectric Probe Interposer |
| 4,603,056 | D. A. MacKinnon, P.-C. Li, and H. C. Schick | Surface Treatment of a Molybdenum Screening Mask |
| 4,603,195 | E. D. Babich, M. Hatzakis, J. R. Paraszczak, and J. M. Shaw | Organosilicon Compound and Use Thereof in Photolithography |
| 4,603,323 | A. Hassitt, A. H. Karp, and R. G. Scarborough | Method for Extending the Exponent Range of an IBM 370-Type Floating Point Processor |
| 4,603,341 | C. L. Bertin and H. L. Kalter | Stacked Double Dense Read Only Memory |
| 4,603,357 | A. M. Ramiréz, T. G. Van Vessem, and C. W. Zell | Magnetic Recording Detection Pointer that Measures the Time Per Data Cell During Which the Analog Waveform Exceeds a Predetermined Detection Threshold |
| 4,603,362 | M. L. Sendelweck | Guiding and Registering Different Thickness Diskettes in a Disk Drive |
| 4,603,380 | M. C. Easton and J. H. Howard | DASD Cache Block Staging |
| 4,603,382 | D. C. Cole, S. M. Fry, H. O. Dempsy, and P. D. Phan | Dynamic Buffer Reallocation |
| 4,603,385 | M. W. Mueller, K. L. Hill, G. T. Hunt, R. Smetana, J. M. McVey, E. L. Miller, and S. F. Clements | Integrated Data Processing/Text Processing System Having a Terminal with Dual Emulation and Enhanced Function Capabilities |
| 4,603,399 | E. A. Cheek, D. R. Thomas, and J. D. Zbrozek | Data Processing Apparatus for Address Substitution |
| 4,603,985 | E. F. Helinski and H. C. Lee | Backstop and Damping Apparatus |
| 4,604,520 | W. D. Pohl | Optical Near-Field Scanning Microscope |
| 4,604,531 | J. E. Gersbach and J. J. Moser | Imbalance Circuits for DC Testing |
| 4,604,534 | W. D. Pricer | Highly Sensitive High Performance Sense Amplifiers |
| 4,604,614 | R. W. E. Farr and I. D. Judd | Video Display System Employing Pulse Stretching to Compensate for Image Distortion |
| 4,604,644 | K. F. Beckham, A. E. Kolman, K. M. McGuire, K. J. Puttlitz, and H. Quinones | Solder Interconnection Structure for Joining Semiconductor Devices to Substrates that Have Improved Fatigue Life, and Process for Making |
| 4,604,690 | R. P. Crabtree and L. F. Kelly, Jr. | Dynamic Configuration for Added Devices |

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| 4,604,694 | R. E. Hough | Shared and Exclusive Access Control |
| 4,604,709 | F. T. Blount, R. S. Capowski, D. F. Casper, L. R. DelSonno, R. F. Geller, J. M. Kusmiss, and T. K. Zimmerman | Channel Communicator |
| 4,604,710 | R. Amezua, S. F. Clements, J. S. Coenen, R. H. DaFoe, M. N. Day, D. M. Ross, and R. O. Simpson | System for Converting Data Processing Information to Text Processing Format and Vice Versa |
| 4,604,713 | D. Godard and E. Morlec | Tone Detector and Multifrequency Receiver Using Said Detector |
| 4,604,739 | D. K. Cohen | Optical Focus Detection Employing Rotated Interference Patterns |
| 4,604,746 | A. Blum | Testing and Diagnostic Device for Digital Computers |
| 4,604,751 | F. J. Aichelmann, Jr., and P. M. Ryan | Error Logging Memory System for Avoiding Miscorrection of Triple Errors |
| 4,604,755 | J. T. Murray | Feed Forward Dual Channel Automatic Level Control for Dual Tone Multi-Frequency Receivers |
| 4,604,966 | H. Kohn | Continuous Solder Paste Dispenser |
| 4,605,194 | G. Binnig, C. Gerber, H. Rohrer, and E. Weibel | High-Performance Vibration Filter |
| 4,605,218 | J. A. Knepper and L. Rose | Constant Force Roll Assembly |
| 4,605,354 | T. P. Daly | Slip Sensitive Robot Gripper System |
| 4,605,828 | D. A. Gostomski, Jr., and J. G. Price | Membrane Keyboard Switch Mounting |
| 4,605,870 | A. H. Dansky and J. P. Norsworthy | High Speed Low Power Current Controlled Gate Circuit |
| 4,605,893 | N. Braslau | Contactless Measurement of Electrical Properties of Wafer Shaped Materials |
| 4,605,928 | C. J. Georgiou | Fault-Tolerant Array of Cross-Point Switching Matrices |
| 4,606,025 | R. M. Peters, H. D. Schnurmann, and L. J. Vidunas | Automatically Testing a Plurality of Memory Arrays on Selected Memory Array Testers |
| 4,606,661 | C. S. Aldrich, J. R. Booth, W. M. Schuller, and L. P. Young | Proportional Vertical and Horizontal Ribbon Tracking for Impact Printers |
| 4,606,662 | S. R. Komplin | Single Stepping Motor Ribbon and Correction Feed and Lift System |
| 4,606,664 | R. A. Pascoe | Control of Movement of Printing Base Line |
| 4,606,931 | B. L. Olsen and A. C. Ouano | Lift-Off Masking Method |
| 4,606,935 | S. E. Blum | Process and Apparatus for Producing High Purity Oxidation on a Semiconductor Substrate |
| 4,606,998 | D. J. Clodgo, R. A. Previti-Kelly, and E. G. Walton | Barrierless High-Temperature Lift-Off Process |
| 4,607,139 | J.-C. Froment, J.-P. Pantani, and M. Verhaeghe | Interface Module for Telephone Lines |
| 4,607,277 | J. K. Hannan, S. Oktay, J. A. Paivanas, and C. J. Spector | Semiconductor Assembly Employing Noneutectic Alloy for Heat Dissipation |
| 4,607,339 | J. W. Davis | Differential Cascode Current Switch (DCCS) Master Slice for High Efficiency/Custom Density Physical Design |
| 4,607,343 | P. Chevillat and D. Maiwald | Apparatus and Method for Performing Division with an Extended Range of Variables |
| 4,607,346 | R. A. Hill | Apparatus and Method for Placing Data on a Partitioned Direct Access Storage Device |
| 4,607,347 | D. A. Kummer and G. E. Webb | Microcomputer System Employing Program Cartridges |
| 4,607,351 | J. L. Gerbeg, J. L. Nazario, and V. M. Simi | Cartridge Memory Protection |
| 4,607,363 | G. Platel, P. Secondo, and S. Wiest | Buffer Device for a Voice Transmission Network |
| 4,608,489 | D. A. Ramsey, C. F. Rohe, and T. E. Sawicki | Method and Apparatus for Dynamically Segmenting a Bar Code |
| 4,608,562 | R. G. Minor and A. Polischuk-Sawtschenko | Address Personalization of a Remotely Attached Device |
| 4,608,589 | G. R. Goth, I. E. Magdo, and S. D. Malaviya | Self-Aligned Metal Structure for Integrated Circuits |
| 4,608,614 | H. O. Rinkleib and W. J. Rueger | Apparatus for Threading Magnetic Tape in a Magnetic Tape Transport |
| 4,608,632 | D. A. Kummer | Memory Paging System in a Microcomputer |
| 4,608,649 | J. W. Davis, V. S. Moore, and N. G. Thoma | Differential Cascode Voltage Switch (DCVS) Master Slice for High Efficiency/Custom Density Physical Design |
| 4,608,664 | G. M. Barlett, J. P. Hofmeister, D. D. Nye, and E. J. Pring | Automatically Balancing and Vertically Justifying a Plurality of Text/Graphics-Columns |
| 4,608,667 | R. L. Barry | Dual Mode Logic Circuit for a Memory Array |
| 4,608,669 | W. S. Klara, T. W. Kwap, V. Marcello, and R. A. Rasmussen | Self Contained Array Timing |
| 4,608,687 | P. F. Dutton | Bit Steering Apparatus and Method for Correcting Errors in Stored Data, Storing the Address of the Corrected Data and Using the Address to Maintain a Correct Data Condition |
| 4,608,706 | Y. E. Chang, L. J. Grasso, A. J. Gruodis, and C. E. Morgan | High-Speed Programmable Timing Generator |
| 4,609,280 | C. A. Queener | Xerographic Apparatus and Process with Backside Photoconductor Imaging |
| 4,609,429 | M. H. Ishaq and W. P. Noble, Jr. | Process for Making a Small Dynamic Memory Cell Structure |
| 4,609,813 | D. K. Cohen | Optical System Employing Ovate Light Beams |

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| 4,609,818 | E. Lennemann and W. Ruppert | Opto-Electronic Scanning Apparatus with Rotary Plate Scanning Element |
| 4,609,907 | R. L. Adler, P. A. Franaszek, M. Hassner, and R. C. Schneider | Dual Channel Partial Response System |
| 4,609,926 | A. Aviram and D. B. Dove | Ribbon Transfer Color-on-Demand Resistive Ribbon Printing |
| 4,609,996 | D. A. Kummer | Memory Access System for a Computer System Adapted to Accept a Memory Expansion Module |
| 4,610,027 | K. L. Anderson, G. Goertzel, and J. L. Mitchell | Method for Converting a Bit Map on an Image to a Run Length or Run End Representation |