

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,629,592	W. Hasler and S. Padmanabhan	Print Hammer for Type Printers
2,746,743	D. Staiger	Pattern Generation System

Sweden

82,236	L. O. Hallberg	Character Recognition System and Its Use in an Optical Document Reader
84,007,814	J. G. Schebesta and D. B. Larsen	Means for Application of Self-Adhering Labels

United States

4,525,507	M. Chaker, R. J. Clark, and J. S. Hayes	High Flash Point/Low Surface Energy Solvent Systems for Polyimide Conformal Coatings
4,543,319	V. W. Chao, F. B. Kaufman, S. R. Kramer, and B. J. Lin	Polystyrene-Tetrathiafulvalene Polymers as Deep-Ultraviolet Mask Material
4,570,746	S. K. Das, N. R. Dixon, and R. F. Gluck	Wind/Breath Screen for a Microphone
4,571,275	T. W. Moksvold	Method for Minimizing Autodoping During Epitaxial Deposition Utilizing a Graded Pattern Subcollector
4,571,451	R. Linsker and R. S. Rutter	Method for Routing Electrical Connections and Resulting Product
4,571,527	W. J. Martin	VFET Driving Circuits for Plasma Panel Display Systems
4,571,671	C. S. Burns, M. R. Crabtree, D. A. Gourneau, S. W. Hinkel, G. A. Lerom, and M. J. Mayfield	Data Processor Having Multiple-Buffer Adapter Between a System Channel and Input/Output Bus
4,571,674	M. H. Hartung	Peripheral Storage System Having Multiple Data Transfer Rates
4,571,678	G. J. Chaitin	Register Allocation and Spilling Via Graph Coloring
4,571,699	A. Herzog, L. L. Honomichl, J. M. Nagda, and T. A. Rehage	Optical Mark Recognition for Controlling Input Devices, Hosts, and Output Devices
4,571,700	C. A. Emry, Jr., G. H. Neuman, L. K. Mersiovsky, and M. S. Pfeuffer	Page Indexing System for Accessing Sequentially Stored Data Representing a Multi-Page Document
4,571,734	F. B. Dolivo, G. Ungerboeck, and T. D. Howell	Method and Apparatus for Decoding the Output Signal of a Partial-Response Class-IV Communication or Recording-Device Channel
4,572,687	D. F. Croley and S. Dyer	Repetitive Mode for Thermal Printing Lift-Off Correction
4,573,138	J. L. Gaudet, H. L. Lineman, R. E. Chukran, G. H. Neuman, and J. G. Barnes	Justifying with Printer Level Data Stream Which Accommodates Footers and Headers
4,573,199	S.-C. Chen and S. C. Tseng	Kanji/Chinese Font Generation by Scaling
4,573,200	G. M. Bednar, B. E. Fitzpatrick, J. C. Harmon, and M. S. I. Narasimha	Video Normalization for Hand Print Recognition
4,573,256	J. S. Lechaton, P. M. Pitner, and G. R. Srinivasan	Method for Making a High Performance Transistor Integrated Circuit
4,573,779	G. A. Deighton and G. W. Moore	Variable Size Circular Aperture Camera
4,573,830	R. H. Richardson and R. T. Valley	Chip Decelerator
4,574,095	T. H. Baum, F. A. Houle, C. R. Jones, and C. A. Kovac	Selective Deposition of Copper
4,574,204	Y. A. Bonnet	Circuit for Holding a Pulse During a Predetermined Time Interval and an Improved Monostable Multivibrator

4,574,228	L. A. Blue and D. E. Conner	Current Controlled Motor Drive Circuit
4,574,346	M. H. Hartung	Method and Apparatus for Peripheral Data Handling Hierarchies
4,574,349	R. N. Rechtschaffen	Apparatus for Addressing a Larger Number of Instruction Addressable Central Processor Registers Than Can Be Identified by a Program Instruction
4,574,351	L. Q. Dang, C. P. Geer, M. E. Houdek, E. R. Jones, F. G. Soltis, J. A. Soyring, and T. M. Walker	Apparatus for Compressing and Buffering Data
4,574,363	R. G. Carlgren, M. A. Reed, and W. S. Rosenbaum	Mixed Mode Enhanced Resolution Hyphenation Function for a Text Processing System
4,574,365	R. E. Scheuerlein	Shared Access Lines Memory Cell
4,574,382	M. A. Ko	Variable Length Character Code System
4,575,166	D. G. Kasdagly and R. W. Little	Circuitry on Mylar and Dual Durometer Rubber Multiple Connector
4,575,190	J. C. Wood, A. C. Lowe, and B. F. Dowden	Hermetic Electro-Optic Display Cell
4,575,630	G. V. Lukianoff	Electron-Beam Testing of Semiconductor Wafers
4,575,647	G. J. Ashton, E. Colao, and J. P. Cavaliere	Reference-Regulated Compensated Current Switch Emitter-Follower Circuit
4,575,731	F. J. Horlander	Electro Resistive Printhead Drive Level Sensing and Control
4,575,740	G. Goldrian and V. Rudolph	Transistor Circuit for Reducing Current After Ignition in a Metal Paper Printer
4,575,741	D. J. Frank	Cryogenic Transistor with a Superconducting Base and a Semiconductor-Isolated Collector
4,575,744	L. B. Caldwell, M. F. Crocetti, and B. Dunham	Interconnection of Elements on Integrated Circuit Substrate
4,575,775	D. W. Albrecht	Magnetic Recording Disk Having a Sector Servo Pattern for Use with a Multiple Element Head
4,575,776	H. C. Stephens and M. L. Workman	Magnetic Recording Disk File Servo Control System Including an Actuator Model for Generating a Simulated Head Position Error Signal
4,575,794	G. A. Veneski, N. G. Thoma, and M. Cases	Clocking Mechanism for Overlapped Dynamic Programmable Logic Arrays Used in a Digital Control Unit
4,575,798	E. E. Lindstrom and J. S. Vitter	External Sorting Using Key Value Distribution and Range Formation
4,575,813	G. M. Bartlett, J. P. Hofmeister, D. D. Nye, and E. J. Pring	Automatically Balancing and Vertically Justifying a Plurality of Tex/Graphics-Columns
4,575,816	T. A. Hendrickson, G. C. Macauley, D. L. Pierce, R. W. Roefer, and A. B. Strickland	Interactive Transactions Processor Using Sequence Table Pointers to Access Function Table Statements Controlling Execution of Specific Interactive Functions
4,575,817	W. H. Allen, T. M. Cory, P. J. Hurley, and R. A. Thornton	Switching of Programming Routine Supporting Storage Stacks
4,575,826	M. E. Dean	Refresh Generator System for a Dynamic Memory
4,575,827	J. E. Kulakowski	Self-Archiving Data Recording
4,575,847	B. N. Fallwell, Jr., M. A. Kaltenbach, and W. B. Ott	Hot Carrier Detection
4,576,652	H. J. Hovel and T. F. Kuech	Incoherent Light Annealing of Gallium Arsenide Substrate
4,576,659	R. K. Lewis and S. K. Ray	Process for Inhibiting Metal Migration During Heat Cycling of Multilayer Thin Metal Film Structures
4,576,698	J. P. Gallagher and H. W. Schmidt, Jr.	Plasma Etch Cleaning in Low Pressure Chemical Vapor Deposition Systems
4,576,736	A. P. Harmuth	Method of Predicting and Controlling the Viscosity of Conductive Pastes
4,576,832	G. A. Walker	Self-Aligning Mask
4,576,895	R. W. Barton, H. J. Coufal, V. B. Jipson, and W. Y. Lee	Optical Recording by Energy-Induced Fractionation and Homogenization
4,577,099	D. S. Goodman	Apparatus for Proximity Detection of an Opaque Pattern on a Translucent Substrate
4,577,187	M. G. Barr, W. H. Barrett, J. W. Braidt, J. F. Driscoll, J. A. Gregory, S. A. Lucente, and L. J. Remsburger	Display Workstation
4,577,212	G. R. Hueckel and G. S. Prokop	Structure for Inhibiting Forward Bias Beta Degradation
4,577,284	A. E. Christy and E. J. Gill	Adaptive Robot Batch Assembly System
4,577,288	D. G. Busch	Data Communications System with Receiving Terminal for Varying the Portion of Received Data Being Displayed
4,577,289	L. D. Comerford and S. R. White	Hardware Key-on-Disk System for Copy-Protecting Magnetic Storage Media
4,577,292	K. Bernstein	Support Circuitry for Multi-Port Systems
4,577,293	R. E. Matick and D. T. Ling	Distributed, On-Chip Cache
4,577,313	K.-B. K. Sy	Routing Mechanism with Encapsulated FCS for a Multi-Ring Local Area Network
4,577,503	W. I. Imaino and A. C. Tam	Method and Device for Detecting Specific Acoustic Spectral Feature
4,577,508	R. Chaplin	Optical Vibration Analyzer
4,577,918	D. G. Kasdagly	Copper and Dual Durometer Rubber Multiple Connector
4,577,926	A. G. Dewey and C. P. Marinelli	Fiber Optic Writing Head
4,577,930	K.-H. Yang	Weak Boundary Storage Liquid Crystal Display Devices with Bias Voltage

4,577,968	G. Makosch	Method and Arrangement for Optical Distance Measurement
4,577,983	A. Aviram, D. B. Dove, and R. Lane	Color-on-Demand Ribbon Printing
4,578,279	A. R. Zingher	Inspection of Multilayer Ceramic Circuit Modules by Electrical Inspection of Unfired Green Sheets
4,578,584	T. Baumann, F. H. Dacol, and R. L. Melcher	Thermal Wave Microscopy Using Areal Infrared Detection
4,578,587	U. Behringer, H. Bohlen, P. Nehmiz, and W. Zapka	Error-Corrected Corpuscular Beam Lithography
4,578,671	D. L. Flowers	Remote Indicating Low Battery Voltage Enunciator Method and Apparatus
4,578,674	D. C. Baker, D. F. Bantz, and G. A. Flurry	Method and Apparatus for Wireless Cursor Position Control
4,578,695	F.-X. Delaporte, G. M. Lebesnerais, and J.-P. Pantani	Monolithic Autobiased Resistor Structure and Application Thereof to Interface Circuits
4,578,711	J. M. White, R. W. May, and M. D. Gulliver	Video Data Signal Digitization and Correction System
4,578,720	T. Matsumoto	Self-Clocking Code Demodulator with Error Detecting Capability
4,578,722	J. L. Lovgren and W. B. Plummer	Method for Checking Disk Identity in a Flexible Disk Recorder
4,578,723	A. J. Betts and P. J. Elliott	Head Positioning System with Automatic Gain Control
4,578,771	A. C. O'Hara, Jr.	Dynamically Reprogrammable Array Logic System
4,578,779	Y. H. Chan and J. R. Struk	Voltage Mode Operation Scheme for Bipolar Arrays
4,578,788	K. Y. Ahn, T. H. DiStefano, and V. B. Jipson	High Performance Optical Storage Medium with Separate Mechanical Servo Tracks Embedded Within the Medium
4,579,055	W. D. Thorne	Printer Having Drive and Control System for Metal Band Holding Print Elements
4,579,760	J. R. Hause and E. Mendel	Wafer Shape and Method of Making Same
4,579,772	A. C. Bhatt, M. J. Cibulsky, D. E. Doran, L. J. Hugaboom, and J. W. Knight	Glass Cloth for Printed Circuits and Method of Manufacture Wherein Yarns Have a Substantially Elliptical Cross-Section
4,580,135	D. A. Kummer, D. P. Rackley, and J. A. Saenz	Raster Scan Display System
4,580,137	T. S. Fiedler and R. P. Moore	LSSD-Testable D-Type Edge-Trigger-Operable Latch with Overriding Set/Reset Asynchronous Control
4,580,176	D. C. Graves, R. C. Schneider, and V. Zimmermann	Adaptive Equalization Circuit for Magnetic Recording Channels Utilizing Signal Timing
4,580,193	J. C. Edwards	Chip to Board Bus Connection
4,580,230	J. W. Jones and V. P. Thomas	Cathode Ray Tube Controller
4,580,265	D. N. Gooding, S. P. Jackowski, J. T. Moyer, and J. W. Plant III	Failure Detection Method and Apparatus
4,581,098	L. V. Gregor	MLC Green Sheet Process
4,581,100	M. Hatzakis, J. R. Paraszczak, and B. Robinson	Mixed Excitation Plasma Etching System
4,581,537	W. J. Guillaume, J. F. Loughran, J. Rogoyski, R. A. Simpson, and E. V. Weber	Method for Generating Inspection Patterns
4,581,563	J. D. Rockrohr	Variable Format Controls CRT Raster
4,581,566	G. E. Ekstrom and L. Szonyi	System for Automatically Calibrating a Robot
4,581,671	D. B. Francis and R. L. Meggison	Magnetic Tape Recording and Playback System
4,581,702	S. F. Saroka and G. C. Smith	Critical System Protection
4,581,710	H. Hasselmeier	Method of Editing Dot Pattern Data for Character and/or Image Representations
4,581,732	M. Boisseau, J. C. Borie, A. Croisier, M. Demange, G. Lebizay, and J.-P. P. Rossi	Time-Space-Time Switching Network Using a Closed-Loop Link
4,581,739	M. T. McMahon, Jr.	Electronically Selectable Redundant Array (ESRA)
4,582,381	J. J. Bisczat and T. M. Paulson	Electrical Interlock
4,582,441	W. A. Carter, R. H. Cecil, and J. C. Tremoulet	Text Editing with Voice Prompting
4,582,722	L. W. Herron, A. H. Kumar, and R. W. Nufer	Diffusion Isolation Layer for Maskless Cladding Process
4,583,088	W. K. Bux and E. H. Rothauser	Method and Apparatus for Controlling Access to a Communication Network
4,583,105	J. J. Rosenberg	Double Heterojunction FET with Ohmic Semiconductor Gate and Controllable Low Threshold Voltage
4,583,106	N. G. Anantha, J. Riseman, and P. J. Tsang	Fabrication Methods for High Performance Lateral Bipolar Transistors
4,583,110	T. N. Jackson, P. D. Kirchner, G. D. Pettit, and J. M. P. Woodall	Intermetallic Semiconductor Ohmic Contact
4,583,123	H. Baier, M. Kallmeyer, P. Koepp, E. Pfeffer, and M. Schneiderhan	Circuit for a Control Device for Focussing a Lens System
4,583,145	A. Mönnich, M. Glatzel, H. Hinkel, G. Kaus, G. Kraus, U. Künzel, and E. Max	Apparatus Comprising a Lubricant-Coated Magnetic Disc and a Magnetic Head, and Method of Making Said Apparatus
4,583,165	P. L. Rosenfeld	Apparatus and Method for Controlling Storage Access in a Multilevel Storage System

4,583,166	M. H. Hartung, A. H. Nolte, and D. G. Reed	Roll Mode for Cached Data Storage
4,583,193	W. R. Kraft, M. Cases, W. L. Stahl, Jr., N. G. Thoma, and V. D. Wyatt	Integrated Circuit Mechanism for Coupling Multiple Programmable Logic Arrays to a Common Bus
4,583,197	B. A. Chappell and H.-H. Hsieh	Multi-Stage Pass Transistor Shifter/Rotator
4,583,201	C. L. Bertin and H. N. Kotecha	Resistor Personalized Memory Device Using a Resistive Gate FET
4,583,488	W. W. Brown, Jr., W. J. Curry, G. P. Dahlke, F. T. Lupul, and P. A. Totta	Variable Axis Rotary Drive Vacuum Deposition System
4,583,857	M. Grammerstorff and H. Pietruschka	Apparatus for Automatic Optical Property Testing
4,583,873	H. M. Parks, P. W. Sobey, and B. J. Wilson, Jr.	Printer with Integral Paper Handling Apparatus
4,584,574	W. F. Beausoleil	Information Display and Editing System
4,584,635	A. G. MacInnis and W. B. Nunnery	Flux Centering and Power Control for High Frequency Switching Power
4,584,643	J. Y. Halpern, B. B. Simons, and H. R. Strong	Decentralized Synchronization of Clocks
4,584,644	A. Larner	Method of Controlling Use of Resources in a Data Processing System by At Least Two Processes
4,584,669	M. D. Moynihan and T. A. Williams	Memory Cell with Latent Image Capabilities
4,584,681	S. Singh and V. P. Singh	Memory Correction Scheme Using Spare Arrays
4,584,682	S. R. Shah, S. Singh, and V. P. Singh	Reconfigurable Memory Using Both Address Permutation and Spare Memory Elements
4,584,703	L. O. Hallberg	Character Recognition System and Its Use in an Optical Document Reader