

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,262,297	H. H. Berger and S. K. Wiedmann	Monolithically Integratable Digital Base Circuit
2,855,723	H. Moritz and K. Schaekert	Method of Making a Negative Pattern Out of a Positive Photoresist
3,173,047,708	H. E. Korth	Method and Apparatus for Determining the Polarization State of a Light Wave Field
3,267,514,308	H. Hinkel, G. Kaus, J. Kempf, M. Schrader, H. G. Nauth, F. Ertingshausen, and V. Heinrich	Magnetic Head Slider of Ceramic Material
3,267,548,808	H. Baier, E. Pfeffer, P. Koeppe, M. Schneiderhan, H. Reimann, and H. Roesch	Method and Apparatus for Automatic Optical Insertion
3,267,765,008	R. W. Hilpert, E. Kohm, M. E. Wohnsdorf, and V. Rudolph	Print Head for an Electroerosion Printer

United States

4,549,374	J. S. Basi and E. Mandel	Method for Polishing Semiconductor Wafers with Montmorillonite Slurry
4,549,407	P. A. Gruber	Evaporative Cooling
4,549,500	A.-M. Lowin and E. G. Wight	Programming Aid Device
4,549,824	K. G. Sachdev and A. Aviram	Ink Additives for Efficient Thermal Ink Transfer Printing Processes
4,549,927	G. R. Goth, T. A. Hansen, and J. S. Makris	Method of Selectively Exposing the Sidewalls of a Trench and Its Use to the Forming of a Metal Silicide Substrate Contact for Dielectric Filled Deep Trench Isolated Devices
4,550,047	T. N. Jackson, P. D. Kirchner, G. D. Pettit, J. J. Rosenberg, J. M. Woodall, and S. L. Wright	Silicon Source Component for Use in Molecular Beam Epitaxial Growth Apparatus
4,550,061	K. G. Sachdev, S. A. Shear, K. S. Pennington, and M. S. Cohen	Electroerosion Printing Media Using Depolymerizable Polymer Coatings
4,550,128	L. N. Chellis	Epoxy Composition
4,550,257	G. K. Binnig, R. M. Feenstra, R. T. Hodgson, H. Rohrer, and J. M. Woodall	Narrow Line Width Pattern Fabrication
4,550,330	A. B. Fowler	Semiconductor Interferometer
4,550,399	F. G. Caron	Odynamical Operating Rate Allocation Process in a Multiplex Communication System
4,550,438	D. B. Convis, P. J. Grimm, and M. A. Reed	Retro-Stroke Compression and Image Generation of Script and Graphic Data Employing an Information Processing System
4,553,320	K. C. Bryant-Jeffries, H. G. Dickie, and E. V. Vaughan	Rotor for a Dynamo-Electric Machine and Method of Making Same
4,553,813	J. L. McNaughton and J. R. Petrozello	Fiber Optic Connector System for Integrated Circuit Modules

4,553,814	A. L. Bahl, G. R. Stilwell, Jr., E. C. Uberbacher, and C. A. Williamson	Detachable Fiber Optic Connector Assembly
4,553,853	J. D. Gregory and J. M. Budnik	End Point Detector for a Tin Lead Evaporator
4,553,862	G. Goldrian, M. Nitschke, V. Rudolph, and M. Wohnsdorf	Apparatus for the Bounce-Free Landing of a Print Head
4,553,869	D. E. Alexander, J. F. Holland, H. G. Leonard, and A. R. Reed	Buckling One Way Clutch
4,554,182	J. R. Bupp, G. K. Lemon, V. Markovich, C. J. Sambucetti, S. L. Tisdale, and D. J. Trevitt	Method for Conditioning a Surface of a Dielectric Substrate for Electroless Plating
4,554,184	M. J. Canestaro, R. A. Kaschak, D. G. McBride, and D. P. Seraphim	Method for Plating from an Electroless Plating Bath
4,554,405	K. J. Varker	High Density Encapsulated Wire Circuit Board
4,554,458	U. Behringer, H. Bohlen, W. Kulcke, and P. Nehmiz	Electron Beam Projection Lithography
4,554,466	G. E. Dillon	Edge-Triggered Latch Circuit Conforming to LSSD Rules
4,554,506	L. H. Faure, D. R. Townsend, and T. J. Walsh	Modular Test Probe
4,554,559	J. S. Heath	Thermal Print Head
4,554,562	A. Afzali-Ardakani, M. S. Cohen, K. S. Pennington, and K. G. Sachdev	Scratch Resistant Recording Materials for Electroerosion Printing Not Requiring a Lubricant Overcoat
4,554,593	S. J. Fox, F. J. Yeskel, and W. J. Zimmermann, Jr.	Universal Threshold/Discriminator
4,554,625	R. H. J. M. Otten	Method for Generating an Optimized Nested Arrangement of Constrained Rectangles
4,554,645	A. Furman	Multi-Port Register Implementation
4,556,448	K. M. Kim, P. Smetana, and G. H. Schwuttke	Method for Controlled Doping of Silicon Crystals by Improved Float Zone Technique
4,556,585	J. R. Abernathey and C. W. Koburger III	Vertically Isolated Complementary Transistors
4,556,597	J. S. Best and D. C. Bullock	Ion Doped Servo Pattern for Magnetic Recording Disk
4,556,628	J. Greschner, F. W. Schwerdt, and H. J. Trumpf	Process for Producing Printed Circuit Boards with Metallic Conductor Structures Embedded in the Insulating Substrate
4,556,823	J. H. Keller and J. R. Winnard	Multi-Function Charged Particle Apparatus
4,556,845	D. H. Strobe and T. E. Wray	Method for Monitoring Deposition Rate Using an Eddy Current Detector
4,556,878	J. D. Dwire and D. T. Crehan	Display of Graphics Using a Non-All Points Addressable Display
4,556,924	F. F. Quist, Jr. and M. L. Steinbrecher	Disk Drive Head Accessing Mechanism
4,556,938	T. E. Parker and G. A. Veneski	Microcode Control Mechanism Utilizing Programmable Microcode Repeat Counter
4,556,948	B. R. Mercy	Multiplier Speed Improvement by Skipping Carry Save Adders
4,556,952	J. A. Brewer, L. C. Eggebrecht, D. A. Kummer, and P. P. McHugh	Refresh Circuit for Dynamic Memory of a Data Processor Employing a Direct Memory Access Controller
4,556,954	H. Advani and G. E. Hayes	Method for Displaying Selected Text Processing Task Options Corresponding to the Contents of the Combined Program Diskette
4,556,959	W. H. Allen, T. M. Cory, and R. A. Thornton	Printer Selection of Options
4,556,969	R. C. Treseder and T. G. Savarese	Hermetically Sealed Disk File
4,556,977	U. Olderdisen and H. Schumacher	Decoding of BCH Double Error Correction-Triple Error Detection (DEC-TED) Codes
4,557,192	W. L. Dollenmayer	Self Restoring Pivoting Means and Print Hammer Using Same
4,557,410	C. D. Holden and A. J. Rood	Stapler Mechanism Powering Means
4,557,616	D. B. Dove, C. D. Makowka, and R. L. Street	Resistive Ribbon Thermal Transfer Printing System and Process
4,557,796	F. Druschke, G. Kraus, U. Kuenzel, W. D. Ruh, and R. Schaefer	Method of Dry Copper Etching and Its Implementation
4,557,995	A. N. Broers and R. B. Laibowitz	Method of Making Submicron Circuit Structures
4,558,313	R. L. Garwin and J. L. Levine	Indicator to Data Processing Interface
4,558,370	J. L. Mitchell and W. B. Pennebaker	Image Processing Method for Graphics Images
4,558,383	M. Johnson	Information Storage Disk Transducer Position Control System Using a Prerecorded Servo Pattern Requiring No Alignment with the Storage Disk
4,558,433	K. Bernstein	Multi-Port Register Implementations
4,558,447	J. W. Freeman, W. R. Kraft, H. L. Kurtz, and I. B. Magrisso	Self-Testing Facilities of Off-Chip Drivers for Processor and the Like
4,558,455	G. Epenoy and R. Kuhne	Data Transmission System
4,560,435	K. H. Brown, D. F. Moore, and B. J. C. van der Hoeven, Jr.	Composite Back-Etch/Lift-Off Stencil for Proximity Effect Minimization
4,560,583	T. W. Moksvold	Resistor Design System
4,560,929	G. Melnyk	Position Detector Apparatus and Method Utilizing Damped Oscillations
4,560,956	W. S. Duncan	Oscillator with Crystal-Resistive Feedback
4,561,015	H.-E. Korch	All-Solid-State Television Camera

4,561,040	D. E. Eastman, J. M. Eldridge, K. E. Petersen, and G. Olive	Cooling System for VLSI Circuit Chips
4,561,094	S. P. Jackowski and J. T. Moyer	Interface Checking Apparatus
4,563,058	R. Yardy	Optical Signal Recorder Employing a Transducer Having an Adjustable Dichroic Mirror
4,563,385	A. C. Bhatt, D. E. Doran, and J. W. Knight	Hybrid Glass Cloth for Printed Circuit Boards
4,563,681	D. Godard	Tone Receiver for Digital Data Communication Systems