

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

3,171,700,408	D. J. Bahr and M. Briska	Process for Producing Compact Lacquer Layers for Record Carriers
3,171,865,508	M. Briska and D. J. Bahr	A Record Carrier
3,171,882,508	D. Schuett and V. Goetze	Programmable Logic Array with Self Correction of Faults
3,171,958,908	D. J. Bahr and M. Briska	Lacquer for Record Carriers and Process for Its Production
3,172,201,608	H. E. Korth	Modular Optical Fiber Communication System
3,172,215,608	D. J. Bahr and M. Briska	Record Carrier for Electroerosion Printers
3,172,304,708	D. Staiger	Method and Apparatus for Generating Pulses of a Predetermined Time Relation Within Predetermined Pulse Intervals with a High Time Resolution
3,172,643,708	H. Schettler and E. E. Stadler	Multilayer Ceramic Substrate with Several Metallization Planes

Switzerland

80,532	H. Beha, H. Jaeckel, and P. Vettiger	Buried Junction Josephson Interferometer
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United Kingdom

52,684	D. L. Bird	Digital Data Display System
55,317	D. H. Kirkman, C. L. Bird, and W. A. Warwick	Passive Display Module and System
65,598	B. Ward and P. L. Edwards	Improvements in Telecommunication Receivers
2,115,008	D. J. Barclay	Electrodeposition of Chromium and Its Alloys

United States

4,518,112	W. R. Miller and C. Prasad	Process for Controlled Braze Joining of Electronic Packaging Elements
4,518,634	D. Gini and D. B. Stockton	Method and Apparatus for Coating Magnetic Recording Media
4,518,846	J. L. Freeouf and T. N. Jackson	Heater Assembly for Molecular Beam Epitaxy Furnace
4,518,874	J. W. Davis and F. D. Jones	Cascoded PLA Array
4,518,891	W. E. Howard, Jr.	Resistive Mesh Structure for Electroluminescent Cell
4,518,910	H. G. Hottenrott, P. B. Hodge, and T. J. Cochran	Buckling Beam Twist Probe Contractor Assembly with Spring Biased Stripper Plate
4,518,979	W. P. Dumke and J. M. P. Woodall	Semiconductor Transistor with Graded Base and Collector
4,521,952	J. Riseman	Method of Making Integrated Circuits Using Metal Silicide Contacts
4,522,486	G. A. Clark, F. W. Johnson, and C. A. Queener	Method and Apparatus for Adaptive Collation
4,522,893	H. Bohlen, G. Käus, J. Greschner, J. H. Keyser, and W. Kulcke	Contact Device for Releasably Connecting Electrical Components
4,522,911	N. J. Clecak, D. R. McKean, R. D. Miller, T. C. Tompkins, and C. G. Willson	Deep Ultra-Violet Lithographic Resists with Diazohomotetramic Acid Compounds
4,523,322	J.-C. Abbiate	Interface Device for Modems
4,533,211	G. C. Bjorklund and G. T. Sincerbox	Frequency Multiplexed Optical Spatial Filter Based upon Photochemical Hole Burning
4,533,582	V. M. DePalma and A. W. Ward	Stabilized Magnetic Recording Materials and Process for Tailoring and Stabilizing of Magnetic Recording Materials

4,533,840	T. R. Gheewala and S. B. Kaplan	Soliton Sampler
4,533,921	W. Goff, Jr. and J. M. Rakes	Electroerosion Printhead with Tungsten Electrodes, and a Method for Making Same
4,533,940	B. A. Chappell, T. I. Chappell, and J. M. Woodall	High Spatial Resolution Energy Discriminator
4,533,995	J. H. Christian, M. H. Hartung, A. H. Nolta, D. G. Reed, R. E. Riech, and J. S. William	Method and System for Handling Sequential Data in a Hierarchical Store
4,533,996	M. H. Hartung and K. P. Nolan	Peripheral Systems Accommodation of Guest Operating Systems
4,534,011	L. P. Andrews, C. A. Heath, J. E. Mead, R. G. VanDuren, and G. A. Janes	Peripheral Attachment Interface for I/O Controller Having Cycle Steal and Off-Line Modes
4,534,017	D. R. Thomas and P. C. Tien	FET Memory with Drift Reversal
4,534,029	S. Singh and V. P. Singh	Fault Alignment Control System and Circuits
4,534,030	R. Paez and J. R. Rodriguez	Self-Clocked Signature Analyzer
4,534,797	D. E. King, V. Markovich, C. J. Sambucetti, S. L. Tisdale, and D. J. Trevitt	Method for Providing an Electroless Copper Plating Bath in the Take Mode
4,534,804	P. E. Cade	Laser Process for Forming Identically Positioned Alignment Marks on the Opposite Sides of a Semiconductor Wafer
4,534,806	I. E. Magdo	Method for Manufacturing Vertical PNP Transistor with Shallow Emitter
4,534,811	N. G. Ainslie and Swie-In Tan	Apparatus for Thermo Bonding Surfaces
4,534,816	L. Chen, C. J. Hendricks, G. S. Mathad, and S. J. Polonci	Single Wafer Plasma Etch Reactor
4,534,826	G. R. Goth, T. A. Hansen, and R. T. Villette, Jr.	Trench Etch Process for Dielectric Isolation
4,535,349	Z. A. Weinberg	Non-Volatile Memory Cell Using a Crystalline Storage Element with Capacitively Coupled Sensing
4,535,371	J. D. Harr and K. R. Hense	Recording Channel with Signal Controlled Integrated Analog Circuits
4,535,388	C. J. Kraus, H. I. Stoller, and L. L. Wu	High Density Wired Module
4,535,425	S. K. Wiedmann	Highly Integrated, High-Speed Memory with Bipolar Transistors
4,535,428	A. Furman	Multi-Port Register Implementations
4,535,467	J. W. Davis, J. C. Leininger, C. Munoz-Bustamante, and G. J. Robbins	Switch Logic for Shift Register Latch Pair
4,535,531	H. S. Bhatia, J. A. Dorler, S. P. Gaur, J. S. Lechaton, J. M. Mosley, and G. R. Srinivasan	Method and Resulting Structure for Selective Multiple Base Width Transistor Structures
4,535,841	P. Kok	High Power Chip Cooling Device and Method of Manufacturing Same
4,536,451	J. C. S. Shen	Rigid Magnetic Recording Media Coating Composition
4,536,470	A. Amendola, R. G. Christensen, and J. G. Yereance, Jr.	Method and Apparatus for Making a Mask Conforming to a Ceramic Substrate Metallization Pattern
4,536,720	H. C. Cranford, Jr. and S. J. Garvin	Programmable Oscillator with Power Down Feature and Frequency Adjustment
4,536,769	D. J. Bahr, K.-H. Burckardt, G. Goldrian, V. Rudolph, and W. G. Spruth	Method of Recording Information on an Electrosensitive Record Carrier
4,536,944	A. M. Bracco, A. R. Edenfeld, and H. N. Kotecha	Method of Making ROM/PLA Semiconductor Device by Late Stage Personalization
4,538,032	D. Ballatore, Y. A. Bonnet, M. F. Ferry, and C. A. Jacquart	Interface Circuit with Impedance Adaptation Means
4,538,067	J. J. Cuomo, J. M. E. Harper, and G. A. Waters	Single Grid Focussed Ion Beam Source
4,538,165	C.-A. Chang, L. L. Chang, L. Esaki, and E. E. Mendez	FET with Heterojunction Induced Channel
4,538,240	J. L. Carter, G. Markowsky, and M. N. Wegman	Method and Apparatus for Performing Hashing Operations Using Galois Field Multiplication
4,538,252	A. Aviram	Light Induced Capacitance Changes in Donor Polymers (or Small Molecules) Films and Its Use for Information Storage in Video Disks and the Like
4,538,259	B. B. Moore	System for Digitized Voice and Data with Means to Compensate for Variable Path Delays
4,538,265	M. N. Day and C. D. Miller	Method and Apparatus for Instruction Parity Error Recovery
4,538,651	L. W. Lykins	Interlocked, Clean Loading Toner Cartridge
4,538,895	R. C. Higgins, M. J. Stanich, and L. L. Wolfe	Scanning Optical System for Use with a Semiconductor Laser Generator
4,538,905	D. B. Griego, J. M. Hopping, T. A. Rehage, and J. H. Rhodes, Jr.	Electronic Image Registration
4,539,058	A. C. Burgess, R. A. Magee, and G. E. Melvin	Forming Multilayer Ceramic Substrates from Large Area Green Sheets
4,539,089	G. K. Binnig, C. E. Gerber, H. Rohrer, and E. Weibel	Method for Depositing Material with Nanometer Dimensions
4,539,222	H. R. Anderson, Jr., C. J. Araps, and C. A. Lotsko	Process for Forming Metal Patterns Wherein Metal Is Deposited on a Thermally Depolymerizable Polymer and Selectively Removed
4,539,227	M. Briska and D. J. Bahr	Method of Making a Metallic Soap Layer as a Gliding Layer on a Metallized Record Carrier

4,539,506	H. Ohtani and K. Toyokawa	Red-Emitting Superlinear Phosphor
4,539,549	S. J. Hong and R. Nair	Method and Apparatus for Determining Minimum/Maximum of Multiple Data Words
4,539,576	D. A. Chance and M. Sampogna	Electrolytic Printing Head
4,539,653	G. M. Bartlett, J. P. Hofmeister, and E. J. Pring	Formatting Text/Graphics Using Plural Independent Formatting Mechanisms
4,539,677	Y.-C. Lo	Multiple Access Data Communication System
4,539,678	M. Ambroise, M. Demange, G. Lebizay, J.-M. Munier, and M. H. P. Peyronnenc	Synchronization System for a Closed-Loop Multiplex Communication Network
4,539,679	W. K. Bux, R. C. Dixon, and E. H. Rothauser	Synchronization in a Communication Network of Interconnected Rings
4,539,680	G. Boudon, P. Mollier, and G. Lebesherais	Chip to Chip Information Bit Transmission Process and Device
4,540,244	G. T. Sincerbox	Opto-Optical Light Deflector/Modulator
4,540,251	D. W. T. Yau and R. A. Modavis	Thermo-Mechanical Overlay Signature Tuning for Perkin-Elmer Mask Aligner
4,540,296	W. D. Thorne	Bar Band Intersectional Matrix Printer
4,540,464	H. Mueller and F. Schwerdt	Method of Renewing Defective Copper Conductors on the External Planes of Multilayer Circuit Boards
4,540,473	P. S. Bindra, A. P. David, R. T. Galasco, C. E. Gasdik, D. N. Light, and P. B. Pickar	Copper Plating Bath Having Increased Plating Rate, and Method
4,540,595	M. A. Acitelli, R. F. Tynan, and A. R. Wayson	Article Identification Material and Method and Apparatus for Using It
4,540,621	C. I. Eggerding and E. A. Giess	Dielectric Substrates Comprising Cordierite and Method of Forming the Same
4,541,027	K. D. Danielsen and D. J. Winarski	Method of Force/Acceleration Reel Tape Sizing
4,541,075	F. H. Dill, D. T. Ling, and R. E. Matick	Random Access Memory Having a Second Input/Output Port
4,541,168	J. R. Galie, G. R. Goth, T. A. Hansen, and R. T. Villetto, Jr.	Method for Making Metal Contact Studs Between First Level Metal and Regions of a Semiconductor Device Compatible with Polyimide-Filled Deep Trench Isolation Schemes
4,541,169	T. A. Bartush	Method for Making Studs for Interconnecting Metallization Layers at Different Levels in a Semiconductor Chip
4,541,623	J. L. Huerta	Alignment Restraint Station
4,541,720	G. Häusler, W. Järisch, and G. Makosch	Apparatus for Phase Symmetrizing Optical Wave Fronts
4,541,746	K. L. Bobart, R. S. Samson, and I. D. Shakib	Initializing Apparatus for Daisy Wheel Printer
4,541,890	J. J. Cuomo and H. R. Kaufman	Hall Ion Generator for Working Surfaces with a Low Energy High Intensity Ion Beam
4,542,071	A. M. Homola and H. Sussner	Lubricated Magnetic Recording Disk
4,542,309	W. Klein, E. Klink, K. Najmann, and F. Wernicke	Phase Splitter with Integrated Latch Circuit
4,542,310	W. F. Ellis, W. R. Griffin, and R. R. Troutman	CMOS Bootstrapped Pull Up Circuit
4,542,340	S. N. Chakravarti, P. L. Garbarino, and D. A. Miller	Testing Method and Structure for Leakage Current Characterization in the Manufacture of Dynamic RAM Cells
4,542,377	M. D. Hagen, P. J. Mendel, and J. L. Regehr	Rotatable Display Work Station
4,542,393	F. Hilpert, E. Kohm, V. Rudolph, and M. Wohnsdorf	Print Head for an Electroerosion Printer
4,542,453	M. J. Patrick and D. M. Snider	Program Patching in Microcomputer
4,542,481	R. C. Lange	One-Device Random Access Memory Cell Having Enhanced Capacitance
4,542,509	G. S. Buchanan and J. J. DeFazio	Fault Testing a Clock Distribution Network
4,542,577	T. N. Jackson	Submicron Conductor Manufacturing
4,542,579	M. R. Poponiak and R. O. Schwenker	Method for Forming Aluminum Oxide Dielectric Isolation in Integrated Circuits
4,542,950	J. B. Gillett and J. A. Miraglia	Zero Insertion Force Edge Connector with Wipe Cycle
4,542,960	K.-H. Yang	Fringe-Field Switched Storage-Effect Liquid Crystal Display Devices
4,543,032	T. W. Leverett and B. Musits	Robot Manipulator with Automatically Changeable Finger Tools
4,544,223	J. B. Gillett	Actuator for Zero Insertion Force Connectors
4,544,264	L. W. Bassetti and S. Kantor	Fine Line Print Enhancement
4,544,317	D. L. Carter	Vacuum-to-Vacuum Entry System Apparatus
4,544,439	J. M. Solomon and K. J. Varker	System for Repairing Electrical Short Circuits Between Parallel Printed Circuits
4,544,576	W.-K. Chu, W. A. Pliskin, and J. Riseman	Deep Dielectric Isolation by Fused Glass
4,544,846	G. O. Langner, H. C. Pfeiffer, and M. A. Sturans	Variable Axis Immersion Lens Electron Beam Projection System
4,544,889	F. Hendriks and R. H. Taylor	Robot Precision Probe Positioner with Guidance Optics
4,544,968	T. B. Anderson, T. W. Glaser, and J. J. Neesz	Sector Servo Seek Control
4,545,074	L. Balliet, R. A. Wantshouse, and T. J. Wylie	Fiber Optic Loop System with Bypass Mode

4,548,451	G. M. Benarr, T. A. Burns, and W. J. Walker	Pinless Connector Interposer and Method for Making the Same
4,548,452	J. B. Gillett	High-Density Electrical Contact Pad Pattern
4,548,463	R. T. Cato, L. D. Dickson, and O. L. Stokes, Jr.	Holographic Scanner Control Based on Monitored Diffraction Efficiency
4,549,118	T. G. Cole and R. M. West	Step Waveform Generator and CRT Vertical Timebase Incorporating Such a Generator
4,549,130	E. A. Dobberstein	Low Leakage Transformers for Efficient Line Isolation in VHF Switching Power Supplies
4,549,200	M. E. Ecker and L. T. Olson	Repairable Multi-Level Overlay System for Semiconductor Device
4,549,232	F. E. Axmear and D. W. Collins	Phase Modulated Servo System
4,549,238	F. Ertingshausen, V. Heinrich, H. Hinkel, G. Kaus, J. Kempf, H.-G. Nauth, and M. Schrader	Magnetic Head Slider of Ceramic Material
4,549,295	D. E. Purvis	System for Identifying Defective Media in Magnetic Tape Storage Systems
4,549,296	R. Castel, G. Dalbousiere, and M. Kulakowski	Device for Reporting Error Conditions Occurring in Adaptors, to the Data Processing Equipment Central Control Unit
4,550,489	B. A. Chappell, T. I. Chappell, and J. M. Woodall	Heterojunction Semiconductor
4,550,493	R. E. Darrow, G. J. Ingraham, J. D. Larnerd, and R. W. Nesky	Apparatus for Connecting Contact Pins to a Substrate
4,551,192	V. Di Milia, J. R. Maldonado, J. L. Speidell, and J. M. Warlaumont	Electrostatic or Vacuum Pinchuck Formed with Microcircuit Lithography
4,551,418	A. Hult, H. Ito, S. A. MacDonald, and C. G. Willson	Process for Preparing Negative Relief Images with Cationic Photopolymerization
4,551,671	E. J. Annunziata and T. E. Stammely	Terminal Disconnect and Media Wire Fault Detect Mechanism
4,551,704	C. J. Anderson	Look-Back Analog to Digital Converter
4,551,776	H. J. Roalson	Automatic Reference Adjustment for Position Error Signal on Disk File Servo System
4,551,789	H. Schettler and E. Stadler	Multilayer Ceramic Substrates with Several Metallization Planes
4,551,835	J. M. Bensadon and N. F. Brickman	X.21 Switching System
4,551,906	S. Ogura, J. Riseman, N. Rovedo, and J. F. Shepard	Method for Making Self-Aligned Lateral Bipolar Transistors
4,551,912	R. Marks, D. W. Phelps, Jr., S. J. Samuelsen, and W. C. Ward	Highly Integrated Universal Tape Bonding
4,552,351	K. Tsukamoto	Bill Storing Mechanism for Automatic Teller Machines
4,552,441	A. G. Dewey	Color Display from a Single Light Valve
4,552,615	A. Amendola, A. F. Schmeckenbecher, and J. T. Sobon	Process for Forming a High Density Metallurgy System on a Substrate and Structure Thereof
4,552,634	T. E. Franey	Radiation Hardened Lift-Off Correction Medium and Process of Manufacture
4,552,710	E. B. Rigby, R. W. Scheidecker, and R. R. Suchomel	Process of Hot Isostatic Pressing of Ferrite Material
4,552,716	A. B. Habich	Method for Manufacturing a Wire Matrix Print Wire Guiding Device
4,552,723	G. E. Adams, J. M. Casper, and R. J. Gritter	LC/FTIR System with Thermospray Solvent Separation
4,552,787	P. Chebiniak, R. A. Kaschak, and L. J. Root	Deposition of a Metal from an Electroless Plating Composition
4,552,831	C.-Y. Liu	Fabrication Method for Controlled Via Hole Process
4,552,833	H. Ito, S. A. MacDonald, R. D. Miller, and C. G. Willson	Radiation Sensitive and Oxygen Plasma Developable Resist
4,553,047	T. E. Dillinger and T. W. Kueper	Regulator for Substrate Voltage Generator
4,553,049	C. W. Cha, J. W. Hartman, D. A. Kiesling, and W. J. Scarpero, Jr.	Oscillation Prevention During Testing of Integrated Circuit Logic Chips
4,553,050	I. Feinberg, L. L. Wu, and L. Yuan	Transmission Line Terminator-Decoupling Capacitor Chip for Off-Chip Driver
4,553,128	D. Pilost	Analog-to-Digital Converter
4,553,192	R. Babuka, J. L. Piechota, and L. J. Poch	High Density Planar Interconnected Integrated Circuit Package
4,553,202	N. Trufyn	User Controlled Dialog Resource Switching in a Multi-Tasking Word Processor
4,553,248	T. R. Reed	Analog Adaptive Magnitude Equalizer
4,553,258	T. J. Chainer and T. K. Worthington	Segmentation Algorithm for Signature Verification
4,553,259	T. J. Chainer, S. C. Gundersen, and T. K. Worthington	Semi-Independent Shifting Technique for Signature Verification