

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,830,589	R. Obmann and R. R. Stroehr	Continuous Furnace for Processing Semiconductor Wafers
3,070,434,808	H. Kolbe and F. Schwarzbach	Mask for Imaging a Pattern on a Photoresist Layer, Method of Making Said Mask, and Use Thereof in a Photolithographic Process
3,070,523,808	L. Fischer, B. Gantz, and W. Hasler	Devices for Guiding a Ribbon Moved in a Longitudinal Direction

France

48,781	J. Calvignac and R. Castel	Communication Line Adapter for Use with a Communications Controller
54,598	C. Linger and G. Locicero	Process for Inspecting and Automatically Sorting Objects Showing Patterns with Constant Dimensional Tolerances and Apparatus
82,901	J. C. Abbiate	Data Transmission System

Japan

1,257,280	J. A. Matino	Gas Panel Fabrication Method
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United Kingdom

61,547	A. R. Gourlay	Method and Apparatus for Tomographical Imaging
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United States

4,458,985	L. Balliet, A. H. Bauman, Jr., and O. R. Gupta	Optical Fiber Connector
4,472,206	R. T. Hodgson, T. N. Jackson, H. S. Rupprecht, and J. M. Woodall	Method of Activating Implanted Impurities in Broad Area Compound Semiconductors by Short Time Contact Annealing
4,477,902	P. Puri and Y. K. Puri	Testing Method for Assuring AC Performance of High Performance Random Logic Designs Using Low Speed Tester
4,477,926	C. J. A. Linger and G. C. Locicero	Process for Inspecting and Automatically Sorting Objects Showing Patterns with Constant Dimensional Tolerances and Apparatus for Carrying Out Said Process
4,478,089	H. E. Aviles, J. A. Gregory, H. A. Panissidi, and H. E. Wattenbarger	Tri-Axial Force Transducer for a Manipulator Gripper
4,478,677	L. Chen, T. J. Chuang, and G. S. Mathad	Laser Induced Dry Etching of Vias in Glass with Non-Contact Masking
4,478,687	P. L. Gendler and R. J. Twieg	Phenazine Leucodyes for Electrochromic Recording
4,478,883	J. R. Bupp, V. Markovich, T. E. Napp, and C. J. Sambucetti	Conditioning of a Substrate for Electroless Direct Bond Plating in Holes and on Surfaces of a Substrate
4,478,947	R. A. Rita	Gas Panel Seal Glass
4,479,090	N. K. Frater, S. S. Hoo, and W. H. McConnell	Circuitry for Measuring Magnetic Head Flying Characteristics
4,479,140	J. L. Horvath	Thermal Conduction Element for Conducting Heat from Semiconductor Devices to a Cold Plate

4,479,158	F. B. Froehlich and P. Y. Hu	Stationary Magnetic Head with a Fluid Operated Tape Lifter
4,479,179	J. M. Dinwiddie, Jr.	Synchronous Cycle Steal Mechanism for Transferring Data Between a Processor Storage Unit and a Separate Data Handling Unit
4,479,213	C. Galand and H. Nussbaumer	Transmission Method for Transmitting Voice and Data Information over Telephone Line and Device for Implementing Said Method
4,479,214	P. M. Ryan	System for Updating Error Map of Fault Tolerant Memory
4,479,852	P. S. Bindra, A. P. David, R. T. Galasco, and D. N. Light	Method for Determination of Concentration of Organic Additive in Plating Bath
4,479,980	R. E. Acosta and E. J. Yarmchuk	Plating Rate Monitor
4,479,983	B. K. Appelt, G. P. Schmitt, and J. F. Shipley	Method and Composition for Applying Coatings on Printed Circuit Boards
4,480,218	V. D. Hair	Direct Detection of Back EMF in Permanent Magnet Step Motors
4,480,288	C. E. Gazdik and D. G. McBride	Multi-Layer Flexible Film Module
4,480,304	C. A. Carr, R. L. Huddleston, and J. P. Strickland	Method and Means for the Retention of Locks Across System, Subsystem, and Communication Failures in a Multiprocessing, Multiprogramming, Shared Data Environment
4,480,308	R. J. Grafe and G. E. Leikam	Text Processing System with Communication Data Save Facility
4,480,314	C. R. McKelley, Jr., L. A. Minshall, and P. J. Pirich, Jr.	Method for Optimizing Printer Response Time for a Key to Print Operation
4,480,375	P. E. Cottrell and H. J. Geipel, Jr.	Simple Process for Making Complementary Transistors
4,480,884	R. Babuka, A. Burke, and P. E. Haiges, Jr.	Zero Insertion Force Connector and Circuit Card Assembly
4,480,932	F. P. Willcox	Keyboard Having Rotationally Positionable Indicia for Indicating Type Font in Use
4,480,934	R. L. Herman	Ballistic Print Wire Actuator Using a Telescopic Armature
4,480,938	D. R. Polk and R. A. Southworth	Printer Escapement Servomotor Drive with a Minimum Power Supply
4,481,046	D. P. Bouldin, D. P. Hallock, S. Roberts, and J. G. Ryan	Method for Making Diffusions into a Substrate and Electrical Connections There to Using Silicon Containing Rare Earth Hexaboride Materials
4,481,071	N. C. Anderson, R. B. Chesnut, and L. E. Daby	Process of Lift Off of Material
4,481,255	T. E. Franey, R. B. Watkins, and A. W. Woolfolk	Radiation Hardened Transfer Medium
4,481,473	J. I. Compton	Electromagnet Drop Time Detection Method
4,481,566	C. R. Hoffman and G. B. Stephens	On Chip Charge Trap Compensated High Voltage Converter
4,481,575	D. Bazlen, J. Hajdu, and G. Knauf	Arrangement in a Data Processing System to Reduce the Cycle Time
4,481,602	K. A. Bohrer and A. D. Edgar	Font Compression and Decompression for Character Generation
4,481,603	R. A. McCaskill, J. W. McInroy, and P. D. Waldo	File Processing Method Using Expanding Display Windows for Selected Records and Text Fields
4,481,646	D. Godard	Method and Apparatus for Receiving a Data Signal Modulated by Means of a Double Sideband-Quadrature Carrier Modulation Technique
4,481,664	C. J. A. Linger and G. C. Locicero	Process for Inspecting Objects Showing Patterns with Dimensional Tolerances and Reject Criteria Varying with the Locations of Said Patterns and Apparatus and Circuits for Carrying Out Said Process
4,482,418	E. B. Rigby	Bonding Method for Producing Very Thin Bond Lines
4,482,427	H. Hiraoka	Process for Forming Via Holes Having Sloped Walls
4,482,815	G. Orenge	Floating Back-up Power Supply
4,482,819	B. J. Oza and T. J. Roche	Data Processor System Clock Checking System
4,482,887	G. L. Crauwels	Integrated Weighted Current Digital to Analog Converter
4,482,906	H. J. Hovel and R. Rosenberg	Gallium Aluminum Arsenide Integrated Circuit Structure Using Germanium
4,482,929	J. L. Beck, R. J. Bornhorst, D. J. Smith, and M. N. Zell	Magnetic Recording Disk Cartridge
4,482,956	P. H. Tallman	Parallel Queueing Method
4,482,999	P. A. Janson, H. R. Mueller, and D. T. W. Sze	Method of Transmitting Information Between Stations Attached to a Unidirectional Transmission Ring
4,483,001	P. M. Ryan	Online Realignment of Memory Faults
4,483,002	J. L. Groom, Jr., P. J. Smith, and G. G. Vair	Digital Device Testing Apparatus and Method
4,483,040	R. A. Magee and L. P. Remsen	In-Line Mask Cleaning System
4,483,389	D. Balderes, J. L. Horvath, and L. D. Lipschutz	Telescoping Thermal Conduction Element for Semiconductor Devices
4,483,726	R. D. Isaac, T. H. Ning, and P. M. Solomon	Double Self-Aligned Fabrication Process for Making a Bipolar Transistor Structure Having a Small Polysilicon-to-Extrinsic Base Contact Area
4,483,902	D. J. Bahr and M. Briska	Record Carrier
4,484,178	J. L. Lovgren, G. C. Thomas, and J. T. Trnka	Digital-to-Analog Converter
4,484,238	A. W. Vinal	Dual Track Magnetic Recording Method
4,484,241	A. P. Brende and J. L. Neubauer	Automatic Lock for Head-Carriage Assembly in a Disk File
4,484,267	R. P. Fletcher	Cache Sharing Control in a Multiprocessor

4,484,268	N. G. Thoma, V. S. Moore, and W. R. Kraft	Apparatus and Method for Decoding an Operation Code Using a Plurality of Multiplexed Programmable Logic Arrays
4,484,292	S. J. Hong, R. K. Nair, and E. Shapiro	High Speed Machine for the Physical Design of Very Large Scale Integrated Circuits
4,484,302	W. C. Cason, W. A. Kuecker, and P. R. Herrold	Single Screen Display System with Multiple Virtual Display Having Prioritized Service Programs and Dedicated Memory Stacks
4,484,304	R. W. Anderson, M. L. Gee, and A. K. McMullen	Transaction Execution System Having Keyboard and Message Customization, Improved Key Function Versatility and Message Segmentation
4,484,826	G. R. Horn and K. O. Shipp, Jr.	Automatic Intertext Column Spacing
4,485,066	V. M. Balthis, H. E. Evans, and J. C. Minor	Method of Making Elastomer-Coated Hot Roll
4,485,419	A. W. Vinal	Complementary Pole Coupling Magnetic Head Structure
4,485,436	P. Austruy and G. Dalbousiere	System for Selecting Interfaces on a Priority Basis
4,485,453	R. H. Taylor	Device and Method for Determining the Location and Orientation of a Drillhole
4,485,471	S. Singh and V. P. Singh	Method of Memory Reconfiguration for Fault Tolerant Memory
4,485,552	I. E. Magdo and H. S. Rupprecht	Complementary Transistor Structure and Method for Manufacture
4,485,758	H. V. Peugh and A. W. Ward	Coating Thickness and Wedge Geometry Control for Magnetic Disks
4,485,914	V. Frank	Variable Drive Pin Projection Mechanism for a Belt Sprocket Drive Wheel
4,486,653	K. E. Powell	Adaptive Rate Card Data Reproducing and Storing Apparatus
4,486,684	F.-J. Hohn	Single Crystal Lanthanum Hexaboride Electron Beam Emitter Having High Brightness
4,486,739	P. A. Franaszek and A. X. Widmer	Byte Oriented DC Balanced (0,4) 8B/10B Partitioned Block Transmission Code
4,486,784	D. G. Abraham and T. W. Gallman	Image Compression Systems
4,486,785	D. A. Lasher and W. K. Stelzenmuller	Enhancement of Video Images by Selective Introduction of Gray-Scale Pels
4,486,797	M. L. Workman	Sliding Mask Variable Resolution Velocity Trajectory for Track Following Servo
4,486,798	N. A. Feliss and T. W. Lile	Self-Cleaning Magnetic Head Air Bearing Slider and Method
4,486,859	C. R. Hoffman	Electrically Alterable Read-Only Storage Cell and Method of Operating Same
4,487,121	J. E. Carrington, J. S. Gregor, and G. R. Westcott	Fault Protection System for a Line Printer
4,488,067	W. R. Kraft, V. S. Moore, W. L. Stahl, Jr., and N. G. Thoma	Tristate Driver Circuit with Low Standby Power Consumption
4,488,142	P. A. Franaszek	Apparatus for Encoding Unconstrained Data onto a (1,7) Format with Rate 2/3
4,488,143	G. N. Martin	Fixed Rate Run Length Limited (RLL) Code String Generation Using Length Oriented Arithmetic Code Data String Expansion and Data String Recovery Using Arithmetic Code Compression of RLL Code Strings
4,488,162	C. G. Jambotkar	Self-Aligned Metal Field Effect Transistor Integrated Circuits Using Polycrystalline Silicon Gate Electrodes
4,488,174	J. L. Mitchell and W. B. Pennebaker	Method for Eliminating Motion Induced Flicker in a Video Image
4,488,188	N. H. Hansen and M. K. Haynes	Buried Servo Recording System Using Phase Encoded Servo Pattern
4,488,189	F. E. Axmear, D. W. Baxter, J. H. Saxman, and M. M. Siverling	Sector Servo Seek Control
4,488,192	R. C. Treseder	Cooling Arrangement for Hermetically Sealed Disk Files
4,488,193	R. A. Davis, S. K. Moss, and S.-Y. Wang	Closed Loop Cooling System for Disk File with Heat Exchanger Integral with Baseplate
4,488,219	C. A. Lemaire and D. A. Luick	Extended Control Word Decoding
4,488,259	B. R. Mercy	On Chip Monitor
4,488,262	D. Basire, A. Bhattacharyya, J. K. Howard, and P. Mollier	Electronically Programmable Read Only Memory
4,488,265	H. N. Kotecha	Integrated Dynamic RAM and ROS
4,488,298	G. L. Bond, F. P. Cartman, and P. M. Ryan	Multi-Bit Error Scattering Arrangement to Provide Fault Tolerant Semiconductor Static Memories
4,488,684	W. S. Ebert, H. D. Reynolds, W. J. Sjostrom, and R. R. Strollo	Precision Liquid Coating Apparatus for an Electrolytic Printer
4,488,782	D. J. Barclay and C. L. Bird	Electrolytic Display Device Employing Metal Ion Catalysis
4,488,801	D. K. Gibson	Maximum Throughput Duplexing System for Xerographic Machines
4,489,128	R. F. Rutz	Structure Containing Epitaxial Crystals on a Substrate
4,489,146	G. Bock, B. Hafner, R. Mühl, and K. P. Thiel	Reverse Process for Making Chromium Masks Using Silicon Dry Etch Mask
4,489,272	M. H. McLeod	Test Circuit for Turn-on and Turn-off Delay Measurements
4,489,309	E. S. Schlig	Pipelined Charge Coupled to Analog to Digital Converter
4,489,317	S. Shiga	Cathode Ray Tube Apparatus
4,489,364	D. A. Chance, A. Platt, C. W. Ho, and S. K. Ray	Chip Carrier with Embedded Engineering Change Lines with Severable Periodically Spaced Bridging Connectors on the Chip Supporting Surface

4,489,378	J. D. Dixon, G. A. Marazas, A. B. McNeill, and G. U. Merkel	Automatic Adjustment of the Quantity of Prefetch Data in a Disk Cache Operation
4,489,379	C. S. Lanier, H. M. Maxwell, R. E. McKay, and L. Weiss	Distributed Data Processing in Ring-Structured Networks Architected for Full Duplex Peer-to-Peer Operation of Processing Stations and Uninterruptible Transfer of Long Data Records Between Stations
4,489,381	R. W. Lavallee, P. M. Ryan, and V. F. Sollitto, Jr.	Hierarchical Memories Having Two Ports at Each Subordinate Memory Level
4,489,403	G. L. Bond	Fault Alignment Control System and Circuits
4,489,417	H. O. Askin, B. K. Ho, and G. Rabbat	Multi-Level Communication Circuitry for Communicating Digital Signals Between Integrated Circuits
4,489,650	W. Hasler, S. Padmanabhan, and K. Staudenmaier	Type Belt Printer with Antifriction Means
4,489,863	L. A. Horchos and H. W. Lorber	Precision Fluid Dispense Valve
4,490,032	D. J. Conly, B. B. Baker, D. D. Larson, and S. T. Riddle	Modulated Light Printing
4,490,055	C. F. Johnson and J. M. Williams	Automatically Adjustable Delay Function for Timed Typamatic
4,490,193	M. H. Ishaq, S. Roberts, and J. G. Ryan	Method for Making Diffusions into a Substrate and Electrical Connections Thereto Using Rare Earth Boride Materials
4,490,210	L. Chen, T. J. Chuang, and G. S. Mathad	Laser Induced Dry Chemical Etching of Metals
4,490,211	L. Chen, J. R. Lankard, and G. S. Mathad	Laser Induced Chemical Etching of Metals with Excimer Lasers
4,490,219	P. S. Bindra and D. N. Light	Method of Manufacture Employing Electrochemically Dispersed Platinum Catalysts Deposited on a Substrate
4,490,430	H. T. Findlay and L. M. Rood	Self-Supporting Thermal Ink
4,490,452	R. B. Champ and Y. T. Chen	Xerographic Photoconductors with Cross-Linked Epoxy Binder
4,490,630	L. B. Freeman	Current Switch Emitter Follower with Current Mirror Coupled Push-Pull Output Stage
4,490,652	V. D. Beck	Flat Cathode Ray Tube with Keystone Compensation
4,490,673	A. Blum and H. Schettler	Testing an Integrated Circuit Containing a Tristate Driver and a Control Signal Generating Network Therefor
4,490,756	M. H. Dost, C. C. Liu, and F. E. Mueller	Servo Encodement and Detection System Using Tetra-Orthogonal Servo Pattern
4,490,766	M. K. Hill and W. H. McConnell	Magnetic Recording Disk Cleaning Using Controlled Actuator Motion
4,490,782	J. D. Dixon, G. A. Marazas, and G. U. Merkel	I/O Storage Controller Cache System with Prefetch Determined by Requested Record's Position Within Data Block
4,490,796	S. E. Bigbie, B. R. Cavill, D. F. Dodgen, and A. A. Nawy	Print Head Motor Control System Using Analog and Digital Feedback
4,490,807	P. R. Chevillat, H. P. Käser, D. G. U. Maiwald, and G. Ungerböck	Arithmetic Device for Concurrently Summing Two Series of Products from Two Sets of Operands
4,490,901	G. J. Clark, R. E. Drake, and S. I. Raider	Adjustment of Josephson Junctions by Ion Implantation
4,491,222	C. M. Gaccetta and T. J. Lutz	Reel Package
4,491,431	A. Aviram and K. K. Shih	Metal-Insulator Resistive Ribbon for Thermal Transfer Printing
4,491,432	A. Aviram and K. K. Shih	Chemical Heat Amplification in Thermal Transfer Printing
4,491,628	H. Ito, C. G. Willson, and J. M. J. Frechet	Positive- and Negative-Working Resist Compositions with Acid Generating Photoinitiator and Polymer with Acid Labile Groups Pendant from Polymer Backbone
4,491,748	B. A. Chappell and S. E. Schuster	High Performance FET Driver Circuit
4,491,762	I. F. Chang	Flat Storage CRT and Projection Display
4,491,838	L. P. West	Starloop Communication Network and Control System Therefor
4,491,909	Y. Shimizu	Data Processing System Having Shared Memory
4,491,913	J. Calvignac, Y. Granger, and A. Masclet	Address Generating Device for a Communication Line Scanning Device
4,492,008	N. G. Anantha, T. H. Ning, and P. J. Tsang	Methods for Making High Performance Lateral Bipolar Transistors
4,492,717	W. A. Pliskin and J. Riseman	Method for Forming a Planarized Integrated Circuit
4,492,842	N. G. Koopman and V. C. Marcotte	Process of Brazing Using Low Temperature Braze Alloy of Gold-Indium Tin
4,492,968	F. C. Lee, R. N. Mills, T. K. Niweigha, and F. E. Talke	Dynamic Control of Nonlinear Ink Properties for Drop-on-Demand Ink Jet Operation
4,493,026	H. T. Olnowich	Set Associative Sector Cache
4,493,028	C. A. Heath	Dual Mode I/O
4,493,051	B. Brezzo, J. Calvignac, R. Dambricourt, A. Masclet, and J.-P. Sanche	Communication Line Scanning Device for a Communication Controller
4,493,056	R. S. Mao	RAM Utilizing Offset Contact Regions for Increased Storage Capacitance
4,493,108	G. B. Fryer and D. A. Stepneski	Video Image Field Cut Processing
4,493,745	L. Chen, H. A. Khoury, and H. R. Seymour	Optical Emission Spectroscopy End Point Detection in Plasma Etching
4,493,855	H. S. Sachdev and K. G. Sachdev	Use of Plasma Polymerized Organosilicon Films in Fabrication of Lift-Off Masks
4,493,856	A. H. Kumar and K. V. Srikrishnan	Selective Coating of Metallurgical Features of a Dielectric Substrate with Diverse Metals

4,494,004	J. L. Mauer IV, M. S. Michail, and O. C. Woodard	Electron Beam System
4,494,017	F. A. Montegari	Complementary Decode Circuit
4,494,018	R. Clemen and W. Haug	Bootstrapped Level Shift Interface Circuit with Fast Rise and Fall Times
4,494,066	P. Goel and M. T. McMahon	Method of Electrically Testing a Packaging Structure Having <i>N</i> Interconnected Integrated Circuit Chips
4,494,080	M. G. Call	Voltage-Controlled Oscillator with Independent Gain and Frequency Controls
4,494,108	G. G. Langdon, Jr., and J. J. Rissanen	Adaptive Source Modeling for Data File Compression Within Bounded Memory
4,494,150	N. F. Brickman and W. S. Rosenbaum	Word Autocorrelation Redundancy Match Facsimile Compression for Text Processing Systems
4,494,189	G. H. Bean and P. H. Gum	Method and Means for Switching System Control for CPUs
4,494,234	A. M. Patel	On-the-Fly Multibyte Error Correcting System
4,494,300	G. H. Schwuttke and K. H. Yang	Process for Forming Transistors Using Silicon Ribbons as Substrates
4,495,377	C. L. Johnson and J. M. Ryba	Substrate Wiring Patterns for Connecting to Integrated-Circuit Chips
4,495,479	K. Hermann	Selective Wiring for Multilayer Printed Circuit Board
4,495,490	B. R. Hopper, D. M. Howell, and R. A. Kolpek	Word Processor and Display
4,495,512	R. D. Isaac and T. H. Ning	Self-Aligned Bipolar Transistor and Inverted Polycide Base Contact
4,495,554	V. M. Simi and J. M. Sublette	Isolated Power Supply Feedback
4,495,559	C. C. Gelatt, Jr., and E. S. Kirkpatrick	Optimization of an Organization of Many Discrete Elements
4,495,564	W. D. Draper and M. T. Laakso	Multi Sub-Channel Adapter with Single Status/Address Register
4,495,590	R. C. Mitchell, Jr.	PLA with Time Division Multiplex Feature for Improved Density
4,495,594	L. C. Eggebrecht, D. A. Kummer, and J. A. Saenz	Synchronization of CRT Controller Chips
4,495,597	J. Barthel and H. R. Bublitz	Microprocessor-Controlled Adapter Circuit for Real-Time Controls, in Particular for Print Hammer Controls
4,495,626	A. Brunin and G. D'Hervilly	Method and Network for Improving Transmission of Data Signals Between Integrated Circuit Chips
4,496,245	F. L. Conrad, L. R. Palmer, and A. R. Roberts	Liquid Chromatography Proportioning Valve and Mixer
4,496,829	A. G. Black, J. H. Dodge, and L. M. Ernst	Bang-Bang Dual-Mode Integral Controller with Proportional Control Output Useful for Temperature Control
4,496,852	E. M. Blaser, P. W. Chung, and R. C. Varshney	Low Power Clock Generator
4,496,854	C.-C. J. Chi, A. Davidson, and C.-C. Tsuei	On-Chip SQUID Cascade
4,496,857	H. H. Chao	High Speed Low Power MOS Buffer Circuit for Converting TTL Logic Signal Levels to MOS Logic Signal Levels
4,496,900	T. H. DiStefano and A. Halperin	Nonlinearity Detection Using Fault-Generated Second Harmonic
4,496,954	J. W. Irwin	Reservo Interval Determination in an Ink Jet System
4,497,022	R. L. Cormier, R. J. Dugan, R. R. Guyette, R. L. Hankison, M. C. Hao, A. L. Levin, G. A. McClain, P. J. Wanish, and C. Zeitler, Jr.	Method and Apparatus for Measurements of Channel Operation
4,497,534	G. T. Sincerbox	Holographic Optical Head
4,497,692	M. H. Gelchinski, L. T. Romankiw, D. R. Vigliotti, and R. J. Von Gutfeld	Laser-Enhanced Jet-Plating and Jet-Etching: High-Speed Maskless Patterning Method
4,497,891	L. H. Kaplan and R. D. Kaplan	Dry-Developed, Negative Working Electron Resist System
4,498,039	R. K. Galwey and K. K. Kanazawa	Instrument for Use with an Electrochemical Cell
4,498,046	S. M. Faris, P. A. Moskowitz, A. Davidson, and G. A. Sai-Halas	Room Temperature Cryogenic Test Interface
4,498,095	P. L. Garbarino, S. R. Makarewicz, and J. F. Shepard	Semiconductor Structure with Improved Isolation Between Two Layers of Polycrystalline Silicon
4,498,136	W. W. Sproul III	Interrupt Processor
4,498,142	H. Advani and W. C. Cason	Method for Updating a Program on a Combined Program Diskette in an Interactive Text Processing System
4,498,145	J. W. Baker, R. A. Crus, and D. J. Haderle	Method for Assuring Atomicity of Multi-Row Update Operations in a Database System
4,498,147	P. W. Agnew, A. S. Kellerman, and G. W. Randall	Methodology for Transforming a First Editable Document Form Prepared with a Batch Text Processing System to a Second Editable Document Form Usable by an Interactive or Batch Text Processing System
4,498,148	D. Glickman	Comparing Input Words to a Word Dictionary for Correct Spelling
4,498,150	J. L. Gaudet and R. E. Chukran	Document Automatic Pagination
4,499,122	J. Economy and A. E. Zachariades	Process for Finishing Surfaces Achieving Improved Magnetic Disk Performance
4,499,460	K. A. Pearson and L. R. Zucker	ROS Control of Gas Panel
4,499,479	F. C.-S. Lee, R. N. Mills, and F. E. Talke	Gray Scale Printing with Ink Jet Drop-On-Demand Printing Head
4,499,499	N. F. Brickman and W. S. Rosenbaum	Method for Identification and Compression of Facsimile Symbols in Text Processing Systems

4,499,523	J. B. Gillett and J. A. Miraglia	Electronic Component Assembly with a Printed Circuit Board Unit and Cover
4,499,539	R. H. Vosacek	Method and Apparatus for Limiting Allocated Data-Storage Space in a Data-Storage Unit
4,499,552	K. K. Kanazawa	Electrochemical Cell Simulating Circuit Arrangement
4,499,596	R. G. Casey and T. D. Friedman	Adaptive Facsimile Compression Using a Dynamic Extendable Decision Network
4,499,608	E. C. Broockman, R. T. Cato, and R. C. Lynch	Terminal Device Having Closed Chamber Communications Between Functional Units
4,499,737	G. K. Binnig and C. E. Gerber	Method and Dilution Refrigerator for Cooling at Temperatures Below 1° K
4,500,198	R. A. Daniels	Multiple Roller Magnetic Brush Developer Having Development Electrode Voltage Switching
4,500,750	R. C. Elander, R. E. Lennon, S. M. Matyas, C. H. W. Meyer, R. E. Shuck, and W. L. Tuchman	Cryptographic Application for Interbank Verification
4,500,790	E. Bretscher and P. Vettiger	System for Improving the Uniformness of Patterns Generated by Electron Beam Lithography
4,500,800	M. Cases, W. R. Kraft, W. L. Stahl, Jr., and N. G. Thoma	Logic Performing Cell for Use in Array Structures
4,500,840	R. K. Galwey and K. K. Kanazawa	High Speed, Power Potentiostat/Galvanostat with IR Compensation for Use with an Electrochemical Cell
4,500,928	F. J. Bucek and F. V. Paxhia	Storage Apparatus for Video Data
4,500,945	L. D. Lipschutz	Directly Sealed Multi-Chip Module
4,500,952	A. R. Heller and W. S. Worley, Jr.	Mechanism for Control of Address Translation by a Program Using a Plurality of Translation Tables
4,500,954	A. H. Duke, M. H. Hartung, F. J. Marschner, and K. P. Nolan	Cache Bypass System with Post-Block Transfer Directory Examinations for Updating Cache and/or Maintaining Bypass
4,500,955	I. F. Chang	Full Word Coding for Information Processing
4,501,768	A. H. Kumar	Thin Film Floating Zone Metal Coating Technique
4,501,996	H. Yamaguchi	Deflection Distortion Correcting Circuit
4,502,063	M. Acciai, C. R. Pigos, and C. G. Speicher	Electrode Configuration for the Print Head of an Electrolytic Printer
4,502,100	S. B. Greenspan and O. R. Gupta	Cooling System with Counter Flow of Coolant
4,502,778	J. H. Dodge, T. F. Eichhorn, and P. A. Stevenson	System for Monitoring and Controlling Electrophotographic Toner Operation
4,502,797	F. W. Hilpert, E. Kohm, V. Rudolph, and M. Wohnsdorf	Arrangement for Pressing a Print Head Against a Record Carrier
4,502,804	F. P. Willcox	Paper Feeding and Guiding System
4,502,913	J. S. Lechaton, S. D. Malaviya, D. J. Schepis, and G. R. Srinivasan	Total Dielectric Isolation for Integrated Circuits
4,502,914	H.-J. Trumpp and J. Greschner	Method of Making Structures with Dimensions in the Sub-Micrometer Range
4,502,927	D. J. Barclay, W. M. Morgan, and J. M. Vigar	Electrodeposition of Chromium and Its Alloys
4,503,386	S. DasGupta, M. C. Graf, R. A. Rasmussen, and T. W. Williams	Chip Partitioning Aid (CPA)—A Structure for Test Pattern Generation for Large Logic Networks
4,503,497	M. A. Krygowski, B. U. Messina, and W. D. Silkman	System for Independent Cache-to-Cache Transfer
4,503,514	R. J. Urquhart	Compact High Speed Hashed Array for Dictionary Storage and Lookup
4,503,515	V. Cuan, R. A. McCaskill, J. T. Repass, and K. O. Shipp, Jr.	Footnote Assembly Management
4,503,516	P. W. Agnew, J. J. Erhard, and A. S. Kellerman	Methodology for Transforming a First Editable Document Form Prepared by an Interactive Text Processing System to a Second Editable Document Form Usable by an Interactive or Batch Text Processing System
4,503,521	J. D. Schick and H. R. Wilson	Non-Volatile Memory and Switching Device
4,503,523	J. R. Cavaliere and P. T. Liu	Dynamic Reference Potential Generating Circuit Arrangement
4,503,537	W. H. McAnney	Parallel Path Self-Testing System
4,504,007	H. R. Anderson, Jr., C. J. Araps, and R. A. Bates	Solder and Braze Fluxes and Processes for Using the Same
4,504,322	A. S. Adwalpalker, A. H. Kumar, and R. L. Weisman	Re-Work Method for Removing Extraneous Metal from Ceramic Substrates
4,504,330	A. K. Gaiind, S. B. Kulkarni, and M. R. Poponiak	Optimum Reduced Pressure Epitaxial Growth Process to Prevent Autodoping
4,504,331	T. F. Kuech and B. S. Meyerson	Silicon Dopant Source in Intermetallic Semiconductor Growth Operations
4,504,340	R. R. Tummala and R. N. Master	Material and Process Set for Fabrication of Molecular Matrix Print Head
4,504,552	K. K. Kim	Integrated Resistor of Niobium Oxide Passivating Ring, Gold Corrosion Barrier, and Titanium Resistive Layer
4,504,558	H. Bohlen, H. Engelke, J. Greschner, and P. Nehmiz	Method of Compensating the Proximity Effect in Electron Beam Projection Systems
4,504,723	F. S. Gobran, F. E. Gros-pin, D. G. Pittwood, and M. F. Shlatz	Renewable Brazing Electrodes

4,504,727	R. L. Melcher and S. H. Wen	Laser Drilling System Utilizing Photoacoustic Feedback
4,504,784	P. Goel and M. T. McMahon	Method of Electrically Testing a Packaging Structure Having <i>N</i> Interconnected Integrated Circuit Chips
4,504,840	J. F. Evans, H. T. Findlay, S. A. Popyach, and D. C. Tao	Thermal Printing with Ink Replenishment
4,504,846	T. I. Chappell and J. M. Woodall	Multiwavelength Optical-to-Electrical Logic Operations
4,504,864	D. Anastassiou and W. B. Pennebaker	Nonlinear Filtering of Gray Scale Images
4,504,873	P. B. Bandy, J. A. Danen, N. N. Heise, and D. C. Holtan	Identification Field Scan Apparatus
4,504,880	M. A. Church and S. K. Decker	Integrated Magnetic Recording Head Assembly Including an Inductive Write Subassembly and a Magnetoresistive Read Subassembly
4,504,901	J. L. Calvignac and R. R. Castel	Communication Line Adapter for Use with a Communications Controller
4,504,904	V. S. Moore and J. C. Leininger	Binary Logic Structure Employing Programmable Logic Arrays and Useful in Microword Generation Apparatus
4,504,924	P. W. Cook, H.-H. Hsieh, and G. S. Miranker	Carry Lookahead Logical Mechanism Using Affirmatively Referenced Transfer Gates
4,504,948	A. M. Patel	Syndrome Processing Unit for Multibyte Error Correcting Systems
4,506,158	R. H. Cadwallader, H. D. Edmonds, and M. V. Kulkarni	Dual Mode Spectrometer Test Station
4,506,215	C. P. Coughlin	Modular Test Probe
4,506,305	S. A. Cardero	Method for Detecting a Fault in a Data Recording System Read Channel
4,506,307	R. B. Watrous	Magnetic Head Slider and Actuator Assembly
4,506,326	P. S. Shaw, J. J. Sordi, and I. L. Traiger	Apparatus and Method for Synthesizing a Query for Accessing a Relational Data Base
4,506,341	H. L. Kalter and F. W. Wiedman	Interlaced Programmable Logic Array Having Shared Elements
4,506,343	K. O. Shipp, Jr., R. B. Wood, and J. T. Repass	Column Layout Reference Area Display Management
4,506,351	R. E. Scheuerlein and S. Ogura	One-Device Random Access Memory Having Enhanced Sense Signal
4,506,364	F. J. Aichelmann, Jr. and L. K. Lange	Memory Address Permutation Apparatus
4,508,204	D. MacWilliams and L. J. Rigbey	Gravity Feed Chute
4,508,579	G. R. Goth and S. D. Malaviya	Lateral Device Structures Using Self-Aligned Fabrication Techniques
4,508,612	K. J. Blackwell, R. T. White, Jr., and J. W. Wilson	Shield for Improved Magnetron Sputter Deposition into Surface Recesses
4,508,749	J. H. Brannon and J. R. Lankard, Sr.	Patterning of Polyimide Films with Ultraviolet Light
4,508,757	K. A. Fabricius and B. M. Kemlage	Method of Manufacturing a Minimum Bird's Beak Recessed Oxide Isolation Structure
4,508,981	J. A. Dorler, J. M. Mosley, and S. D. Weitzel	Driver Circuitry for Reducing On-Chip Delta-I Noise
4,509,002	R. L. Hollis, Jr.	Precision X-Y Positioner
4,509,007	R. H. Barsotti and A. W. Grove	Differential Sensor Measuring Apparatus and Method Including Sensor Compensator Circuitry
4,509,008	S. DasGupta, M. C. Graf, R. A. Rasmussen, and T. W. Williams	Method of Concurrently Testing Each of a Plurality of Interconnected Integrated Circuit Chips
4,509,086	C. R. Hickethier, R. E. Hogan, and M. E. Prah	Method and Apparatus for Loading and Registering a Diskette in a Diskette Drive
4,509,113	C. A. Heath	Peripheral Interface Adapter Circuit for Use in I/O Controller Card Having Multiple Modes of Operation
4,509,114	J. C. Leininger, V. S. Moore, and W. L. Stahl, Jr.	Microword Control Mechanism Utilizing a Programmable Logic Array and a Sequence Counter
4,509,119	R. A. Gumaer and J. A. Mortenson	Method for Managing a Buffer Pool Referenced by Batch and Interactive Processes
4,509,122	P. W. Agnew, J. S. Coviello, L. S. Green, and A. S. Kellerman	Method for Controlling the File Transfer Capability of an Interactive Text Processing System That Is Emulating a Host Processing System Terminal
4,509,172	C.-L. Chen	Double Error Correction-Triple Error Detection Code
4,509,530	H. W. Curtis, F. A. Dodge, D. B. Francis, and J. V. Mizzi	System for Plotting a Miniature ECG
4,509,991	Y. Taur	Single Mask Process for Fabricating CMOS Structure
4,510,000	A. H. Kumar and B. Schwartz	Method for Palladium Activating Molybdenum Metallized Features on a Ceramic Substrate
4,510,172	A. K. Ray	Technique for Thin Insulator Growth
4,510,493	W. K. Bux, H. J. Keller, and H. R. Mueller	Method and Arrangement for Local Address Acquisition by a Station in a Communication System
4,510,567	P. Y. Chang and J. W. McInroy	Qualifying and Sorting File Record Data
4,510,582	F. T. Blount	Binary Number Substitution Mechanism
4,510,615	G. D. Rohrer	Magnetic Character Reader with Double Document Detection
4,512,263	F. E. Lanning	Depository Apparatus with Sequential Stacking
4,512,841	G. F. Cunningham, Jr., J. W. Lewis, R. B. McClure, and D. J. Poindexter	RF Coupling Techniques
4,512,847	A. Brunsch, W.-D. Ruh, and G. Trippel	Method of Measuring the Thickness of the Removed Layer in Subtractive Workpiece Processing

4,512,862	E. B. Rigby	Method of Making a Thin Film Insulator
4,512,900	G. J. Macur, W. R. Pratt, and J. E. Sharkness	Method for Treating Waste Compositions
4,513,203	H. Bohlen, H. Engelke, J. Greschner, R. Mühl, P. Nehmiz, and H. J. Trumpp	Mask and System for Mutually Aligning Objects in Ray Exposure Systems
4,513,254	J. D. Harr	Integrated Circuit Filter with Adjustable Characteristics
4,513,283	J. C. Leininger	Latch Circuits with Differential Cascode Current Switch Logic
4,513,299	F. C. Lee, R. N. Mills, R. N. Payne, and F. E. Talke	Spot Size Modulation Using Multiple Pulse Resonance Drop Ejection
4,513,303	S. A. Abbas and I. E. Magdo	Self-Aligned Metal Field Effect Transistor Integrated Circuit
4,513,351	M. I. Davis, M. J. Garrett, and J. A. Wiseman	Electronic Assembly with Forced Convection Cooling
4,513,367	S. K. Chan, J. A. Gerardi, and B. L. McGilvray	Cache Locking Controls in a Multiprocessor
4,513,391	R. F. Maddock	Text Processing Apparatus with Two-Stage Formatting of Text
4,513,393	O. A. Edlund, M. A. Ensér, J. W. Strage, S. B. Thunberg, and E. I. Wallmark	Document Handling Terminal Computer
4,513,418	P. H. Bardell, Jr. and W. H. McAnney	Simultaneous Self-Testing System
4,513,431	S. G. Chamberlain and E. S. Schlig	Charge Coupled Device Output Circuit Structure
4,513,437	T. J. Chainer, R. A. Scranton, and T. K. Worthington	Data Input Pen for Signature Verification
4,513,613	Y. Darves-Bornoz, G. E. Melvin, M. G. Ryan, and D. L. Saylor	Particle Detection System
4,514,232	J. G. Ameen, R. H. Hogrogian, and S. L. Smey	Process for Stripping Silicon Oil Base Thermal Grease
4,514,254	S. P. Klepner	Groundplane Post-Etch Anodization
4,514,667	W. H. Sakmann and V. Zimmermann	Method and Apparatus for the Constant Speed Control of Brushless DC Motors
4,514,674	R. L. Hollis, Jr. and B. L. Musits	Electromagnetic X-Y-Theta Precision Positioner
4,514,751	S. Bhattacharya	Compressively Stresses Titanium Metallurgy for Contacting Passivated Semiconductor Devices
4,514,752	P. A. Engel, D. H. Strobe, and T. E. Wray	Displacement Compensating Module
4,514,803	P. W. Agnew, J. P. Buonomo, S. R. Houghtalen, A. S. Kellerman, R. E. Losinger, and J. W. Valashinas	Methods for Partitioning Mainframe Instruction Sets to Implement Microprocessor Based Emulation Thereof
4,514,823	R. N. Mendelson, D. M. O'Neal, and L. A. Sharp, Jr.	Apparatus and Method for Extending a Parallel Channel to a Serial I/O Device
4,514,829	H. H. Chao	Word Line Decoder and Driver Circuits for High Density Semiconductor Memory