

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,461,410	H. Helke, J. Hajdu, and D. Wollschlaeger	Control Program for Executing Quasi-subroutines
3,069,706,408	B. Gantz and H. D. Matthaei	Support for a Type Belt of a Line Printer
3,069,837,408	D. J. Bahr and G. Goldrian	Method and Arrangement for Controlling the Print Current in Metal Paper Printers
3,167,808,408	M. Briska	Process for Producing a Sliding Layer on the Surface of an Aluminum-Coated Record Carrier
3,167,990,008	M. Briska	Process for Reducing Traces of Abrasion or Scratches on the Surface of a Record Carrier
3,168,266,908	M. Briska	Process for Preparing a Pigmented Lacquer
3,168,282,008	M. Briska	Process for Reducing Traces of Abrasion on Metal Paper

France

46,831	R. A. Donnan	Method and System for Retransmitting Incorrectly Received Numbered Frames in a Data Transmission System
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Sweden

67,267	J. W. Strage, E. I. Wallmark, A. O. Edlund, M. A. Enser, and S. B. Thunberg	Terminal Programming Without Branches
77,069,110	M. Enser and N. G. Stalberg	Method and Device for Reducing the Number of Rejected Documents

Switzerland

128,971	G. K. Binnig, C. E. Gerber, H. Oh, H. Rohrer, and E. Weibel	Vibration Filter
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United States

4,458,162	P. M. Solomon and S. K. Wiedmann	TTL Logic Gate
4,458,253	W. Goff, Jr. and T. C. Ky	Thermal Print Apparatus Using a Thermal Transfer Ribbon Such as a Multi-Colored One, and a Printing Method
4,458,274	P. M. Charlson, R. H. Estry, and C. R. Schwieters	Manufacturing Method and Apparatus for Adjusting the Position of a Magnetic Head in a Flexible Disk Drive
4,458,280	E. A. Cunningham	Servo Writing Transducer Design and Writing Method
4,458,311	S. F. Clements, P. D. Motola, D. M. Ross, R. O. Simpson, S. F. Swift, and M. D. Day	Text Processor Having an Interactive Display Terminal Which Alternately Functions as a Data Processing Terminal
4,458,312	T. A. Stranko and R. L. Swann	Rapid Instruction Redirection
4,458,316	S. M. Fry, H. O. Hempy, C. R. Kirkpatrick, and B. E. Kittinger	Queuing Commands in a Peripheral Data Storage System
4,458,331	R. Amezcua, S. F. Clements, R. O. Dachowski, and P. D. Motola	Interactive Display Terminal with Alternating Data Processing and Text Processing Sessions with Text Processing Status Line Operable During Data Processing Session
4,458,333	B. A. Smith	Dot Matrix Character Printing Using Constrained Memory
4,458,335	W. J. Kabelac and I. L. Sanders	Detector Structure for Ion-implanted Contiguous-Disk Bubble Devices
4,458,345	G. C. Bjorklund and D. M. Burland	Process for Optical Information Storage

4,458,349	F. J. Aichelmann, Jr. and J. K. Lange	Method for Storing Data Words in Fault Tolerant Memory to Recover Uncorrectable Errors
4,464,070	R. F. Hanft and G. G. Pechanek	Multi-Character Display Controller for Text Recorder
4,464,212	H. S. Bhatia and J. Riseman	Method for Making High Sheet Resistivity Resistors
4,464,458	M.-F. Chow, E. C. Fredericks, and W. M. Moreau	Process for Forming Resist Masks Utilizing O-quinone Diazide and Pyrene
4,464,460	H. Hiraoka, D. C. Hofer, R. D. Miller, L. A. Pederson, and C. G. Willson	Process for Making an Imaged Oxygen-Reactive Ion Etch Barrier
4,464,655	D. L. Bird	Testcase Generator with Marker Symbols Displayed with Primary Data
4,464,701	S. Roberts and J. G. Ryan	Process for Making High Dielectric Constant Nitride Based Materials and Devices Using the Same
4,464,712	R. P. Fletcher	Second Level Cache Replacement Method and Apparatus
4,464,713	M. T. Benhase and A. H. Duke	Method and Apparatus for Converting Addresses of a Backing Store Having Addressable Data Storage Devices for Accessing a Cache Attached to the Backing Store
4,464,718	J. D. Dixon, R. H. Farrell, G. U. Merckel, J. D. Neely, S. A. Schmitt, W. G. Verdoorn, Jr., and P. B. Bandy	Associative File Processing Method and Apparatus
4,464,730	S. G. Lawrence and B. H. Middleton	Text Processing Apparatus with Editing of Stored Document at Each Keystroke
4,464,753	C. L. Chen	Two Bit Symbol Sec/Ded Code
4,464,782	J.-P. Béraud and C. Galand	Transmission Process and Device for Implementing the So-improved Process
4,464,783	J.-P. Beraud, D. J. Esteban, and C. Galand	Speech Coding Method and Device for Implementing the Improved Method
4,465,223	A. S. Cammarano and G. DiGiacomo	Process for Brazing
4,465,646	H. E. Evans and J. C. Minor	Method of Making Elastomer-Coated Hot Roll
4,466,059	A. L. Bastian, M. E. Goldfeder, and M. H. Hartung	Method and Apparatus for Limiting Data Occupancy in a Cache
4,466,065	H. Advani and J. A. Hooten	Queuing Capability in a Foreground Task
4,466,077	R. A. Iannucci and J. R. Kleinsteinber	Method and Apparatus for Division Employing Associative Memory
4,466,096	D. E. Heins and R. C. McGaffin	Apparatus and Method for Providing Transmitter Hierarchy Control for Efficient Channel Management
4,466,099	D. Meltzer	Information System Using Error Syndrome for Special Control
4,466,177	H. H. Chao	Storage Capacitor Optimization for One Device FET Dynamic RAM Cell
4,466,218	J. C. Ottman and J. C. S. Shen	Fixed Abrasive Polishing Media
4,466,302	R. H. Harris	Nutating Snap Action Switch Mechanism
4,466,520	R. L. Herman	Self-Contained Clutch Useful for Single Revolution Applications
4,466,731	J. R. Champion and R. W. Pries	Electrophotographic Machine with High Density Toner Concentration Control
4,466,813	M. D. Avritt and M. M. Farrow	Plasticizer Removal Process and System
4,467,000	J. Economy, W. Volksen, and D. Y. Yoon	Process for Coating a Substrate with Polyimide
4,467,196	L. Balliet and D. H. Coffman	Single Fiber Optic Wand System
4,467,317	G. G. Langdon, Jr. and J. J. Rissanen	High-Speed Arithmetic Compression Coding Using Concurrent Value Updating
4,467,363	J. G. Tench, Jr.	Graphic Data Compression
4,467,411	S. M. Fry, H. O. Hempy, C. R. Kirkpatrick, and B. E. Kittinger	Scheduling Device Operations in a Buffered Peripheral Subsystem
4,467,439	K. E. Rhodes	OR Product Term Function in the Search Array of a PLA
4,467,445	M. W. Mueller, T. S. Parker, D. M. Benignus, and J. L. Frye	Communication Adapter Circuit
4,466,753	F. P. Willcox	Carriage Guiding System and Frame for a Printer
4,467,448	J. L. Regehr and L. A. Sendelbach	Image Rotate Control Circuitry
4,467,518	J. P. Bansal, C. L. Bertin, and R. R. Troutman	Process for Fabrication of Stacked, Complementary MOS Field Effect Transistor Circuits
4,467,519	R. Glang, S.-M. Ku, and A. Schmitt	Process for Fabricating Polycrystalline Silicon Film Resistors
4,467,976	F. W. Bogaczyk, R. C. Croll, B. D. Purcell, and E. E. Toutant	Ribbon Cartridge Comprising a Stuffer Box Intermediate a Supply Reel and Take-Up Reel
4,468,219	D. R. George, M. J. Steinmetz, and R. A. Weber	Pump Flow Rate Compensation System
4,468,565	W. W. Blair, S. K. Doran, and G. O. Langner	Automatic Focus and Deflection Correction in E-Beam System Using Optical Target Height Measurements
4,468,586	F.-J. Hohn	Shaped Electron Emission from Single Crystal Lanthanum Hexaboride with Intensity Distribution
4,468,694	A. D. Edgar	Apparatus and Method for Remote Displaying and Sensing of Information Using Shadow Parallax
4,468,732	N. Raver	Automated Logical File Design System with Reduced Data Base Redundancy
4,468,734	C. S. Lanier, H. M. Maxwell, R. E. McKay, and L. Weiss	Method of Purging Erroneous Signals from Closed Ring Data Communication Networks Capable of Repeatedly Circulating Such Signals
4,468,735	V. Götze and G. Potz	Highly Integrated Programmable Logic Array Utilizing AND and OR Arrays

4,468,750	M. E. Chamoff and D. W. Piller	Clustered Terminals with Writable Microcode Memories & Removable Media for Applications Code & Transactions Data
4,469,026	J. W. Irwin	Method and Apparatus for Controlling Drying and Detaching of Printed Material
4,469,460	J. J. Hughes and D. R. Polk	Matrix Printer with Optimum Printing Velocity
4,470,029	B. D. Chase	Convergence Unit for In-Line Color Cathode Ray Tube
4,470,056	L. V. Galetto and J. M. Rakes	Controlling a Multi-Wire Printhead
4,470,128	J. H. Higdon	Control Arrangement for Magnetic Bubble Memories
4,470,129	J. R. Disbrow, E. T. Eiselen, G. I. Findley, S. G. Luning, and D. T. Wang	Page Modification Method in a Printer Subsystem of the Partial Page Buffer Composing Type
4,470,189	S. Roberts and F. R. White	Process for Making Polycide Structures
4,470,191	P. E. Cottrell, H. J. Geipel, Jr., and D. M. Kenney	Process for Making Complementary Transistors by Sequential Implantations Using Oxidation Barrier Masking Layer
4,470,714	A. Aviram and K. K. Shih	Metal-Semiconductor Resistive Ribbon for Thermal Transfer Printing and Method for Using
4,470,874	T. A. Bartush, G. A. Brooks, and J. R. Kitcher	Planarization of Multi-Level Interconnected Metallization System
4,471,206	K. Nagai	Method of Sealing Vacuum Bottle Evacuation Chambers
4,471,224	J. J. Cuomo and H. R. Kaufman	Apparatus and Method for Generating High Current Negative Ions
4,471,283	D. R. Presley	Average Current Regulation for Stepper Motors
4,471,395	J. L. Beck, D. O. Castrodale, R. W. Luoma, and J. M. Rigotti	Self-Ventilated Recirculating Airflow System
4,471,405	J. K. Howard and K. V. Srikrishnan	Thin Film Capacitor with a Dual Bottom Electrode Structure
4,471,454	Z. T. Dearden, Y. K. Puri, and W. W. Sproul III	Fast, Efficient, Small Adder
4,471,457	Edwin R. Videki II	Supervisory Control of Peripheral Subsystems
4,471,471	D. J. DiMaria	Non-Volatile RAM Device
4,471,522	C. G. Jambotkar	Self-Aligned Metal Process for Field Effect Transistor Integrated Circuits Using Polycrystalline Silicon Gate Electrodes
4,471,523	G. J. Hu	Self-Aligned Field Implant for Oxide-Isolated CMOS FET
4,472,250	D. J. Barclay, W. M. Morgan, and J. M. Vigar	Bath and Process for the Electrodeposition of Chromium
4,472,790	J. L. Burk, J. R. Butwell, C. E. Clark, J. T. Rodell, and D. E. Stucki	Storage Fetch Protect Override Controls
4,473,598	L. M. Ephrath, V. J. Silvestri, and D. D. Tang	Method of Filling Trenches with Silicon and Structures
4,473,602	V. Puligandla and D. K. Verma	Palladium Activation of 2.5% Silicon Iron Prior to Electroless Nickel Plating
4,473,795	R. L. Wood	System for Resist Defect Measurement
4,474,065	M. H. Miessler	Ultrasonic Probe
4,474,320	W. J. Rueger	Air Bearing for Tape Drives
4,474,397	J. K. Hassan and J. A. Paivanas	Pick-up Head Utilizing Aspirated Air Flow
4,474,465	A. Amendola, R. G. Christensen, and J. G. Yereance, Jr.	Method and Apparatus for Making a Mask Conforming to a Ceramic Substrate Metallization Pattern
4,474,731	J. M. Brownlow and T. S. Plaskett	Process for the Removal of Carbon Residues During Sintering of Ceramics
4,474,864	M.-F. Chow, A. D. Lopata, C. F. Lyons, R. C. McIntosh, and A. F. Scaduto	Method for Dose Calculation of Photolithography Projection Printers Through Bleaching of Photo-Active Compound in a Photoresist
4,475,034	R. A. Maddox and J. W. Rudolph	Modular Shaft Encoder
4,475,037	P. Vettiger and A. D. Wilson	Method of Inspecting a Mask Using an Electron Beam Vector Scan System
4,475,051	H. D. Chai and J. P. Pawletko	Low Inertia High Torque Variable Reluctance Motor
4,475,060	M. O. Aboelfotoh	Stabilized Plasma Display Device
4,475,113	F. C. Lee, R. N. Mills, and F. E. Talke	Drop-on-demand Method and Apparatus Using Converging Nozzles and High Viscosity Fluids
4,475,135	M. W. Warner and M. F. Garnier	Magnetic Head Air Bearing Slider
4,475,194	R. W. LaVallee, P. M. Ryan, and V. F. Sollitto, Jr.	Dynamic Replacement of Defective Memory Words
4,475,281	J. J. Holecek	Transducer Head Positioning Adapter for Fixed Head Flexible Disk Drives
4,475,478	E. J. Berdinner, Jr. and J. P. Throckmorton	Coating Apparatus
4,475,829	W. Goff, Jr. and E. R. Williams, Jr.	Capacitive Metering Means for Uniform Ribbon Feed and Take-up Mechanism
4,475,830	J. O'N. Schaefer	Spliceless Ribbon Structure Having Leader and Trailer and Method of Manufacture Therefor
4,475,831	J. E. Drejza	Position Tracking Emitter for a Printer with Emitter Pattern on Lead Screw
4,475,982	F. J. Lai and R. N. Schulz	Deep Trench Etching Process Using $\text{CCl}_2\text{F}_2/\text{Ar}$ and $\text{CCl}_2\text{F}_2/\text{O}_2$ RIE
4,476,431	A. Blum	Shift Register Latch Circuit Means Contained in LSI Circuitry Conforming to Level Sensitive Scan Design (LSSD) Rules and Techniques and Utilized at Least in Part for Check and Test Purposes
4,476,454	J. A. Aboaf, E. Klokholm, and T. R. McGuire	New Magnetoresistive Materials