

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,965,610,808 K. Hartmann
3,063,756,008 F. C. Wernicke

3,063,949,708 R. Zuehlke
3,063,986,208 H. Stadler
3,064,212,708 F. C. Wernicke

France

11,700 J. L. Peter
17,716 D. Godard

United States

4,378,627 C. G. Jambotkar

4,378,630 C. T. Horng, R. R. Konian,
R. O. Schwenker, and A. W. Weider
4,379,005 H. J. Hovel and J. M. Woodall
4,379,022 R. L. Melcher, L. T. Romankiw, and
R. J. Von Gutfeld
4,379,218 K. R. Grebe and J. M. E. Harper
4,379,727 H. H. Hansen, J. B. Lasky, and
R. R. Silverman
4,379,740 A. I. Nazzari and
U. T. Mueller-Westerhoff
4,379,826 J. Economy, R. J. Gritter, and
H. Hiraoka
4,379,832 H. M. Dalal and J. J. Lowney

4,379,833 B. J. Canavella and M. Hatzakis
4,380,005 P. Debord and J. -L. Marijon
4,380,057 H. N. Kotecha, W. P. Noble, Jr., and
F. W. Wiedman III
4,380,063 P. A. Janson, H. R. Müller, and
E. H. Rothauer
4,380,067 B. C. Beardsley and A. C. Brailey
4,380,076 D. S. Bethune
4,380,811 V. Götze and D. Schütt
4,380,813 L. L. Fogell, S. R. Levine, and
A. Weinberger
4,381,322 D. J. Bahr and M. Briska

4,381,453 J. J. Cuomo and J. M. E. Harper
4,381,502 E. S. Prame
4,381,524 R. J. E. Shay and W. L. Smith
4,381,532 R. W. Arnold
4,381,540 D. O. Lewis and J. W. Reed
4,381,543 R. J. Bunten III and J. E. Hickman

4,381,563 J. L. Groom, Jr., J. D. Perine,
J. W. Snyder, and G. G. Vair
4,383,600 M. E. Richard and H. O. Rinkleib
4,383,777 D. H. Eskew, R. M. Marowski, and
D. R. Sevy
4,383,859 M. T. Moore, J. M. Olson, and
D. A. Stremel

Electrode Guide
Method of and Circuit Arrangement for Reading and/or Writing an
Integrated Semiconductor Storage with Storage Cells in MTL Technology
Multi-layer Module with Constant Characteristic Impedance
Cutting Mechanism for a Gradual Transport Hammer
Integrated Semiconductor Memory and Method of Operating Same

Power Supply System for Monolithic Cells
Method and Device for Training an Adaptive Equalizer by Means of an
Unknown Data Signal in a Transmission System

Self-Aligned Metal Process for Field Effect Transistor Integrated Circuits
Using Polycrystalline Silicon Gate Electrodes
Process for Fabricating a High Performance PNP and NPN Structure

Semiconductor Device Fabrication
Method for Maskless Chemical Machining

Fluxless Ion Beam Soldering Process
Method of Laser Annealing of Subsurface Ion Implanted Regions

Photoassisted Generation of Hydrogen from Water

Positive Electron Beam Resists of Ortho Chloro Substituted Phenol or Cresol
Condensed with Formaldehyde
Method for Making Low Barrier Schottky Devices of the Electron Beam
Evaporation of Reactive Metals
Self-Aligned Photoresist Process
Dynamic Zero Offset Compensating Circuit for A/D Converter
Electrically Alterable Double Dense Memory

Flow Control Mechanism for Block Switching Nodes

Error Control in a Hierarchical System
Apparatus for Four Side Transverse Irradiation of a Region
Programmable Logic Array with Self Correction of Faults
Error Checking of Mutually Exclusive Control Signals

Process for Producing a Sliding Layer on the Surface of an Aluminum-Coated
Record Carrier
System and Method for Deflecting and Focusing a Broad Ion Beam
Method and Apparatus for Character Generation
Self-Clocking Write Head
Constant Energy Drive Circuit for Electromagnetic Print Hammers
Asynchronous Channel Error Mechanism
Controller Port Switch Arrangement for Sharing Stored Data Among
Different Systems
Apparatus and Method for Visually Presenting Analytical Representations of
Digital Signals
Single Reel Tape Cartridge with Leader Block Door Seal
Torque-Limiting Carriage Return Knock-Off Mechanism

Ink Jet Inks and Method of Making

4,384,216	W. D. Pricer	Controlled Power Performance Driver Circuit
4,384,329	W. S. Rosenbaum and A. R. Tannenbaum	Retrieval of Related Linked Linguistic Expressions Including Synonyms and Antonyms
4,384,761	M. J. Brady, L. V. Gregor, and M. Johnson	Ferrofluid Optical Switches
4,384,797	C. W. Anderson, H. T. Findlay, and N. C. Watkins	Single Laminated Element for Thermal Printing and Lift-Off Correction, Control Therefor, and Process
4,384,798	J. S. Alexander	Manually Positionable Correction Media Holder and Dispensing Apparatus
4,384,938	B. H. Desilets, T. A. Gunther, and W. C. Heybruck	Reactive Ion Etching Chamber
4,385,262	C. S. Cambier, G. W. Jorgenson, and L. P. Trubell	Method and Apparatus for Variable Current Control of a Negative Resistance Device such as a Fluorescent Lamp
4,385,349	T. J. Ashford, R. Krishnamurty, and J. A. Voltin	Central Processor Supervised Controller System Having a Simulation of the Controller in the Central Processor for Test Purposes
4,385,975	S.-F. Chu, A. P. Ho, C. T. Horng, and B. M. Krenlage	Method of Forming Wide, Deep Dielectric Filled Isolation Trenches in the Surface of a Silicon Semiconductor Substrate
4,386,116	K. K. Nair and K. A. Snyder	Process for Making Multilayer Integrated Circuit Substrate
4,386,234	W. F. Ehrsam, R. C. Elander, S. M. Matyas, C. H. W. Meyer, R. L. Powers, P. N. Prentice, J. L. Smith, and W. L. Tuchman	Cryptographic Communication and File Security Using Terminals
4,386,266	D. J. Chesarek	Method for Operating a Transaction Execution System Having Improved Verification of Personal Identification
4,386,333	G. O. Dillan	Universal Electrical Connection Apparatus
4,386,346	J. L. Levine	Cursor Controller
4,386,359	J. L. Regehr and P. C. Schloss	Line Printer Attachment
4,386,400	B. Cope, K. R. Lynch, D. H. O'Donnell, J. T. Rodell, W. W. Turechek, and R. M. Untenberger	Reset of a Selected I/O Channel and Associated Peripheral Equipment by Means Independent of the Channel
4,386,403	H. Y. Hsieh and N. B. G. Rabbat	System and Method for LSI Circuit Analysis
4,386,417	T. W. Collins and M. G. Hurley	High Performance Bubble Chip Architecture
4,386,418	K. Ju	Conductorless Swap Gate for Ion-Implanted Contiguous-Disk Bubble Devices
4,386,730	A. P. Mulzet	Centrifuge Assembly
4,386,840	A. N. Garthwaite and L. A. Suarez, Jr.	Dual Flash Fuser Reflector with Alternating Flash for Power Reduction
4,386,846	W. D. Clark, E. G. Edwards, and C. A. Queener	Method and Apparatus for Limiting Book Wear While Being Copied
4,386,849	G. Häeusler and W. Jäerisch	Method of Moiré-Metrical Testing of Optical Imaging Systems
4,386,968	H. Hinkel, J. Kempf, G. Kraus, and G. E. Schmid	Method of Making Semiconductor Device Structures by Means of Ion Implantation under a Partial Pressure of Oxygen
4,387,124	D. J. Pipkin	Coating Apparatus and Method
4,387,131	L. C. Anderson, R. W. Nufer, and F. G. Pugliese	Ceramic Dielectrics
4,387,433	P. A. Cardenia, T. V. Landon, and A. W. Muir	High Speed Data Interface Buffer for Digitally Controlled Electron Beam Exposure System
4,387,442	J. E. Stuehler	Controlled Machine Inhibition when Control Module is Absent
4,387,445	B. A. Denis and D. B. Eardley	Random Access Memory Cell
4,387,639	C. R. Brown, T. J. Kohne, and D. C. Lancto	Multi-Function Financial Document Processor
4,387,848	R. M. Kellogg and A. P. Mulzet	Centrifuge Assembly
4,388,010	W. E. Mott and I. D. Shakib	Font Module for Matrix Printer
4,388,386	B. D. King and J. P. Levin	Mask Set Mismatch
4,388,526	E. N. Schroeder	Measuring Apparatus with Response Adjustment
4,388,619	V. D. Beck	Corrector for Bundle Deflection Distortion in Multibeam Cathode Ray Tubes
4,388,688	T. O. Curlee III and E. L. Richardson	Shared TOD Clock Modification Bit
4,388,701	F. J. Aichelmann, Jr. and F. Neves	Recirculating Loop Memory Array Having a Shift Register Buffer for Parallel Fetching and Storing
4,388,704	C. L. Bertin, H. N. Kotecha, and F. W. Wiedman	Non-Volatile RAM Cell with Enhanced Conduction Insulators
4,388,861	A. Bohg and K. Hartmann	Bank for Accommodating Several Print Hammer Units
4,389,008	R. E. Boyanowski, F. A. Gregory, and S. E. Nemier	Web Supply Roll Jogger
4,389,257	H. J. Geipel, Jr., N. Hsieh, C. W. Koburger III, and L. A. Nesbit	Fabrication Method for High Conductivity, Void-Free Polysilicon-Silicide Integrated Circuit Electrodes
4,389,281	N. G. Anantha, H. S. Bhatia, J. S. Lechaton, and J. L. Walsh	Method of Planarizing Silicon Dioxide in Semiconductor Devices
4,389,294	N. G. Anantha, H. S. Bhatia, J. L. Mauer IV, and H. G. Sarkary	Method for Avoiding Residue on a Vertical Walled Mesa
4,389,600	C. A. Milligan and D. J. Winarski	Tape Media Interlayer Tension Check

4,389,614	D. E. Staiger	Method and Apparatus for Generating Pulses of a Predetermined Time Relation Within Predetermined Pulse Intervals with a High Time Resolution
4,389,768	A. B. Fowler, R. Rosenberg, and H. S. Rupprecht	Self-Aligned Process for Fabricating Gallium Arsenide Metal-Semiconductor Field Effect Transistors
4,389,770	T. E. Bocinski and A. D. Knight	Tri-Lead Cable Housing Removal Tool
4,390,297	J. N. Gaitos, M. D. Shattuck, and C. V. Wilbur	Method and Apparatus for Correction of Non-Impact Printing
4,390,499	H. W. Curtis, R. M. Kellogg, R. W. Kissinger, R. P. Mappes, and E. J. Stephans	Chemical Analysis System Including a Test Package and Rotor Combination
4,390,571	J. F. Cosgrove, G. P. Dahlke, and C. Wurms	Boatless Point Source Evaporation Method
4,390,610	L. D. Bowden, A. H. Miyashita, and C. I. Ravenelle	Layered Electrophotographic Imaging Element, Apparatus and Method Sensitive to Gallium Arsenide Laser, the Element Including Two Charge Generation Layers and a Polycarbonate Adhesive Layer
4,390,868	R. L. Garwin	Security of Manufactured Apparatus
4,390,884	S. L. Applegate and E. R. Lloyd	Printhead Stylus Assembly
4,390,890	D. L. Bergeron and P. T. Patel	Saturation-Limited Bipolar Transistor Device
4,390,911	K. B. Klaassen	Signal Separation in Magnetic Recording Using Buried Servo
4,390,916	W. W. Chow and L. P. Dunn	Single Track Magnetic Head Assembly