

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,309,616	S. K. Wiedmann	Hybrid Storage Circuit
2,460,849	H. G. Seifert	Access Arm with Magnetic Head for Magnetic Disk Storage with Enhanced Disk Stabilizing Features
2,612,666	S. K. Wiedmann	Highly Integrated, Inverting Logic Circuit
2,715,889	D. J. Bahr, K. H. Burckardt, and H. Hasselmeier	Control Circuit for Metal Paper Printer Head
2,758,141	H. E. Korth and F. Schedewie	Spectrophotometer
2,964,606,808	R. Clemen	TTL-Compatible Address Latch with Field Effect Transistors
2,964,619,308	A. Blum	Method of and Arrangement for an Electronic Data Processing System for Checking the Control Signals Derived from an Instruction

France

7,732,158	C. J. Gergaud, A. Tracol, and E. Grimanelli	Decentralized Data Transmission System
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United Kingdom

1,005,614	M. H. Sharpe	Document Processing Apparatus
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United States

4,356,730	P. E. Cade	Electrostatically Deformographic Switches
4,357,006	L. P. Hayes	Distortion Free Three Point Vacuum Fixture
4,357,093	A. H. Caudill and W. H. Bruce, Jr.	Photoconductor Tensioning Device
4,357,178	D. L. Bergeron, D. J. Fleming, and G. B. Stephens	Schottky Barrier Diode with Controlled Characteristics and Fabrication Method
4,357,202	R. F. Rutz	Refractory Oxide Fabrication
4,357,388	J. C. Minor	Hot Roll Fuser
4,357,540	C. E. Benjamin and D. J. Crawford	Semiconductor Device Array Mask Inspection Method and Apparatus
4,357,622	I. E. Magdo and H. S. Rupprecht	Complementary Transistor Structure
4,357,678	G. T. Davis	Programmable Sequential Logic Array Mechanism
4,357,680	J. C. Greek, Jr. and C. N. Sprott	Selective Formatting of Blocks of Text Codes in a Memory of a Word Processing System
4,357,700	J. A. Alvarez III, B. D. Gobioff, and L. P. West	Adaptive Error Encoding in Multiple Access Systems
4,357,899	D. P. Jones and D. J. Pipkin	Coating Apparatus
4,358,201	G. Makosch	Interferometric Measurement Apparatus and Method Having Increased Measuring Range
4,358,209	J. M. Rigotti	Print Ribbon Driving Mechanism
4,358,291	J. J. Cuomo and J. M. Woodall	Solid State Renewable Energy Supply
4,358,326	V. Y. Doo	Epitaxially Extended Polycrystalline Structures Utilizing a Predeposit of Amorphous Silicon with Subsequent Annealing
4,358,479	M. J. Canestaro and E. O. Fey	Treatment of Copper and Use Thereof
4,358,627	J. G. Ameen, C. A. Joseph, D. L. Rivenburgh, and D. W. Sissenstein	Reducing Chloride Concentration
4,358,745	R. W. Keyes	Semiconductor Surface Acoustic Wave Device
4,358,826	R. E. Bodner, T. L. Crooks, and A. H. Wottreng	Apparatus for Enabling Byte or Word Addressing of Storage Organized on a Word Basis
4,358,831	J. D. Tompkins	Self-Biasing Circuit for Analog Shift Registers, with Fat Zero Compensation
4,358,848	A. M. Patel	Dual Function ECC System With Block Check Byte
4,358,890	L. G. Heller, H. J. Jones, H. N. Kotecha, and D. A. Soderman	Process for Making a Dual Implanted Drain Extension for Bucket Brigade Device Tetrode Structure
4,359,286	J. G. Barnes and P. D. Waldo	Character Set Expansion

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4,359,288	R. L. Bullock, B. D. Purcell, W. J. Thornhill, and E. E. Toutant	Single Pass Ribbon Cartridge for Impact Printers Having Means to Prevent Incorrect Insertion
4,359,484	D. J. Bahr and M. Briska	Process for Reducing Traces of Abrasion or Scratches on the Surface of a Record Carrier
4,359,520	J. A. Carothers, J. Economy, and A. C. Ouano	Enhancement of Resist Development
4,359,772	A. M. Patel	Dual Function Error Correcting System
4,359,816	S. A. Abbas and I. E. Magdo	Self-Aligned Metal Process for Field Effect Transistor Integrated Circuits
4,359,937	E. F. Helinski	Dot Matrix Printer
4,360,142	C. Carpenter, J. F. Fugardi, L. V. Gregor, P. S. Grosewald, and M. D. Reeber	Method of Forming a Solder Interconnection Capable of Sustained High Power Levels Between a Semiconductor Device and a Supporting Substrate
4,360,289	A. S. Cammarano and G. DiGiacomo	Pin for Brazing to a Substrate and Improved Package Resulting Therefrom
4,360,583	E. M. Engler, F. B. Kaufman, S. R. Kramer, and B. A. Scott	High Resolution Video Storage Disk
4,360,768	G. W. Corner and G. W. Jorgenson	High Current Acceleration Servomotor Driver
4,360,870	J. M. McVey	Programmable I/O Device Identification
4,360,898	S. M. Faris	Programmable Logic Array System Incorporating Josephson Devices