

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

France

7,525,820

F. Gergaud

Keyboard

7,619,832

P. De Bord and P. L. Beaudouin

Noise Reduction Scheme in Photosensitive Self Scan Array

Germany

2,010,987

D. Rogalla and E. Max

Methods for Making Magnetic Thin Film Heads with Magnetic Anisotropy

United Kingdom

1,517,869

J. F. Minshull and I. D. Judd

Improvements Relating to Image Encoding Apparatus

1,517,870

R. H. Lovie, J. F. Minshull,
I. D. Judd, and J. M. Evans

Improvements Relating to Apparatus for Producing Vectors

1,517,940

D. J. Barclay and M. H. Hallett

Method of Operating an Electrochromic Display

1,518,249

D. H. Martin and D. J. Barclay

Electrochromic Displays

1,520,350

R. D. Commander, J. R. Taylor, and
P. A. Gardner

Data Storage Apparatus

1,521,435

R. W. Mitchell and J. H. Purdie

Keyboard Button Manufacture

1,522,049

W. M. Morgan

Display Electrode

United States

4,104,418

K. C. Park and E. J. Weitzman

Glass Layer Fabrication

4,104,563

E. S. Schlig and G. R. Stilwell, Jr.

Writing and Erasing in AC Plasma Displays

4,104,675

D. J. DiMaria and D. R. Young

Moderate Field Hole and Electron Injection from One Interface of MIM or MIS Structures

4,104,721

P. W. Markstein and A. L. Tritter

Hierarchical Security Mechanism for Dynamically Assigning Security Levels to Object Programs

4,105,327

D. K. Gibson, M. J. Miller, and
S. A. Snell

Multiple Reduction Indicators for Continuously Variable Reduction Copier Machines

4,105,924

H. Hinkel

Apparatus for Making a Rastered Photoconductive Layer

4,105,980

R. L. Cowardin and G. J. Laurer

Glitch Filter Circuit

4,106,050

J. Riseman

Integrated Circuit Structure with Fully Enclosed Air Isolation

4,106,102

A. E. Desblache

Self-Adaptive Digital Filter for Noise and Phase Jitter Reduction

4,106,667

J. J. Lynott

Apparatus and Method for Conducting Financial Transactions

4,106,873

J. E. Drejza and J. V. Riley

Disk Printer Inking Mechanism

4,106,975

M. Berkenblit, S. A. Chan,
A. Reisman, and S. Zirinsky

Process for Etching Holes

4,106,976

C. Chiou, G. Galli, K. H. Loeffler,
and M. R. Lorenz

Ink Jet Nozzle Method of Manufacture

4,107,063

Z. Kovac and C. J. Sambucetti

Water Based Selectable Charge Magnetic Inks

4,107,550

C. A. Jacquart, H. N. Leighton, and
R. J. Wilfinger

Bucket Brigade Circuits

4,107,554

Y. L. Yao

Data Bus Arrangement for Josephson Tunneling Device Logic Interconnections

4,107,698

L. V. Galetto and D. F. Jensen

Ink Jet Printer Apparatus and Method of Operation

4,107,722

S. G. Chamberlain

Photodiode Structure Having an Enhanced Blue Color Response

4,108,360

W. Beismann, H. H. Lampe, and
W. Pohle

Method of Error Analysis and Diagnosis in Electronic Data Processing Systems

4,109,030

M. Briska and E. Eisenbraun

Method for Thermally Oxidizing Silicon

4,109,372

R. M. Geffken

Method for Making an Insulated Gate Field Effect Transistor Utilizing a Silicon Gate and Silicide Interconnection Vias

4,109,377

L. A. Blazick and L. F. Miller

Method for Preparing a Multilayer Ceramic

4,110,032	J. H. Hubbard, W. L. Hubert, and P. R. Spivey	Copy Production Machines Having Supply Sheet Pick Retry
4,110,068	L. C. Brown, E. G. Edwards, C. E. Peterson, P. G. Robinson, and C. C. Wilson	Hot Roller Fuser Having Manually Operable Jam Clearance Mechanism
4,110,125	K. D. Beyer	Method for Fabricating Semiconductor Devices
4,110,569	H. R. Schindler and P. Vettiger	Method and Apparatus for Monitoring the On/Off-Hook Status of a Telephone Set
4,110,594	G. H. May	Pattern Mask Generating Method and Apparatus
4,110,625	J. A. Cairns, A. Lurio, and J. F. Ziegler	Method and Apparatus for Monitoring the Dose of Ion Implanted into a Target by Counting Emitted X-Rays
4,110,633	E. M. Blaser and D. A. Conrad	Depletion/Enhancement Mode FET Logic Circuit
4,110,681	D. C. Hofer, V. N. Kahwaty, and C. R. Valentino	NMR Field Frequency Lock System
4,110,705	W. G. Crouse	Noise Reduction Method and Apparatus for Companded Delta Modulators
4,110,804	D. O. Castrodale, J. L. Lovgren, R. A. Russell, and K. A. Shidler	Read/Write and Tunnel Erase Magnetic Head Assembly
4,110,830	M. A. Krygowski	Channel Storage Adapter
4,110,831	G. G. Langdon, Jr.	Method and Means for Tracking Digit Significance in Arithmetic Operations Executed on Decimal Computers
4,110,832	J. C. Leininger and G. P. Taylor	Carry Save Adder
4,110,835	J. F. Dubil, H. N. Leighton, and R. J. Wilfinger	Bucket Brigade Circuit for Signal Scaling
4,110,837	T. C. Chen	Apparatus for the Sorting of Records Overlapped with Loading and Unloading of Records into a Storage Apparatus
4,111,719	S. R. Mader, B. J. Masters, and H. B. Pogge	Minimization of Misfit Dislocations in Silicon by Double Implantation of Arsenic and Germanium
4,111,720	A. E. Michel, R. O. Schwenker, and J. F. Ziegler	Method for Forming a Non-Epitaxial Bipolar Integrated Circuit
4,111,857	E. M. Engler, K. H. Nichols, V. V. Patel, N. M. Rivera, and R. R. Schumaker	Highly Conducting Organometallic Polymers
4,112,314	V. L. Gani and F. A. Montegari	Logical Current Switch
4,112,472	F. E. Hauke, E. R. McCully, R. M. McElroy, Jr., P. A. Stevenson, S. M. Vogel, and J. W. Woods	Longitudinal Scan, Cam Shaped, Turntable Rotor for Magnetic Recording
4,116,554	E. L. Libby and M. J. Miller	Optical Reduction System Featuring the Provision of Borders
4,116,562	E. L. Libby and M. J. Miller	Optical System Featuring Change in Magnification by Combined Lens and Mirror Motion
4,116,641	T. F. Ciszek	Apparatus for Pulling Crystal Ribbons from a Truncated Wedge Shaped Die
4,116,714	J. S. Basi	Post-Polishing Semiconductor Surface Cleaning Process
4,116,721	T. H. Ning, C. M. Osburn, and H. N. Yu	Gate Charge Neutralization for Insulated Gate Field-Effect Transistors
4,116,754	F. A. Huntley and W. M. Morgan	Method of Forming Matt Display Electrode
4,117,093	F. C. Brunner, F. W. Schwerdt, and H.-H. Steinbeck	Method of Making an Amorphous Silicon Dioxide Free of Metal Ions
4,117,354	T. R. Geewala	Two-Part Current Injection Interferometer Amplifiers and Logic Circuits
4,117,471	E. S. Schlig and G. R. Stilwell, Jr.	Light Pen Detection and Tracking with AC Plasma Display Panel
4,117,497	J. C. McGroddy and J. A. Van Vechten	Printing and Displaying Technology Using Selective Laser Beam Pricking of Liquid Film for Writing Information
4,117,503	H. H. Zappe	Josephson Interferometer Structure which Suppresses Resonances
4,117,536	R. E. Bodner	Instruction Processing Control Apparatus
4,117,546	N. G. Anantha, M. Cases, F. Y. Chang, and B. J. Rubin	Interlaced CCD Memory
4,117,737	P. J. Mulholland and C. A. Thomas	Drive Belt Loading System
4,118,250	C. T. Hornig, A. E. Michel, H. S. Rupprecht, and R. O. Schwenker	Process for Producing Integrated Circuit Devices by Ion Implantation
4,118,611	R. H. Harris	Buckling Spring Torsional Snap Actuator
4,118,630	C. M. McKenna and W. F. Mueller	Ion Implantation Apparatus with a Cooled Structure Controlling the Surface Potential of a Target Surface
4,118,784	H. J. Nussbaumer	Differential DFT Digital Filtering Device
4,118,786	S. R. Levine, S. Singh, and A. Weinberger	Integrated Binary-BCD Look-Ahead Adder
4,118,793	B. E. Argyle, P. Dekker, and J. C. Slonczewski	Gradientless Propulsion of Magnetic Bubble Domains Using Modulated In-Plane Fields
4,119,688	H. Hiraoka	Electro-Lithography Method
4,119,947	H. N. Leighton, M. M. Siverling, and R. J. Wilfinger	Optical Signal Processor

4,120,035	M. Cases, F. Y. Chang, and B. J. Rubin	Electrically Reprogrammable Transversal Filter Using Charge Coupled Devices
4,120,505	A. R. Cox, M. R. Hatchett, and L. J. Rigbey	Stabilizing Backing Plate for a Flexible Disk Store
4,120,578	R. A. Daniels, D. K. Gibson, P. Kummli, S. A. Snell, and M. H. Ulrich	Continuously Variable Reduction Scanning Optics Drive
4,120,629	J. H. Christian and J. L. Overacker	Production Control Apparatus
4,120,843	J. C. Ameen, G. V. Elmore, and A. E. Peter	Strippable Solder Mask Material Comprising Polysulfone, Silicon Dioxide Filler, and Solvent
4,120,995	P. B. Phipps, B. S. Sharma, and A. W. Wu	Process for Bonding a Durable Low Surface Energy Coating
4,121,089	R. E. Bishop	Apparatus for the Reversal of a Hot Roll in a Fusing Assembly
4,121,882	J. M. White	Flat Scan Holographic Laser Beam Deflector
4,122,361	R. Clemen, W. Haug, and R. Schnadt	Delay Circuit with Field Effect Transistors
4,122,407	J. A. Van Vechten	Heterostructure Junction Light Emitting or Responding or Modulating Devices
4,122,440	G. G. Langdon, Jr. and J. J. Rissanen	Method and Means for Arithmetic String Coding
4,122,460	J. N. Humenik, J. L. Powell, and R. R. Tummala	Ink Jet Nozzle Structures
4,122,476	H. J. Hovel and J. M. Woodall	Semiconductor Heterostructure
4,122,494	J. L. Levine	Synchronization for Oscillating Optical Beam Deflecting Device
4,122,496	L. S. Childress, T. E. Gagnon, D. R. Thompson, and L. R. Zucker	Apparatus and Method for Digital Halftone Reproduction
4,122,536	C.-K. Wong and P. C. Yue	Self-Organizing Magnetic Bubble Lattice File
4,122,548	K. Heuber, W. Klein, K. Najmann, F. Wernicke, and S. K. Wiedmann	Memory Storage Array with Restore Circuit
4,123,135	M. E. Rabedeau	Optical System for Rotating Mirror Line Scanning Apparatus
4,123,155	W. L. Hubert	Copy Production Machine Having a Duplex Copy Mode
4,123,270	W. P. Heil and D. B. Sclove	Method of Making Electrophotographic Imaging Element
4,123,300	M. L. Joshi, P. F. Landler, and R. Silverman	Integrated Circuit Process Utilizing Lift-Off Techniques
4,123,571	M. Balog, M. Berkenblit, S.-A. Chan, and A. Reisman	Method for Forming Smooth Self Limiting and Pin Hole Free SiC Films on Si
4,123,669	W. T. Devine and W. F. Washburn	Logical OR Circuit for Programmed Logic Arrays
4,123,747	D. C. Lanceto and R. E. Shuck	Identity Verification Method and Apparatus
4,124,293	R. A. Daniels and P. Kummli	Cam-Operated Scanning Optics Drive for a Continuously Variable Magnification System
4,124,870	B. R. Schatz and K. Y. Wong	Method for Improving Print Quality of Coarse-Scan/Fine-Print Character Reproduction
4,124,871	T. H. Morrin II	Image Data Resolution Change Apparatus and Process Utilizing Boundary Compression Coding of Objects
4,125,440	G. Markovits	Method for Non-Destructive Testing of Semiconductor Articles
4,125,650	G. T. Chiu and E. C. Fredericks	Resist Image Hardening Process
4,125,765	R. L. Cowardin and G. J. Laurer	Label Find Method and Circuit
4,125,873	D. J. Chesarek	Display Compressed Image Refresh System
4,125,881	J. J. Eige, A. M. Patel, S. D. Roberts, and D. Stedman	Tape Motion Control for Reel-to-Reel Drive
4,126,305	D. F. Colglazier, J. L. Fallon, E. P. Kollar, and F. R. Mares	Combing Wheel
4,126,380	D. S. Borm	Probe with Contact Indicating Means
4,126,456	R. V. Pole, M. D. Shattuck, G. T. Sincerbox, and H. W. Werlich	Visual Image Recording Device
4,126,714	R. S. Haines	Process for Treating Chromium Dioxide
4,126,897	R. S. Capowski, M. A. Krygowski, and T. K. Zimmerman	Request Forwarding System
4,127,438	T. C. Babcock, T. L. Ellis, and H. C. Majka	Adhesion Promoter for Additively Plated Printed Circuit Boards
4,127,861	A. F. G. Deneuville	Metal Base Transistor with Thin Film Amorphous Semiconductors
4,127,899	W. R. Dachtera	Self-Quenching Memory Cell
4,128,439	C. G. Jambotkar	Method for Forming Self-Aligned Field Effect Device by Ion Implantation and Outdiffusion
4,128,670	F. H. Gaensslen	Fabrication Method for Integrated Circuits with Polysilicon Lines Having Low Sheet Resistance
4,128,829	N. M. Herbst and C. N. Liu	Signature Verification Method and Apparatus Utilizing Both Acceleration and Pressure Characteristics
4,128,830	G. A. Weythman	Apparatus for Providing a Compensation Signal for Individual Light Sensors Arranged in a Predetermined Relation

4,128,876	R. N. Ames, D. K. Hardin, J. C. Leininger, and G. P. Taylor	Synchronous Microcode Generated Interface for System of Microcoded Data Processors
4,128,891	Y. S. Lin, C. N. Liu, and D. T. Tang	Magnetic Bubble Domain Relational Data Base System
4,128,895	G. S. Almasi, G. E. Keefe, and Y. S. Lin	Magnetic Wall Assisted Bubble Domain Nucleator
4,129,457	J. S. Basi	Post-Polishing Cleaning of Semiconductor Surfaces
4,129,790	V. L. Gani and F. A. Montegari	High Density Integrated Logic Circuit
4,129,793	J. Bula and L. C. Martin	High Speed True/Complement Driver
4,129,853	W. E. Althausen and R. A. Barnes	Method and Apparatus for Producing Compensated Signals for Individual Light Sensors Arranged in a Predetermined Relation
4,129,880	A. W. Vinal	Channel Depletion Boundary Modulation Magnetic Field Sensor
4,129,937	G. R. Knutson and K. A. Shidler	Method of Making Grounded Transducer for Magnetic Record Disks
4,130,126	W. L. Chocholaty, R. K. Evans, and F. J. Perry	Ink Maintenance Sensor
4,130,240	E. H. Millham, R. J. Scaccia, and F. J. Villante	Dynamic Error Location
4,130,739	M. A. Patten	Circuitry for Compression of Silence in Dictation Speech Recording
4,130,756	R. L. Arndt, W. L. Meyerhofer, Jr., and D. M. Steinbauer	Encoding Mechanism for Holed Badge Readers
4,130,766	M. P. Patel and H.-B. Yin	Bucket Brigade Circuit
4,130,768	J. Bula and A. C. Patrawala	Lower Power True/Complement Driver
4,130,847	N. L. Head, R. D. Hempstead, T. N. Kennedy, and F. Y. Lieu	Corrosion Resistant Thin Film Head Assembly and Method for Making
4,130,868	D. A. Heuer, C. W. McCallister, and P. C. Schloss	Independently Controllable Multiple Address Registers for a Data Processor
4,130,893	W. H. Henkels	Josephson Memory Cells Having Improved NDRO Sensing
4,130,894	R. B. Merrill and Y. S. Yee	Loop Organized Serial-Parallel-Serial Memory Storage System