

Jorma J. Rissanen

Research Division, San Jose, California

Dr. Rissanen is a member of the computer science department at the IBM Research Laboratory in San Jose. He joined IBM in 1960 at the IBM Nordic Laboratories, Stockholm, Sweden, and transferred to the Research Laboratory in San Jose in 1965. He spent the year 1973-74 in Linköping University, Sweden, as professor of control engineering. His current research interests are in combinatorial information theory, algebraic system theory, and estimation problems in time series. He received the Doctor of Technology Degree in control theory and mathematics in 1965 from Helsinki Institute of Technology.

Claus Schünemann

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Dr. Schünemann is a member of the advanced technology group at the System Products Laboratory. His current technical interests include storage hierarchy systems and associated technologies. After receiving the degree of Dipl.-Ing. in electrical engineering from the technical university of Darmstadt, Germany, he joined AEG, Berlin, working in electrical railway control. In 1963, Dr. Schünemann joined the Boeblingen laboratory of IBM, working in the areas of systems design and test, and in memory and logic technology. From 1970 to 1973 he was manager of the advanced technology systems group and worked in the printer carriage drive area during 1973. Dr. Schünemann received a Ph.D. in power electronics from the Technische Hochschule Darmstadt, Germany.

Gisbert Skudelny

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Dr. Skudelny is a senior associate engineer in the advanced technology department. He joined IBM in 1970 to work on electromechanical devices. Dr. Skudelny got his master's degree in physics (Dipl. Phys.) from the University of Saarland, Germany, in 1967. He received his Ph.D. in Physics (Dr. rer. nat.) from the University of Stuttgart, Germany, in 1971, where he worked on metallurgical problems for the Max-Planck-Institute. He is a member of the German Physical Society (DPG).

Charles H. Stapper

System Products Division, Essex Junction, Vermont

Dr. Stapper is an advisory engineer in the process characterization department at the development laboratory in Essex Junction. He is responsible for yield planning and the development of statistical models for large scale integration memory yield analysis. He received his B.S. and M.S. in electrical engineering from the Massachusetts Institute of Technology in 1959 and 1960, respectively. He joined the IBM development laboratory in Poughkeepsie in 1960, where he worked on magnetic recording and the memory applications of tunnel diodes, magnetic thin films, electron beams, and lasers. He has contributed to a number of patents in these fields. From 1965 to 1967 he studied at the University of Minnesota on an IBM fellowship. Upon receiving his Ph.D. degree in solid state physics in 1967, he joined the IBM development laboratory in Essex Junction,

Vermont. His work there included magnetic thin film array development, magnetic bubble testing and device theory, bipolar and field effect transistor device theory, and large-scale integration yield modeling. He is a member of Sigma Xi and the Institute for Electrical and Electronics Engineers.

Volker Zimmermann

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Mr. Zimmermann is assigned to an advanced technology input/output group currently working on circuitry and control problems in I/O devices. He has been with IBM since 1970. Mr. Zimmermann received the Dipl.-Ing. degree in 1967 from the University of Stuttgart. He was employed by Brown Boveri & Cie, in Mannheim, from 1967-1970, where he worked on the development of electronic instruments. He is a member of Verein Deutscher Elektrotechniker.

Erratum

Figure 6 in "Digital Image Processing of Earth Observation Sensor Data" (R. Bernstein, *IBM J. Res. Develop.*, Vol. 20, p. 40, January 1976) was mislabeled. The correct sequence of subimages proceeds counterclockwise from (a); i.e., (c) becomes (b), (d) becomes (c), and (b) becomes (d).