

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

Canada

0,893,337 Data Communication System J. A. Arulpragasam

France

6,935,813 Delta Coder with a Plurality of Quantization Step Sizes H. R. Schindler

7,026,336 Process and Device for Linearizing the Phases of a Frequency Set J. M. Pierret

Germany

1,303,406 Method of Transmitting Measuring Values R. Hartwig

1,774,201 Monolithic Integrated Storage Cell S. K. Wiedmann

MR00527 Personnel Data Identifying and Transmitting Terminal J. Roessner

Italy

0,908,121 Transmission Error Recording N. Colecchia and R. de Giuseppe

Switzerland

0,519,521 Integrated Semiconductor Circuit for Data Storage W. Jutzi

0,519,269 Method for Generating Timing Pulses with Varying Distances, and Timing Pulse Generator for Executing that Method H. R. Schindler and P. Vettiger

United Kingdom

1,249,377 A Magnetic Film Memory F. Ainscroe and B. R. Sowter

1,248,716 Associative Storage Systems S. Wrona

1,251,165 Electromagnetic Activator D. E. Cuzner, L. J. Rigbey, and G. M. Smith

United States

3,647,041 System for Determining Line Measure W. H. Davis, L. M. Hornung, R. D. Lindsey, and H. C. Tanner

3,647,293 Copying System Featuring Combined Developing-Cleaning Station Alternately Activated C. A. Queener

3,647,334 Consumable Fixture for use in Assembling a Magnetic Tape Head L. H. Faure

3,647,456 Method of Making Conductive Silver Images and Emulsion Therefor H. H. Herd and T. I. Sun

3,647,501 Method for Producing Photographic Emulsion Coatings R. L. Buckingham and H. H. Herd

3,647,540 Magnetic Compositions of Praseodymium Modified Chromium Oxide, Methods of Manufacture, and Magnetic Recording Media Containing Same ...

3,647,582 Method for Locating Printed Circuit Work Piece R. S. Haines

3,647,643 Process of Fabricating a Hybrid Magnetic Film D. T. Putnam and W. C. Tatum

3,647,656 Method of Preparing α Substituted β,γ Unsaturated Lactones from Alkoxy-Furans G. S. Alberts and B. L. Flur

3,647,949 Video Multiplexing System R. Srinivasan and H. Hiraoka
F. H. Closs, D. Seitzer, and P. Stucki

3,647,964 Run-length-limited Coding for Modified Raised-Cosine Equalization Channel.....

3,647,977 Multiplexer D. T. Tang

3,647,985 Tone-actuated Dictation Systems with Voice Buffer Option F. H. Closs

3,648,038 Apparatus and Method for Obtaining the Reciprocal of a Number and the Quotient of Two Numbers M. P. Langendori and C. L. Ridings

3,648,061 All-transistor Logic Employing Transistors of a Single-conductivity Type ... H. M. Sierra

3,648,063 Modified Storage Circuit for Shift Register J. C. Black and F. Buckley

3,648,065 Storage Circuit for Shift Register W. K. Hoffman and J. W. Sumilas

3,648,066 Three-phase Dynamic Shift Register W. K. Hoffman

3,648,072 High-speed Gating Circuit L. M. Terman

3,648,081 Piezoelectric Acoustic Surface Wave Device Utilizing an Amorphous Semiconductive Sensing Material L. R. Harper

3,648,124 Gated Metal-Semiconductor Transition Device E. G. Lean, V. Sadagopan, and S.C.-C. Tseng

3,648,130 Common Emitter Transistor Integrated Circuit Structure W. E. Howard, Jr., R. Ludeke, and P. J. Stiles

3,648,131 Hourglass-shaped Conductive Connection Through Semiconductor Structures P. P. Castrucci, E. G. Grochowski, W. D. North, and T. L. Palfi

3,648,175 Computer-oriented Test System Having Digital Measuring Means with Automatic Range-changing Feature K. P. Stuby

3,648,175 Computer-oriented Test System Having Digital Measuring Means with Automatic Range-changing Feature J. D. Barnard, C. C. Gaito, G. R. Giedd, T. G. Greene, J. W. Lind, M. H. Perkins, and C. M. Pross

3,648,237	Apparatus and Method for Obtaining Synchronization of a Maximum Length Pseudorandom Sequence	<i>A. H. Frey, Jr., B. E. Schlosburg, and R. E. Tygieski</i>
3,648,239	System for Translating to and from Single Error Correction-Double Error Detection Hamming Code and Byte Parity Code	<i>W. C. Carter, K. A. Duke, and D. C. Jessep, Jr.</i>
3,648,249	Audio-responsive Visual Display System Incorporating Audio and Digital Information Segmentation and Coordination	<i>P. E. Goldsberry</i>
3,648,253	Program Scheduler for Processing Systems	<i>A. P. Mullery and F. W. Zurcher, Jr.</i>
3,648,254	High-speed Associative Memory	<i>W. F. Beausoleil</i>
3,648,255	Auxiliary Storage Apparatus	<i>W. F. Beausoleil</i>
3,648,265	Magnetic Data Storage System with Interleaved NRZI Coding	<i>H. Kobayashi and D. T. Tang</i>
3,648,271	Visual Editing System Incorporating Selectable Letter Spacing Display and Associated Scale Display	<i>R. L. McConnell and J. W. Simpson</i>
3,648,281	Electrostatic Display Panel	<i>H. Dahms and H. E. Hunziker</i>
3,649,081	Fluid Vibration Transport System	<i>R. A. Johnson, E. H. Richards, and R. H. A. Watson</i>
3,649,358	Method for Reducing the Viscosity of Non-Newtonian Liquids by the Use of Ultrasonics	<i>H. K. Johnston, II</i>
3,649,388	Method for Making a Semiconductor Device Having a Shallow, Flat Front Diffusion Layer	<i>M. L. Joshi, A. Platt, and E. S. Wajda</i>
3,649,393	Variable Depth Etching of Film Layers Using Variable Exposures of Photoresists	<i>M. Hatzakis</i>
3,649,501	Preparation of Single Crystal Films of Lithium Niobate by Radio Frequency Sputtering	<i>V. Sadagopan</i>
3,649,817	Arithmetic and Logical Unit with Error Checking	<i>G. Keller, G. Knauff, P. Skuin, and E. Vogt</i>
3,649,857	Mechanical Energy Storage and Release Device	<i>L. F. Knappe</i>
3,649,940	Adjustable Ferriresonant Transformer	<i>H. S. Hoffman, Jr.</i>
3,649,941	Adjustable Output Voltage Regulator Transformer	<i>B. E. Hart</i>
3,650,021	Method of Manufacturing a Tubular Printed Circuit Armature	<i>K. N. Karol</i>
3,650,042	Gas Barrier for Interconnecting and Isolating Two Atmospheres	<i>F. E. Boeger and W. H. White</i>
3,650,600	Optical Pulse Doubler Utilizing Self-induced Transparency	<i>E. L. Courtens</i>
3,650,737	Imaging Method Using Photoconductive Element Having a Protective Coating	<i>L. I. Maissel and B. Sunners</i>
3,650,854	Method of Fabricating a Transistor Having Improved Emitter-Base Junction Breakdown Voltage Characteristics	<i>H. M. Demsky, A. Platt, A. J. Rideout, and R. S. Samson</i>
3,651,462	Single Scan Character Registration	<i>G. J. Balm</i>
3,651,474	Synchronization System which uses the Carrier and Bit Timing of an Adjacent Terminal	<i>R. A. Liberman</i>
3,651,475	Address Modification by Main/Control Store Boundary Register in a Microprogrammed Processor	<i>R. G. Dunbar, Jr. and K. K. Womack</i>
3,651,476	Processor with Improved Controls for Selecting an Operand from a Local Storage Unit, an ALU Output Register of Both	<i>T. A. Metz and K. K. Womack</i>
3,651,483	Method and Means for Searching a Compressed Index	<i>W. A. Clark, IV, K. A. Salmond, and T. S. Stafford</i>
3,651,485	Holographic Data Processing System	<i>J. A. McDonnell</i>
3,651,487	Printer Control System	<i>R. S. Washington</i>
3,651,498	Holographic Storage and Retrieval System	<i>P. J. Magill, R. E. McCurry, and C. A. Speicher</i>
3,652,273	Process Using Polyvinyl Butral Topcoat on Photoresist Layer	<i>M. S. Htoo</i>
3,652,442	Electroplating Cell Including Means to Agitate the Electrolyte in Laminar Flow	<i>J. V. Powers and L. T. Romankiw</i>
3,652,444	Continuous Vacuum Process Apparatus	<i>W. C. Lester, C. Nuccio, and E. S. Ward</i>
3,652,750	Chromium-Silicon Monoxide Film Resistors	<i>R. Glang, R. A. Holmwood, L. I. Maissel, and J. Vergnolie</i>
3,652,902	Electrochemical Double Layer Capacitor	<i>B. E. Hart, and R. M. Peekema</i>
3,652,987	Synchronization Verifying System	<i>J. H. McCarthy, Jr.</i>
3,653,037	Apparatus and a Method For Automatically Testing a System Which Receives an Analog Input Signal	<i>B. G. Utley</i>
3,653,061	Digital Data Write Deskewing Means	<i>L. S. Frauenfelder and J. A. McDowell</i>
3,653,062	Information Storage Recording Signal Amplitude Control	<i>C. P. Hollstein, Jr.</i>
3,653,066	Magnetic Transfer Imprinter	<i>D. Royse and G. K. Machol</i>
3,653,321	Type Mounting Means For High-speed Front Printer	<i>J. M. Cunningham</i>
3,653,568	Tape Transport Mechanism	<i>D. H. Cronquist</i>
3,653,572	Hot Gas Solder Removal	<i>S. Z. Dushkes and C. Trollmann</i>
3,654,109	Apparatus and Method for Measuring Rate in Flow Process	<i>J. H. Hohl and K. H. Raacke</i>
3,654,389	Coordinate Input Device	<i>R. V. Pole</i>

3,654,448	Instruction Execution and Re-execution With In-line Branch Sequences	<i>D. C. Hitt</i>
3,654,530	Integrated Clamping Circuit	<i>R. H. F. Lloyd</i>
3,654,567	Vapor Discharge Cell	<i>R. T. Hodgson</i>
3,654,589	Electrical Connector	<i>E. J. Di Stefano, A. E. Enderley, and R. C. Hoffman</i>
3,654,609	Proportional Spacing Visual Editing System	<i>R. G. Bluethman</i>
3,654,611	Visual Editing System Incorporating Controls for Justifying and Dejustifying Display Text	<i>R. G. Bluethman and J. W. Simpson</i>
3,654,615	Element Placement System	<i>H. Freitag</i>
3,654,617	Microprogrammable I/O Controller	<i>J. W. Irwin</i>
3,654,622	Auxiliary Storage Apparatus with Continuous Data Transfer	<i>W. F. Beausoleil</i>
3,655,457	Method of Making or Modifying a PN-Junction by Ion Implantation	<i>M. C. Duffy, P. A. Schumann, Jr., and T. Yeh</i>
3,655,999	Shift Register	<i>S. K. Wiedmann</i>
3,656,004	Bipolar Capacitor Driver	<i>D. W. Kemerer and J. N. Pomeranz</i>
3,656,028	Construction of Monolithic Chip and Method of Distributing Power Therein for Individual Electronic Devices Constructed Thereon	<i>J. L. Langdon</i>
3,656,029	Bistable Resistor of Europium Oxide, Europium Sulfide, or Europium Selenium Doped with Three-D Transition or VA Element	<i>K. Y. Ahn and K. C. Park</i>
3,656,034	Integrated Lateral Transistor Having Increased Beta and Bandwidth	<i>A. J. Rideout</i>
3,656,060	Time Interval Measuring and Accumulating Device	<i>C. J. Bauernfeind and R. F. Zeman</i>
3,656,107	Automatic Double Error Detection and Correction Apparatus	<i>M. Hsiao and W. F. Mikhail</i>
3,656,117	Ternary Read-only Memory	<i>G. A. Maley and J. L. Walsh</i>
3,656,123	Microprogrammed Processor with Variable Basic Machine Cycle Lengths	<i>R. J. Carnevale, L. D. Howe, Jr., T. A. Metz, K. K. Womack, and F. A. Zurla</i>
3,656,601	Pneumatic Phrase/Format Writer Utilizing Card Cartridge and Featuring Cyclical Mechanism for Reading a Succession of Cards as well as Programming the Tabulation and Other Functions of an Interconnected Printer	<i>W. A. Abell, Jr.</i>
3,656,671	Frangible Projection Removal	<i>R. J. Bratek</i>
3,656,827	Holographic Read/Write Storage System	<i>R. L. Gamblin and C. D. Southard</i>
3,656,834	Additive For Liquid Crystal Material	<i>I. Haller and H. A. Huggins</i>
3,657,524	Dual Mode Process Control With Continuous Feedback During Coarse Mode	<i>R. M. Bakke</i>
3,657,554	Super-radiant Short-pulse Laser	<i>O. J. Lumpkin and N. S. Shiren</i>
3,657,612	Inverse Transistor with High Current Gain	<i>S. K. Wiedmann</i>
3,657,699	Multipath Encoder-Decoder Arrangement	<i>E. Y. Rocher and S. E. Schuster</i>
3,657,709	Storage Tube With Pointwise Erase Capability	<i>R. W. Dreyfus</i>
3,657,716	Character Generator for Cathode Ray Tube Display Device	<i>L. E. Ambrico</i>
3,657,806	Method for Manufacturing a Thin-film Magnetic Head Assembly	<i>P. Simon</i>
3,658,088	Packaging System for Pneumatic Logic	<i>D. F. Jensen, D. H. Rickenback, and R. R. Schaffer</i>
3,658,606	Diffusion Source and Method of Producing Same	<i>V. J. Lyons and J. S. Sandhu</i>
3,658,629	Photopolymer Resist Sheet Laminator	<i>L. P. Cramer, H. B. Irons, H. Kohn, and J. P. Mathis</i>
3,658,678	Glass-annealing Process for Encapsulating and Stabilizing FET Devices	<i>L. V. Gregor and M. Zuegel</i>
3,659,087	Controllable Digital Pulse Generator and a Test System Incorporating the Pulse Generator	<i>R. F. Green, E. J. Scray, Jr., and D. L. Wilder</i>
3,659,089	Error Detecting and Correcting System and Method	<i>A. D. Payne and H. J. Reinheimer</i>
3,659,238	Permanent Magnet Electromagnetic Actuator	<i>B. M. Griffing</i>
3,659,273	Error Checking Arrangement	<i>G. Knauf, F. Koederitz, H. Painke, L. Reichl, H. H. Lampe, R. Vachenauer, E. Vogt, and H. Weber</i>