

Recent IBM Patents

March through June 1964

March 1964

Associative Memory	W. K. French	3,123,706
Multiplying Arrangements for Digital Computing Machines	T. Kilburn, D. B. G. Edwards and D. Aspinall	3,123,707
Dynamic Magnetic Device	J. G. Hewitt, J. C. Sagnis and P. E. Stuckert	3,123,717
Ferrite Composition	J. M. Brownlow	3,123,748
Magnetic Storage Device	R. L. Ward	3,123,808
Magnetic Core Analog to Digital Converter	R. K. Golden	3,123,817
Data Storage	L. C. Tidball	3,124,260
Document Handling Apparatus	J. A. Weidenhammer	3,124,352
Non-Coincidence Magnetic Gate	H. R. Burns, Jr.	3,124,700
Transistor Switching Circuit Responsive in Push-Pull Manner to Single-Ended Input	P. D. Bellamy and G. Mueller	3,124,758
Data Transmission Checking Means	F. V. Adams	3,124,783
Cathode Ray Tube Printer	F. W. Schaaf, G. L. Scott and S. Rubin	3,124,784
Apparatus for Reading Magnetic or Electrostatic Bits	J. D. Kuehler	3,124,790
Magnetic Transducer	W. J. Homan	3,124,803
Automatic Tool Setting System	D. G. Pittwood	3,124,976
Magnetic Core Logical Circuit	G. E. Olson	3,125,744
Data Storage Access Mechanism	J. R. Geddes	3,126,008
High-Speed Punching Mechanism	G. E. Lyons and R. A. Barbeau	3,126,150
Phase Stable Switching Circuits	W. L. McMillan	3,126,486
Magnetic Actuators	J. Flora	3,126,501
File Search Data Selector	E. J. Rabenda, W. D. Brodd and E. E. Marquardt	3,126,523
Magnetic Switching Devices	G. Constantine, Jr.	3,126,528
Non-Destructive Read-Out	H. P. Hempel	3,126,529
Magnetic Core Device	F. L. Post and S. K. Raker	3,126,530
Magnetic Switching Device	G. Constantine, Jr.	3,126,533
Automatic Regeneration Memory	W. T. Siegle	3,126,534
Transducer Positioning System	D. N. Streeter	3,126,535
Binary to Ternary Converter	K. M. Trampel	3,126,537
Binary Adder Matrix Positioner	F. S. Stauffer	3,126,799
Escapement Mechanism for Typewriter	L. E. Palmer	3,126,998
Feedback Amplifier Employing Tunnel Diode	D. A. Bishop	3,127,526

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Multi-Stable Fluid Device	R. E. Norwood	3,128,039
Fluid Logic Device	R. E. Norwood	3,128,040
Fiber Printing	W. E. Semple	3,128,336
Magnetoresistive Logical Circuitry	C. Pettus and T. Young	3,128,390
Triggered Pulse Generator Transistor Circuit	G. D. Bruce	3,128,391
Back Voltage Limiting Circuit	R. W. Jones	3,128,392
Electromagnetic Logic Apparatus	E. A. Brown and G. H. Schacht	3,128,417
Drive Ring	D. F. Busch	3,128,453
Device for Winding Annular Cores	G. Stutz	3,128,955
Synthetic Wax Substitutes for Carnauba Wax and Transfer Ink Compositions Containing Such Substitutes	T. D. Callinan, J. S. Crimi, H. L. McGee, A. M. Parks and P. M. Schwartz	3,129,104
Specimen Identification System	R. Bakis	3,129,287
Arithmetic System	H-T. Lee	3,129,324
Matrix Switch	G. Constantine, Jr.	3,129,336
Magnetic Core Switching System	J. H. Hohl	3,129,337
Logical and Memory Circuits Utilizing Tri-Level Signals	H. B. Baskin	3,129,340
Electron Spin Echo Memory System	P. P. Sorokin	3,129,410
Magnetostriuctive Thin Film Delay Line	J. E. Lovell	3,129,412
Safety Circuit for Transistor Amplifier	A. B. Chase	3,129,428
Method and Apparatus for Winding Magnetic Cores	N. K. Perkins	3,129,494
Handling Device for Strand Material	H. L. Gerth and R. L. Judge	3,129,679
Circuit Board Assembly	R. Rice and J. J. Shea	3,129,990
Plated Circuit Magnetic Core Array	R. A. Jones	3,130,134
Optical Maser Having Terminal Level Above Ground State	P. P. Sorokin and M. J. Stevenson	3,130,254
Optical System for Retrieving Stored Information	I. E. Sutherland	3,130,305
Pulse Input Control Circuit	G. A. Maley and W. L. Stahl	3,130,321
Three Level Logical Circuit Suitable for Signal Comparison	D. S. Swallow	3,130,324
Magnetic Storage Devices	A. C. Moore and A. S. Young	3,130,390

Deskewing Using Last Bit of a Byte	E. W. Miller	3, 130, 392
Information Handling Apparatus	A. J. Paul, Jr.	3, 130, 399
Hydraulic Positioning System	M. E. Freeman	3, 130, 549
Associative Memory	W. K. French	3, 131, 291
Computing System	G. H. Bush and J. E. Carrington	3, 131, 293
Unidirectional Traveling Wave Parametric Circuits Using Resonant Sections	K. E. Schreiner	3, 131, 358
Balanced Transistor Multivibrator	W. C. Dersch	3, 131, 362
Single Bit Recording Technique	W. E. Brandt	3, 131, 383

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Two Speed Incremental Drive	M. J. White	3, 131, 570
Badge-Reading Apparatus	A. D. Wolfheimer	3, 132, 241
Recording Sensing Apparatus	A. Cutaia	3, 132, 242
Data Transfer Device	L. R. Harper	3, 132, 245
Photoelectrostatic Apparatus	R. M. Schaffert	3, 132, 267
Computer Memory Unit and Addressing Means	E. Estrems	3, 132, 324
Analog-to-Digital Converter	E. J. Zola, Jr.	3, 132, 336
Variable Reluctance Slotted Drum Position Indicating Device	V. C. Martin	3, 132, 337
Preparation of Coatings and Printing Inks	M. J. Vogel	3, 133, 023
Vapor Cell Light Amplifier	J. W. Horton	3, 133, 199
Semiconductor Device Fabrication	J. C. Marinace	3, 133, 336
Wear Compensator with Clamping Device	S. A. Okcuoglu and G. A. Walker	3, 133, 620
Protection of Transistor Circuits Against Predictable Overloading	L. A. Russell	3, 134, 023
Binary Logic Circuits	C. Battarel	3, 134, 025
Multi Collector Transistor Forming Bistable Circuit	J. Earle	3, 134, 026
Null Detecting Bridge Circuit for Testing Back Resistance of a Diode	J. A. Dickerson	3, 134, 073
Proportional Space Recording Devices	R. T. Blakely	3, 134, 090
Means to Read Out Less than all Bits in a Register	A. F. Shugart	3, 134, 091
Electronic Digital Computers	E. A. Newman and D. O. Clayden	3, 134, 092
Cryogenic Memory Systems	H. F. Heath, Jr.	3, 134, 095
Magnetic Memory	E. A. Bartkus, R. F. Elfant, K. R. Grebe and N. J. Mazzeo	3, 134, 096
Data Storage Machine	L. D. Stevens, W. A. Goddard and J. J. Lynott	3, 134, 097
Ultrasonic Data Converter	P. W. Woo	3, 134, 099
Method for Winding and Assembling Magnetic Cores	H. P. Luhn	3, 134, 163
Shaft Coupling	W. E. Mesh	3, 134, 246
Optical Phase Measuring Apparatus	H. Gamo	3, 134, 840
Common Channel Transfer Error Check	F. Mazzioti	3, 134, 960
Esaki Diode Memory	R. A. Henle, W. W. Lawrence, Jr., J. B. Pace and H. P. Wolff	3, 134, 963

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Means to Prevent Machine Operation Upon Depression of Plural Keys	R. J. Young	3, 135, 371
Thermal Copying Process	E. P. Damm, Jr. and J. A. Underhill	3, 135, 621
Ring Counter Employing Four-Layer Diodes and Scaling Resistors to Effect Counting	R. A. Leightner	3, 135, 875
Coupling Means for Control Systems	R. E. Tomek	3, 135, 903
Error Correction Device	R. E. Miller and J. P. Roth	3, 135, 946
Analog-to-Digital Converter	S. G. Francisco	3, 135, 954
Comparing Matrix	R. C. Lamy and A. J. Atrubin	3, 136, 977
Punching Device	B. E. Forsyth and F. Moring	3, 137, 441
Signal Generator with External Start Pulse Phase Control	G. L. Clapper	3, 137, 818
Self-Checking Numbering Devices	R. A. Davis	3, 138, 701
Photologic Arithmetic Circuits	R. Rice	3, 138, 704
Electro-Optical Circuit Using Quenching Electroluminescence	A. B. Fowler	3, 138, 713
Amplitude Bistable Circuits	C. L. Heizman	3, 138, 718
Magnetic Core Logic Circuits	N. G. Vogl, Jr.	3, 138, 719
Punched Card to Internal Storage Translator with Parity Check	E. Estrems and M. Papo	3, 138, 782
Superconductor Guard Strip Gating Device	J. J. Lentz and D. J. Dumin	3, 138, 784
Deposited Magnetic Memory Array	E. B. Chapman, K. F. Greene and B. J. Ronkese	3, 138, 785
Magnetic Core Matrix	E. J. Smura	3, 138, 786
Double Triangular Array Memory Drive	W. P. Hanf	3, 138, 787
Magnetostriuctive Delay Line	E. W. Pugh	3, 138, 789