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Simulation of Indexing Registers and Automatic  
Floating-Decimal Arithmetic for a 4000-Word I.B.M. 650

DONALD BURD

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MAY, 1961

Supported by the  
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Nonr-266(72)

Simulation of Indexing Registers and Automatic  
Floating-Decimal Arithmetic for a 4000-Word I.B.M. 650 \*

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ABSTRACT

This report describes an interpretive program which will allow programs written for a 2000-word I.B.M. 650 with indexing registers and automatic floating-decimal device to be run (possibly with minor changes) on a basic 4000-word 650. Because of the low relative speed of the interpreter (the minimum estimated ratio is about 20/1), its use is recommended only with programs requiring a short time when run as originally intended. Even with this limitation, it is expected that many useful "one-shot" library routines will become available to users of the 4000-word machine without expenditure of additional programming time.

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\* This work is supported by the Office of Naval Research.

## METHOD

The program has been coded in the SOAP II language and assembled by SOAP IIA-4000 in locations 2000-3011 and 3999. The attempt has been made in the case of each simulated operation to duplicate the numerical result which would have been obtained on the 2000-word machine (see I.B.M. 650 Data Processing Bulletin G24-5003-0 6/59 for a description of these operations). It can therefore be expected that test results given in a program write-up will be duplicated identically by running in the interpretive mode.

## RESTRICTIONS

1. The drum should be free of blank cells.

This restriction is necessary since the contents of the address given by the data portion of the instruction are examined regardless of whether this portion has any meaning in the program as an address (i.e., it has none in the case of a shift instruction). One simple way to comply with this restriction is to clear the drum before running, and then to make sure that any blank positions on the input cards are filled with zeros.

2. No illegal address should appear in an instruction being executed in the interpretive mode.

This will apply only to the instruction address portion of a reading instruction intended for operation on load cards, which will usually be set in the console switches.

If it is necessary to violate this restriction, a stop will occur at S11(01 2138 2138) when the interpreter examines the illegal address. After verifying that the offending instruction is the one in the switches, the program can be restarted at E11(2100).

3. Load cards cannot be used directly in the interpretive mode, nor can they be used to transfer control to parts of the program which must be executed in the interpretive mode.

This restriction arises because the automatic branch caused by the "load" feature of the 650 interferes with the control of the program by the interpreter. Load cards can be used, however, if revised as follows:

The first location to which control is transferred (given by the data address of the read instruction) must read 65  $X_1$  2115, where  $X_1$  contains the location of the next program instruction to be executed (00 0000  $X_2$ ).

Thus in the case of a conventional "transfer" card which has 00 0000 1234 as its first word and is read by the instruction 70 1951 9999, the revised card becomes:

word 1: 65 1952 2115  
word 2: 00 0000 1234

If the load card scheme is more complex than this, some other method must be devised to adhere to the general rules given above.

It should be noted that a failure to observe one of these restrictions is not dangerous in the sense that it can result in a machine stop, but not in erroneous calculations.

It would be advisable, however, to examine the program for possible violations prior to running in order to conserve machine time.

#### INSTRUCTIONS FOR USE

1. Insert a standard 80-80 input-output panel in the 533 and load the interpreter (70 1951 1950 in the switches; press computer reset, program start, read feed start, ..... end of file).

2. Change the console switch settings and the input-output panel if necessary for loading the program. If the loading procedure does not depend on the features being simulated, it can be carried out as under normal running conditions with the exception that any load cards which transfer control to the program must be revised as described above. If the loading procedure uses either the indexing registers or the automatic floating-decimal arithmetic, then loading must be done in the interpretive mode (described in section 3 below). In this case also, load cards which transfer control to the program must be revised.

3. To start computing in the interpretive mode manually (this may be necessary before or after loading as explained in section 2 above, or not at all if the program is normally initiated by a "transfer" card), read the address of the first instruction into BEGIN (3999) in the form 00 0000 XXXX and start the program manually at START (3000). If the first instruction is to be taken from the switches, then 00 0000 8000 must be read into BEGIN.

ACKNOWLEDGEMENT

I would like to express my thanks to Dr. Daniel Tycko, without whose help and encouragement the project would not have been possible.

TABLE OF PSEUDO-REGISTERS

<u>Register</u>	<u>Soap Name</u>	<u>Address</u>	<u>Contents</u>
1. Distributor	DISTR	3001	Current contents of distributor.
2. Lower Accumulator	LOWER	3002	Current contents of lower accumulator.
3. Upper Accumulator	UPPER	3003	Current contents of upper accumulator.
4. Indexing Register A	IRA	3005	Current contents of indexing register A.
5. Indexing Register B	IRB	3006	Current contents of indexing register B.
6. Indexing Register C	IRC	3007	Current contents of indexing register C.
7. Operation Code	OP	3008	Last two digits are current operation code.
8. Data Address	DATA	3009	Data portion is current data address.
9. Instruction Address	INSTR	3010	Instruction portion is current instruction address.
10. Contents of Data Address	CDATA	3011	Contents of current data address.

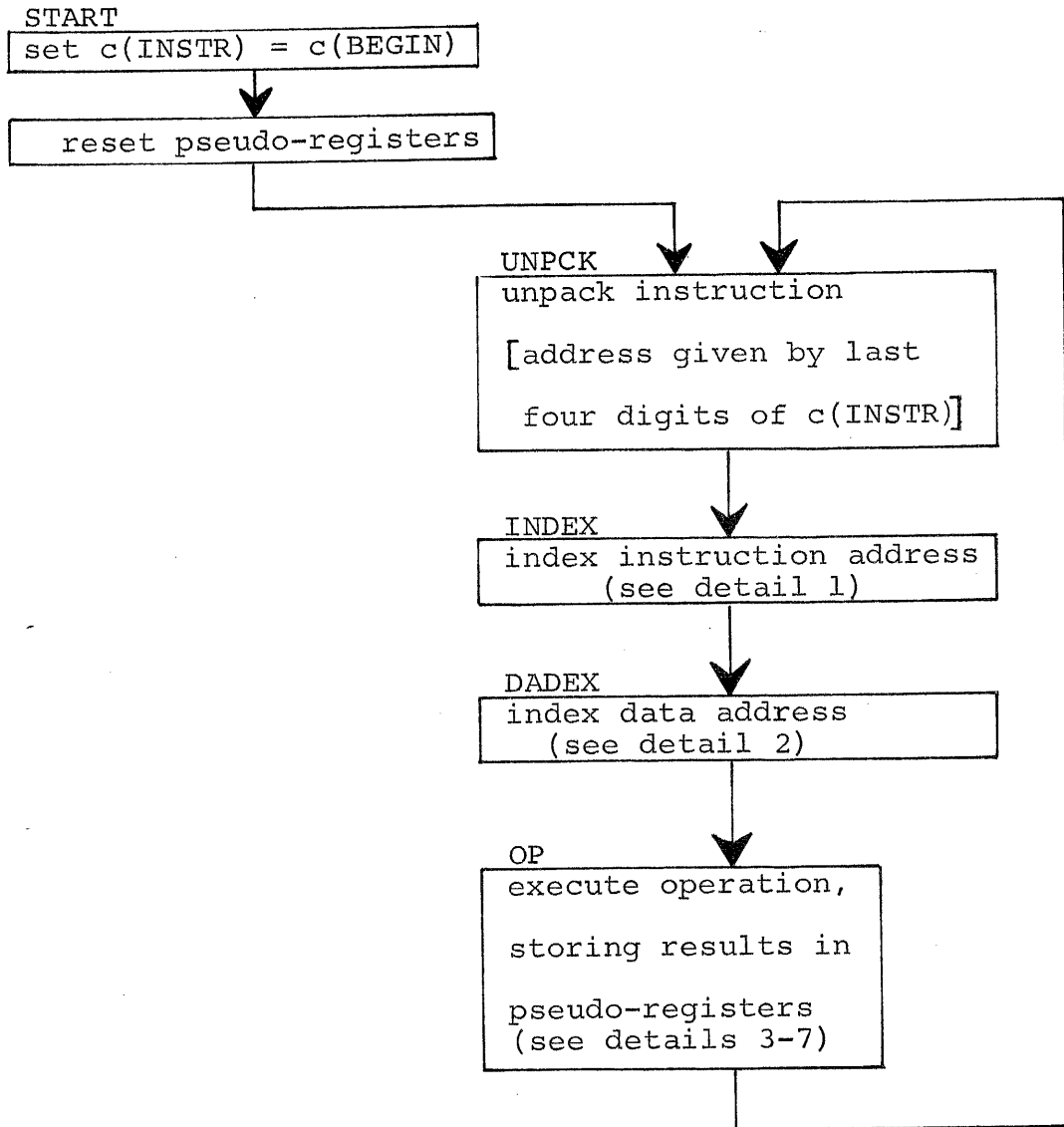
PROGRAMMED STOPS

The instruction in parentheses will be found in the program register. The correspondence with the original program can be established by using the table of pseudo-registers to reconstruct the instruction currently being interpreted and examining the operands.

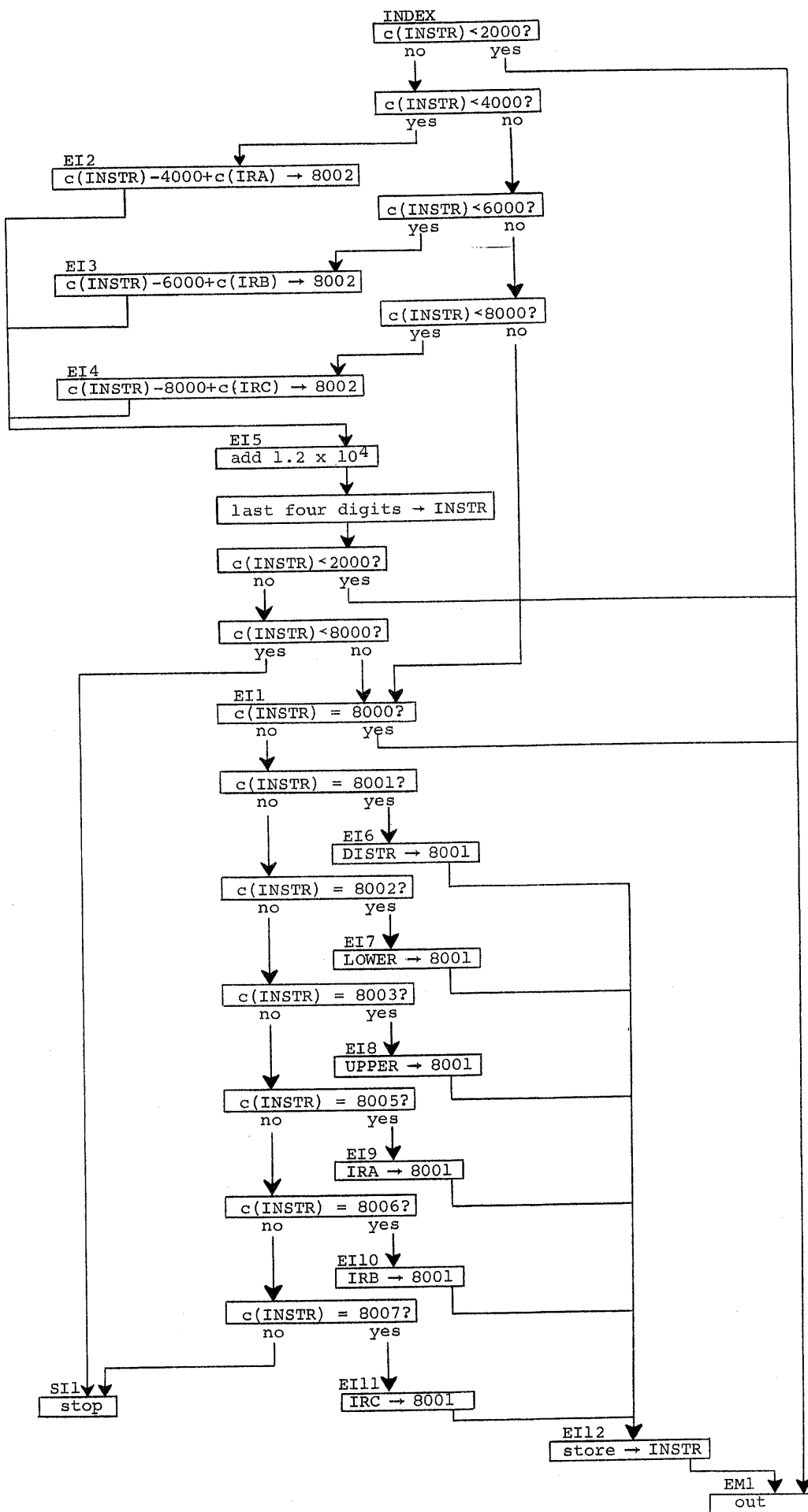
1. SI1 (01 2138 2138)      Illegal instruction address.
2. SD1 (01 2136 2136)      Illegal data address.
3. 2001 (01 3009 2113)      Programmed stop in original  
program (press program start  
button to continue).
4. 20XX (01 20XX 20XX)      Illegal operation code.
5. S341 (01 2378 2378)      Zero divisor.
6. S342 (01 2216 2216)      Mantissa of the divisor has  
more leading zeros than mantissa  
of the dividend.



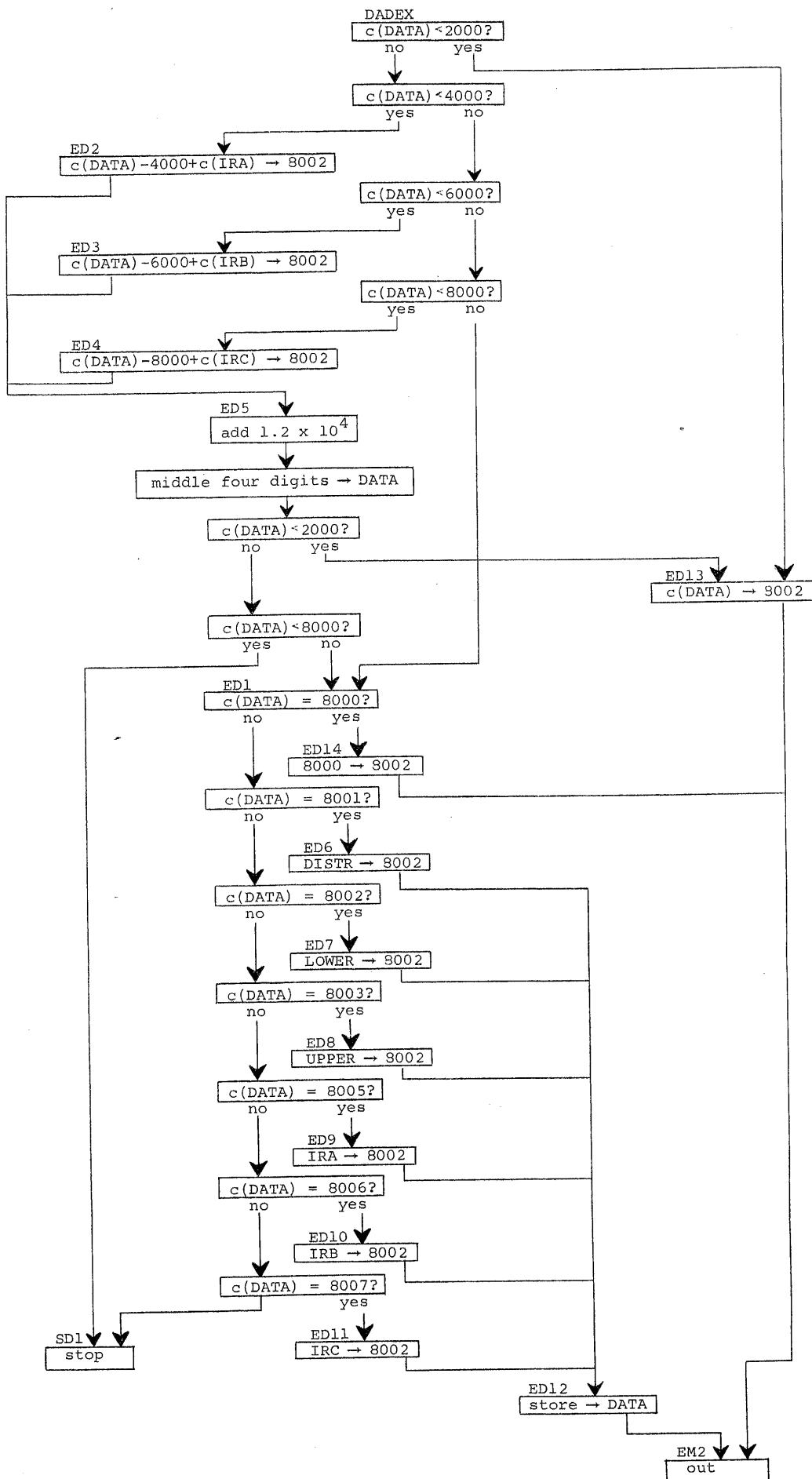
Master Flow Chart



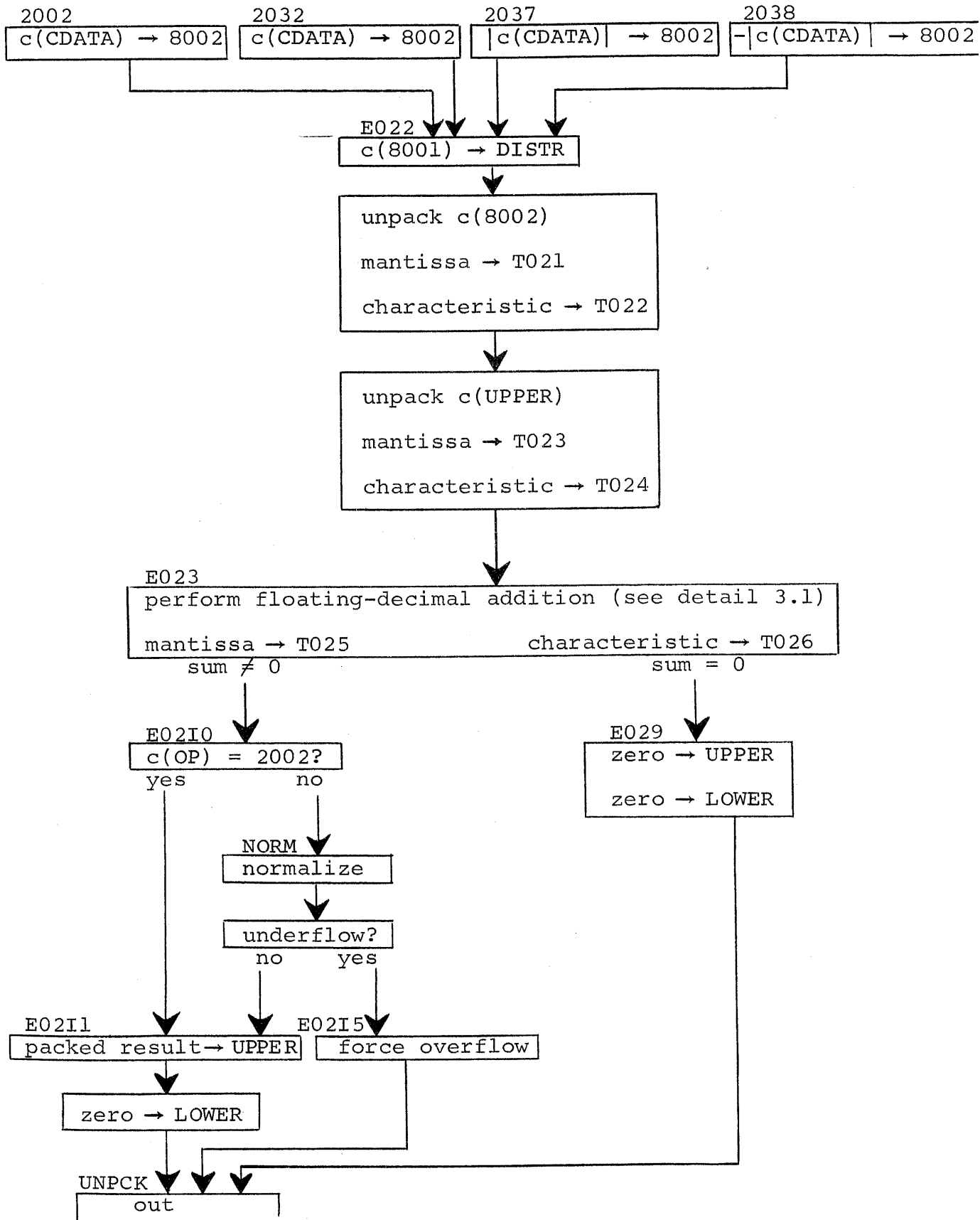
DETAIL 1. Instruction Address Indexing Routine



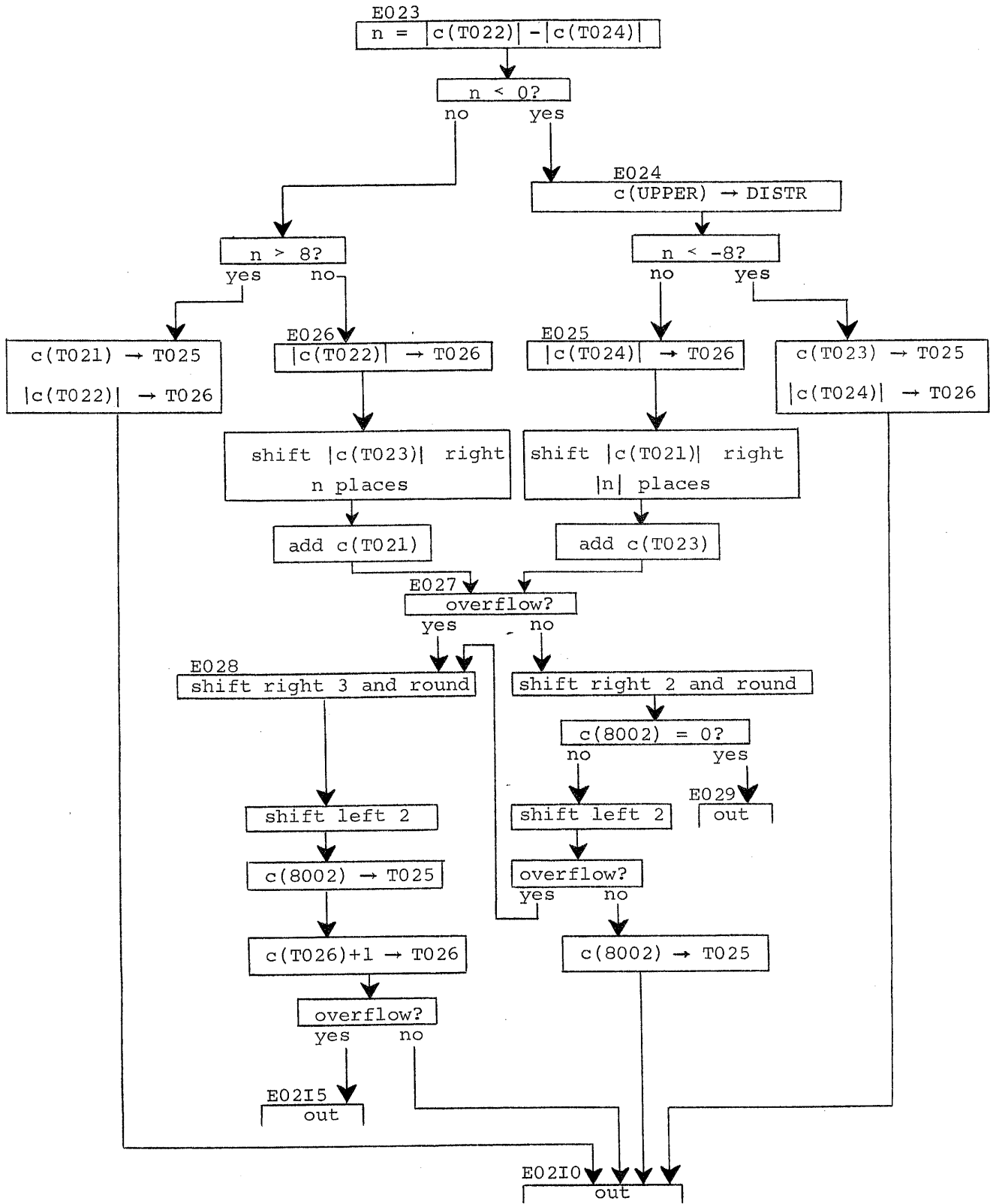
DETAIL 2. Data Address Indexing Routine



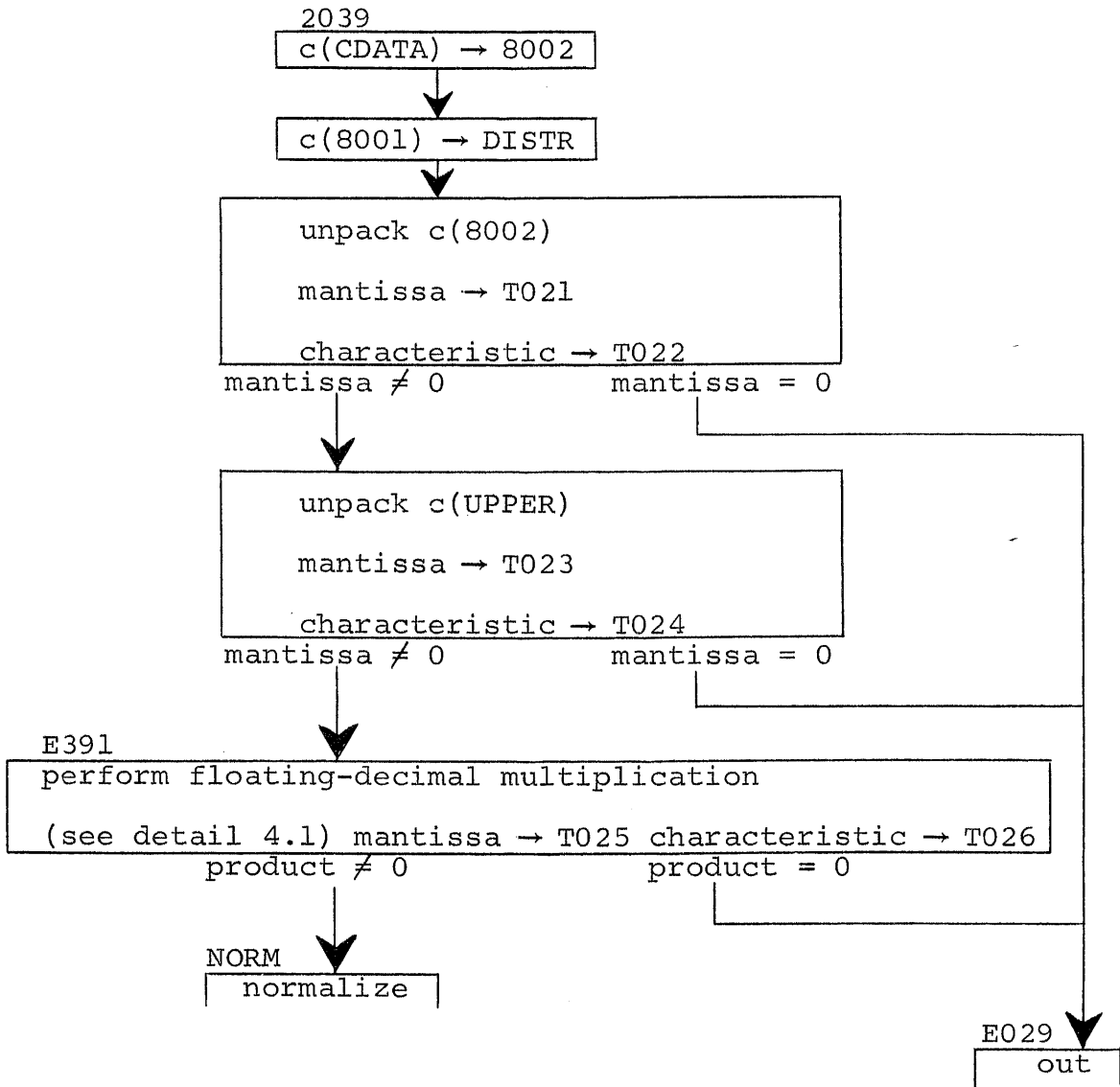
DETAIL 3. Floating-Decimal Addition Routine



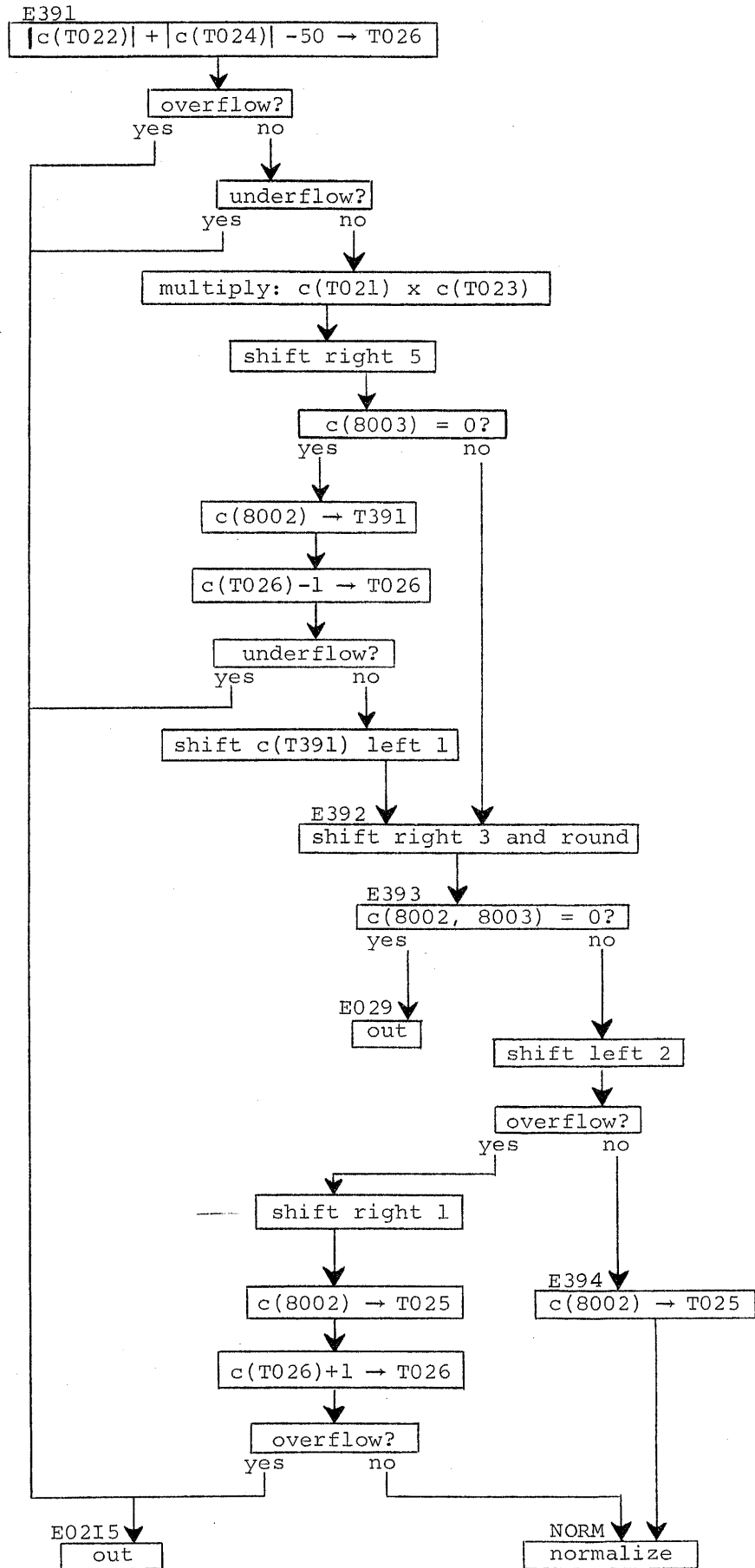
DETAIL3.1 Floating-Decimal Addition



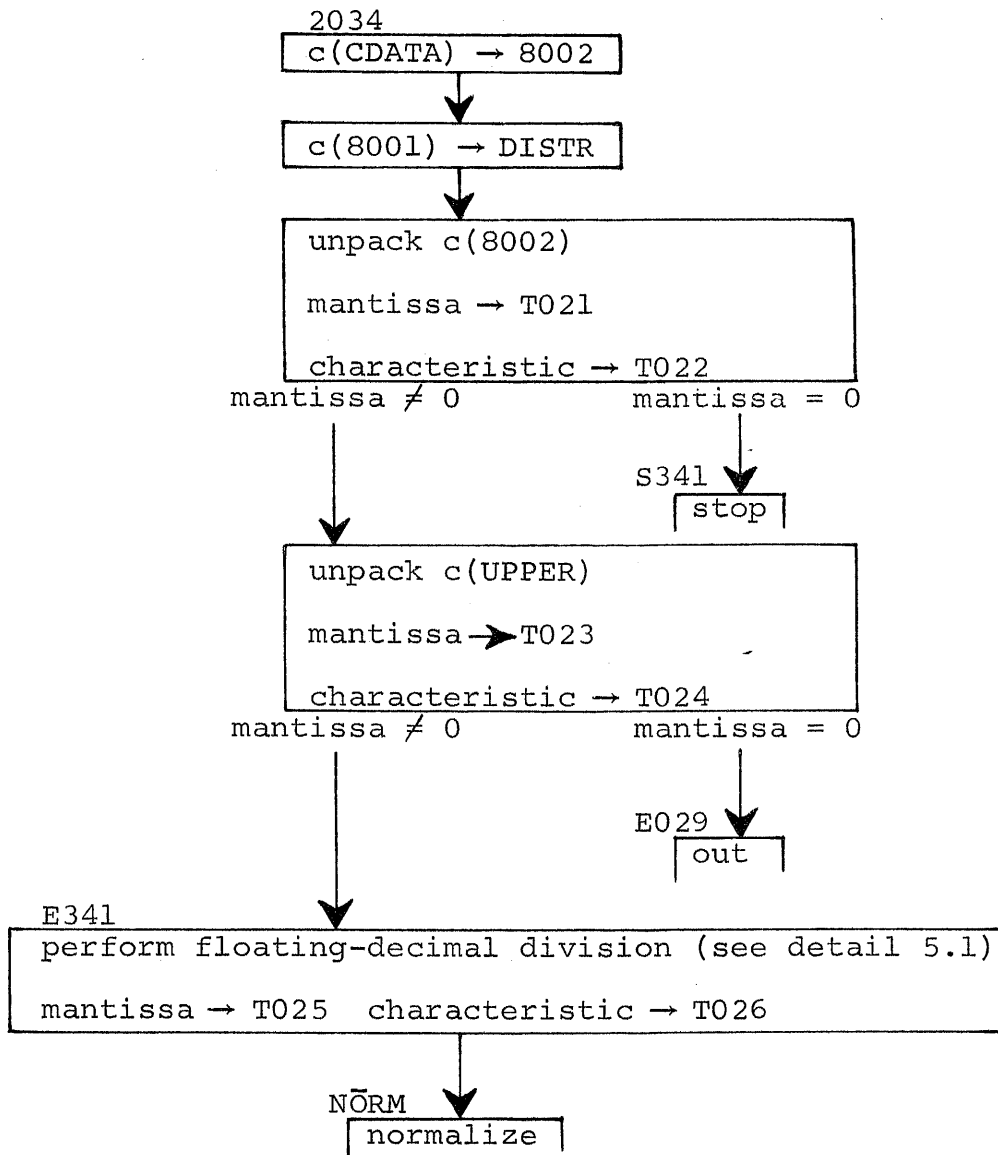
DETAIL 4. Floating-Decimal Multiplication Routine



DETAIL 4.1 Floating-Decimal Multiplication

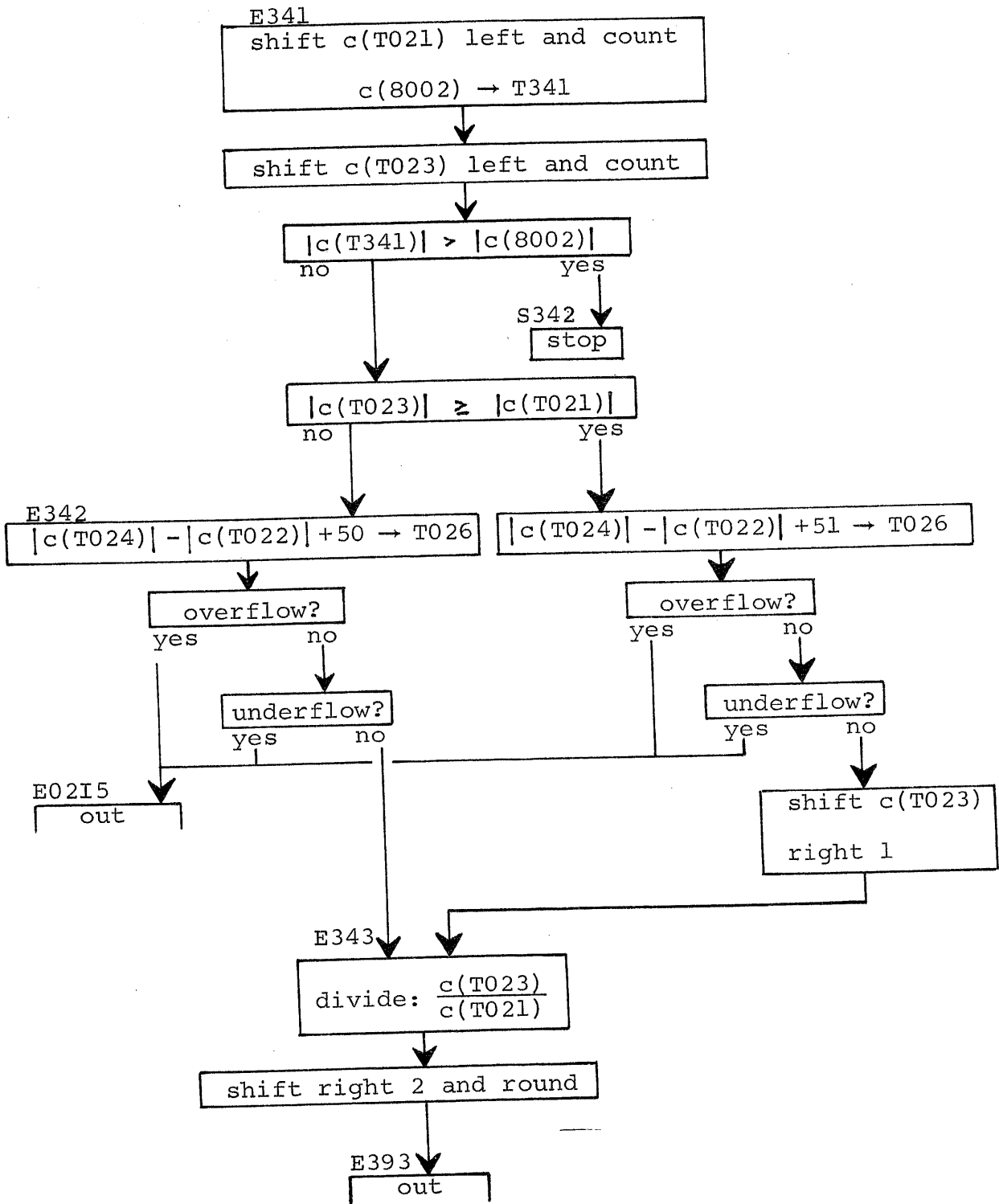


DETAIL 5. Floating-Decimal Division Routine





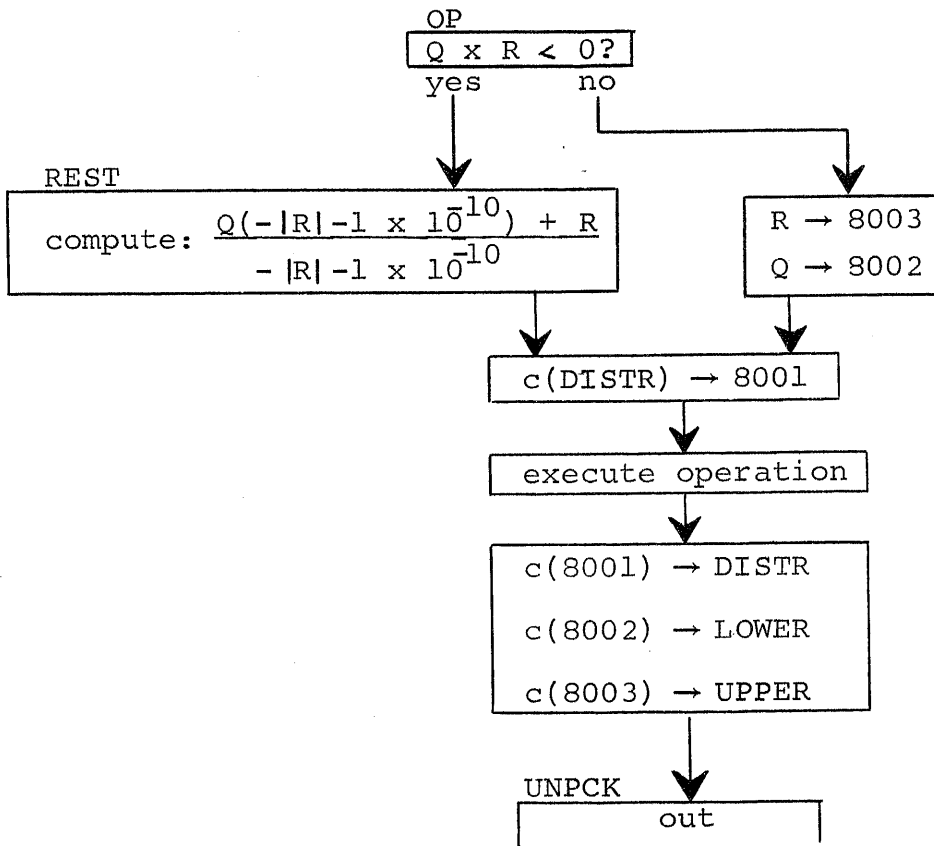
DETAIL 5.1 Floating-Decimal Division



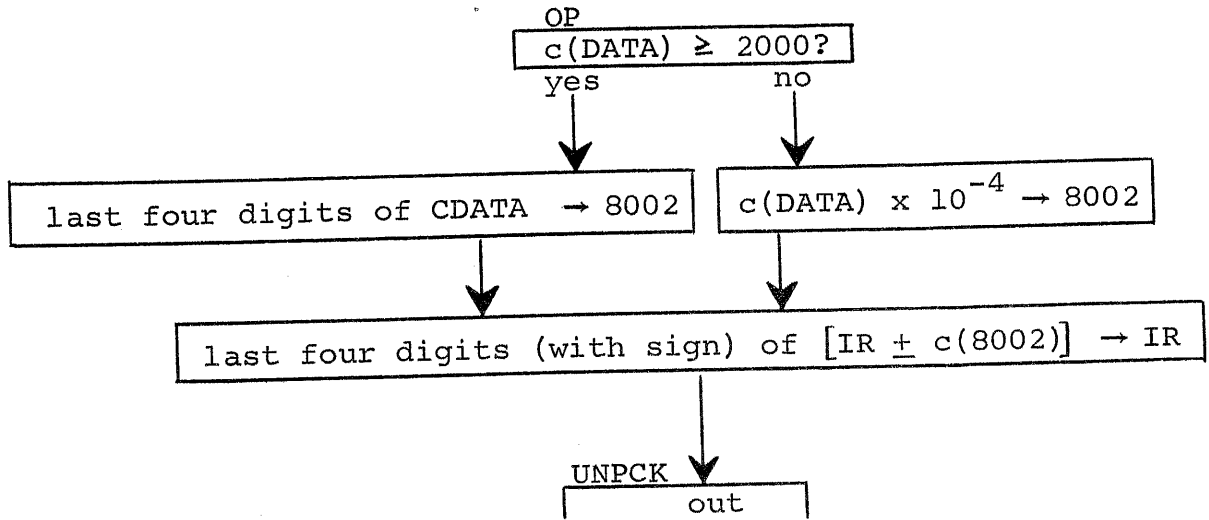
DETAIL 6. Execution of a Basic 650 Arithmetic Operation

Q ≡ c(LOWER)

R ≡ c(UPPER)



DETAIL 7. Execution of an Arithmetic Indexing Register Operation



1	3000	3000
2	3999	2103
3	3001	2104
4	3002	2105
5	3003	2106
6	3005	2108
7	3006	2109
8	3007	2110
9	3008	2111
10	3009	2113
11	3010	2115
12	3011	2125
13	2099	8002
14	3999	8003
15		8004
16		8005
17		8006
18		8007
19		8008
20		8009
21		8010
22		8011
23		8012
24		8013
25		8014
26		8015
27		8016
28		8017
29		8018
30		8019
31		8020
32		8021
33		8022
34		8023
35		8024
36		8025
37		8026
38		8027
39		8028
40		8029
41		8030
42		8031
43		8032
44		8033
45		8034
46		8035
47		8036
48		8037
49		8038
50		8039
51		8040
52		8041
53		8042
54		8043
55		8044
56		8045
57		8046

MAIN PROGRAM

INDEX  
INSTR  
ADDRESS

START	3000	3000
UNPCK	3999	2103
C1	3001	2104
EM1	3002	2105
EM2	3003	2106
C34	3005	2108
INDEX	3006	2109
E12	3007	2110
E13	3008	2111
E14	3009	2113
E15	3010	2115
	3011	2125
	2099	8002
	3999	8003
		8004
		8005
		8006
		8007
		8008
		8009
		8010
		8011
		8012
		8013
		8014
		8015
		8016
		8017
		8018
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		8020
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		8042
		8043
		8044
		8045
		8046

E I 1	NZE	C 15	E M 1	3 2 7	4 5	1 8 0	1 0 0
	SLO	C 15	E I 6	1 8 0	1 6	1 3 3	1 3 7
	NZE	C 15	E I 7	2 1 3 7	4 5	1 4 0	1 4 1
	SLO	C 15	E I 8	2 1 8 7	4 5	1 9 0	1 9 1
	NZE	C 3 7	E I 9	2 1 9 0	4 5	2 4 0	2 4 1
	SLO	C 15	E I 1 0	2 2 3 7	4 5	2 4 0	2 4 1
	NZE	C 15	E I 1 1	2 2 4 0	4 5	2 8 7	2 8 7
	SLO	C 15	E I 1 2	2 2 8 7	4 5	3 3 7	3 3 7
E I 6	NZE	S I 1	E I 1 1	2 3 3 7	4 5	3 8	3 4 1
E I 7	LDD	C 1 6	E I 1 2	2 1 4 1	6 9	1 4 4	1 9 7
E I 8	LDD	C 1 7	E I 1 2	2 1 9 1	6 9	1 4 4	1 9 7
E I 9	LDD	C 1 8	E I 1 2	2 2 4 1	6 9	1 4 4	1 9 7
E I 1 0	LDD	C 1 9	E I 1 2	2 2 5 0	6 9	2 5 3	1 9 7
E I 1 1	LDD	C 2 0	E I 1 2	2 2 9 1	6 9	2 5 4	1 9 7
E I 1 2	LDD	C 2 0	E M 1	2 3 4 1	6 9	3 4 4	1 9 7
S I 1	STR	I N S T R	S I 1	2 1 9 7	2 4	3 1 0	1 0 0
DADEX	H L T	S I 1		2 1 3 8	1 6	1 3 8	1 3 8
	SLO	C 3 8		2 1 3 8	1 6	1 1 6	1 2 1
	B M I	E D 1 3		2 1 7 1	4 6	1 1 6	1 7 5
	SLO	C 3 8		2 1 7 5	4 6	2 2 4	2 2 5
	B M I	E D 2		2 1 7 1	4 6	2 2 4	2 2 5
	SLO	C 3 8		2 2 2 5	4 6	2 1 6	2 2 5
	B M I	E D 3		2 2 2 1	4 6	2 1 6	2 2 5
	SLO	C 3 8		2 2 2 1	4 6	2 1 6	2 2 5
	B M I	E D 4		2 2 7 1	4 6	2 3 2	2 3 5
E D 2	SRT	0 0 0 4	E D 1	2 2 7 1	4 6	0 0 0	2 3 5
	AL	I R A	E D 5	2 1 8 5	1 5	0 0 0	2 1 8 5
	SRT	0 0 0 4		2 2 0 9	3 5	0 0 0	2 2 0 9
E D 3	AL	0 0 0 4		2 2 7 4	3 5	0 0 0	2 2 3 5
	SRT	I R B		2 2 3 5	3 5	0 0 0	2 1 6 1
	AL	0 0 0 4		2 1 6 1	3 5	0 0 0	2 2 8 5
E D 4	SRT	0 0 0 4		2 3 2 4	3 5	0 0 0	2 2 1 1
	AL	I R C		2 2 8 5	3 5	0 0 0	2 4 2 7
	SRT	0 0 0 4		2 2 1 9	1 5	0 0 0	2 1 3 4
E D 5	AL	0 0 0 4		2 4 2 7	1 5	8 0 0	2 1 3 4
	SRT	C 3 9		2 1 3 4	6 9	3 0 0	2 2 6 9
	LDD	8 0 0 3		2 2 6 9	2 5	8 0 0	2 3 2 1
	SDA	D A T A		2 3 2 1	1 6	1 1 6	2 3 7 5
	R A L	8 0 0 1		2 3 7 5	4 6	1 7 4	2 3 8 5
	SLO	C 3 8		2 3 7 5	4 6	1 3 6	2 1 2 9
	B M I	E D 1 3		2 3 8 5	4 6	2 2 8	2 3 3 5
	SLO	C 4 8	E D 1	2 3 8 5	4 6	2 1 3	2 1 3 5
	B M I	S D 1	E D 1 4	2 3 8 5	4 5	2 1 3	2 3 5
	SLO	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5
	NZE	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5
	SLO	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5
	NZE	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5
	SLO	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5
	NZE	C 4 1		2 3 8 5	4 5	2 1 3	2 3 5
	SLO	C 4 1		2 3 8 5	4 5	2 1 3	2 3 5
	NZE	C 4 0		2 3 8 5	4 5	2 1 3	2 3 5

INDEX  
DATA  
ADDRESS



E0212	T0211	170	2450	109	2278	3336
E024	UPPER	171	2249	624	3003	2204
	DI STR	172	2206	265	2193	2447
	ALN	173	2204	15	2353	2357
	C10	174	2447	46	2260	2311
		175	2357	60	2230	2635
	T023	176	2635	55	0002	2447
	T0002	177	2441	21	2394	2491
	T025	178	2497	67	2437	2196
	T024	179	2491	20	2243	2541
	T026	180	2491	67	2243	2296
	T024	181	2311	20	2193	2547
	T026	182	2547	67	0004	2407
	RAM	183	2296	30	2310	2315
	N 0004	184	2547	15	2319	2272
	SRT C22	185	2407	20	2230	2333
	ALO C27	186	2315	60	0008	2201
	STL T021	187	2272	10	0002	2256
E0213	AUP T023	188	2500	34	2168	2369
E027	T 0008	189	2333	10	0002	2475
	E 0002	190	2201	44	2205	2196
	NRZD	191	2256	31	2394	2597
	NZE	192	2365	45	2243	2255
	SLT 0002	193	2475	35	2343	2346
	NTZU T025	194	2280	44	3008	2421
	T 0003	195	2205	20	2166	2276
	SLT 0002	196	2415	11	2525	2299
	T025	197	2371	5	2243	2465
	STL T026	198	2597	15	2243	2396
	RAL C23	199	2647	20	0000	2471
	STL T026	200	2555	6	2575	2328
	NZU E0215	201	2346	44	2394	2446
	OP	202	2196	60	0000	2399
	C24	203	2313	11	2394	2403
	SUP NORM	204	2313	45	2575	2747
	NZU T026	205	2276	6	2243	2797
	RAL T0008	206	2697	30	0008	2465
	STL T026	207	2465	20	2243	2396
	RAU T025	208	2349	60	0000	2349
	STL 0000	209	2349	36	2575	2471
	T028	210	2471	20	2394	2328
	T025	211	2328	21	2394	2747
	RAL T026	212	2747	65	2243	2797
	T028	213	2797	30	0008	2515
	T0008	214	2515	18	2243	2179
	STL T026	215	2179	20	2243	2446
	E0215	216	2446	46	2299	2396
	RAM T025	217	2396	65	2394	2399
	BMI E0217	218	2399	46	2152	2403
	STL T0216	219	2403	15	2243	2847
	ALO T026	220	2152	16	2243	2847
	STL UPPER	221	2847	20	3002	2306
	LDD C14	222	2369	21	3002	2113
	UPPER	223	2625	69	2322	2625
		224	2625	4	3003	2356

E0211	226	2356	24	3002	2113
E0216	227	2299	60	2202	2457
E0216				2201	2113
UNPCK					

E 0216  
 E 029  
 E 0215  
 2039  
 E 391  
 E 392  
 E 393  
 E 394  
 2034  
 E 341

STL UPPER  
 STU LOWER  
 LDU C14  
 STD UPPER

Code	Label	UNPCK	FMP	FDV
E 0216	STD			
E 0216	RAU			
E 0216	AUP			
E 0216	C26			
E 0216	8001			
E 0216	CDATA			
E 0216	DISIR			
E 0216	0008			
E 0216	SLT			
E 0216	NZU			
E 0216	T021			
E 0216	T022			
E 0216	UPPER			
E 0216	0008			
E 0216	SLT			
E 0216	NZU			
E 0216	T023			
E 0216	8002			
E 0216	RAM			
E 0216	T022			
E 0216	C26			
E 0216	T026			
E 0216	STL			
E 0216	E0215			
E 0216	E0215			
E 0216	T021			
E 0216	T023			
E 0216	T023			
E 0216	0005			
E 0216	E392			
E 0216	T391			
E 0216	T026			
E 0216	STL			
E 0216	C23			
E 0216	STL			
E 0216	E0215			
E 0216	BMI			
E 0216	RALT			
E 0216	T391			
E 0216	0001			
E 0216	0003			
E 0216	SRD			
E 0216	NZE			
E 0216	SLT			
E 0216	NZU			
E 0216	SRT			
E 0216	0001			
E 0216	T025			
E 0216	T026			
E 0216	T023			
E 0216	T026			
E 0216	E0215			
E 0216	T025			
E 0216	CDATA			
E 0216	DISIR			
E 0216	0008			
E 0216	SLT			
E 0216	NZU			
E 0216	T021			
E 0216	T022			
E 0216	UPPER			
E 0216	0008			
E 0216	SLT			
E 0216	NZU			
E 0216	T023			
E 0216	T024			
E 0216	T021			
E 0216	0000			
E 0216	T341			
E 0216	T023			
E 0216	RAU			
E 0216	SCT			
E 0216	STL			
E 0216	T023			
E 0216	RAU			

16  
 20  
 21  
 69  
 24

2152  
 2847  
 2306  
 2369  
 2625

221  
 222  
 223  
 224  
 225

2243  
 2003  
 3002  
 2113  
 2565  
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 2231  
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 2299  
 2523  
 0001  
 0003  
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 0001  
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 0003  
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 2281  
 2488  
 2607  
 2725  
 2369  
 2483  
 2279  
 2279  
 2483  
 2390  
 2333  
 2405  
 2312





E 151  
 BMI LDD UPPER E 152  
 LDD UPPER REST  
 ALO LOWER E 152

E 152	2016	ALO	C DATA	E 103	SLO	338	2479	15	3011	2715
		RAU	UPPER	E 161		339	2016	16	3002	2308
		MPY	LOWER	REST		340	2308	19	3002	2827
E 161		BMI	E 162	E 103	AML	341	2823	46	2576	2877
		LDD	UPPER	REST		342	2877	69	3003	2358
		RAU	LOWER	E 171		343	2358	69	3002	2529
E 162	2017	ALO	C DATA	E 172		344	2529	15	3011	2715
		SLO	UPPER	E 103		345	2715	16	3003	2408
		RAU	LOWER	REST		346	2408	69	3002	2873
		MPY	E 172	E 103		347	2873	19	2626	2927
		BMI	UPPER	REST		348	2626	46	2579	2300
		LDD	LOWER	E 181		349	2458	69	3003	2458
E 171		RAU	C DATA	E 103		350	2579	60	3002	2579
		ALO	UPPER	REST		351	2579	15	3011	2715
E 172	2018	RAU	LOWER	E 182		352	2018	17	3003	2715
		MPY	E 181	E 103		353	2923	69	3002	2923
		BMI	UPPER	REST		354	2676	19	3003	2977
		LDD	LOWER	E 191		355	2676	46	2676	2300
E 181		RAU	C DATA	E 192		356	2977	69	3003	2558
		ALO	UPPER	E 103		357	2558	60	3011	2715
E 182	2019	RAU	LOWER	REST		358	2629	18	3002	2629
		MPY	E 191	E 103		359	2629	60	3011	2715
		BMI	UPPER	REST		360	2019	69	3003	2608
		LDD	LOWER	E 192		361	2608	19	2726	2428
E 191		RAU	C DATA	E 202		362	2726	69	2679	2300
		ALO	UPPER	UNPCK		363	2428	60	3003	2658
E 192	2020	RAU	LOWER	E 192		364	2658	60	3002	2679
		MPY	E 202	E 103		365	2679	15	3011	2715
		BMI	UPPER	REST		366	2020	69	3009	2463
		LDD	LOWER	E 202		367	2463	69	2266	2419
E 201	2021	RAU	C DATA	UNPCK		368	2478	22	2374	2478
		ALO	UPPER	E 202		369	2478	69	3002	2374
		MPY	LOWER	E 202		370	2800	24	3001	2113
		BMI	C DATA	E 202		371	2021	65	3009	2513
		LDD	UPPER	E 202		372	2513	69	2266	2469
		RAU	LOWER	E 202		373	2469	69	3003	2374
E 202		MPY	E 202	E 202		374	2528	69	2374	2528
		BMI	UPPER	E 202		375	2022	65	3009	2563
		LDD	LOWER	E 202		376	2563	69	2374	2519
E 201	2021	RAU	C DATA	E 202		377	2519	69	3002	2708
		ALO	UPPER	E 202		378	2578	69	3001	2374
		MPY	LOWER	E 202		379	2708	69	3009	2613
		BMI	C DATA	E 202		380	2023	69	2366	2519
		LDD	UPPER	E 202		381	2613	69	3009	2663
		RAU	LOWER	E 202		382	2024	69	2374	2569
E 203		MPY	E 202	E 202		383	2663	69	2374	2628
		BMI	UPPER	E 202		384	2569	69	3001	2374
		LDD	LOWER	E 202		385	2628	69	3009	2713
E 204		RAU	C DATA	E 301		386	2030	69	2466	2619
		ALO	UPPER	REST		387	2713	69	2374	2678
		MPY	LOWER	E 302		388	2678	69	3003	2424
E 202		BMI	C DATA	E 302		389	2424	69	2374	2778
		LDD	UPPER	REST		390	2778	69	3002	2424
		RAU	LOWER	E 302		391	2424	69	2374	2778
E 202		MPY	C DATA	E 302		392	2778	69	3002	2424
		BMI	UPPER	REST		393	2424	69	2374	2778
		LDD	LOWER	E 302		394	2778	69	3002	2424



E 522	AL0	IRB	UNPK	445	2878	65	8001	2671	2891
	LDD	8003		446	2891	15	3006	2674	2361
	SIA	IRB	UNPK	447	2361	69	8003	3009	2268
	RAL	DATA	E532	448	2258	23	3006	3009	2113
				449	2053	65	3009	3009	2264
E 532	SLO	C25		450	2664	16	2666	2666	2671
	BMI	DATA	E 531	451	2671	46	2674	2674	2776
	RSL	DATA		452	2314	66	3009	3009	2314
	SRL	0004		453	2776	30	3011	3011	2891
	RAL	DATA		454	2058	66	3011	3011	2815
	SLO	C25	A X C	455	2364	65	3009	3009	2364
	BMI	8001		456	2721	16	2666	2666	2826
	AL0	0004		457	2721	46	2724	2724	2431
	SRT	0004		458	2431	15	8001	8001	2431
	RAL	CDATA		459	2826	30	0004	0004	2941
	LDD	8003		460	2865	69	3011	3011	2865
	SIA	T581		461	2472	65	8003	8003	2472
	RAL	8001		462	2729	23	2876	2876	2729
	AL0	IRC		463	2941	65	8001	8001	2941
	LDD	8003		464	2411	15	3007	3007	2411
	SIA	DATA	S X C	465	2318	69	8003	8003	2318
	RAL	DATA		466	2059	23	3007	3007	2113
	SLO	C25		467	2414	65	3009	3009	2414
	BMI	DATA		468	2771	46	2666	2666	2771
	RSL	0004		469	2774	66	2774	2774	2926
	RSL	CDATA		470	2464	66	3009	3009	2464
	RSL	CDATA		471	2866	30	0004	0004	2941
	RSU	CDATA		472	2060	60	3011	3011	2865
	RSU	CDATA	RAU	473	2060	60	3011	3011	2715
	RAU	UPPER	RSU	474	2061	61	3011	3011	2715
	MPY	LOWER	DVR	475	2064	60	3003	3003	2908
	BMI	E642		476	2908	60	3002	3002	2828
	LDD	UPPER		477	2824	46	2928	2928	2978
	AL0	LOWER		478	2928	69	2481	2481	2300
	DVR	CDATA		479	2978	60	3003	3003	2958
	RAL	CDATA		480	2481	15	3011	3011	2715
	RSL	CDATA		481	2065	65	3011	3011	2715
	RAM	CDATA		482	2066	65	3011	3011	2715
	RAM	CDATA		483	2066	66	3011	3011	2715
	RSM	CDATA		484	2067	66	3011	3011	2715
	LDD	CDATA		485	2068	68	3011	3011	2514
	STD	DISTR	UNPK	486	2069	69	3011	3011	2514
	RAL	DATA		487	2514	24	3001	3001	2564
	AL0	C31		488	2070	65	3009	3009	8002
	AL0	DATA		489	2564	15	2267	2267	2614
	AL0	C32		490	2071	15	3009	3009	2614
	RAL	DATA		491	2614	15	2317	2317	2664
	RAL	DATA		492	2080	15	3005	3005	2963
	STU	IRA		493	2664	21	3009	3009	2714
	STU	IRA		494	2081	65	3009	3009	2114
	STU	DATA		495	2714	21	3005	3005	2764
	RAL	DATA		496	2082	15	3009	3009	2214
	STU	IRB		497	2764	21	3006	3006	2214
	STU	DATA		498	2083	15	3009	3009	2814
	STU	DATA		499	2814	21	3006	3006	2864
	RAL	DATA		500	2084	69	3009	3009	2170
	LDD	C33		501	2864	62	3367	3367	2779
	SDA	E202		502	2170	25	2374	2374	2459
	RAL	LOWER		503	2779	69	3002	3002	2459
	LDD	LOWER		504	2459	62	3002	3002	2374
	LDU	DISTR		505	2850	60	3002	3002	2313
	STL	LOWER							
E 641	BMI	E642							
E 642	AL0	LOWER							
2065	RAL	CDATA							
2066	RSL	CDATA							
2067	RAM	CDATA							
2068	RSM	CDATA							
2069	LDD	CDATA							
2070	STD	DISTR							
2071	RAL	DATA							
2080	AL0	C31							
2081	AL0	C32							
2082	RAL	DATA							
2083	STU	IRA							
2084	STU	IRA							
	STU	DATA							
	RAL	DATA							
	LDD	C33							
	SDA	E202							
	LDU	DISTR							
E 841	STL	LOWER							







722520000	2400002800	3005	3500082473	2022432346	3100022365	1621932297	1530022479
722590000	462262327	6022302635	6180032160	6580012169	6580012169	1626662671	2023192222
722730000	2023942196	7000002113	23300622113	16221162321	*	46223242325	6022782319
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722870000	2023942196	2025852488	6522922247	* 3500022439	*	1530072211	*
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723080000	1930022823	4523622113	7100002113	* 6022782433	*	6022022457	
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723220000	1030112715	4621002377	3000042841	2330072113	6022302685	3000042891	
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724130000	4622162217	1626662771	2329752878	2021932146	* 2021932146	6780022591	
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724340000	724340000	4522882239	1080012113	2400002113	CR4622782181	692322625	
724410000	2123942497	2023942255	3000042941	2122782181	CR3000042941	16221302135	
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724690000	6924292300	2122782231	6930022374	2328762729	* 4622992396	6923792300	
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725040000	20224372390	3600002405	2022432296	* 1022302333	* 1930022504	4422052280	
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725180000	725250000	6924792300	3500012167	2430012113	* 18225752179	2022432196	
725220000	6023942349	3600002405	2022432296	* 46225242775	* 6530032607	CR4422992600	
725250000	2022432246	7000042407	2022432296	6930032374	CR	CR	
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725670000	725740000		4621862537	* 6923162519	2430012254	CR4426272378	
725740000	725810000		65224322647	4523302369	1730112715	CR	
725880000	725950000		1622022557	6530022708	* * 4622992304	CR	
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