

**Honeywell**

SERIES 6000

GENERAL MACRO ASSEMBLY  
PROGRAMMING (GMAP)  
COURSE CODE 605

STUDENT HANDBOOK

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Order Number: AF52, Rev. 1



**SERIES 6000**

**GMAP PROGRAMMING**

**COURSE 605**

**STUDENT HANDBOOK**

REVISED: 10/1/72

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
1-1	General Course Outline	1
1-2	Course Topic Map	8
2-1	Series 6000 Processor	9
2-2	Basic System	10
2-3	Functional Modularity	11
2-4	Hardware Components	12
2-5	Multidimensional System	13
2-6	Multiprocessor System	14
2-7	Peripheral Subsystems	15
2-8	H-6000 Hardware Components	16
2-9	Multiprocessor System	17
2-10	Series 6000 Characteristics	18
2-11	Program Accessible Registers	19
2-12	Processor Indicators	20
2-13	Software System Major Components	21
2-14	System Software--GCOS	22
2-15	Other System Software	23
2-16	Language Processors	24
2-17	Time-Sharing System	25
2-18	Application Software	25.1
3-1	Coding Form	26
3-2	GMAP Compile (Single Assembly)	27

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
3-3	GMAP Compile (Multiple Assemblies)	28
3-4	GMAP Card Deck Arrangement	29
3-5	Multiple GMAP Assembly - Card Deck	30
3-6	GMAP Control Records - Compile and Execute	31
3-7	Executing Object Programs	32
3-8	Compile and Execute with Object Decks	33
3-9	Compile and Execute - Deck Example	34
4-1	Word Formats	35
4-2	Single/Double Precision	36
4-3	Instruction Precision	37
4-4	Functional Groups of Pseudo-Operations	38
4-5	Storage Allocation Pseudo Operations	39
4-6	Binary Floating Point	40
4-7	Number Conversion Technique	41
5-1	Types of Instructions	43
5-2	Load and Store	44
5-2.1	Load/Store Sample Coding	44.1
5-2.2	LDAC Instruction	44.2
5-3	Address Modification	45
5-4	Indexing	46
5-5	Three Phases of an Instruction	47
5-6	Direct Operand	48
5-7	Effective Address	49

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
6-1	Literals and Data Generating Pseudo Ops	50
6-2	Decimal Literals	51
6-3	Decimal Values	52
6-4	Floating Point Values	53
6-5	Octal Literals	54
6-6	Octal Values	55
6-7	Hollerith Data	56
6-8	Instructional Literal	57
6-9	ARG Pseudo Operation	58
6-10	Variable Field Definition	59
6-11	Literals and Direct Operands	60
6-12	Hollerith Literal and DU	61
6-13	Floating Point Literal (DU)	62
6-14	Fixed Point Literal (DU)	63
6-15	Fixed Point Literal (Example 2)	64
7-1	Store Character (6-bit)	65
7-2	Store Byte (9-bit)	66
7-3	STC1 and STC2	67
7-4	Special Load/Store	68
7-5	Shift Instructions	69
7-6	Fixed Point Arithmetic	70
7-7	Logical Add/Subtract	71

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
7-8	Carry and Overflow Indicators	72
7-9	Special Add/Subtract	73
7-10	Multiply/Divide	74
7-11	Transfer of Control	75
8-1	Assembly Listing	76
8-2	Saving Instruction Counter in Index Register	83.1
8-3	Saving Instruction Counter in Memory	83.2
8-4	Saving Instruction Counter and Indicators (STC1)	83.3
8-5	Saving Instruction Counter and Indicators (STI)	83.4
8-6	Saving Registers, Indicators and Instruction Counter	83.5
8-7	CALL Pseudo Operation	83.6
8-8	CALL Pseudo Operation - Expanded Version	83.7
8-9	SAVE Pseudo Operation - Expanded Version	83.8
8-10	CALL, SAVE and RETURN Pseudo Ops	83.9
8-11	CALL, SAVE, RETURN Coding Example	83.10
9-1	Compare Logic Flow	84
9-2	Positive Numbers & Comparison Indicators	85
9-3	Negative Numbers & Comparison Indicators	86
9-4	Comparison Indicators - Positive Register - Negative Memory	87
9-5	Algebraic and Logical Comparison	88
9-6	Fixed Point Compare	89



COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
9-7	Special Purpose Compares	90
9-8	Boolean Operations Truth Table	91
9-9	Example of AND	92
9-10	Example of OR	93
9-11	Example of Exclusive OR	94
9-12	Boolean Compare Operations	95
9-13	Comparative AND	96
9-14	Comparative NOT AND	97
9-15	Example of Boolean Operations and Boolean Compares	98
10-1	Indirect then Tally Modification	99
10-2	Indirection (I) Example	100
10-3	Indirect Diagram	101
10-4	Indirect Variation Flowchart	102
10-4.1	Tally Word Formats	102.1
10-5	Address Tally Pseudo-Operations	103
10-6	Increment Address Decrement Tally Diagram	104
10-7	Increment Address, Decrement Tally Flowchart	105
10-8	Indexing Example	106
10-9	I and ID Example	107
10-10	Tally Refreshing	108
10-11	Decrement Address, Increment Tally Diagram	109
10-12	Decrement Address, Increment Tally Flowchart	110
10-13	DI Example	111

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
10-14	I, ID, and DI Example	112
10-15	Add Delta (AD) Diagram	113
10-16	Add Delta (AD) Flowchart	114
10-17	Subtract Delta (SD) Diagram	115
10-18	Subtract Delta (SD) Flowchart	116
10-19	AD and SD Example	117
10-20	Character Operations (6-bit) to Register	118
10-21	Character Operations (9-bit) to Register	119
10-22	Character Operations (6-bit) to Memory	120
10-23	Character from Indirect (CI) Diagram	121
10-24	Character from Indirect (CI) Flowchart	122
10-25	Sequence Character (SC) Flowchart	123
10-25.1	Sequence Character Reverse (SCR) Flowchart	123.1
10-25.2	Reversing Table -- Character by Character	123.2
10-26	CI and SC Example	124
10-27	CI and SC Example No. 2	125
10-28	Check Protection and Character Insertion	126
10-29	IDC Diagram	127
10-30	DIC Diagram	128
11-1	Repeat Instruction Format	129
11-2	Repeat Exit Conditions	130
11-3	Rules for Repeat Instructions	131

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT</u> <u>REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
11-4	Address Modification of Repeated Instruction	132
11-5	Coding Example Using Repeat	133
11-6	RPTX and RPDY Diagram	134
11-7	RPTX Coding Examples	135
11-8	Repeat Double Format	136
11-9	Repeat Double Examples	137
11-10	Repeat Link Format and Execution	138
11-11	Key Words and Tables	139
11-11.1	RPL Coding Example	139.1
11-12	Execution of Repeat Link	140
11-13	BCD Instruction Flowchart	141
11-14	Binary to BCD Conversion Constants	142
11-15	BCD and A & Q Registers	143
11-16	Converting Values to 6 Digits	144
11-17	Converting Maximum Values	145
11-18	XED Coding Example	146
12-1	Examples of Pseudo Operations	147
13-1	Macro Prototype and Expansion Call	160
13-2	Macro with Substitutable Arguments	161
13-3	Refreshing Tally Words with Macro Call	162
13-4	Location Field Symbols	163
13-5	Nesting of Macros	164

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
13-6	Macro with Conditional Pseudo Op	165
13-7	Macro with Indefinite Repeat	166
13-8	Macro with Set and Indefinite Repeat	167
14-1	External Interrupts	168
14-2	Internal Interrupts	169
15-1	Levels of Input/Output	170
16-1	File and Record Control (GFRC) General Loader (GLOAD)	183
16-2	\$ TAPE File Card	184
16-3	Mass Storage File Card	185
16-4	SYSOUT and DATA File Cards	186
16-5	Media Conversion	187
16-6	Logical and Physical Records	188
16-7	Variable Add Fixed Length Records	189
16-8	Partitioned Records	190
16-9	File Control Block Arguments	191
16-10	File Designator Word	192
16-11	Call OPEN and FILCB	193
16-12	Call CLOSE and FILCB	194
16-13	GET Logical Record	195
16-14	PUT (WRITE) Logical Record	196
16-15	Initializing Editing Parameters	197
16-16	Write (CALL/WTRMC) Logical Record	198
16-17	Write (CALL PRINT) Logical Record	199
16-18	Write (CALL PUNCH) Logical Record	200

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
16-19	Physical Record Processing - Read Sequential Files	201
16-20	Physical Record Processing - Read Random Files	202
16-21	Physical Record Processing - Write Sequential Files	203
16-22	Physical Record Processing - Write Random Files	204
16-23	File Control Block Contents	205
16-24	File and Record Control (GFRC) Examples - Program Listing	206
16-25	Floating Load/Store Operations	228
17-2	Floating Load/Store Mnemonics	229
17-3	Loading Exponent Register	230
17-4	Floating Point Arithmetic Mnemonics	231
17-5	Floating Point Multiply and Divide	232
17-6	Floating Point Compare Mnemonics	233
17-7	Examples of Floating Point Numbers in Memory	234
17-8	Examples of Negative Values in Memory	235
17-9	Mixed Values in Memory	236
19-1	Register then Indirect Flowchart	237
19-2	Register then Indirect Modification	238
19-3	RI Coding Examples	239
19-4	Summary of Register Indirect (RI)	240
19-5	Algorithms: Register then Indirect (RI)	241
19-6	Indirect then Register Flowchart	242
19-7	Indirect then Register Modification	243

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
19-8	Summary of Indirect Register (IR)	244
19-9	Algorithms: Indirect then Register (IR)	245
19-10	IR and RI Modification	246
19-11	Coding Examples 1 to 3	247
19-12	Coding Examples 4 to 6	248
19-13	Coding Examples 7 and 8	249
20-1	Fixed-Point Data Formats	250
20-2	Floating Point Data Formats	251
20-3	Alphanumeric Data Formats	252
20-4	Decimal Formats for EIS	252.1
20-5	Floating Point Decimal Formats	253
20-6	Decimal Sign Position Formats	253.1
20-7	Operation Code Map for EIS	253.2
20-8	Operation Code Map (cont.)	253.3
20-9	EIS Multi-word Instructions	253.4
20-10	Single Word Instructions	253.5
20-11	Micro Operations	253.6
20-12	Operand Descriptors	253.7
20-13	EIS Address Modification	253.8
20-14	Indirect Word	253.9
20-15	Address Registers	253.91
20-16	Address Modification Flowchart	253.92

COURSE NAME: GMAP PROGRAMMING  
COURSE CODE: 605

STUDENT HANDBOOK  
TABLE OF CONTENTS

<u>HANDOUT REFERENCE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
21-1	Assembly Listing	254
21-2	Common System Files	285

Course Name: GMAP Programming  
Course Code: 605

### General Course Outline

#### I. INTRODUCTION

- A. Introduction of instructor, students, course
- B. Class Administration
- C. Course Outline

#### II. OVERVIEW OF 6000

- A. Hardware Overview
- B. Software Overview
- C. General Characteristics of Processor

#### III. PROGRAM ORGANIZATION

- A. GMAP Coding Form and Symbolic Card Format
- B. Programming Conventions
- C. Submitting GMAP Programs
- D. Typical Deck Setups
- E. Requirements for Submitting GMAP Programs

#### IV. DATA REPRESENTATION

- A. Internal Data Representation
- B. Processor Handling of Information
- C. Instruction Formats
- D. Data Word Formats
- E. Introduction to Pseudo Operations
  - 1. Definition
  - 2. Functional Groups



- F. Storage Allocation Pseudo Operations
- G. Data Generation Pseudo Operations
- H. Summary

V. INTRODUCTION TO INSTRUCTIONS

- A. Format
- B. Types
- C. Data Movement
- D. Introduction to Address Modification
- E. Using Address Modification
- F. Direct Operands
- G. Effective Address Instructions
- H. Store Register Instructions

VI. LITERALS

- A. Introduction
- B. Decimal Literals
- C. Octal Literals
- D. Alphanumeric Literals
- E. Instruction Literals
- F. Variable Field Literals
- G. Literals Modified by Direct Operand

VII. INSTRUCTION REPERTOIRE

- A. Review
- B. Instructions to be Discussed
- C. Miscellaneous Store Instructions
- D. Register Load and Store

- E. Shift Instructions
- F. Arithmetic Operations In 6000
- G. Fixed Point Arithmetic
- H. Fixed Point Instructions
- I. Transfer of Control

#### VIII. LISTINGS AND LINKAGE

- A. Contents
- B. Introduction to Program Linkage
  - 1. Subroutines
  - 2. Subprograms
- C. Program Linkage
- D. CALL Pseudo Operation
- E. SAVE and RETURN

#### IX. COMPARE AND LOGICAL INSTRUCTIONS

- A. Introduction to Compare Instructions
- B. Basic Compare Instructions
- C. Boolean Functions
- D. Boolean Analogy
- E. Boolean Instructions
- F. Other Boolean Operations
- G. Comprehensive Example

#### X. TALLY WORDS AND ADDRESS MODIFICATION

- A. Four Types of Modification
- B. Indirect then Tally Variations
- C. Indirect Addressing
- D. Tally Pseudo Operations

- E. Increment Address, Decrement Tally
- F. Refreshing Tally Words
- G. Decrement Address, Increment Tally
- H. Add Delta and Subtract Delta Modification
- I. Character Operations
- J. Other Variations of IT Modification

#### XI. MISCELLANEOUS INSTRUCTIONS

- A. Introduction
- B. Repeat Instructions - Introduction
- C. Repeat (RPT)
- D. RPTX Instruction
- E. Repeat Double (RPD)
- F. Repeat Link (RPL-RPLX)
- G. BCD Instruction
- H. XEC and XED Instructions
- I. Master Mode Entries

#### XII. PSEUDO OPERATIONS

- A. General Description
- B. Functional Groups
- C. Control Pseudo Operations
- D. Location Counter Pseudo Operations
- E. Symbol-Defining Pseudo Operations
- F. Data Generating Pseudo Ops
- G. Storage Allocation
- H. Conditional Pseudo Operations
- I. Special Word Format

XIII. MACRO OPERATIONS

- A. Introduction
- B. Defining the Macro Prototype
- C. Using the Macro Prototype
- D. Arguments for Macros
- E. Created Symbols
- F. Nesting of Macros
- G. Pseudo Operations within Macros
- H. System Macros

XIV. EXTERNAL AND INTERNAL INTERRUPTS

- A. Execution of Interrupts
- B. Internal Interrupts
- C. Categories of Internal Interrupts
- D. Master Mode Entry Routines
- E. MME's for Activity Termination
- F. MME GESNAP

XV. INTRODUCTION TO INPUT/OUTPUT

- A. Levels of Input/Output in 6000
- B. Necessary Ingredients of I/O Coding
  - 1. Logical File Designator
  - 2. Input/Output Buffer
  - 3. Data Control List
  - 4. Input/Output Command

XVI. File and Record Control (GFRC)

- A. Introduction to Input/Output
- B. Introduction to File and Record Control (GFRC)

- C. File Control Records
- D. Standard System Formay
- E. Logical Record Formats
- F. File Control Block
- G. File Designator Word
- H. File and Record Control (GFRC) Calling Sequences
- I. Functional Groups of Calling Sequences
- J. Calling Sequences for File Preparation
- K. Logical Record Processing
- L. Input/Output Editor
- M. Physical Record Processing

XVII. FLOATING POINT

- A. Introduction
- B. Purpose
- C. Load and Store
- D. Arithmetic Instructions
- E. Miscellaneous Instructions
- F. Compare Instructions
- G. Summary of Arithmetic Operations

XVIII. REVIEW OF INSTRUCTIONS

- A. Data Movement
- B. Fixed-Point Arithmetic
- C. Boolean Operations
- D. Comparison
- E. Transfer of Control
- F. Miscellaneous Operations

XIX. ADDRESS MODIFICATION - REVIEW AND EXTENSION

- A. Review of Modification Capabilities
- B. Register Modification
- C. Indirect then Tally Modification
- D. Register then Indirect Modification
- E. Indirect then Register Modification
- F. Coding Examples

XX. EXTENDED INSTRUCTION SET

- A. Introduction
- B. Data Word Formats
- C. Instruction Word Formats
- D. Multi-word Instructions
- E. Single Word Instructions
- F. Micro Operations

XXI. ANALYSIS OF LISTINGS

- A. Review
- B. Files Produced by System Input (GIN)
- C. Accounting Information
  - 1. Job Information
  - 2. Activity Information
- D. Assembly Listing
- E. Memory Map

605 COURSE TOPIC MAP

Week 1

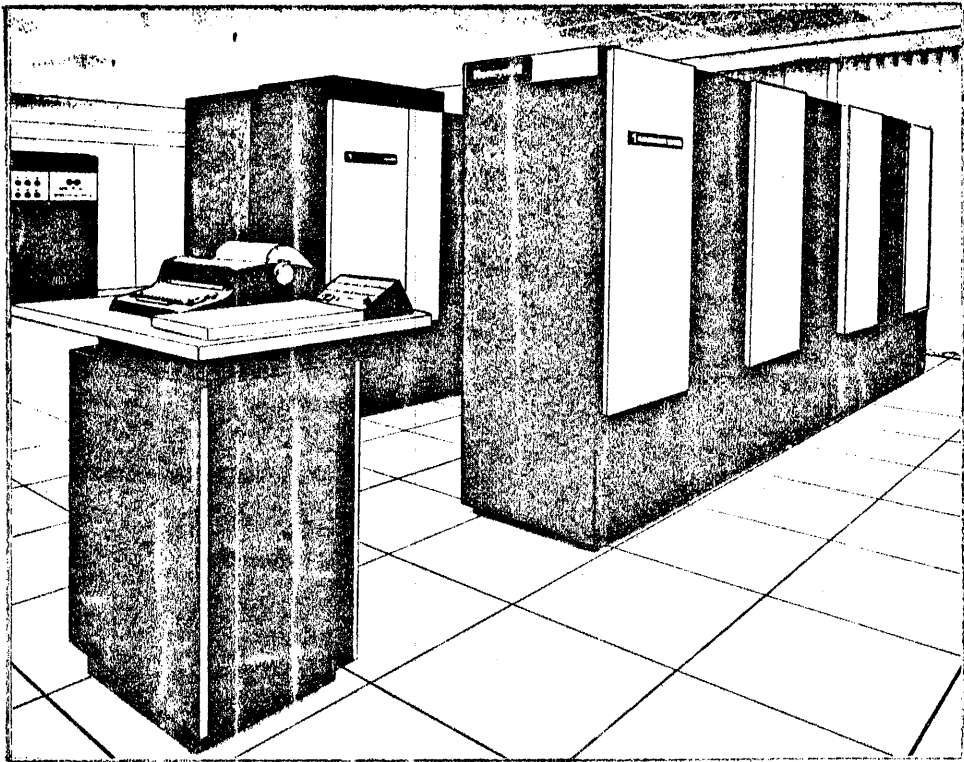
	M	T	W	T	F
A.M.	Introduction	Data Representation	Instruction Repertoire	Compare and Logical Operations	Miscellaneous Instructions
P.M.	Overview ↓ Program Organization	Introduction to Instructions Literals	Listings and Linkage	Tally Words and Address Modification	Pseudo Operations

Week 2

	M	T	W	T	F
A.M.	Macro Operations	File and Record Control (GFRC)	Floating Point	Extended Instruction Set	Final Exam
P.M.	Interrupts Introduction to Input/Output	↓	Review of Instructions Address Modification RI & IR	Analysis of Listings	Review of Exam

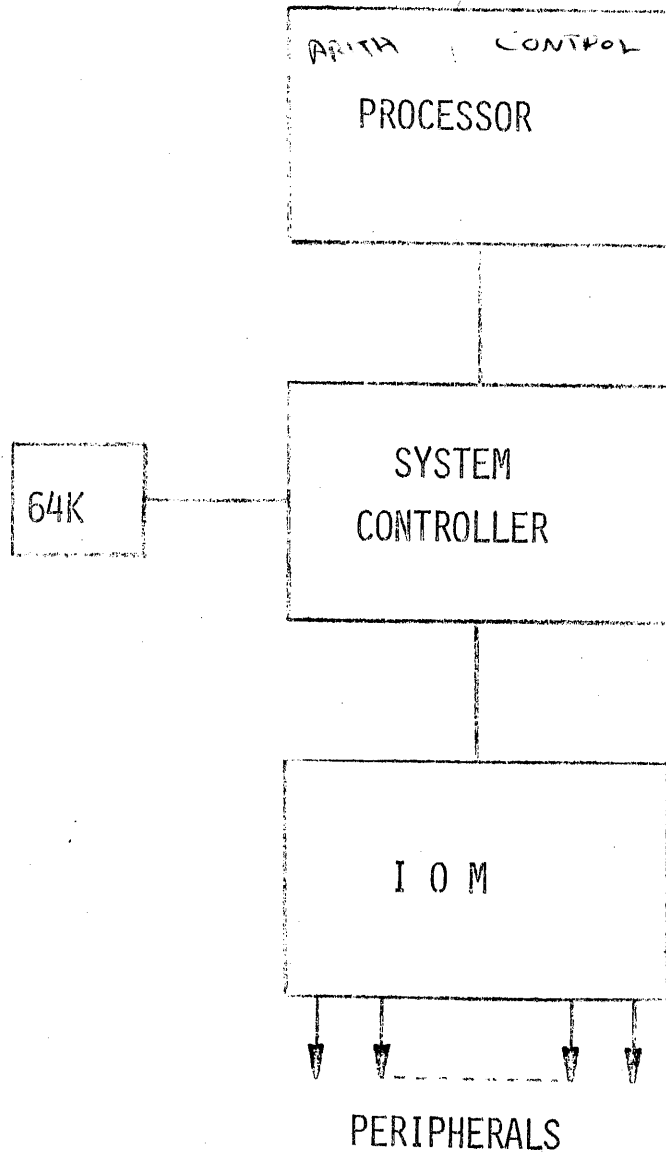
# Honeywell

## SERIES 6000

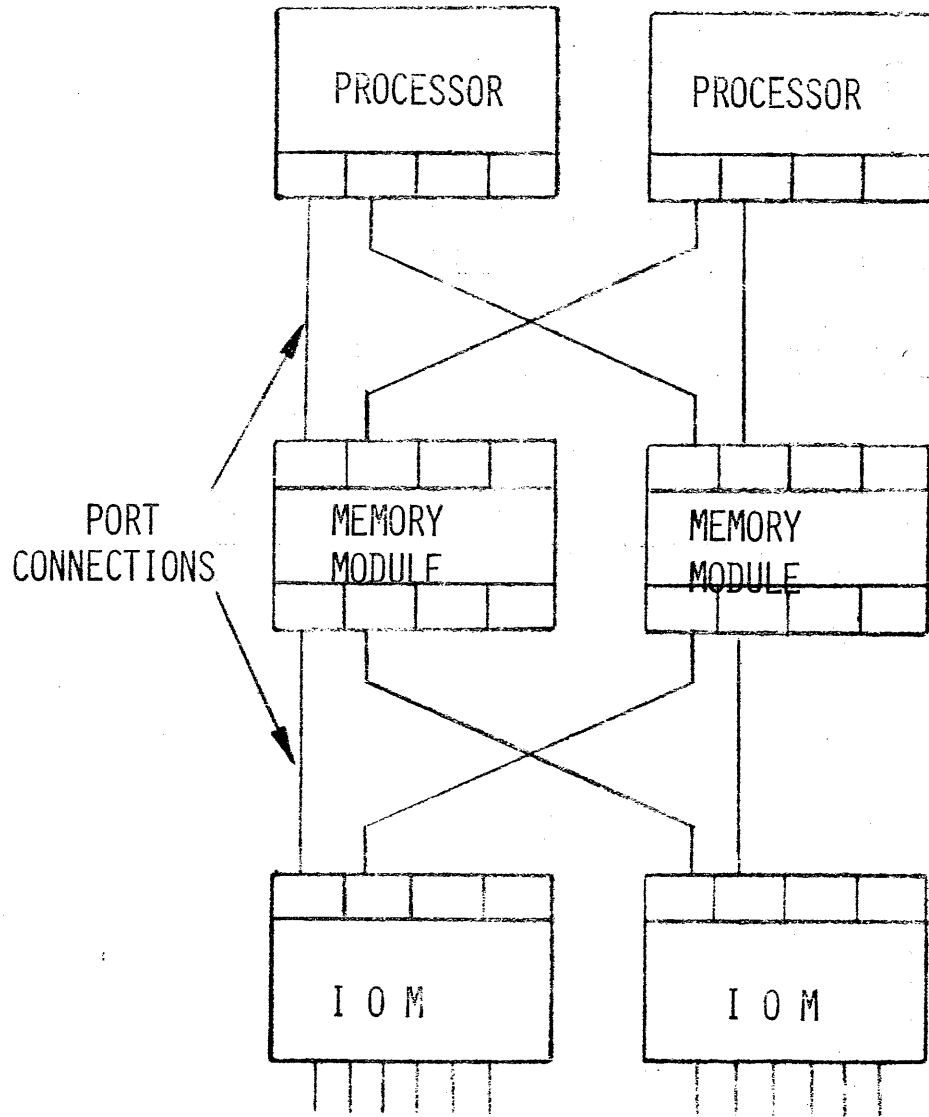




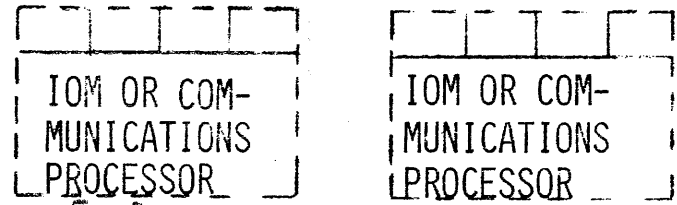
# BASIC SYSTEM



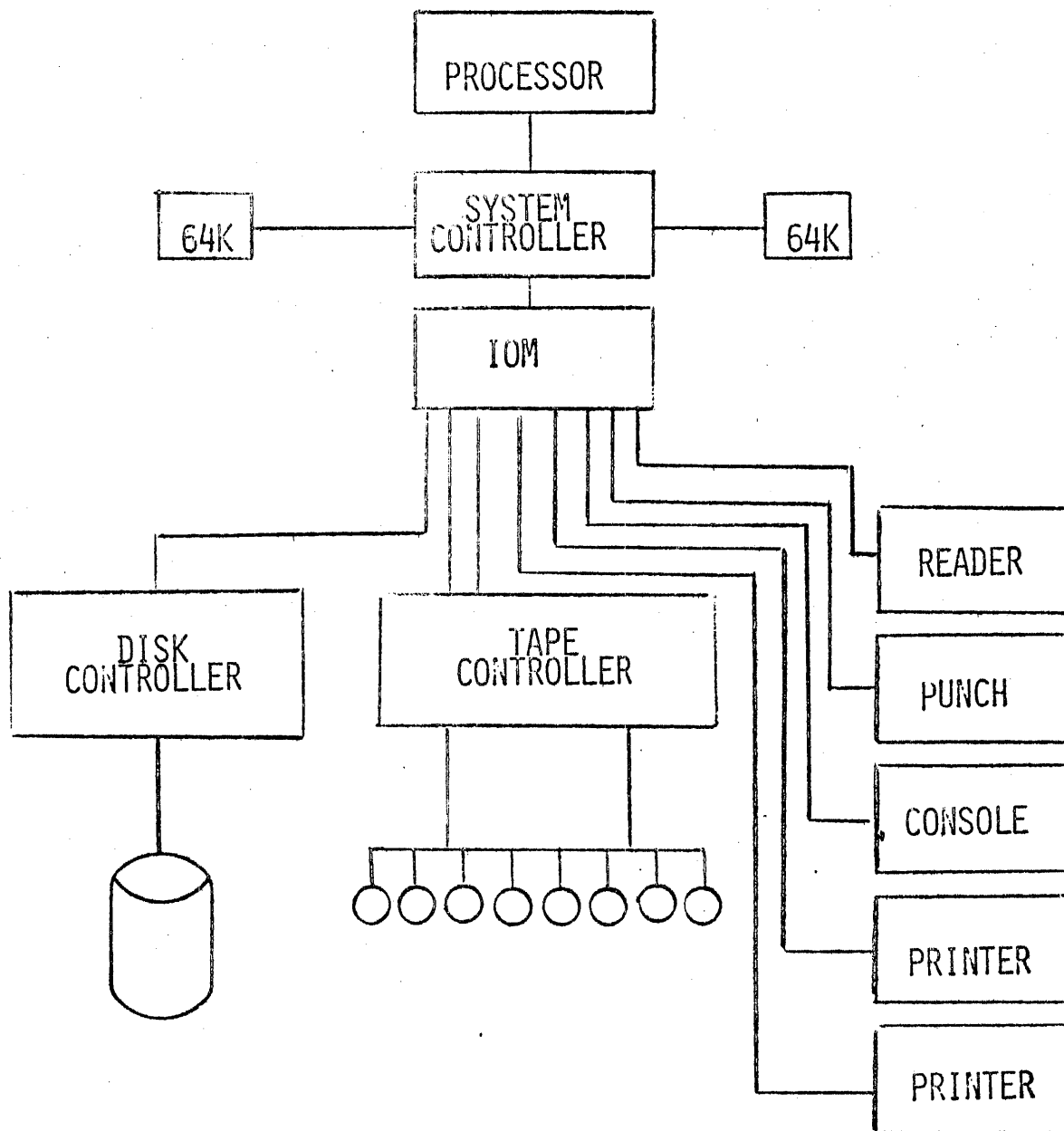
# FUNCTIONAL MODULARITY



CONNECTIONS ARE MADE FROM EVERY MEMORY MODULE TO EVERY ACTIVE MODULE (IOM, PROCESSOR, OR COMMUNICATIONS PROCESSOR).

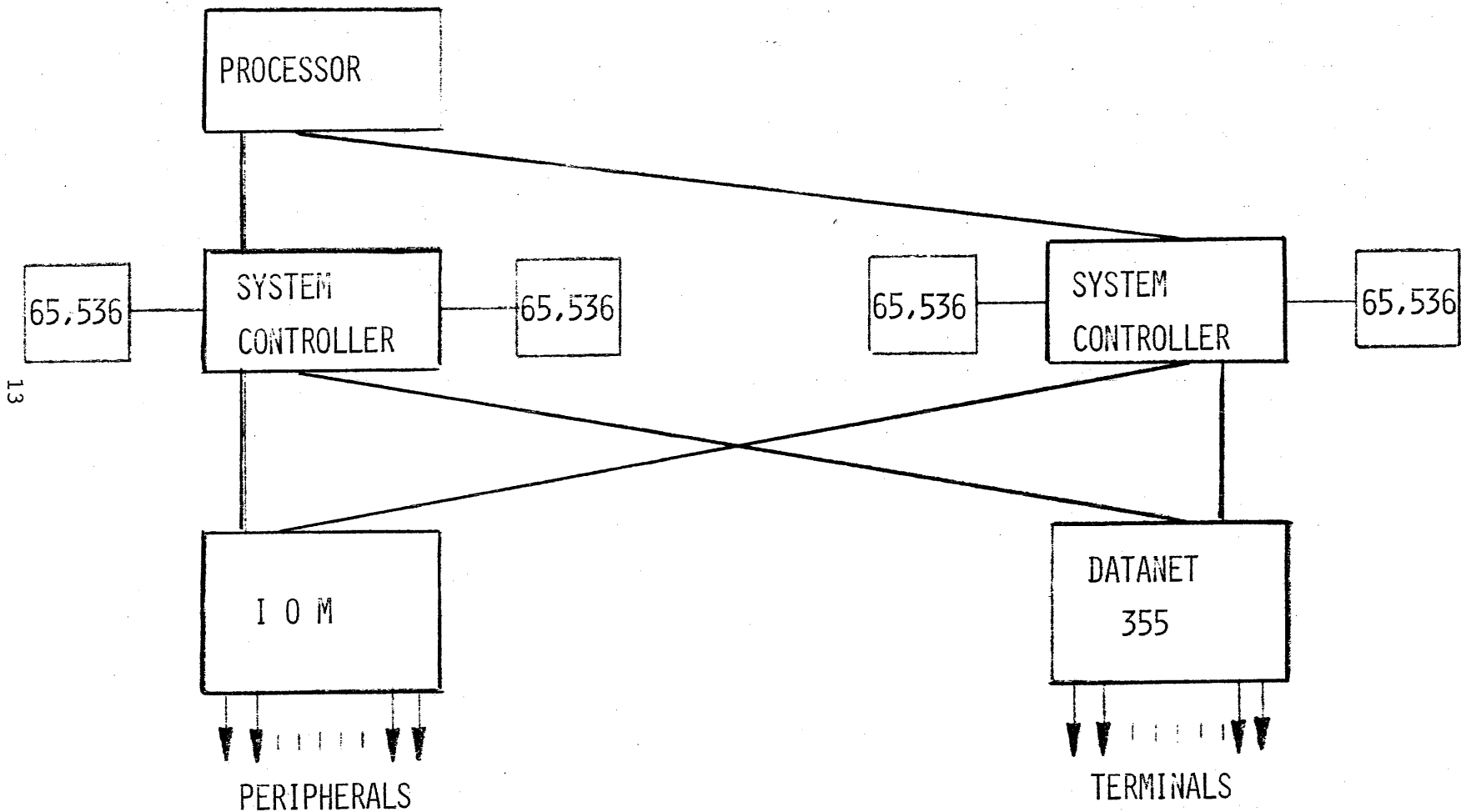


SERIES 6000

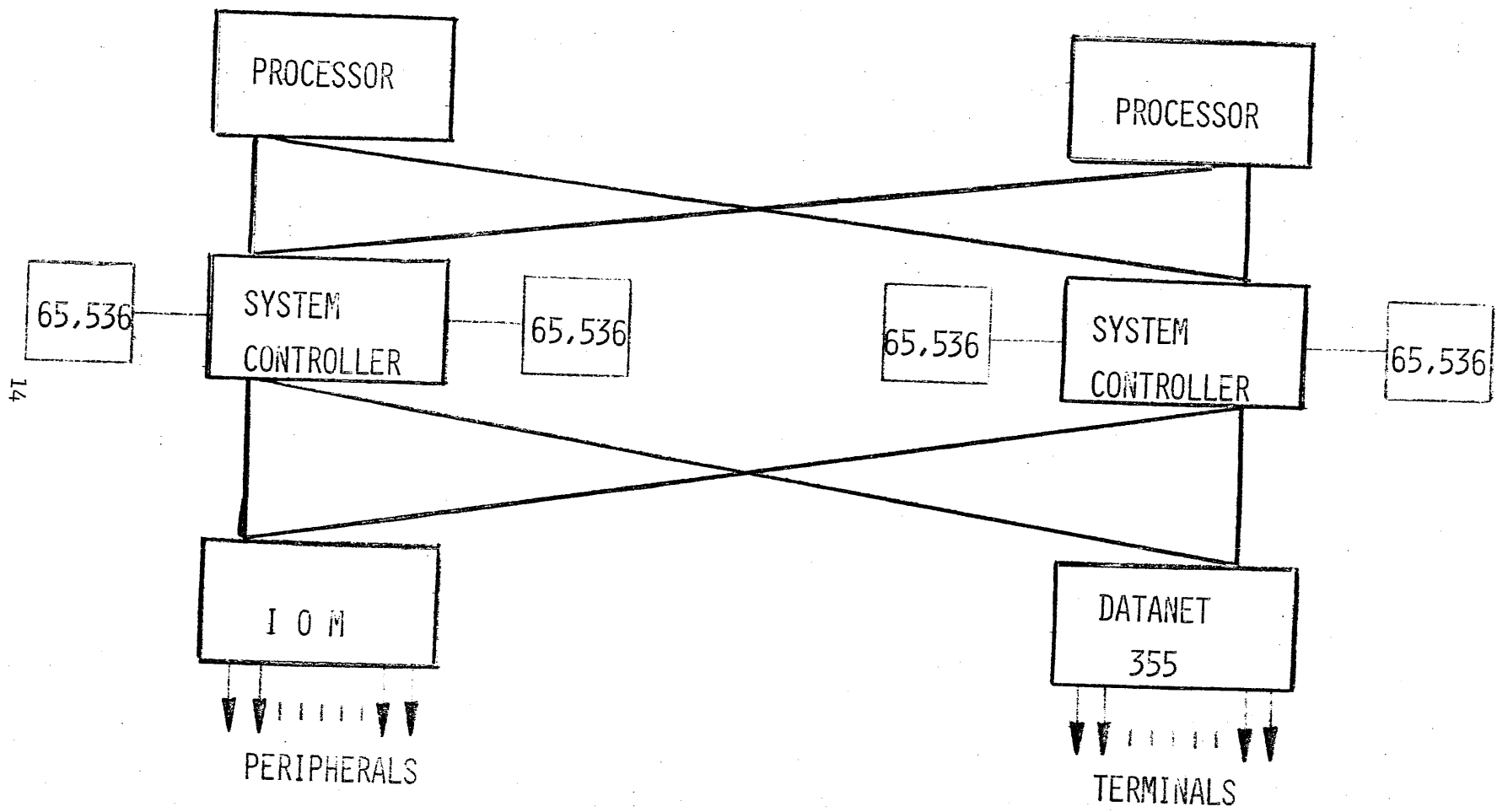


HARDWARE COMPONENTS

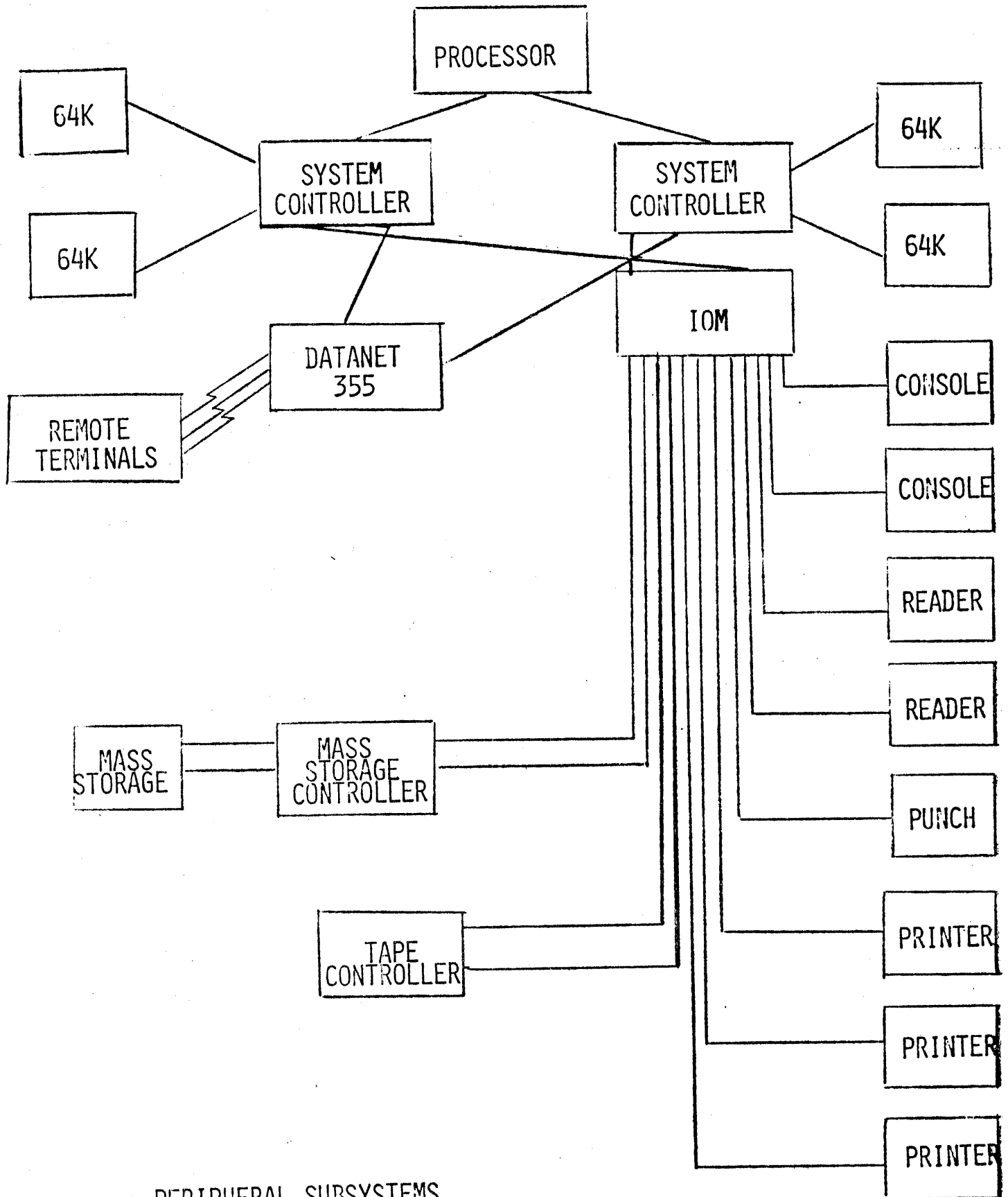
# MULTIDIMENSIONAL SYSTEM



# MULTIPROCESSOR SYSTEM



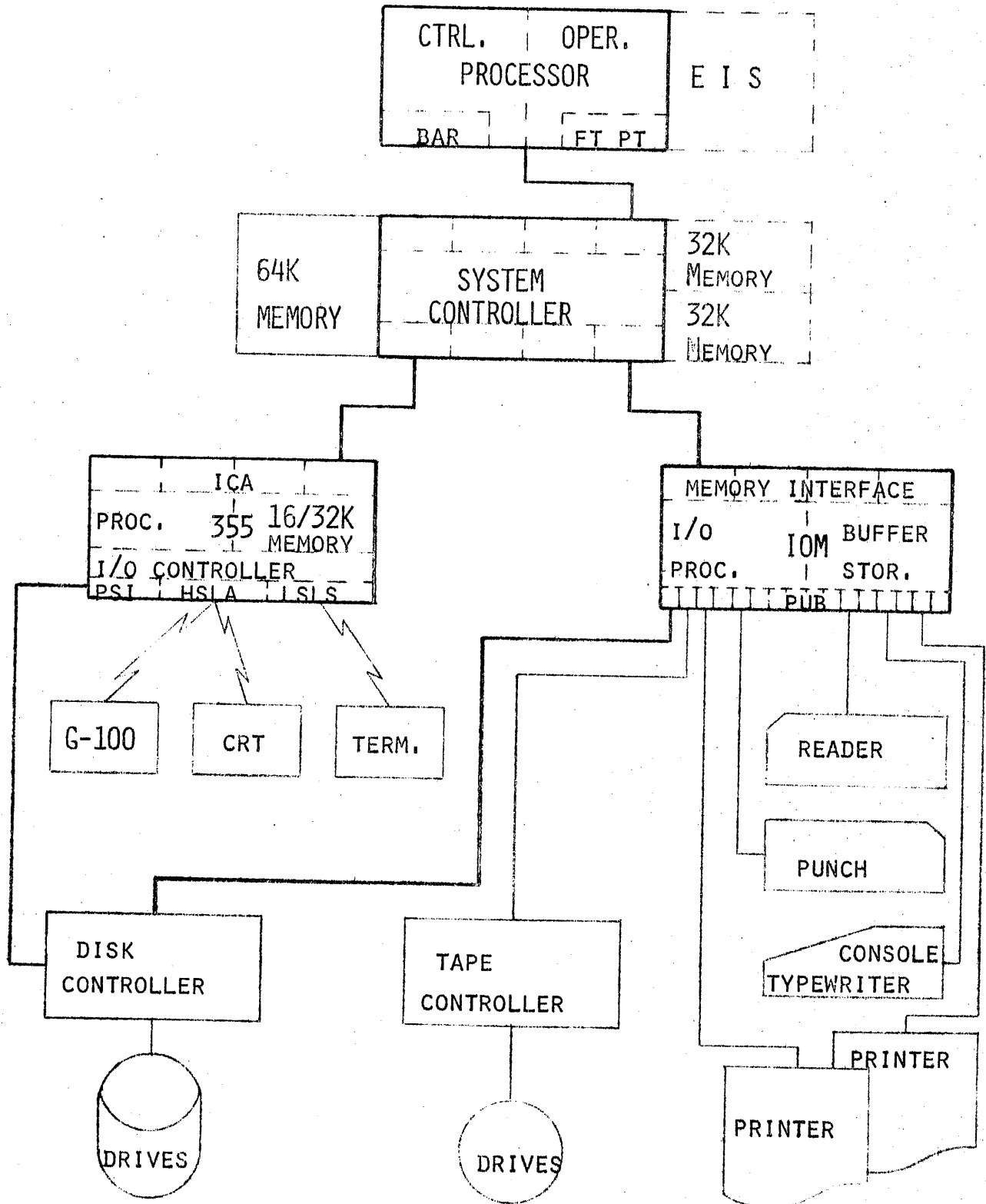
SERIES 6000



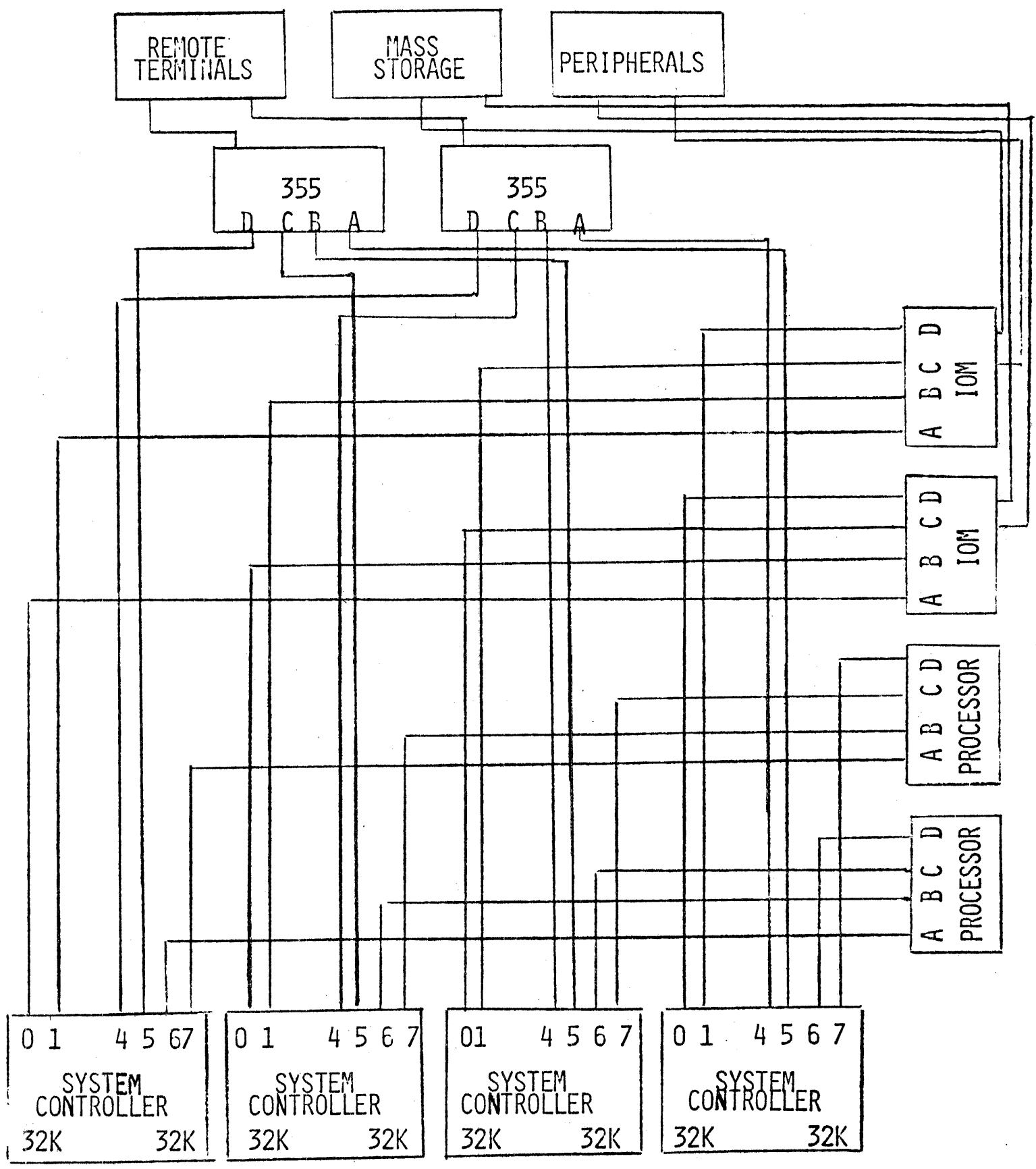
PERIPHERAL SUBSYSTEMS

# H-6000 HARDWARE COMPONENTS

STUDENT HANDBOOK  
REFERENCE NO. 2-8



SERIES 6000



MULTIPROCESSOR SYSTEM



## SERIES 6000 CHARACTERISTICS

	MODEL 6030/6040	MODEL 6050/6060	MODEL 6070/6080
MEMORY SIZE IN WORDS (36-BIT WORD)	65,536 - 131,072	98,304 - 524,288	131,072 - 1,048,576
CYCLE TIME (MICROSECONDS PER TWO WORDS)	1.2	1.2	0.5
NO. OF DATA CHANNELS	16	24	24
MAX. TRANSFER RATE PER IOM (CHARS/SEC.)	1.3M	3.7M	6.0M
PERIPHERAL CAPACITY (SUBSYSTEMS)	16	24	24
INTERLEAVING	No	2&4-WAY	2&4-WAY
INSTRUCTION OVERLAPPING	No	Yes	Yes
INSTRUCTIONS PER SECOND (MAX.)	340,000	550,000	1,400,000

PROGRAM ACCESSIBLE REGISTERS

0 35 A REGISTER

0 35 Q REGISTER

0 A 35 Q 71 ACCUMULATOR

0 7 E REGISTER (FLOATING POINT EXPONENTS)

0 E 8 A 35  
 EXP MANTISSA E/A REGISTER (FLOATING POINT)

0 E 8 A 35 Q 71  
 EXP MANTISSA E/A/Q REG.

0 23 26 TIMER REGISTER

0 17 INSTRUCTION COUNTER

0 17 INDEX REGISTERS (0-7)

0 8 17 BAR 0 0 WORD 0 17 19 23 CH BIT ADDR. REG.

# INDICATOR REGISTER

STUDENT HANDBOOK  
REFERENCE NO. 2-12

NOT USED	INDICATOR BITS		NOT USED	
0	1	1	3	3
	7	8	0	1
			3	5
		18		
		19		
		20		
		21		
		22		
		23		
		24		
		25		
		26		
		27		
		28		
		29		
		30		

		ZERO		
		NEGATIVE		
		CARRY		
		OVERFLOW		
		EXP. OVERFLOW		
		EXP. UNDERFLOW		
		OVERFLOW MASK		
		TALLY RUNOUT		
		PARITY ERROR		
		PARITY MASK		
		MASTER MODE		
		TRUNCATION (EIS)		
		MULTIWORD INSTRUCTION (EIS)		
		INTERRUPT		

H-6000 SOFTWARE SYSTEM MAJOR COMPONENTS

OPERATING SYSTEM (GCOS)

OTHER SYSTEM SOFTWARE

LANGUAGE PROCESSORS

TIME-SHARING SYSTEM

APPLICATIONS SOFTWARE

## SYSTEM SOFTWARE

### OPERATING SYSTEM - GCOS

#### GENERAL COMPREHENSIVE OPERATING SUPERVISOR

#### GENERAL CAPABILITIES

MULTIDIMENSIONAL MODES OF PROCESSING

LOCAL BATCH PROCESSING

REMOTE BATCH PROCESSING

REMOTE ACCESS PROCESSING

TIME-SHARING

TRANSACTION PROCESSING

MULTIPROGRAMMING

MULTIPROCESSING

CENTRAL FILE SYSTEM

#### FUNCTIONAL CHARACTERISTICS

RESOURCE MANAGEMENT

JOB SCHEDULING

JOB PRIORITY ALLOCATION

DATA BASE MANAGEMENT

PROGRAM AND DATA FILE SECURITY CONTROL

ON-LINE SYSTEM DEVELOPMENT

EASE OF USE

SYSTEM SOFTWARE

GENERAL LOADER

FILE AND RECORD CONTROL (GFRC)

UTILITY PACKAGE

SYSTEM EDITOR

BULK MEDIA CONVERSION (BMC)

SYSTEM MEDIA CONVERSION

    INPUT MEDIA CONVERSION (GIN)

    OUTPUT MEDIA CONVERSION (SYSOUT)

LANGUAGE PROCESSORS

MACRO ASSEMBLER (GMAP)

FORTRAN COMPILER

COBOL COMPILER

ALGOL COMPILER

JOVIAL COMPILER

MISC. PROCESSORS

SORT/MERGE

IDS

ISP

PROGRAMMING LANGUAGES

BASIC

FORTRAN

ABACUS

REMOTE JOB ENTRY

CARDIN

JOUT

SCAN

RBUG

FILE EDITING, INSPECTION AND MAINTENANCE

EDITOR

RUNOFF

DATA BASIC

ACCESS

IDS DATA QUERY

TIME-SHARING SYSTEM EXTENSION & MAINTENANCE

LODX SUBSYSTEM

TDS

SABT

MASTER

TIME-SHARING LIBRARY

APPLICATION PROGRAMS

FORTRAN LIBRARY GENERATOR AND EDITOR

TSLG

LIBED



## APPLICATION SOFTWARE

LP6000 - LINEAR PROGRAMMING SYSTEM

APT - AUTOMATIC PROGRAMMED TOOLS

PERT/TIME - PROGRAM EVALUATION REVIEW TECHNIQUE/TIME

PERT/COST - PROGRAM EVALUATION REVIEW TECHNIQUE/COST

SIMSCRIPT - SIMULATION LANGUAGE

BMD - BIOMEDICAL STATISTICAL PROGRAMS

MATHPAC - MATHEMATICAL/ENGINEERING PROGRAMS

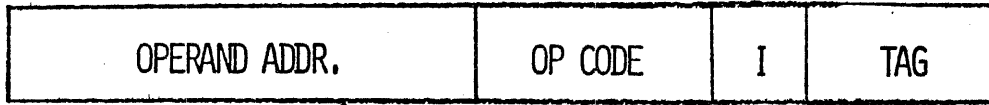
SERIES 6000 PARTS EXPLOSION SYSTEM (GEPEXS)

SERIES 6000 INVENTORY MANAGEMENT SYSTEM (GEIMS)

CEP - CIVIL ENGINEERING PACKAGE

SERIES 6000 TIME SERIES FORECASTING (GECASST)

CODING FORM



LOCATION	E/O	OPERATION	ADDR/MOD	COMMENTS	ID
1	6 7	8 15	16 32		73 80
GO	E	LDA	WORD, 7	THIS IS A SAMPLE	0010
6 CHAR. MAX. (A-Z) (0-9) (.) 1ST CHAR. MUST NOT BE ZERO AND MUST HAVE AT LEAST ONE NON-NUMERIC CHAR.	E Ø 8	INST. MNEMONICS, PSEUDO OPS, MACRO NAMES. NO SPECIAL CHARS. OR BLANKS PERMITTED	VARIABLE LENGTH FIELD. SUBFIELDS SEPARATED BY COMMAS. ABSOLUTE, SYMBOLIC, OR RELATIVE ADDRESSING. ALGEBRAIC- BOOLEAN EXPRESSIONS. OPERATORS, * + - / ALSO =	← OPTIONAL →	

GMAP COMPILE

(SINGLE GMAP ASSEMBLY)

1                    8                    16

---

\$	SNUMB	34871
\$	IDENT	1234,FIELDTRNG.....
\$	GMAP	NDECK,COMDK

(SOURCE DECK)

·  
·  
·  
\$  
ENDJOB

\*\*\*EOF

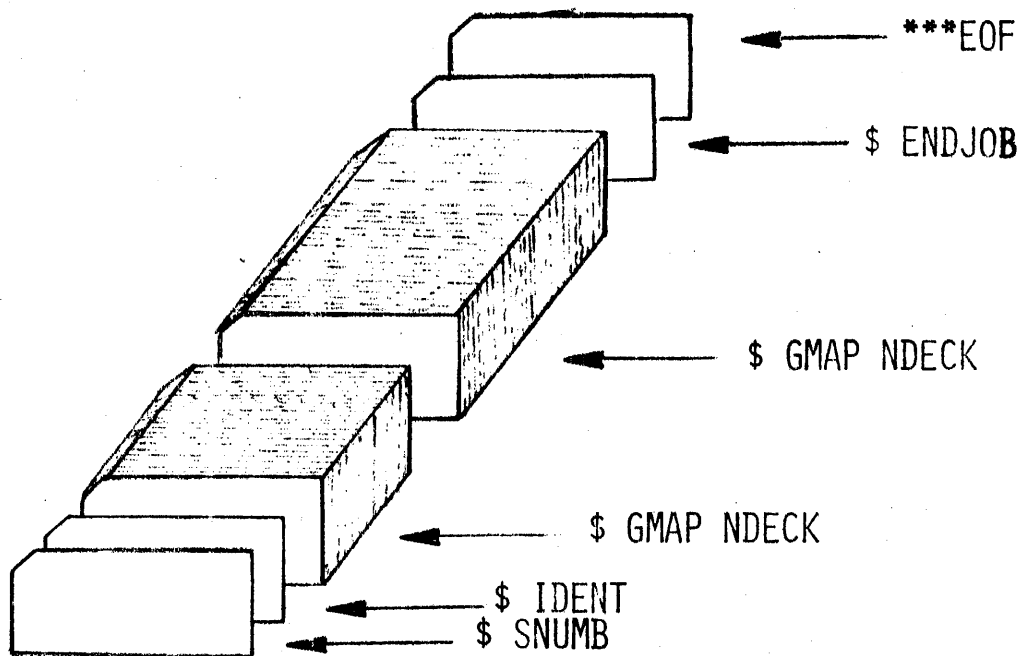
GMAP COMPILE  
(MULTIPLE GMAP ASSEMBLIES)

1	8	16
\$	SNUMB	34872
\$	IDENT	1234, FIELDTRNG, . . . .
\$	GMAP	
	(SOURCE DECK)	
\$	GMAP	
	(SOURCE DECK)	
\$	GMAP	
	(SOURCE DECK)	
\$	GMAP	
	(SOURCE DECK)	
\$	ENDJOB	

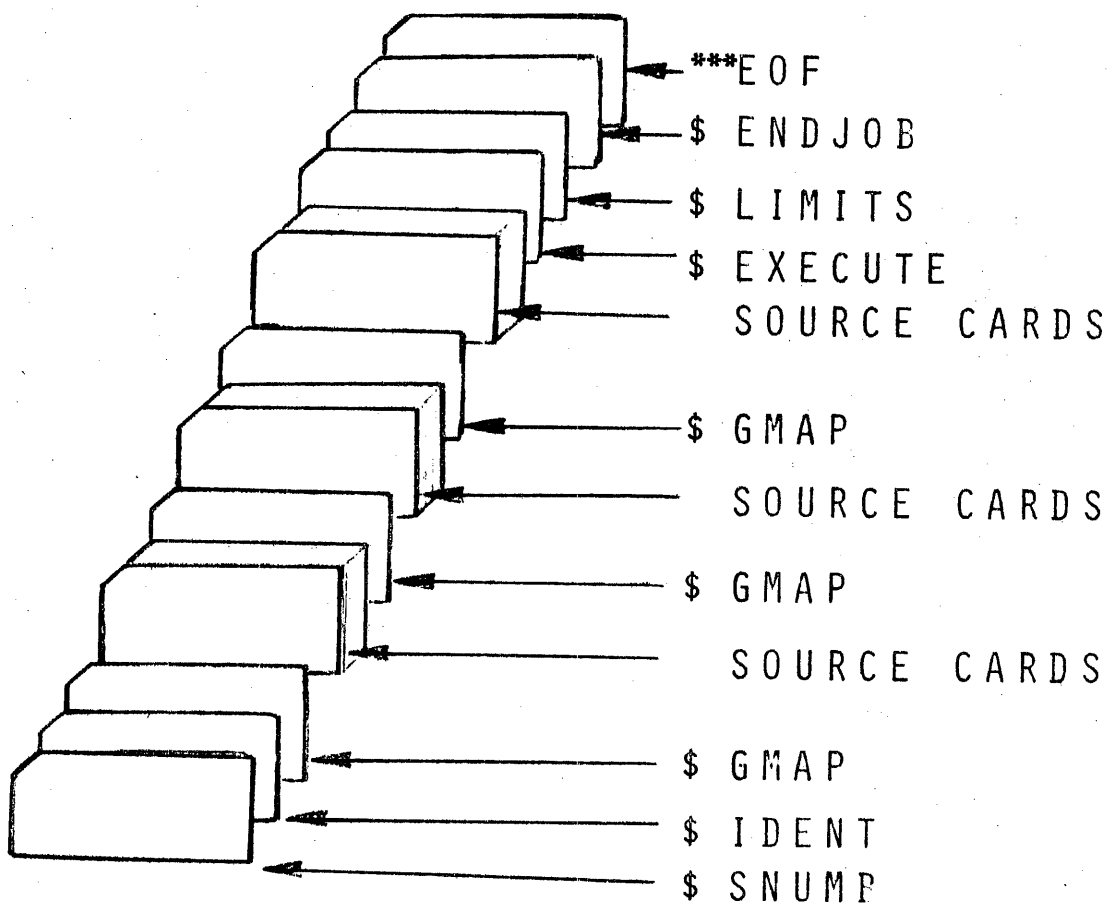
\*\*\*EOF

# GMAP COMPILE

## CARD DECK ARRANGEMENT



MULTIPLE GMAP ASSEMBLY JOB



GMAP CONTROL RECORDS  
(COMPILE AND EXECUTE)

1	8	16
\$	SNUMB	34873
\$	IDENT	1234, FIELDTRNG, . . . .
\$	GMAP	NDECK, COMDK
	(SOURCE DECK)	
\$	EXECUTE	
\$	LIMITS	3,4K, ,1000
\$	SYSOUT	AL (PRINTER OUTPUT)
\$	DATA	IN
	(DATA FOR EXECUTION)	
\$	ENDJOB	
	***EOF	

## EXECUTING OBJECT PROGRAMS

1	8	16
\$	SNUMB	34874
\$	IDENT	1234, FIELDTRNG, . . . .
\$	OBJECT	
	(BINARY DECK)	
\$	DKEND	
\$	OBJECT	
	(BINARY DECK)	
\$	DKEND	
\$	EXECUTE	DUMP
\$	LIMITS	4,6K, ,2000
\$	SYSOUT	X2 (PRINTER OUTPUT)
\$	DATA	IN
	(DATA FOR OBJECT PROGRAM)	
\$	ENDJOB	

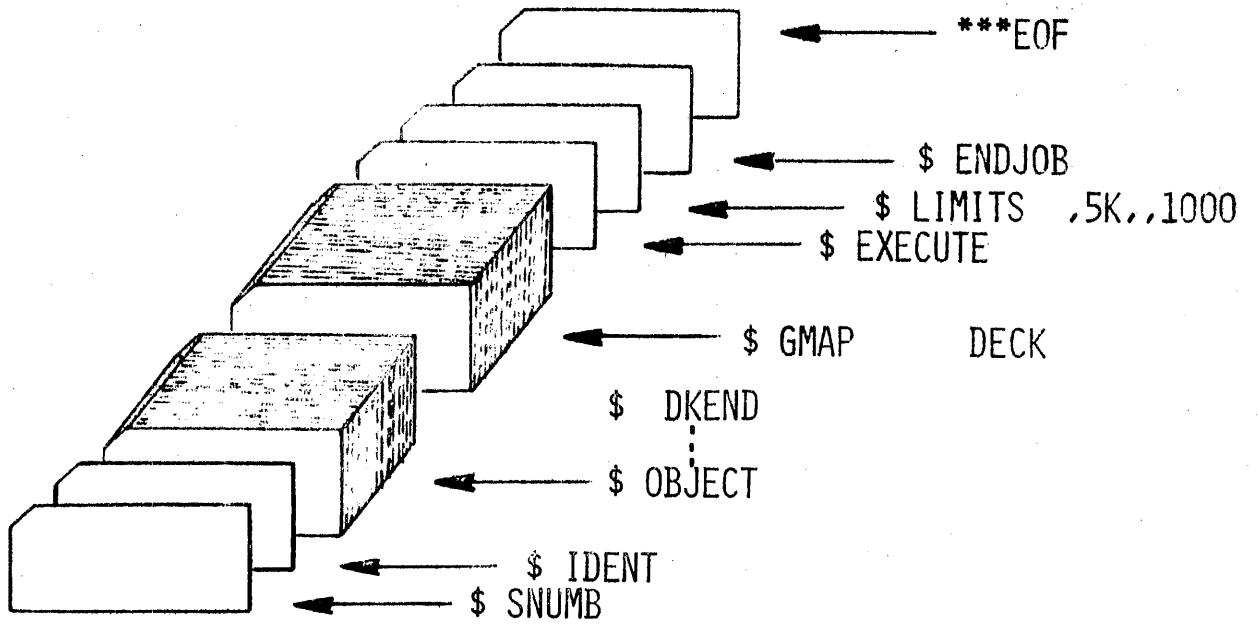
\*\*\*EOF



COMPILE AND EXECUTE  
WITH OBJECT DECKS

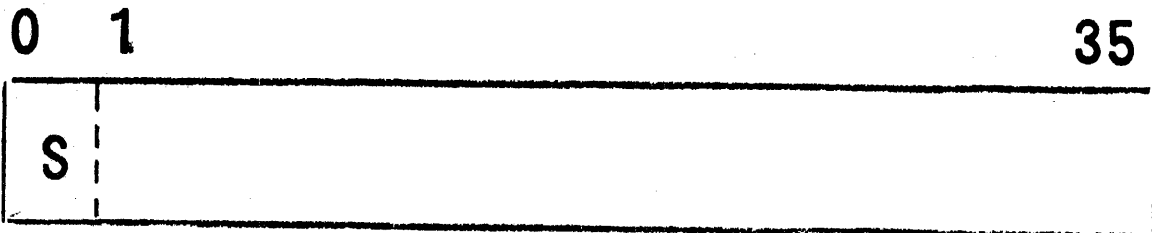
1	8	16	
\$	SNUMB	34875	
\$	IDENT	1234, FIELDTRNG, ...	
\$	GMAP		
	(SOURCE DECK)		
\$	OBJECT		
	(OBJECT DECK)		
\$	DKEND		
\$	EXECUTE		
\$	LIMITS	3,5K,,2500	
\$	SYSOUT	B3	(PUNCH OUTPUT)
\$	DATA		
	(DATA FOR OBJECT PROGRAM)		
\$	ENDJOB		
***	EOF		

### COMPILE AND EXECUTE WITH OBJECT DECK

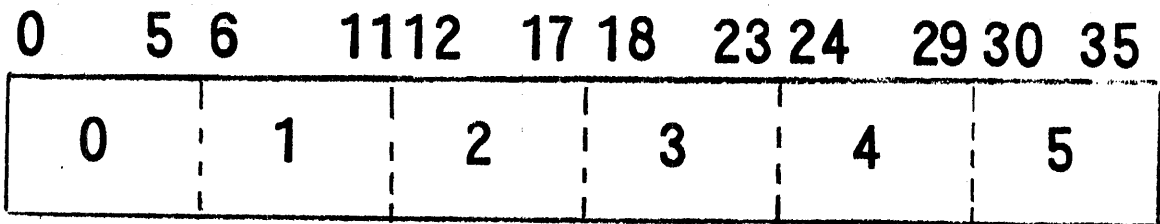


# WORD FORMATS

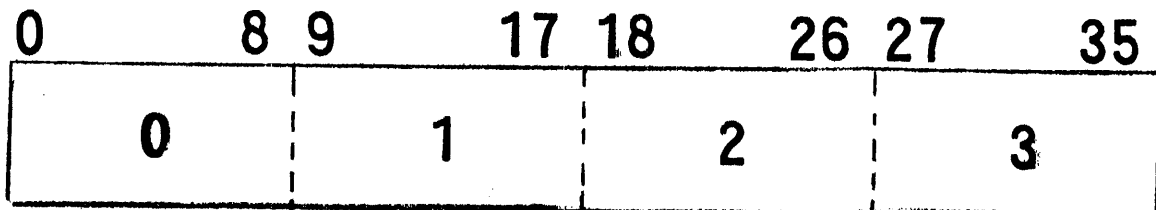
## BINARY



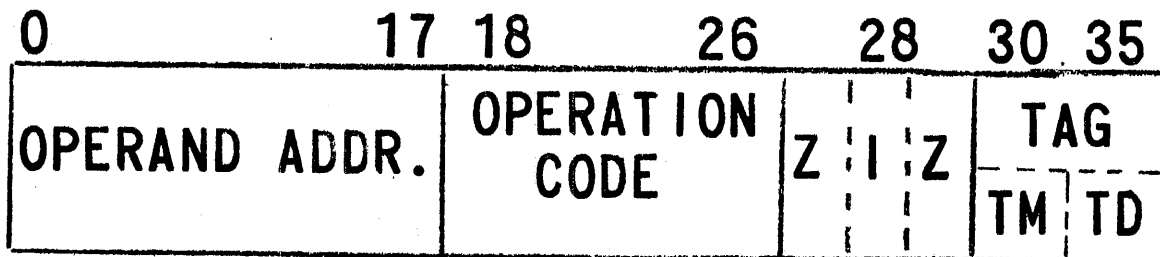
## BCD



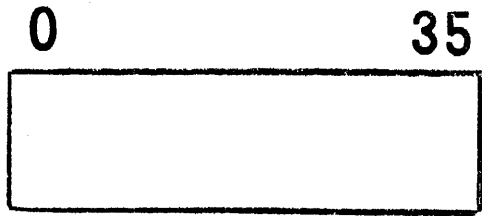
## 9-BIT BYTE



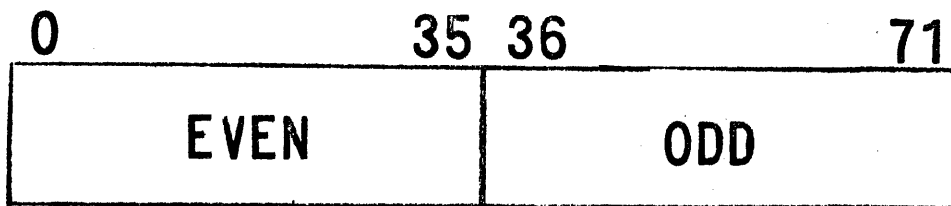
## INSTRUCTION



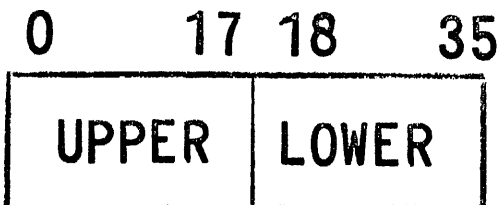
# SINGLE/DOUBLE PRECISION



SINGLE



DOUBLE      OR      Y-PAIR



HALF

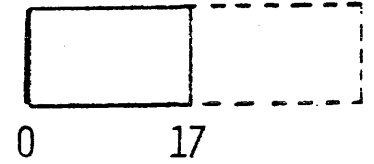
INSTRUCTION PRECISION

PRECISION

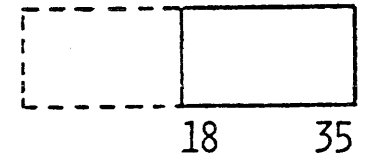
REPRESENTATION

HALF-WORD

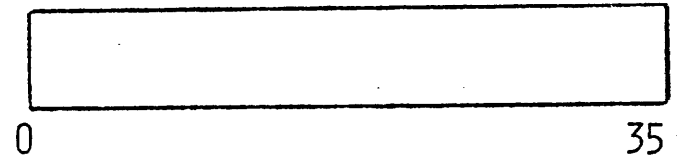
UPPER HALF



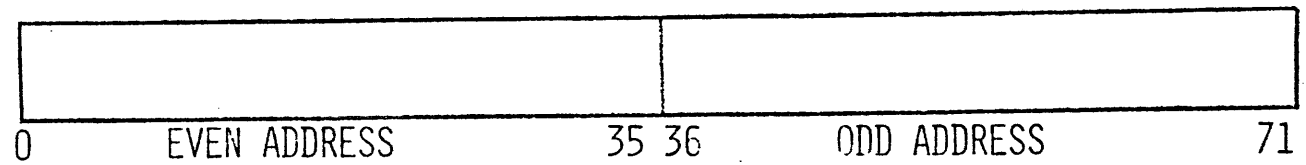
LOWER HALF



SINGLE WORD



DOUBLE WORD



FUNCTIONAL GROUPS  
OF  
PSEUDO OPERATIONS

CONTROL

LOCATION COUNTER

SYMBOL DEFINING

DATA GENERATING

STORAGE ALLOCATION

SPECIAL

MACRO

CONDITIONAL

PROGRAM LINKAGE

ADDRESS TALLY

MISCELLANEOUS

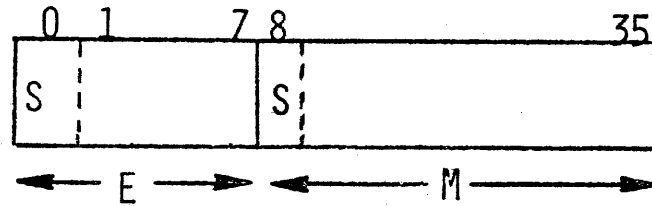
## STORAGE ALLOCATION PSEUDO-OPERATIONS

<u>1</u> TABLE	<u>8</u> BSS	<u>16</u> 1000		
			(TABLE)	2500
TABLA	BFS	1000		
	STA	TABLE		
	STA	TABLA-1000		3499
				3500
	STAQ	TABLE		
	STAQ	TABLA-2		
	* LDAQ	TABLE+5		
	STAQ	TABLA-2		
				4499
			(TABLA)	4500

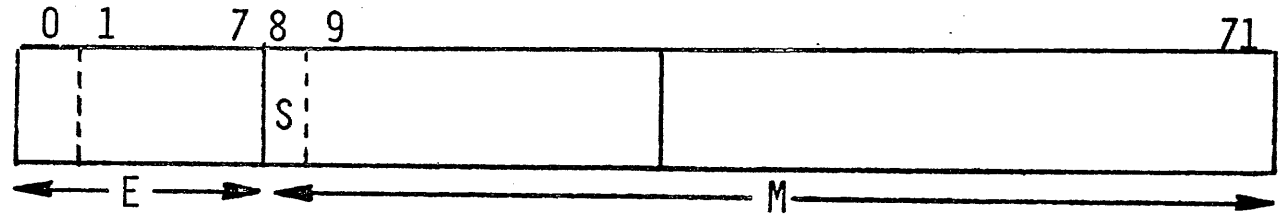
\* Addressing Error

BINARY FLOATING POINT

SINGLE - WORD  
PRECISION



DOUBLE - WORD  
PRECISION



LOWEST PERMISSIBLE EXPONENT = -128

LARGEST POSSIBLE EXPONENT = +127



# NUMBER CONVERSION TECHNIQUE

1. DECIMAL  $11.054_{10}$

2. OCTAL INTEGER CONVERSION

8	11	3
8	1	1
0		

13.

3. OCTAL FRACTION CONVERSION

	.054
	X 8
①	432
	X 8
③	456
	X 8
③	648
	X 8
⑤	184
	X 8
①	472
	X 8
④	576

↓ ↓ ↓ ↓ ↓ ↓

$.033514_8$

4. OCTAL  $+13.033514_8$

5. BINARY CONVERSION

$001011.000011011101001100_2$

6. FLOATING POINT

S	EXPONENT	S	MANTISSA
0	0000100	0	$1011.000011011101001100_2$

MOVE FOUR PLACES LEFT

7. FLOATING POINT (OCTAL)

$010541564600_8$

41

# NUMBER CONVERSION TECHNIQUE

## 8. OCTAL INTEGER CONVERSION

$$\begin{array}{r}
 13_8 \\
 \times 8 \\
 \hline
 8 \\
 + 3 \\
 \hline
 \textcircled{11}_{10}
 \end{array}$$

## 10. DECIMAL (BCD)

$$11.054_{10}$$

## 9. OCTAL FRACTION CONVERSION

$$\begin{array}{r}
 033514_8 \\
 \times 12 \\
 \hline
 \end{array}$$

$$\begin{array}{r}
 067230 \\
 033514 \\
 \hline
 \end{array}$$

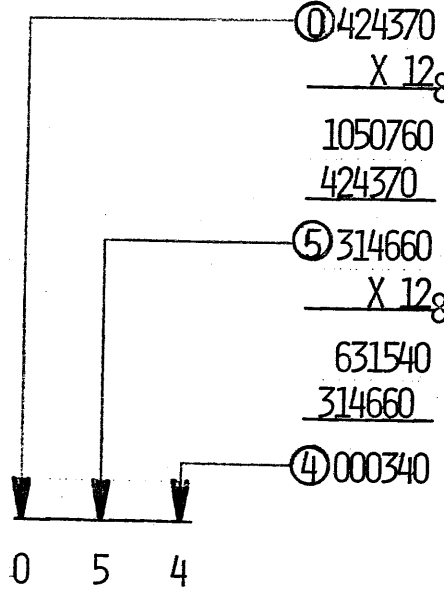
$$\begin{array}{r}
 \textcircled{1} 424370 \\
 \times 12_8 \\
 \hline
 \end{array}$$

$$\begin{array}{r}
 1050760 \\
 424370 \\
 \hline
 \end{array}$$

$$\begin{array}{r}
 \textcircled{5} 314660 \\
 \times 12_8 \\
 \hline
 \end{array}$$

$$\begin{array}{r}
 631540 \\
 314660 \\
 \hline
 \end{array}$$

$$\textcircled{4} 000340$$



0 5 4

42

1. DATA MOVEMENT  
LOAD STORE SHIFT
2. FIXED POINT ARITHMETIC  
ADD-SUBTRACT-MULTIPLY-DIVIDE
3. LOGICAL  
BOOLEAN (AND-OR-EXOR), COMPARE
4. TRANSFER OF CONTROL  
CONDITIONAL & UNCONDITIONAL
5. FLOATING POINT  
F. P. DATA MOVEMENT  
F. P. ARITHMETIC  
F. P. COMPARES
6. MASTER MODE  
CONNECT I/O, M. C. CMNDS., DIS
7. MISCELLANEOUS  
REPEATS, BCD, EFFECTIVE ADDRESS

## LOAD/STORE

LDA

STA

LCA

LDQ

STQ

LCQ

LDAQ

STAQ

LCAQ

LDX<sub>n</sub>

STX<sub>n</sub>

LCX<sub>n</sub>

LXL<sub>n</sub>

SXL<sub>n</sub>

ZERO

NONE

ZERO

NEG

NEG

O'FLO

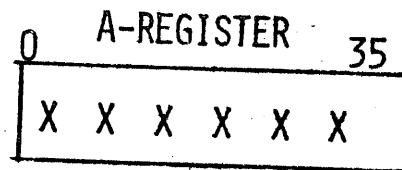
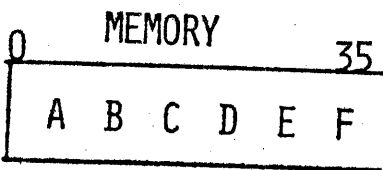
LOAD/STORE  
 SAMPLE CODING

①	⑧	①⑥
	LDA	IN+2
R1	STA	B
	}	
R4	LDQ	A
	LDA	=6H8FLD8A
	STAQ	P1
	STQ	P2
	LDX7	R1
	STX7	R4
	LCAQ	=5D0B71
	STAQ	C1
	}	
C1	EDEC	0,0
P1	BCI	2,
P2	DEC	0
A	BSS	1
B	OCT	-777777777777

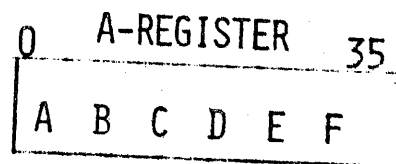
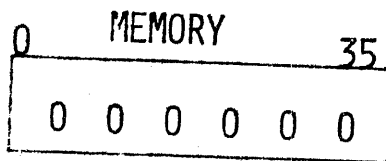
# LDAC INSTRUCTION

(LDAC MEMORY)

BEFORE EXECUTION:



AFTER EXECUTION:



# ADDRESS MODIFICATION

## INSTRUCTION FORMAT:

ADDRESS FIELD	OP CODE		TM	TD
---------------	---------	--	----	----

### TM - TYPE OF MODIFICATION

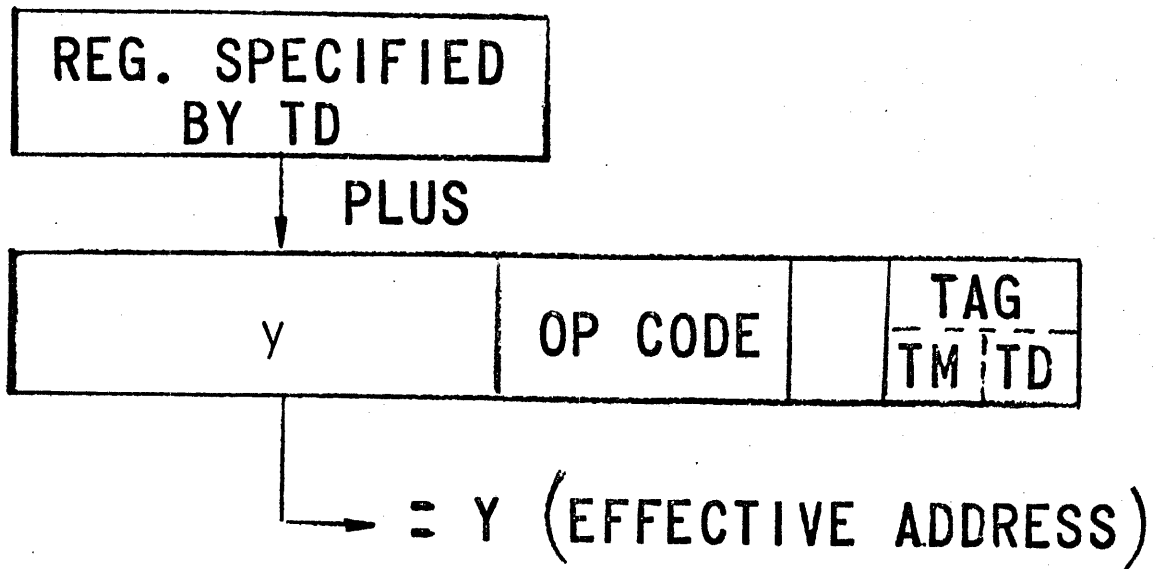
- 00 - REGISTER R
- 01 - REGISTER THEN INDIRECT RI
- 11 - INDIRECT THEN REGISTER IR
- 10 - INDIRECT THEN TALLY IT

### TD - REGISTER OR TALLY DESIGNATOR

R  
IR  
RI } REGISTER DESIGNATOR

IT - TALLY DESIGNATOR

## INDEXING



### ALGORITHM

### CODING

$$X_n Y = y + C(X_n) \quad 0-17$$

Y, 0-7

$$AU Y = y + C(A) \quad 0-17$$

Y, AU

$$AL Y = y + C(A) \quad 18-35$$

Y, AL

$$QU Y = y + C(Q) \quad 0-17$$

Y, QU

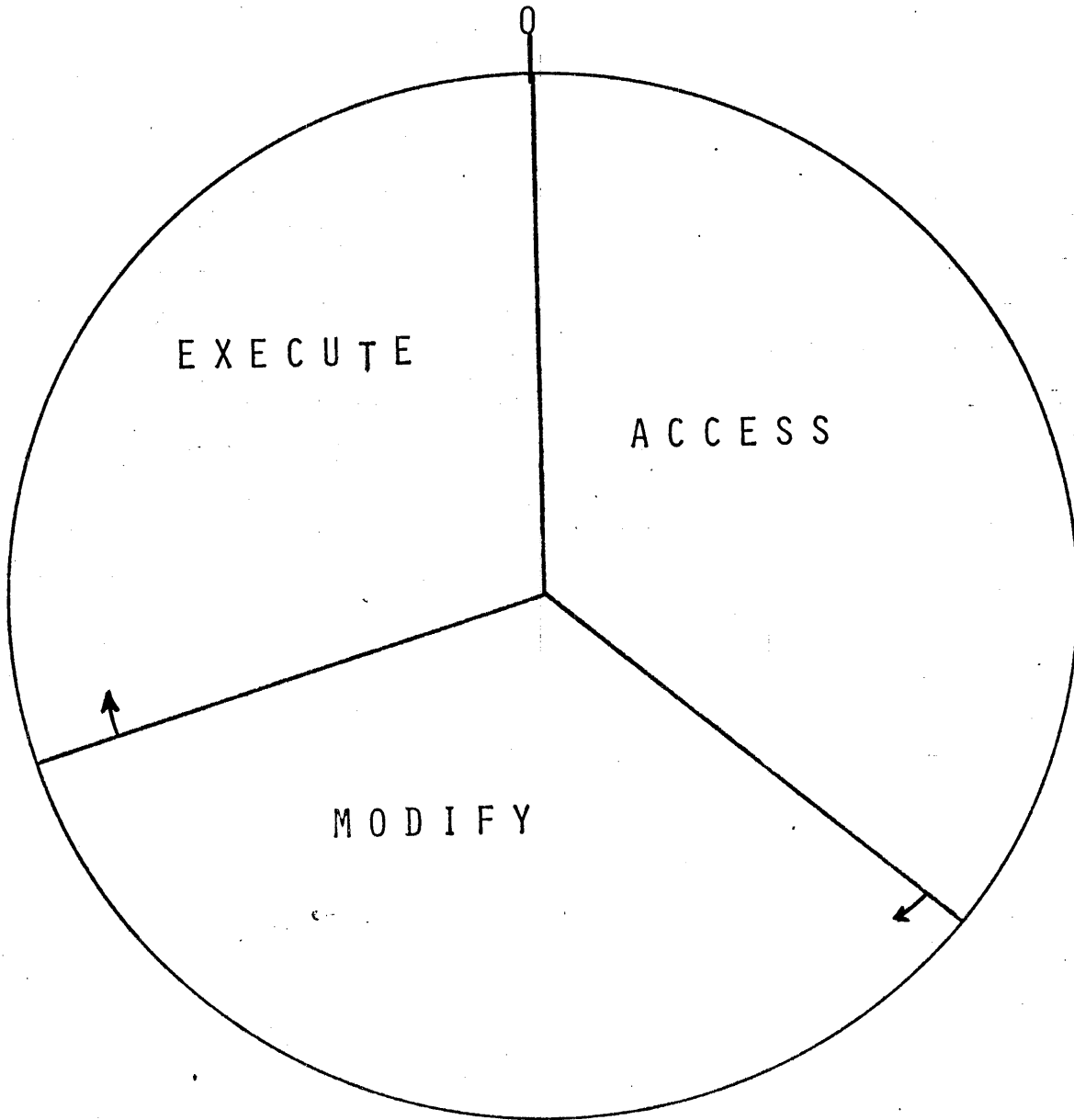
$$QL Y = y + C(Q) \quad 18-35$$

Y, QL

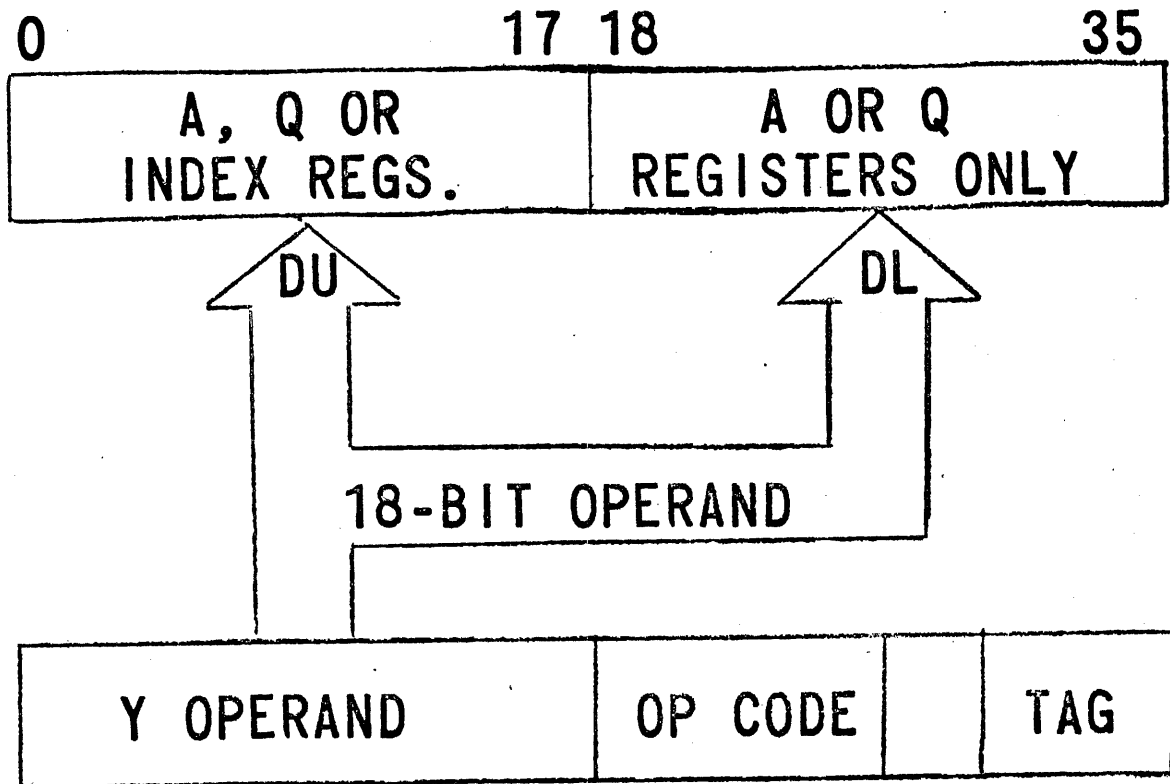
$$IC Y = y + C(IC) \quad 0-17$$

Y, IC





# DIRECT OPERAND



## ALGORITHM

DU  $Y \Rightarrow C(R)$  0-17

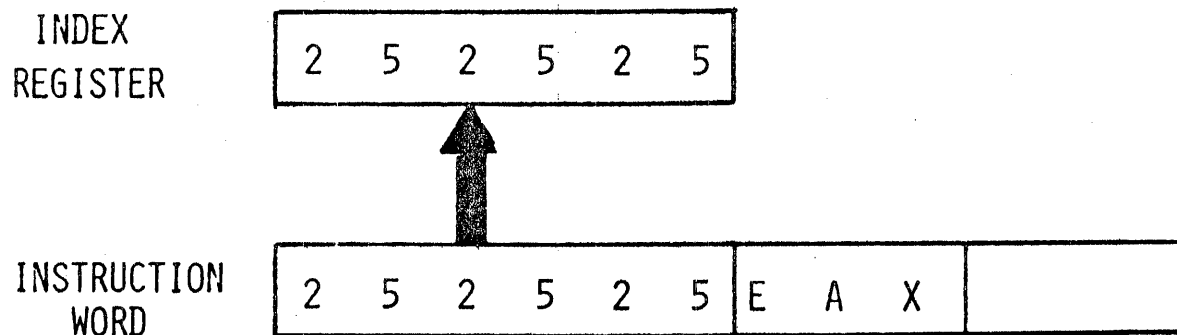
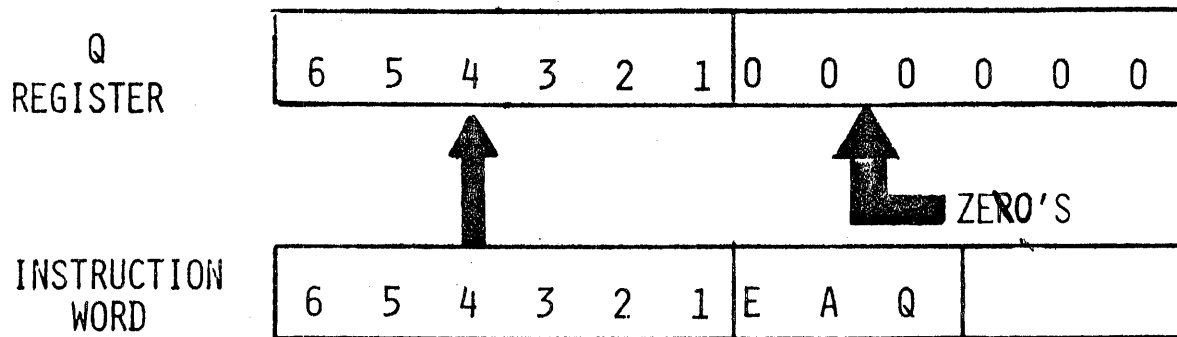
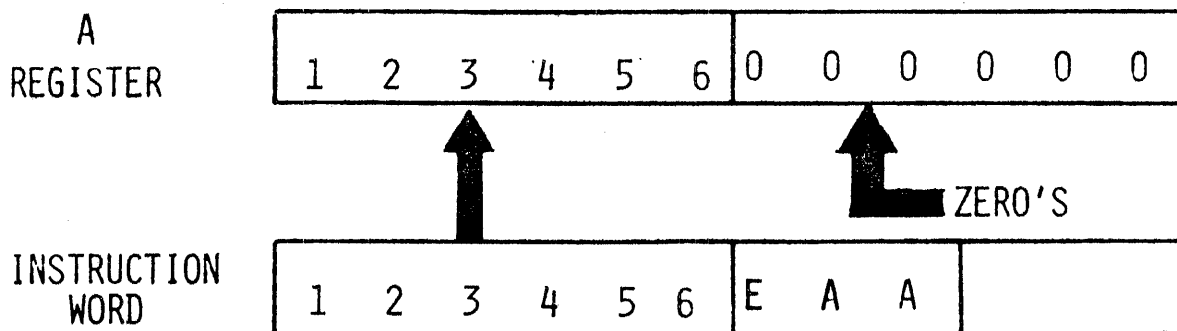
DL  $Y \Rightarrow C(R)$  18-35

## CODING

Y, DU

Y, DL

EFFECTIVE ADDRESS



## LITERALS AND DATA GENERATING PSEUDO OPS

<u>LITERALS</u>	<u>PSEUDO OPS</u>
DECIMAL	DEC
OCTAL (=O)	OCT
HOLLERITH (=H)	BCI ASCII UASCII
INSTRUCTION (=M)	ARG
VARIABLE FIELD (=V)	VFD

ALL LITERALS MUST BE PRECEDED WITH  
AN EQUAL SIGN IN COLUMN 16 OF THE  
VARIABLE FIELD OF THE CODING SHEET.

## DECIMAL LITERALS

### INTEGERS:

DISTINGUISHED BY THE ABSENCE  
OF A DECIMAL POINT, THE LETTER  
B, THE LETTER, E, OR THE LETTER  
D.

### SINGLE PRECISION FLOATING POINT:

DISTINGUISHED BY THE PRESENCE  
OF A DECIMAL POINT, THE LETTER  
E, OR BOTH.

### DOUBLE PRECISION FLOATING POINT:

DISTINGUISHED BY THE PRESENCE  
OF THE LETTER D.

### FIXED POINT:

DISTINGUISHED BY THE PRESENCE  
OF THE LETTER B. (OVERRIDES  
E OR D)

## DECIMAL VALUES

### SINGLE PRECISION

DEC 5

LDA =5

0-----01 01
-------------

DEC 5B35

0	3
	5

LDA =5B35

DEC 5E0B35

### DOUBLE PRECISION

DEC 0,5

DEC 5D0B71

0-----0101
------------

LDAQ =5D0B71

0	7
	1

### SIGNED VALUES

DEC -5

LDA =-5

1111111111111011
------------------

0	3
	5

## FLOATING POINT VALUES

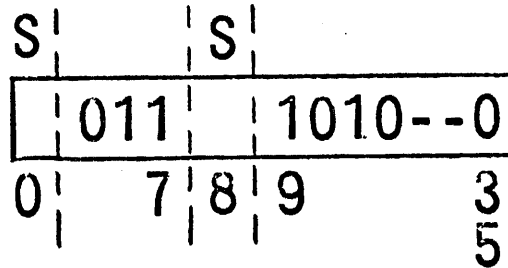
### SINGLE PRECISION

DEC        5.

LDA        =5.

DEC        5E0

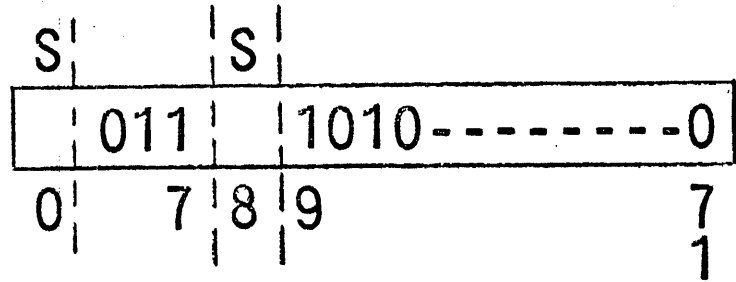
LDA        =5E0



### DOUBLE PRECISION

DEC        5D0

LDAQ       =5D0

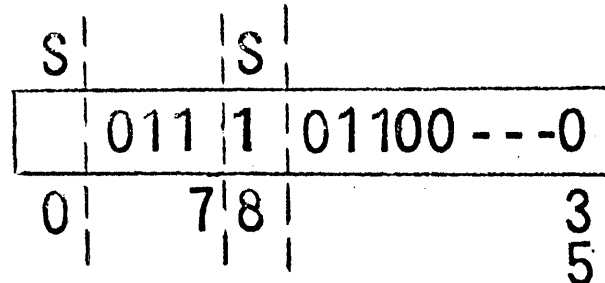


### NEGATIVE VALUES

DEC        -5.

DEC        -5E0

LDA        =-5.



## OCTAL LITERALS

THE OCTAL LITERAL CONSISTS OF THE CHARACTER Ø, FOLLOWED BY A SIGNED OR UNSIGNED OCTAL INTEGER WHICH MAY BE FROM ONE (1) TO TWELVE (12) DIGITS IN LENGTH PLUS A SIGN.

THE ASSEMBLER WILL STORE THE INTEGER IN A WORD RIGHT-JUSTIFIED. THE WORD WILL BE STORED IN ITS REAL FORM AND WILL NOT BE COMPLEMENTED IF THERE IS A PRESENCE OF A MINUS SIGN (-).



## OCTAL VALUES

### SINGLE PRECISION

OCT            5

LDA           =Ø5

0-----0101

0

3  
5

### DOUBLE PRECISION

OCT            ,5

0-----0101

0

7  
1

### SIGNED VALUES

OCT            -5

100-----0101

0

3  
5

LDA           =Ø-5

## HOLLERITH DATA

### SINGLE PRECISION

BCI 1,ABCDEF

A	B	C	D	E	F
0					3
					5

LDA = 6HABCDEF

### DOUBLE PRECISION

BCI 2,ABCDEFABCDEF

LDAQ = 12HABCDEFABCDEF

A	B	C	D	E	F	A	B	C	D	E	F
0											7
											1

INSTRUCTION    LITERAL

LDA        = MTRA CAT, 7

ADDR	OP	TAG
CAT	TRA	07

LDA        = MLDA DOG, DU

ADDR	OP	TAG
DOG	LDA	DU

## ARG PSEUDO OPERATION

ARG

4

ADDR	OP	TAG
4	000	00

ARG

5,4

ADDR	OP	TAG
5	000	04

ARG

CAT

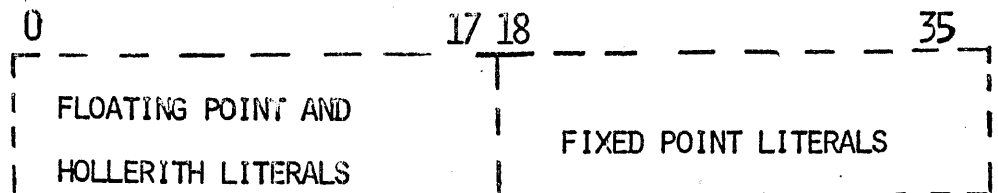
ADDR	OP	TAG
CAT	000	00

# LITERALS MODIFIED BY DIRECT OPERANDS

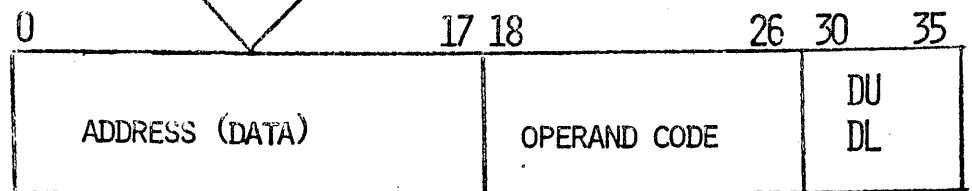
LDA = 3HAAA, DU  
 LDQ = 3HQQQ, DL  
 FLD = 327D2, DU

LDQ = 10, DU  
 LXL<sub>N</sub> = 124, DL  
 LDX<sub>N</sub> = 1B18, DU  
 \* LDA = 1B5, DU

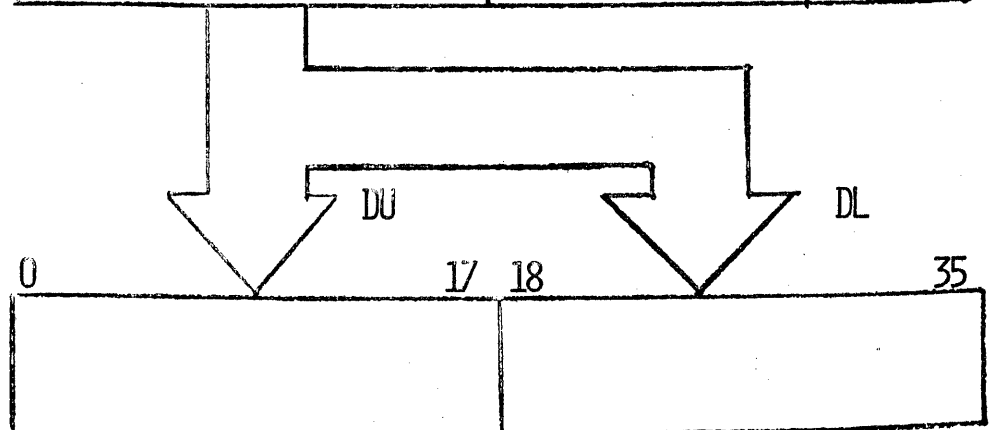
ASSEMBLY  
 TIME



INSTRUCTION  
 WORD



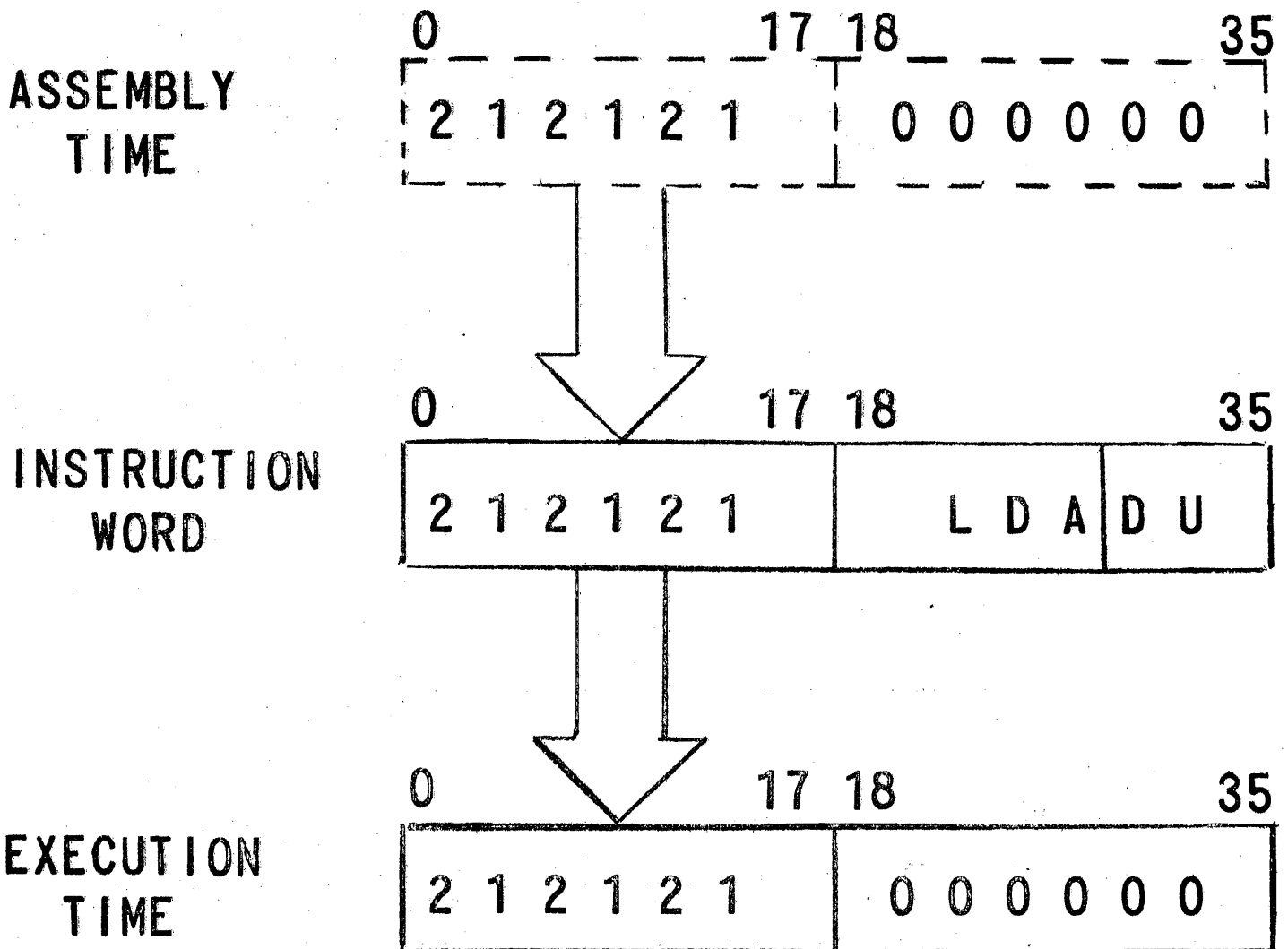
EXECUTION  
 TIME



\*Incorrect coding - will not generate proper literal.

# HOLLERITH LITERAL

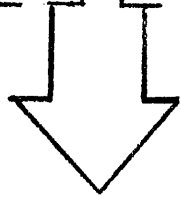
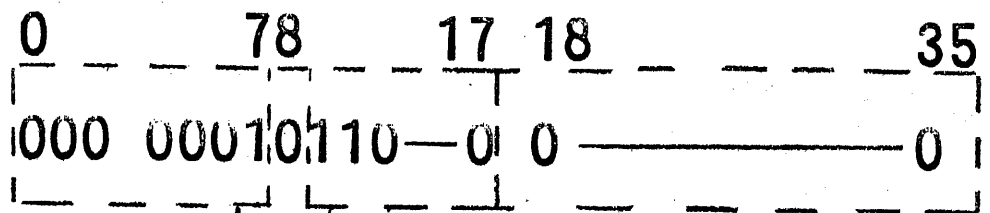
EXAMPLE: LDA =3HAAA,DU



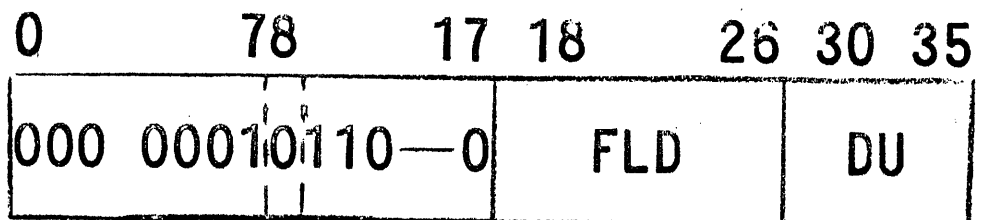
# FLOATING-POINT LITERAL

EXAMPLE: FLD = 1.5, DU

ASSEMBLY  
 TIME



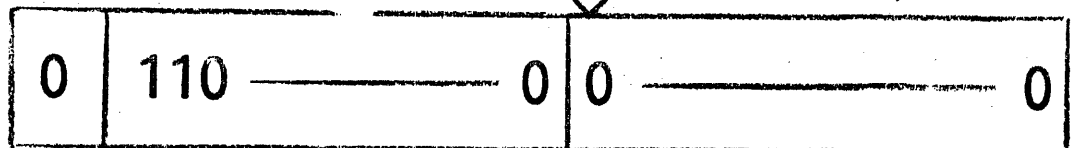
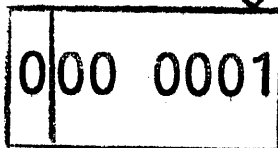
INSTRUCTION  
 WORD



E

A

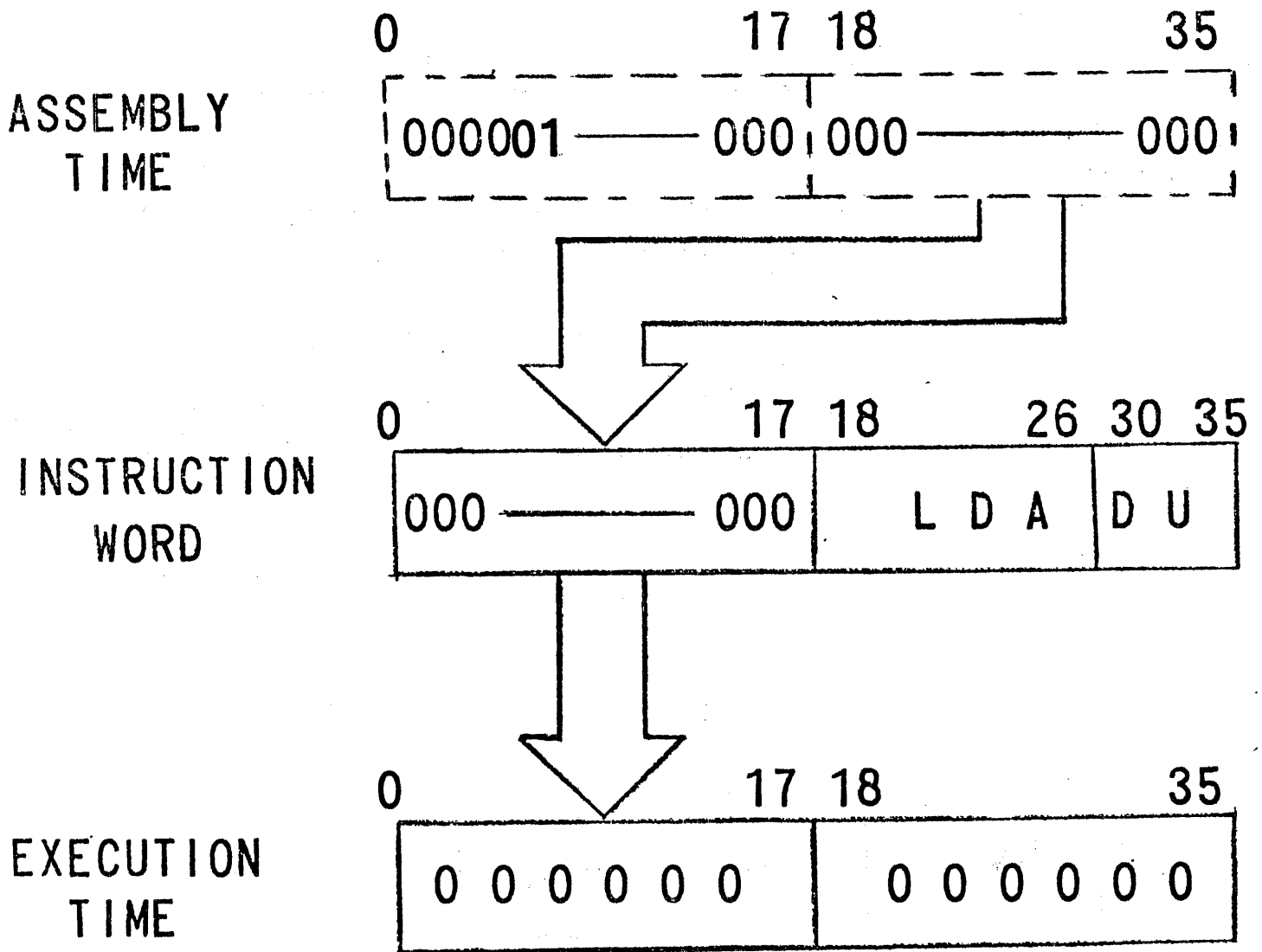
Q



EXECUTION TIME

# FIXED-POINT LITERAL

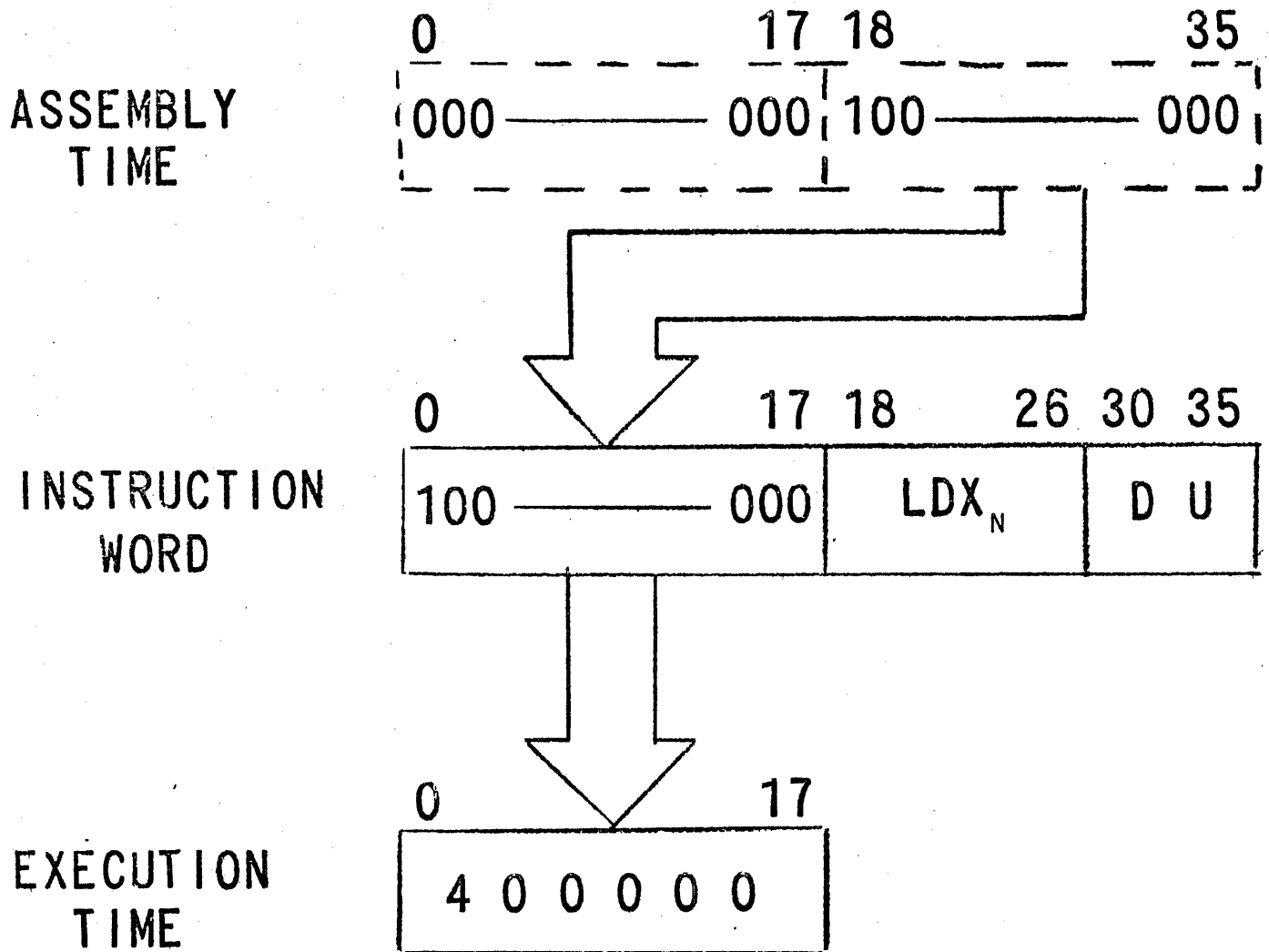
EXAMPLE: LDA =1B5,DU





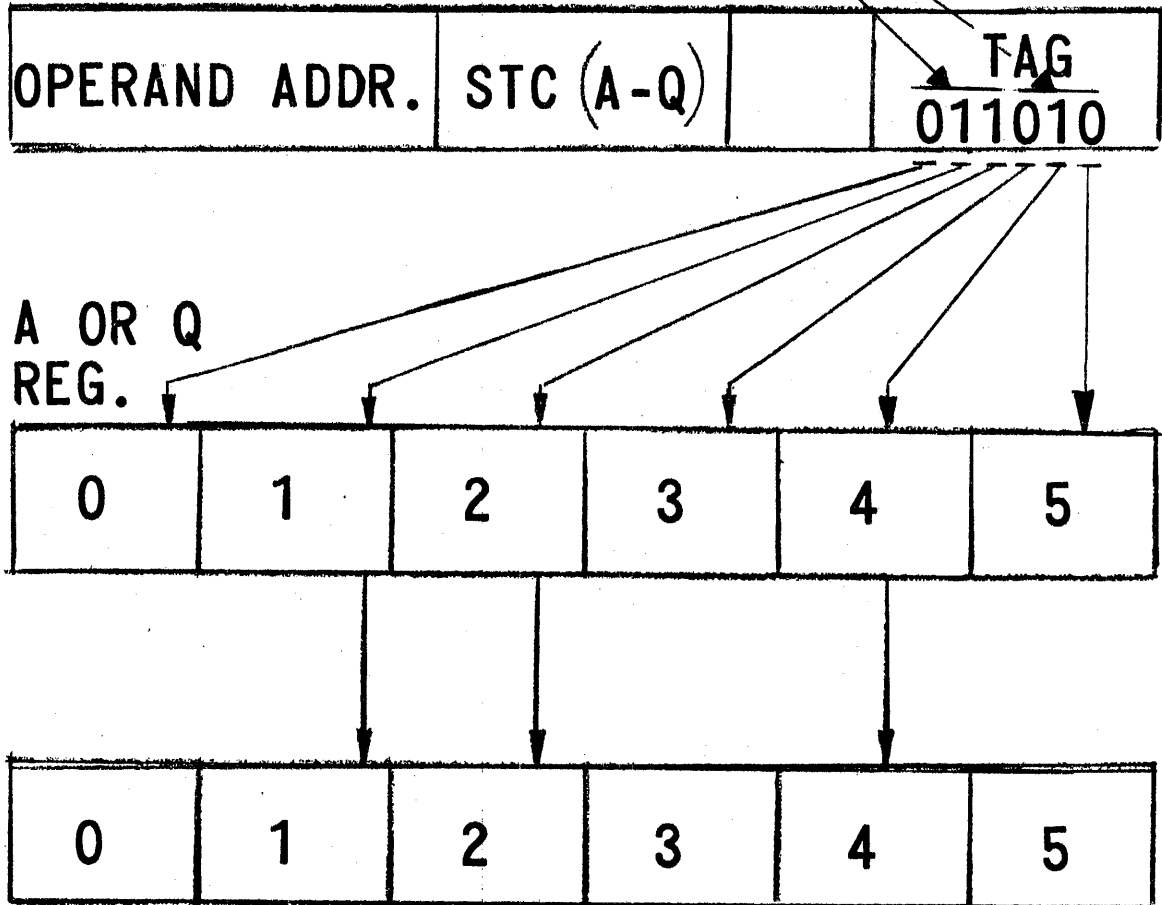
# FIXED-POINT LITERAL

EXAMPLE:  $LDX_N = 1B18, DU$



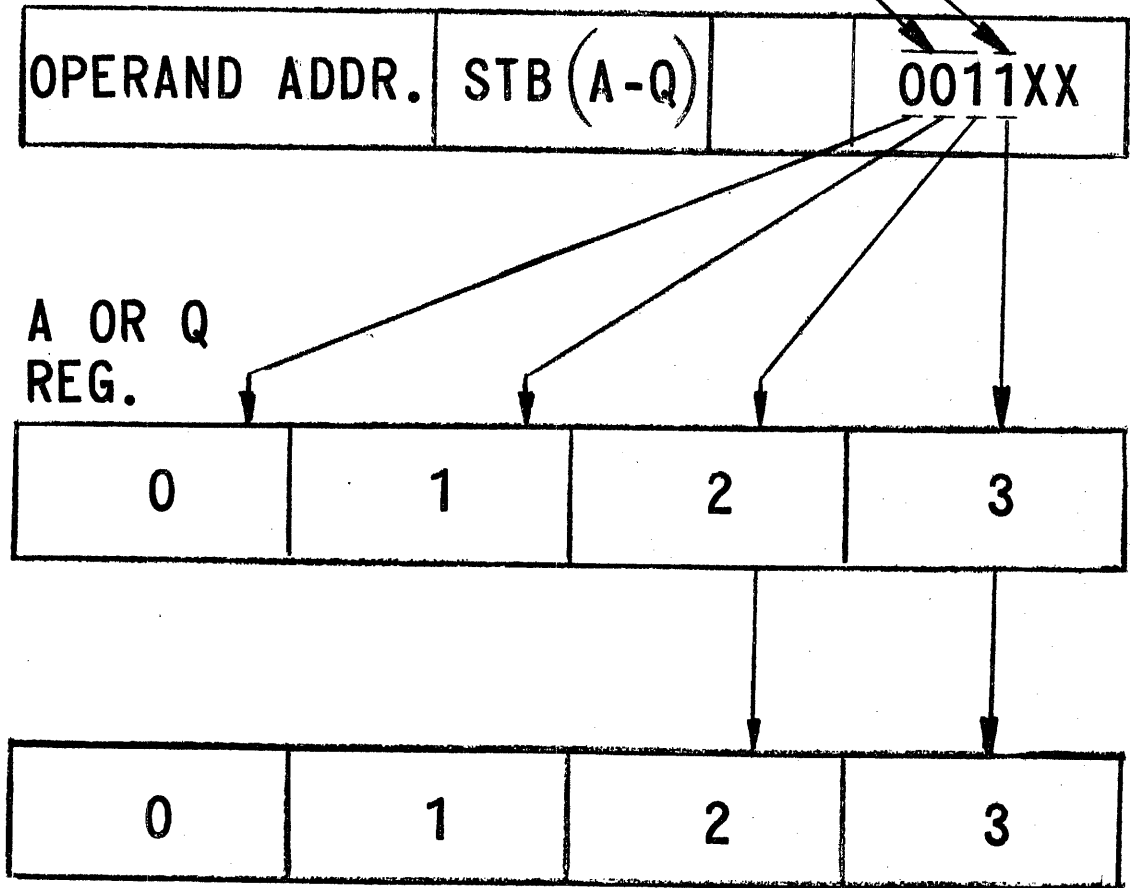
# STORE CHARACTER

STC (A-Q) WORD, 32

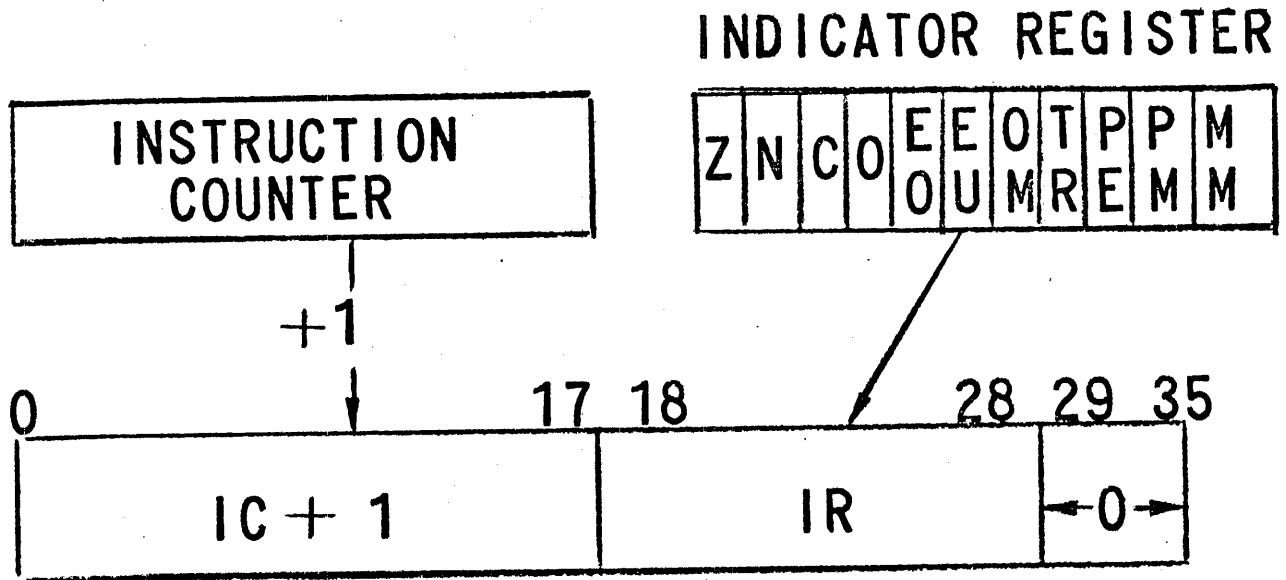


# STORE BYTE

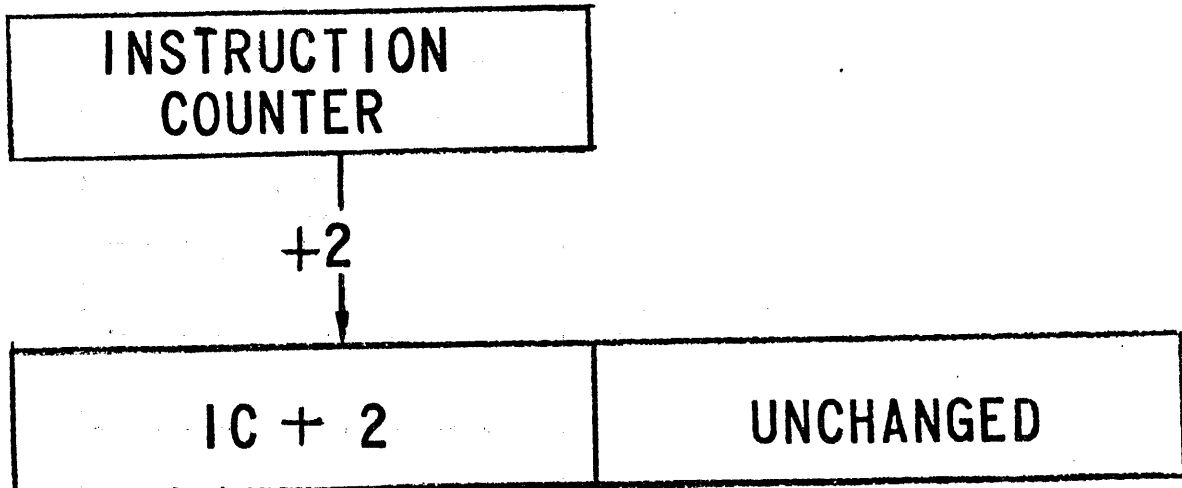
STB (A-Q) WORD, 14



STC1



STC2



## SPECIAL LOAD/STORE

* LBAR * LDT LDI LREG * MASTER MODE	SBAR STT STI SREG STZ
---	-----------------------------------

WORD

LREG/SREG

0	X0	X1
1	X2	X3
2	X4	X5
3	X6	X7
4	A	
5	Q	
6	E	← 0 →
7	TR (SREG only)	← 0 →

## SHIFT INSTRUCTIONS



### REGISTER LEFT SHIFTS

1. VACATED POSITIONS ARE FILLED WITH ZEROS.
  2. BITS VACATING BIT POSITION ZERO ARE LOST.
- 



### REGISTER RIGHT SHIFTS

1. ALL VACATED POSITIONS ARE FILLED WITH THE CONTENTS OF BIT ZERO.
  2. BITS VACATING BIT POSITION 35 ARE LOST.
- 



### REGISTER RIGHT LOGICAL SHIFTS

1. ALL VACATED POSITIONS ARE FILLED WITH ZEROS.
  2. BITS VACATING BIT POSITION 35 ARE LOST.
- 



### REGISTER LEFT ROTATE

THE REGISTER BECOMES CIRCULAR. BITS VACATING POSITION ZERO ENTER AGAIN AT BIT POSITION 35.

## FIXED POINT ARITHMETIC

ADA	SBA
ADQ	SBQ
ADAQ	SBAQ
ADX <sub>n</sub>	SBX <sub>n</sub>
ASA	SSA
ASQ	SSQ
ASX <sub>n</sub>	SSX <sub>n</sub>

## INDICATORS

ZERO-NEGATIVE-CARRY-OVERFLOW

## LOGICAL ADD/SUBTRACT

ADLA	SBLA
ADLQ	SBLQ
ADLAQ	SBLAQ
ADLX <sub>n</sub>	SBLX <sub>n</sub>

## INDICATORS

ZERO-NEGATIVE-CARRY

O'FLO INDICATOR NOT AFFECTED



## CARRY AND OVERFLOW INDICATORS

\*  $CARRY = SCO$

\*  $OVERFLOW = (SCI \cdot \overline{SCO}) + (\overline{SCI} \cdot SCO)$

EXAMPLES USING ALGEBRAIC ADDITION:

	<u>4-BIT REPRESENTATION</u>	<u>INDICATOR SET ON</u>
1. R = +7	0111	
<u>Y = +1</u>	<u>0001</u>	SCI · SCO
+8		
2. R = -5	1011	
<u>Y = -4</u>	<u>1100</u>	SCI · SCO
-9		
3. R = -1	1111	
<u>Y = -4</u>	<u>1100</u>	SCI · SCO
-5		
4. R = -5	1011	
<u>Y = +4</u>	<u>0100</u>	SCI · SCO
-1		

## SPECIAL ADD/SUBTRACT

AWCA

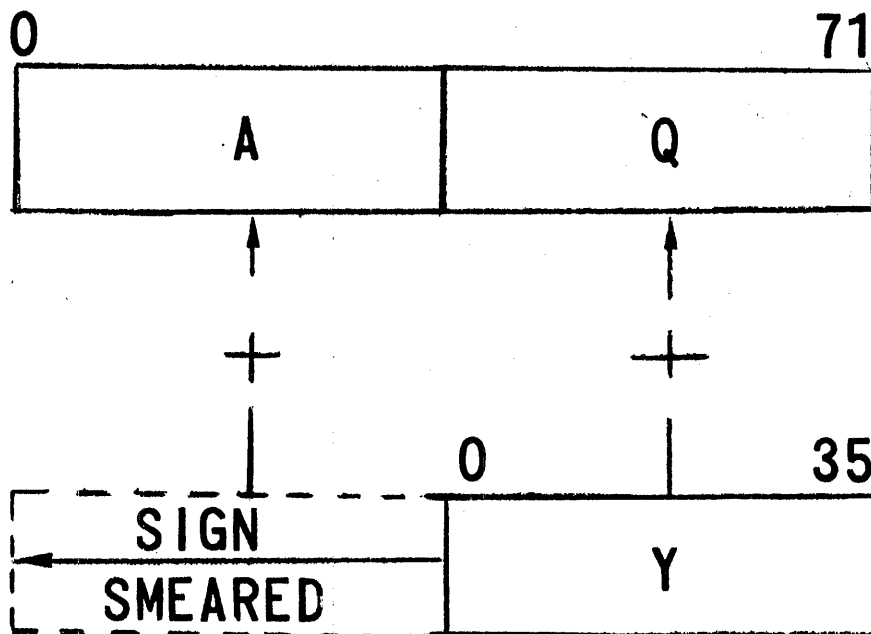
SWCA

AWCQ

SWCQ

AOS

ADL



## MULTIPLY/DIVIDE

MPY  
MPF

MULTIPLICAND  
MPY-Q MPF-A

X

MULTIPLIER  
MEM. LOC. Y

PRODUCT  
COMBINED A-Q REGISTER

DIV

DIVIDEND  
Q-REGISTER

÷

DIVISOR  
MEM. LOC. Y

REMAINDER  
A-REGISTER

QUOTIENT  
Q-REGISTER

DVF

DIVIDEND  
COMBINED A-Q REGISTER

÷

DIVISOR  
MEMORY LOCATION Y

QUOTIENT  
A-REGISTER

REMAINDER  
Q-REGISTER

## TRANSFER CONTROL

### UNCONDITIONAL

TRA	TSS
RET	TSX <sub>N</sub>

### CONDITIONAL

TZE/TNZ	* TOV
TMI/TPL	* TEO
TRC/TNC	* TEU

\* RESETS RESPECTIVE INDICATOR

XX  
XX

```
  XX      X      XXXX      XXXX      X  
  X X      X      X X      X X      XX  
  X X      XXXX      X      X      X  
 XXXXXX      X X      XXX      XXX      X  
  X      X X      X      X      X  
  X      XXXX      XXXXXX      XXXXXX      XXX
```

XX  
XX

0002 5 IDENT 4817, TRNG-RGK, J959, C959100, 8177  
XX  
XX

```
  XXXX      XXXX      X      X      XXXX      XXXX      X X  
  X      X X      XX      X      X      X X      X X  
  XXXX      XXXX      X X      X      XXXX      XXXX      XXX  
  X X      X X      X X      X XXX      X X      X X  
  X X      X X      XX      X X      X X      X X  
  X X      X X      X      XXXX      X X      XXXX      X X
```

XX  
XX

38 46221 ENTERED FIN635 AT 17,419 FROM CD RDR 0-06-00

0001 \$ SNLMB 46221  
0002 \$ IDENT 4817, TRNG-RGK, J959, C959100, 0177  
0003 \$ GMAP NDECK, COMDK  
0004 \$ EXECUTE DUMP  
0005 \$ LIMITS ,1K,,1000  
0006 \$ ENDJOB

TOTAL CARD COUNT THIS JOB = 000068

\* BEGIN ACTIVITY -01- GMAP 10/06/71 SW=215000000000  
\* NORMAL TERMINATION AT 002420 INDICATORS 4080

START 17,443 LINES 166 PROC 0,0003 I/O 0,001 IU 5 MEMORY 24K  
STOP 17,444 LIMIT 10000 LIMIT 0,0400 LIMIT CU 5 MoT 120

LAPSE	C,001	FC D TYPE	BUSY	IP/AT	EPART	IS/RC	MS/BE	ADDRESS	L#T#
		G* R D167 *	98	0	3	3	3	0-00-08	30
		P* SYOUT							
		K* SYOUT							
		C* SYOUT							
		*1 R D167	356	0	0	4	4	0-00-03	153
		B* S D167	80	0	1	2	2	0-00-04	139

COMDK 10 CARDS  
LIST 156 LINES

\* BEGIN ACTIVITY -02- GELOAD 10/06/71 SW=400000000000  
\* USERS 00 PME GEBORT AT 001601 INDICATORS 0000

START 17,444 LINES 89 PROC 0,0002 I/O 0,001 IU 5 MEMORY 1K  
STOP 17,448 LIMIT 10000 LIMIT 0,0500 LIMIT CU 5 MoT 59

LAPSE	0,004	FC D TYPE	BUSY	IP/AT	EPART	IS/RC	MS/BE	ADDRESS	L#T#
		B* R D167	30	1	1	2	2	0-00-04	138
		R* R D167 *	38	0	0	1	1	0-00-08	30
		P* SYOUT							
		L* R D167	573	0	0	28	28R	0-00-01	300

LIST 89 LINES

PREFACE

PROGRAM BREAK 225  
COMMON LENGTH 0  
V COUNT BITS 5

PRIMARY SYMDEF ENTRY

FLOAT 0

SECONDARY SYMDEF ENTRY

BLOCK LENGTH

SYMPREF

78

Address	Label	Operation	Comments
000000	000000		
000001	000222630000	010	
000002	000222754000	010	
000003	000222741000	010	
000004	000101 2350 00	010	
000005	000100 7550 00	010	
000006	000304 6260 00	000	
000007	000310 6270 00	000	
000010	000311 6170 00	010	
000011	000300 6360 00	000	
000012	106300 4110 03	000	
000013	777777 0660 03	000	
000014	000302 1670 03	000	
000015	000320 2350 07	000	
000016	000352 2350 07	000	
000017	000104 0540 00	010	
000020	000140 2370 00	010	
000021	000014 7370 00	000	
000022	000014 7320 00	000	
000023	000140 7570 00	010	
000024	000110 3770 00	010	
000025	000224 4020 00	010	
000026	000112 0360 52	010	
000027	000113 2350 00	010	
000030	000112 7550 00	010	
000031	000310 0010 00	000	
000032	000300000010	000	
000033	000300011007	000	
000036	00002710004	000	
	000040		
	000040		
000100	000340 0004 10	010	
000101	000340 0004 10	010	
000102	000300000000	000	
000103	000300000000	000	
000104	000300000000	000	
000105	000300000000	000	
000106	000300000000	000	
000107	000300011007	000	
000110	171717171717	000	
000111	000300000303	000	
000112	000300 0006 00	000	
000113	000300 0006 00	000	
000114	000300001440	000	
000115	000300001200	000	
000116	000300001000	000	
000117	073465450000	000	
000120	057536040000	000	
000121	046113200000	000	

```

1 LBL R6K3
2 FLOAT SAVE
3 LDA TLYREG
4 STA REGTLY
5 EAX6 4 LOAD X6 WITH 4
6 EAX7 8 LOAD X7 WITH 8
7 AGAIN TOV *+1 RESET OVERFLOW INDICATOR
8 EAC 0 ZERO O-REGISTER
9 LDE =35B25,DU SET UP E-REGISTER FOR FP CONVERSION
10 ABX6 -1,DU DECREMENT X6 BY ONE
11 SRX7 2,DU DECREMENT X7 BY TWO
12 LDA =C20,CL
13 EMINUS LDA =C92,CL
14 AOS SEQBIA
15 RTNX LDA R6WRK
16 LLS 12
17 ORS 12
18 STAQ R6WRK
19 CONVRT ANAQ MASKA ZERO ZONE BITS OF A, ALL OF Q
20 MPY =10
21 ADLO TAL1,SC
22 LDA TAL2
23 STA TAL1
24 MHE GEBCRT
25 ABIN DEC 8
26 AFLOATEBSS 2
27 EIGHT
28 SAVREG BSS 32
29 REGTLY TALLYD SAVREG,4,8
30 TLYREG TALLYD SAVREG,4,8
31 EBIN DEC 0
32 FBIN DEC 0
33 SEQBIN DEC 0
34 ESIGN DEC 0
35 FSIGN DEC 0
36 MASKA EOCT 171717171717
37 OCT 000000000000
38 TAL1 TALLY MYWRK,6,0
39 TAL2 TALLY MYWRK,6,0
40 CONTB DGC 800,640,522
41 CONTAB DEC 8E9B35,6468B35,312E7B35,4096E6B35,3276000000,2621448000
    
```

79

23



000122	036411000000	000																		
000123	030324000000	000																		
000124	023420000000	000																		
000125	017500000000	000	42	DEC																2097192009,1697721600,1342177280,1673741024
000126	014400000000	000																		
000127	012000000000	000																		
000130	010000000000	000																		
000131	000000011007	000																		
	000132		43	EBCD	EBS		2													
	000134		44	FBCD	BSS		2													
	000136		45	DUMMY	BSS		1													
	000137		46	SEQCD	BSS		1													
000140	202020202020	000	47	RDWRK	EBCI		5,		8		2		2							1
000141	201020202002	000																		
000142	202020202092	000																		
000143	202020202020	000																		
000144	202020202001	000																		
	000145		48		BSS		9													
000156	202020202020	000	49	PRTOUT	OCT															202020202020
000157	202020000000	000	50	PRSEQ	OCT															202020000000
000160	202020202020	000	51		OCT															202020202020
000161	202020202020	000	52	PRE	BCI		3,													
000162	202020202020	000																		
000163	202020202020	000																		
000164	202020202020	000	53	PRF	BCI		3,													
000165	202020202020	000																		
000166	202020202020	000																		
000167	202020202020	000	54	FRVAR	BCI		6,													
000170	202020202020	000																		
000171	202020202020	000																		
000172	202020202020	000																		
000173	202020202020	000																		
000174	202020202020	000																		
000175	202020202020	000	55		BGI		6,													
000176	202020202020	000																		
000177	202020202020	000																		
000200	202020202020	000																		
000201	202020202020	000																		
000202	202020202020	000																		
000203	770200000000	000	56		OCT		770200000000													
	000204		57	PUNOUT	BSS		0													
000204	202020202020	000	58	PUE	BCI		2,													
000205	202020202020	000																		
000206	202020202020	000	59	PUF	BCI		2,													
000207	202020202020	000																		
000210	202020202020	000	60	PUSEQ	BCI		1,													
000211	202020202020	000	61	PUVAR	BCI		9,													
000212	202020202020	000																		
000213	202020202020	000																		
000214	202020202020	000																		
000215	202020202020	000																		
000216	202020202020	000																		

80

STUDENT HANDBOOK  
REFERENCE NO. 8-1  
Page 5

000217 2020202020 000  
000220 2020202020 000  
000221 2020202020 000

ERROR LINKAGE

000222 000000000000 000  
000223 264346216320 000

LITERALS

000224 000000000012 000

62 END

225 IS THE NEXT AVAILABLE LOCATION, CHAP VERSION JKRA/030271 JPRB/030271 JMPC/030871  
THERE WERE 2 WARNING FLAGS IN THE ABOVE ASSEMBLY  
ON PAGE NO. 2

18

DCTAL SYMBCL REFERENCES BY ALTER NO.

DCTAL	SYMBCL	REFERENCES BY ALTER NO.
10	GEBORT	24
222	E.L.	2
110	PASKA	36 19 36
140	RCWRK	47 15 18 47
100	REGTY	29 4 29
40	SAVREG	28 28 29 30
104	SEGBIN	33 14 33
112	TAL1	38 21 23 38
113	TAL2	39 22 39
101	TLYREG	30 3 30

\*\* 16235 WORDS OF MEMORY WERE USED BY GMAP FOR THIS ASSEMBLY,

UNDEFINED SYMBOLS

MYWRK

82

ORIGIN 082170 ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION

SUBPROGRAMS INCLUDED IN DECK,

001550 100671 FLOAT 001550

SUBPROGRAMS OBTAINED FROM SYSTEM LIBRARY,

001472 021271 SETU, 001500 ,FRSIG 001472
001464 073069 FLTPR 001464

ALLOCATED CORE 000000 THRU 001777 002000
OBJECT PROGRAM

RELOCATABLE 001464 THRU 001777 000314
1K, IS THE MINIMUM MEMORY NEEDED TO LOAD THIS ACTIVITY

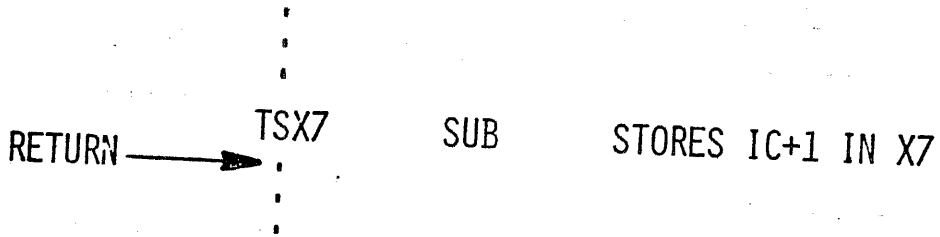
EXECUTION PROGRAM ENTERED AT 001550

EI 001662755000 OI 000010001000 IC 001601 IR 000001 ER 043 AR 000000000000 QR 000000000000 TR 00007064
BA 434002 X0 000000 X1 001542 X2 000000 X3 000000 X4 000000 X5 000000 X6 000000 X7 000000

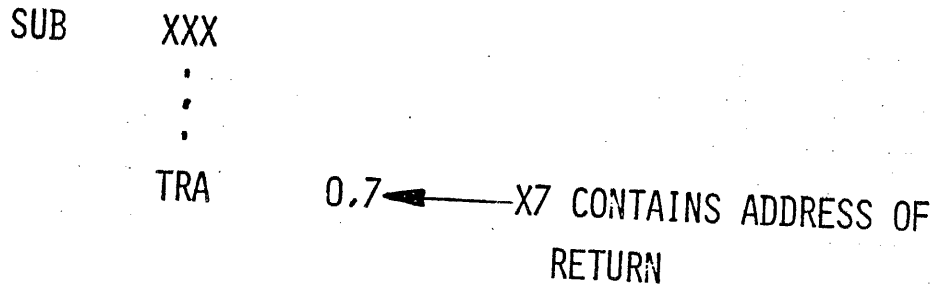
Table with 10 columns of numerical data, likely representing memory addresses or program parameters. The first column contains values from 000000S to 001770. The remaining columns contain various numerical sequences.

# SAVING INSTRUCTION COUNTER IN INDEX REGISTER

## MAIN LINE CODING:



## SUBROUTINE CODING:



SAVING INSTRUCTION COUNTER  
IN MEMORY

MAIN LINE CODING:

```
      :  
      :  
STC2   BACK   STORES IC+2  
TRA    SUB  
RETURN → :
```

SUBROUTINE CODING:

```
SUB    XXX  
      :  
      :  
BACK   TRA    ** ← INDICATES ADDRESS REPLACED  
                       BEFORE USE
```

## SAVING INSTRUCTION COUNTER

### AND INDICATORS (STC1)

#### MAIN LINE CODING:

```

      .
      .
      .
      STC1    BACK    STORES IR + IC+1
      TRA     SUB
RETURN → .
      .
      .
  
```

#### SUBROUTINE CODING:

```

SUB      XXX
      .
      .
      .
      EAX7    1
      ASX7    BACK    INCREMENTS ADDRESS PORTION OF
*                                     BACK BY 1 - ADDRESS OF RETURN
      RET     BACK    RESTORES INDICATORS AND TRANS-
*                                     FERS INDIRECT TO RETURN
      .
      .
      .
BACK     BSS    1      MEMORY LOCATION FOR INDICATORS
*                                     AND IC
  
```

SAVING INSTRUCTION COUNTER

AND INDICATORS (STI)

MAIN LINE CODING:

```

      :
      :
      STI    BACK    STORES IR
      STC2   BACK    STORES IC+2
      TRA    SUB
RETURN → :
      :
```

SUBROUTINE CODING:

```

SUB      XXX
      :
      :
      RET    BACK    RESTORES INDICATORS AND
*        :          TRANSFERS INDIRECT
      :
BACK     BSS    1     STORAGE FOR IC & IR
```



SAVING REGISTERS  
 INDICATORS AND IC

MAIN LINE CODING:

```

      :
      :
      SREG      SAVREG
      STI       SAVIND
      STC2      SAVIND
      TRA       SUB
RETURN → :
      :
      :
  
```

SUBROUTINE CODING:

```

SUB      XXX
      :
      :
      LREG      SAVREG      RESTORE REGISTERS
      RET       SAVIND      RESTORE IR & IC
      :
      :
      EIGHT
SAVREG   BSS      8          JUMP TO 0-MOD-8 LOCATION
SAVIND   BSS      1          STORE REGISTERS HERE
                                SAVE IR & IC
  
```

## CALL PSEUDO-OPERATION

### CODING:

CALL        SUB

### GENERATED CODING:

TSX1	SUB
TRA	* + 2
ZERO	.E.L..., <u>N</u> *
:	
:	
:	

\*N = ALTER NUMBER OF CALL

CALL PSEUDO-OP  
EXPANDED VERSION

CODING:

CALL SUB(A1,A2,A3)E1,E2'E.I.'

GENERATED CODING:

TSX1	SUB	
TRA	* + 2 + 3 + 2	
ZERO	.E.L.,E.I.	
ARG	A1	} ARGUMENTS FOR SUB
ARG	A2	
ARG	A3	
TRA	E2	} ERROR RETURNS*
TRA	E1	
:		
:		

\* NOTE REVERSE ORDER

SAVE PSEUDO OPERATION

EXPANDED VERSION

CODING:

SYMBOL	SAVE	0,5,7
--------	------	-------

GENERATED INSTRUCTIONS:

SYMBOL	TRA	*+2+3
	LDX0	** ,DU
	LDX5	** ,DU
	LDX7	** ,DU
	RET	.E.L..
	STI	.E.L..
	STX1	.E.L..
	STX0	SYMBOL+1
	STX5	SYMBOL+2
	STX7	SYMBOL+3
	:	
	:	
	:	

# CALL SAVE AND RETURN PSEUDO OPS

1 NOW	8 CALL	16 SUB
NOW	TSX1 TRA ZERO XXX	SUB *+2 .E.L...,0

SUB      SAVE

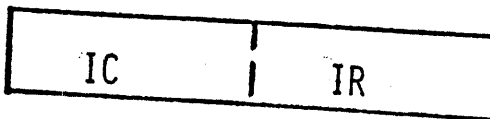
SUB	TRA RET STI STX1	*+2 .E.L.. .E.L.. .E.L..
-----	---------------------------	-----------------------------------

}  
RETURN

SUBROUTINE BODY

TRA      SUB+1

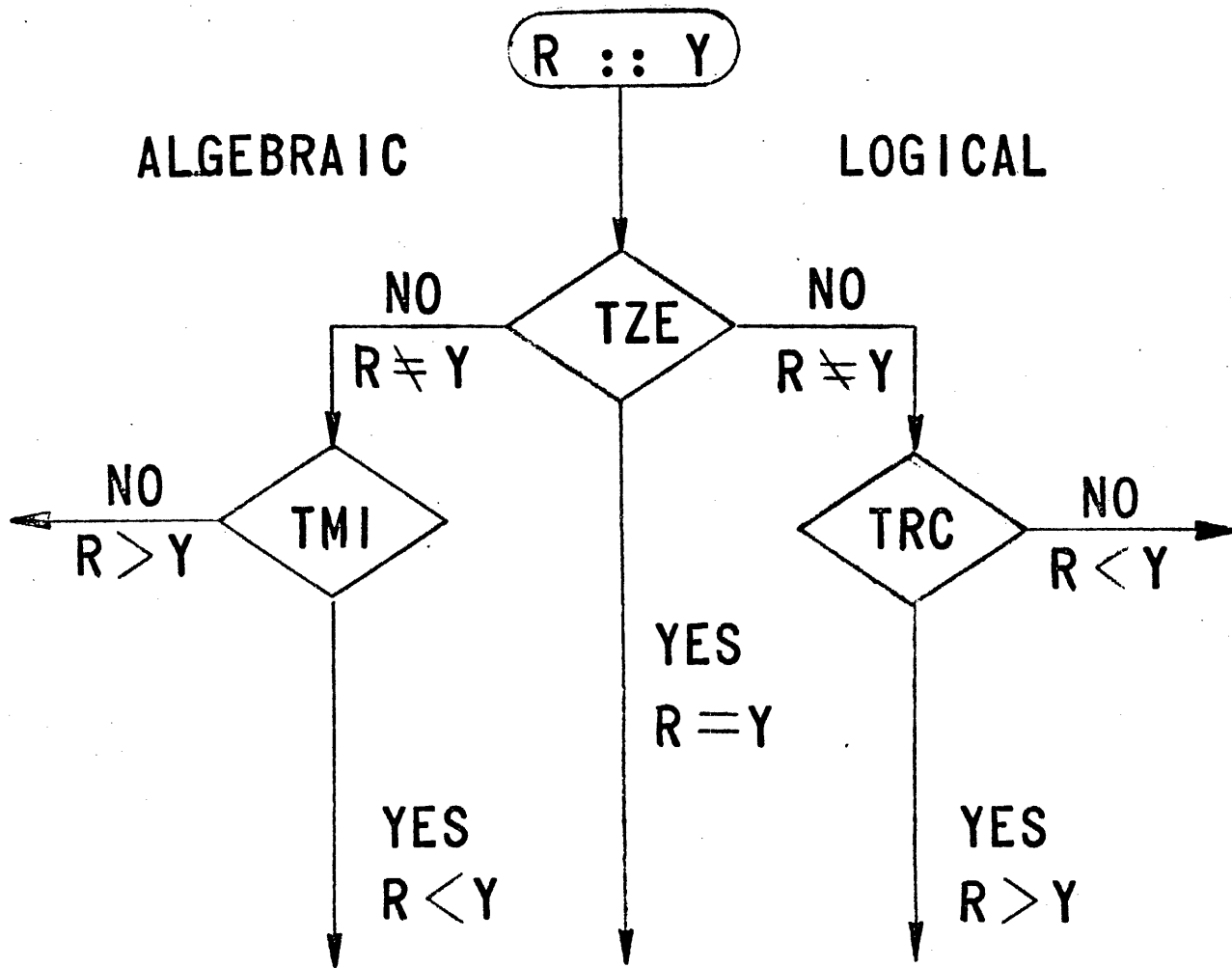
NOTE: .E.L..



CALL SAVE AND RETURN PSEUDO OPS

START	CALL	SUB, (LOC1, LOC2, LOC3), ERR1, ERR2	
START	TSX1	SUB	
	TRA	*+7	
	ZERO	.E.L., 0	
	ARG	LOC1	DATA LOCATIONS TO BE PASSED
	ARG	LOC2	TO SUBROUTINE 'SUB'.
	ARG	LOC3	
	TRA	ERR2	LOCATIONS OF ERROR ROUTINES,
	TRA	ERR1	OR MME GEBORTS.
	XXX		
<hr/>			
SUB	SAVE	5, 7, 2	
SUB	TRA	*+5	
	LDX5	** , DU	RESTORE INDEX REGISTERS 5, 7, & 2.
	LDX7	** , DU	
	LDX2	** , DU	
	RET	.E.L..	
	STI	.E.L..	
	STX1	.E.L..	
	STX5	SUB+1	SAVE INDEX REGISTERS 5, 7, & 2.
	STX7	SUB+2	
	STX2	SUB+3	
	.	SUBROUTINE BODY	
	.		
ERROR1	RETURN	SUB, 1	
ERROR1	LDX1	.E.L., *	REGISTER & INDIRECT TO .E.L..
	SBX1	1, DU	
	STX1	.E.L..	
	TRA	SUB+1	
ERROR2	RETURN	SUB, 2	
ERROR2	LDX1	.E.L., *	
	SBX1	2, DU	
	STX1	.E.L..	
	TRA	SUB+1	

# COMPARE LOGIC FLOW



# POSITIVE NUMBERS AND COMPARISON INDICATORS

REGISTER = 8	001000	001000
MEMORY = 8	001000	<u>111000</u>
		1000000

ZERO  
 CARRY

SCO · SCI

REGISTER = 8	001000	001000
MEMORY = 7	000111	<u>111001</u>
		1000001

CARRY

SCO · SCI

REGISTER = 8	001000	001000
MEMORY = 9	001001	<u>110111</u>
		111111

NEGATIVE



## NEGATIVE NUMBERS AND COMPARISON INDICATORS

REGISTER = -8	111000	111000
MEMORY = -8	111000	<u>001000</u>
		1000000

SCO · SCI      ZERO  
 CARRY

REGISTER = -8	111000	111000
MEMORY = -7	111001	<u>000111</u>
		111111

NEGATIVE

REGISTER = -8	111000	111000
MEMORY = -9	110111	<u>001001</u>
		1000001

SCO · SCI      CARRY

## COMPARISON INDICATORS

### POSITIVE REGISTER - NEGATIVE MEMORY

REGISTER = 8	001000	001000
MEMORY = -8	111000	<u>001000</u>
		010000

NO INDICATORS SET

REGISTER = 8	001000	001000
MEMORY = -7	111001	<u>000111</u>
		001111

NO INDICATORS SET

### NEGATIVE REGISTER - POSITIVE MEMORY

REGISTER = -8	111000	111000
MEMORY = 8	001000	<u>111000</u>
		1110000

SCO . SCI      NEGATIVE  
 CARRY

### ALGEBRAIC COMPARISON

ZERO	NEGATIVE	CARRY	RELATION	SIGN
0	0	0	$C(A) > C(Y)$	$C(A)_0 = 0, C(Y)_0 = 1$
0	0	1	$C(A) > C(Y)$	} $C(A)_0 = C(Y)_0$
1	0	1	$C(A) = C(Y)$	
0	1	0	$C(A) < C(Y)$	
0	1	1	$C(A) < C(Y)$	$C(A)_0 = 1, C(Y)_0 = 0$

### LOGICAL COMPARISON

ZERO	CARRY	RELATION
0	0	$C(A) < C(Y)$
1	1	$C(A) = C(Y)$
0	1	$C(A) > C(Y)$

## FIXED POINT COMPARE

CMPA	Y
CMPQ	Y
CMPAQ	Y-PAIR
CMPX <sub>N</sub>	Y <sub>0...17</sub>

SPECIAL PURPOSE COMPARES

ALGEBRAIC COMPARE WITH LIMITS

CWL Y

COMPARE MAGNITUDE

CMG Y

COMPARE MASKED

CMK Y

## BOOLEAN OPERATIONS

### TRUTH TABLE

	<u>ANDING</u>	<u>ELIMINATES BITS</u>
ANA	ANSA	$0 \cdot 0 = 0$
		$0 \cdot 1 = 0$
ANQ	ANSQ	$1 \cdot 0 = 0$
		$1 \cdot 1 = 1$
ANAQ		
ANXN	ANXN	
	<u>ORING</u>	<u>INSERTS BITS</u>
ORA	ORSA	$0 + 0 = 0$
		$0 + 1 = 1$
ORQ	ORSQ	$1 + 0 = 1$
		$1 + 1 = 1$
ORAQ		
ORXN	ORSXN	
	<u>EXCLUSIVE OR</u>	<u>BINARY ADD (NO CARRY)</u>
ERA	ERSA	$0 \neq 0 = 0$
		$0 \neq 1 = 1$
ERQ	ERSQ	$1 \neq 0 = 1$
		$1 \neq 1 = 0$
ERAQ		
ERXN	ERSXN	

EXAMPLE OF ANDING

ANA     MASK  
ANA     = 020401020402

AREG BEFORE	0	7	0	6	0	5	0	4	0	3	0	2
AREG BEFORE	000	111	000	110	000	101	000	100	000	011	000	010
MASK	000	010	000	100	000	001	000	010	000	100	000	010
AREG AFTER	000	010	000	100	000	001	000	000	000	000	000	010
AREG AFTER	0	2	0	4	0	1	0	0	0	0	0	2

EXAMPLE OF ORING

ORA MASK

ORA = 020401020402

AREG BEFORE	0	7	0	6	0	5	0	4	0	3	0	2
AREG BEFORE	000	111	000	110	000	101	000	100	000	011	000	010
MASK	000	010	000	100	000	001	000	010	000	100	000	010
AREG AFTER	000	111	000	110	000	101	000	110	000	111	000	010
AREG AFTER	0	7	0	6	0	5	0	6	0	7	0	2



EXAMPLE OF THE EXCLUSIVE OR

ERA      MASK  
ERA      =020401020402

AREG BEFORE	0	7	0	5	0	5	0	4	0	3	0	2
AREG BEFORE	000	111	000	110	000	101	000	100	000	011	000	010
MASK	000	010	000	100	000	001	000	010	000	100	000	010
AREG AFTER	000	101	000	010	000	100	000	110	000	111	000	000
AREG AFTER	0	5	0	2	0	4	0	6	0	7	0	0

## BOOLEAN COMPARE OPERATIONS

### COMPARATIVE AND

CANA Y

CANQ Y

CANAQ Y-PAIR

CANX<sub>N</sub> Y

### COMPARATIVE NOT AND

CNAA Y

CNAQ Y

CNAAQ Y-PAIR

CNAX<sub>N</sub> Y

COMPARATIVE AND

CANA	MASK
TNZ	BITON

MASK	0010
AREG	1111
Z=	0010

---

CANA	COMPARATIVE AND TO AREG
CANQ	COMPARATIVE AND TO QREG
CANAQ	COMPARATIVE AND TO AQREG
CANX <sub>N</sub>	COMPARATIVE AND TO XREG

COMPARATIVE NOT AND

CNAA	MASK
TNZ	BITON

MASK	1101
1 <sup>S</sup> COMPLEMENT	0010
AREG	1111
Z=	0010

---

CNAA	COMPARATIVE <u>NOT</u> AND TO AREG
CNAQ	COMPARATIVE <u>NOT</u> AND TO QREG
CNAAQ	COMPARATIVE <u>NOT</u> AND TO AQREG
CNAX <sub>N</sub>	COMPARATIVE <u>NOT</u> AND TO XREG

## EXAMPLE OF BOOLEAN OPERATIONS AND BOOLEAN COMPARES

CHECK	LDA	SWITCH
	ANA	=Ø241000, DU
	CANA	=Ø200000, DU
	TNZ	BCD
	CANA	=Ø040000, DU
	TNZ	ASCII
	CNAA	=Ø776000, DU
	TNZ	BINARY
ERROR	LDA	=Ø400000, DU
	ORSA	SWITCH
	TRA	COMRET
BCD	CANA	=Ø041000, DU
	TNZ	ERROR
	.	
OKBCD	LDA	=Ø200040, DU
	ERSA	SWITCH
	TRA	COMRET
NOKBCD	LDA	=Ø200004, DU
	ERSA	SWITCH
COMRET	TRA	<del>XX</del>

INDIRECT THEN TALLY (IT) MODIFICATION  
VARIATIONS

I     INDIRECT  
ID    INCREMENT ADDRESS - DECREMENT TALLY  
DI    DECREMENT ADDRESS - INCREMENT TALLY  
AD    ADD DELTA  
SD    SUBTRACT DELTA  
\*CI   CHARACTER FROM INDIRECT  
\*SC   SEQUENCE CHARACTER  
\*SCR  SEQUENCE CHARACTER REVERSE  
F     FAULT  
IDC   INCREMENT ADDRESS - DECREMENT TALLY  
      AND CONTINUE  
DIC   DECREMENT ADDRESS - INCREMENT TALLY  
      AND CONTINUE

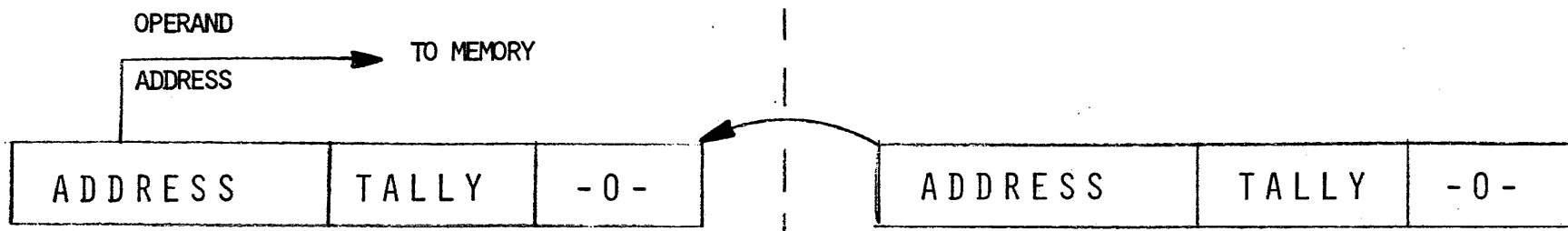
\*CHARACTER HANDLING

INDIRECT -I-  
EXAMPLE

1	8	1
	TSX1	SUBR
	.	
	.	
SUBR	STX1	ERLKG
	.	
	.	
	TRA	ERLKG, I
	.	
	.	
	.	
ERLKG	BSS	1

PROCESSOR  
TALLY I  
LDA TALI, I

MEMORY  
TALI



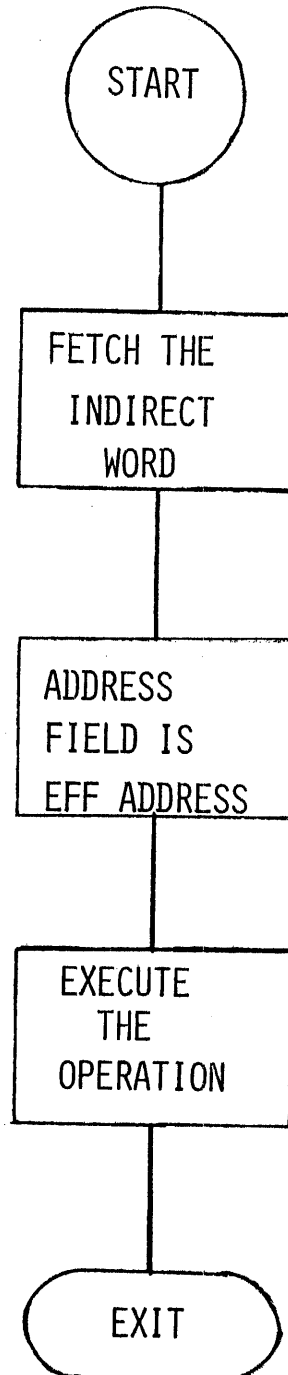
101

1. FETCH TALLY INDIRECT WORD.
2. USE OPERAND ADDRESS FOR INSTRUCTION EXECUTION.
3. TALLY INDIRECT WORD IS NOT ALTERED IN MEMORY.

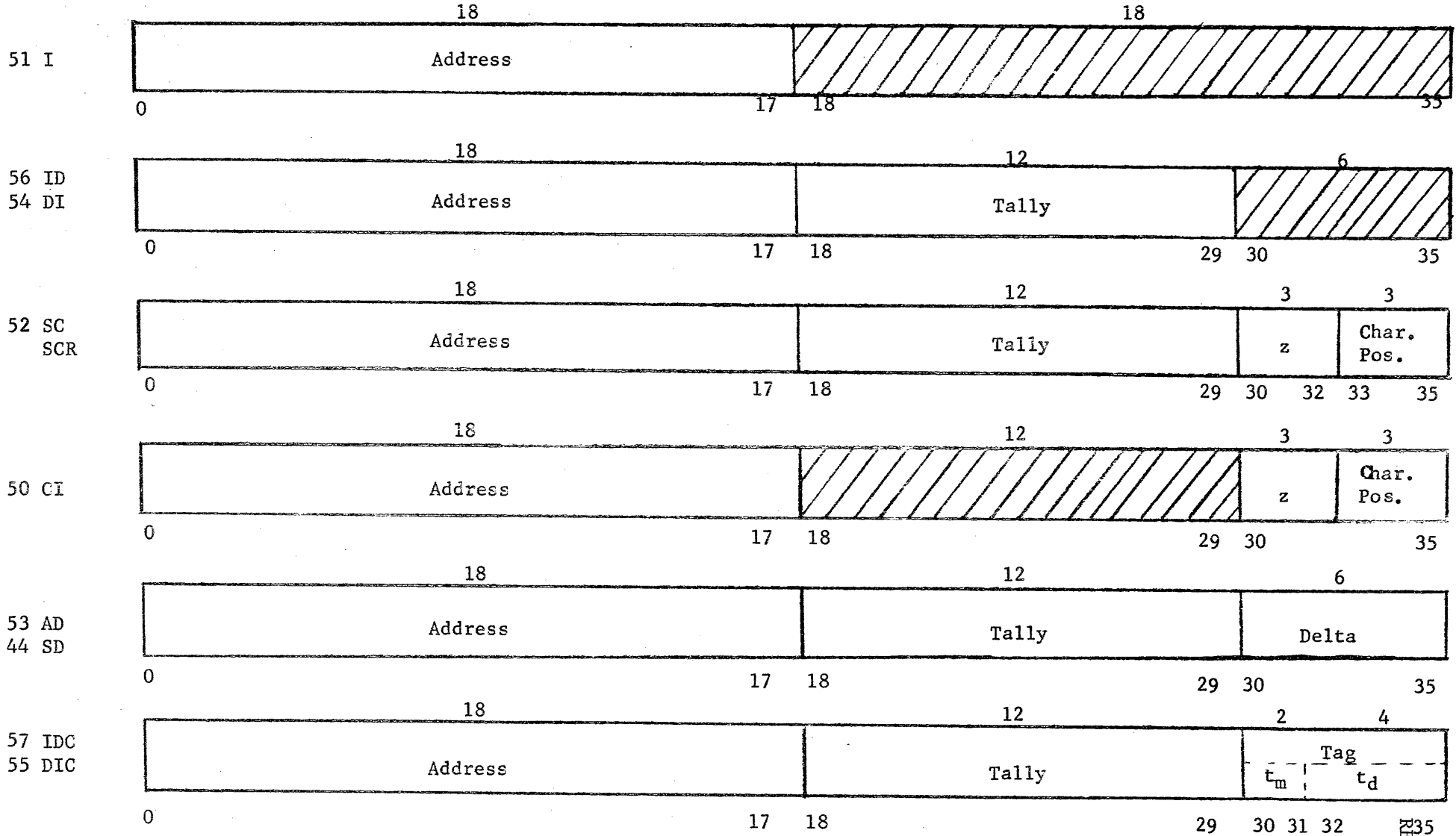


INDIRECT VARIATION (I)

FLOWCHART



INDIRECT WORD Formats for IT Modification



102.1

## ADDRESS TALLY PSEUDO-OPERATIONS

### TALLY A,T,C,(TALLY)

USED FOR I, ID, DI, SC AND CI TYPES OF TALLY MODIFICATION, WHERE SC AND CI ARE FOR SIX (6) BIT CHARACTERS.

### TALLYB A,T,B,(TALLY BYTE)

USED FOR SC AND CI TYPES OF TALLY MODIFICATION, WHERE NINE (9) BIT BYTES (CHARACTERS) ARE DESIRED.

### TALLYD A,T,D,(TALLY AND DELTA)

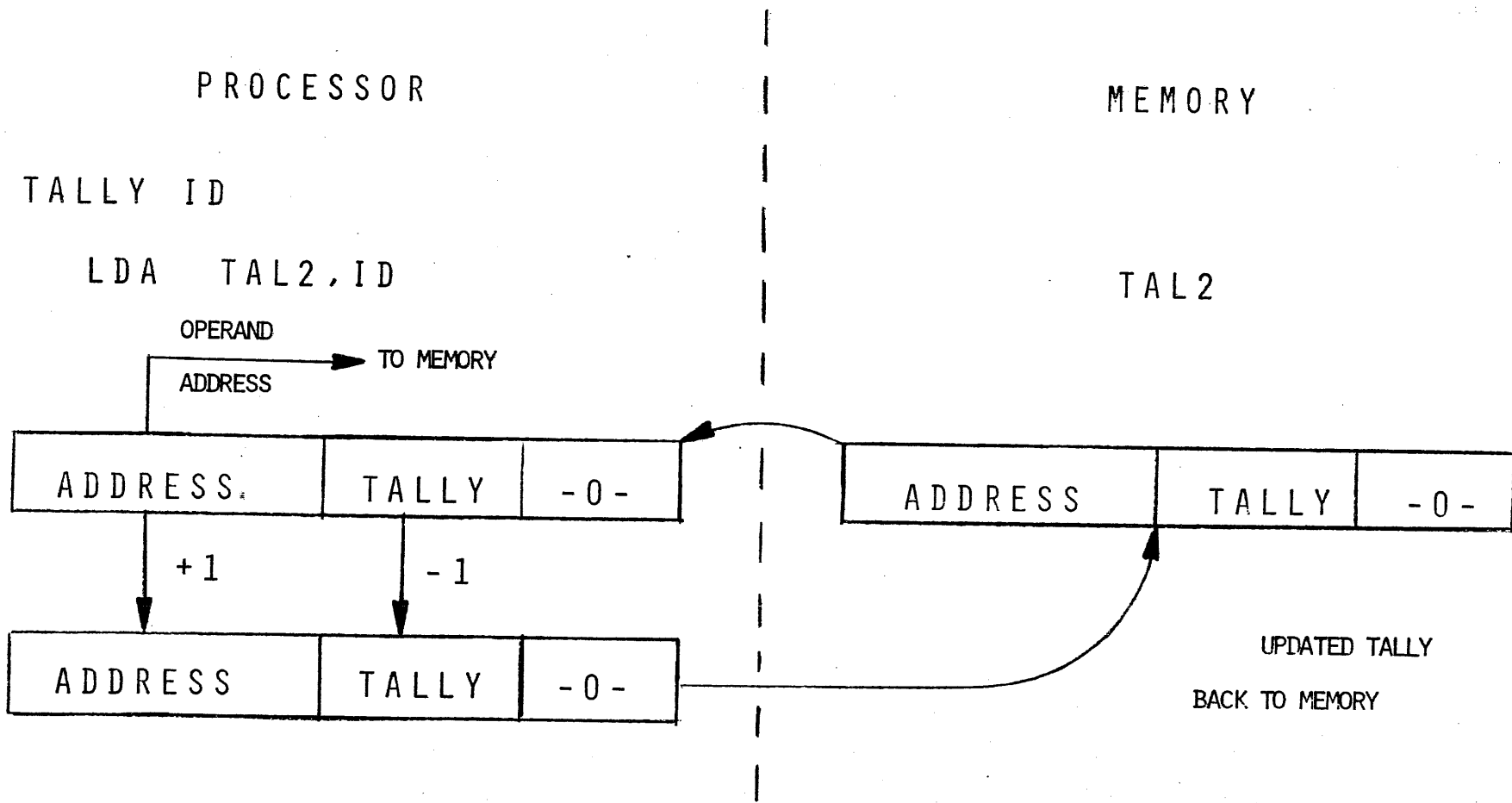
USED FOR AD AND SD TYPES OF TALLY MODIFICATION.

### TALLYC A,T,MOD,(TALLY AND CONTINUE)

USED FOR IDC AND DIC TYPES OF TALLY MODIFICATION.

## ABBREVIATIONS:

<u>CODE</u>	<u>MEANING</u>
A	ADDRESS
T	TALLY
C	CHARACTER POSITION
B	BYTE
D	DELTA ( $\Delta$ )
MOD	MODIFIER

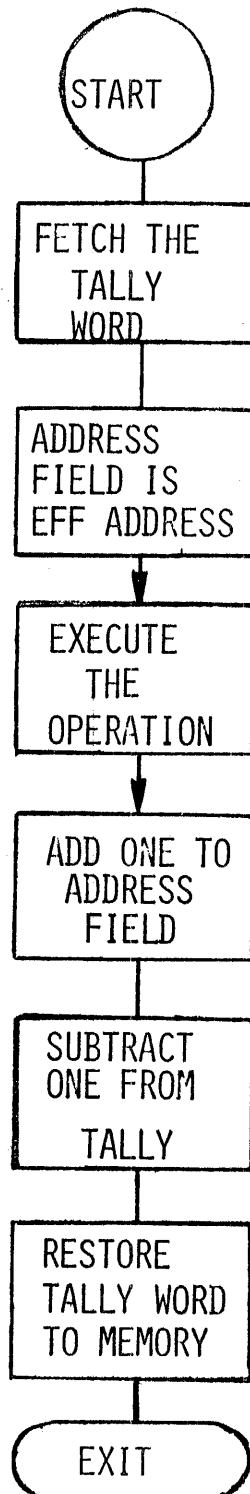


104

1. FETCH TALLY INDIRECT WORD
2. USE OPERAND ADDRESS FOR INSTRUCTION EXECUTION.
3. ADD ONE (1) TO OPERAND ADDRESS AND RESTORE IN TALLY WORD.
4. SUBTRACT ONE (1) FROM TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.

# INCREMENT ADDRESS-DECREMENT TALLY (ID)

## FLOWCHART



## INDEXING EXAMPLE

PROBLEM: MOVE 50 WORDS OF DATA FROM HERE  
 TO THERE.

1	8	1
	LDX7	6
	LDX6	50, DU
GO	LDA	0, DU
	STA	HERE, 6
	ADX6	THERE, 6
	SBX7	1, DU
	TNZ	1, DU
		GO

## INDIRECT THEN TALLY -ID- EXAMPLE

	LDA	TAL1, ID
	STA	TAL2, ID
	TTF	*-2
	:	
	:	
TAL1	TALLY	HERE, 50
TAL2	TALLY	THERE, 50

## I AND ID EXAMPLE

PROBLEM: SEARCH A 100 WORD TABLE AND COMMENT ALL NEGATIVES FOUND THEREIN.

1	8	1 6
START	LDA TPL NEG STA TTF :	TAL1, I *+2  TAL1, ID START
TAL1	TALLY	TABLE, 100

## TALLY REFRESHING

1	8	1 6
	LDA	TALREF
	STA	TAL1
	.	
	.	
TALREF	TALLY	TABLE, 100
TAL1	BSS	1



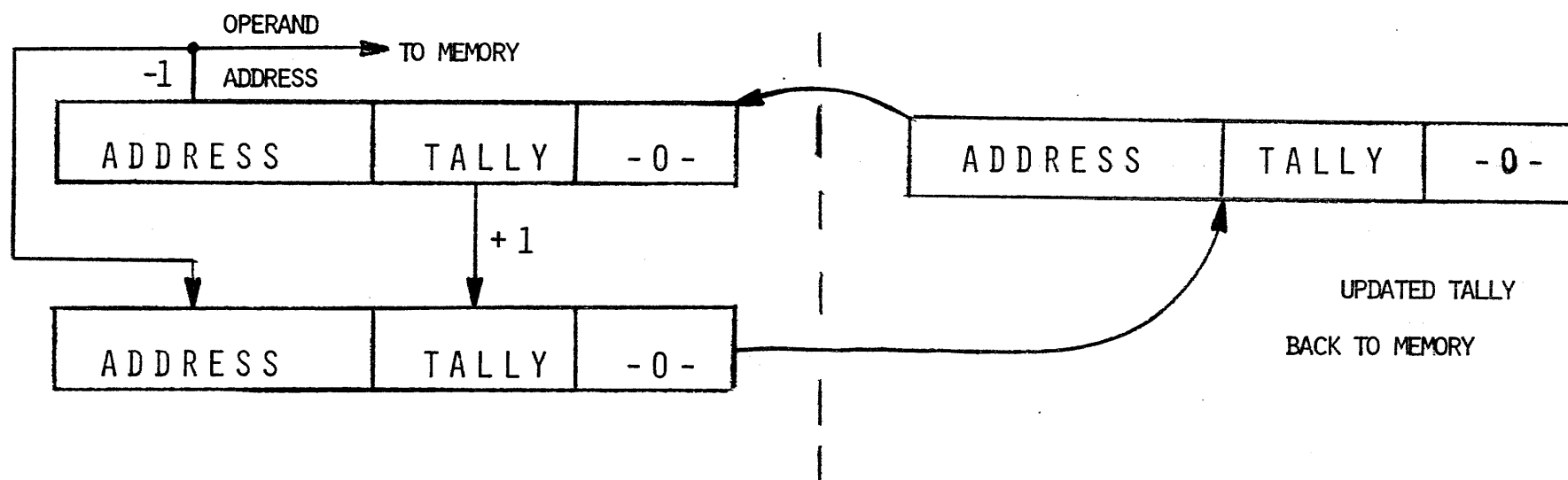
PROCESSOR

MEMORY

TALLY DI

STA TAL3,DI

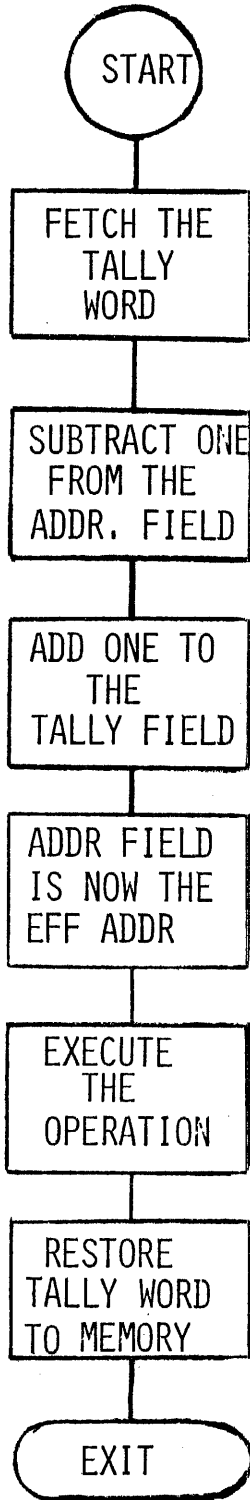
TAL3



109

1. FETCH TALLY INDIRECT WORD
2. SUBTRACT ONE (1) FROM OPERAND ADDRESS AND USE.
3. RESTORE UPDATED ADDRESS IN TALLY WORD.
4. ADD ONE (1) TO TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.

DECREMENT ADDRESS-INCREMENT TALLY (DI)  
FLOWCHART



## DECREMENT ADDRESS INCREMENT TALLY -DI- EXAMPLE

PROBLEM: TRANSFER 500 WORDS OF DATA FROM ONE TABLE TO ANOTHER IN A REVERSE ORDER.

1	8	1 6
	LDAQ	TALR
	STAQ	TAL1
	LDA	TAL1, ID
	STA	TAL2, DI
	TTF	*-2
	.	
	.	
TALR	ETALLY	FORWRD, 500
	TALLY	REVRSE, -500
TAL1	BSS	1
TAL2	BSS	1
FORWRD	BSS	500
REVRSE	BFS	500

# I, ID AND DI EXAMPLE

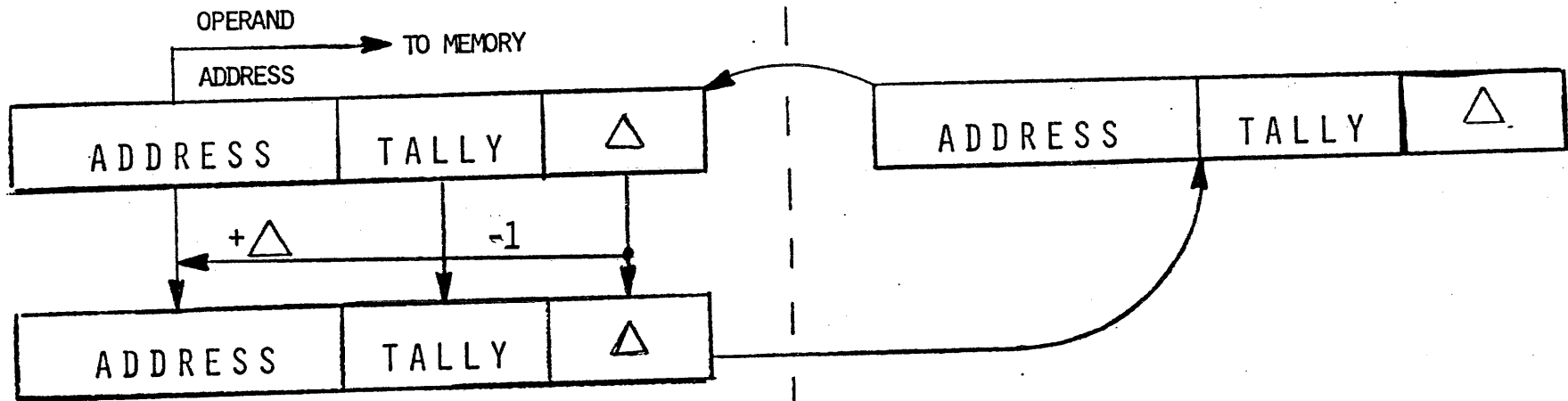
PROBLEM: REVERSE THE ORDER OF THE WORDS  
 OF A 500 WORD TABLE WITHIN ITSELF.

		I
I	8	6
	.	
	.	
	.	
GOOD	LDA	TAL1, I
	LDQ	TAL2, DI
	STA	TAL2, I
	STQ	TAL1, ID
	TTF	GOOD
	.	
	.	
TAL1	TALLY	TABLE, 250
TAL2	TALLY	TABLE+500, -250
TABLE	BSS	500

PROCESSOR  
 TALLY AD  
 LDA TAL4,AD

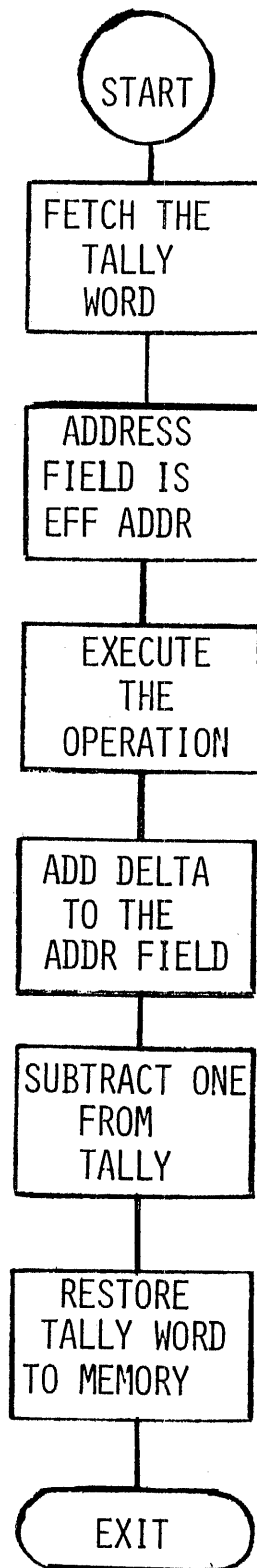
MEMORY

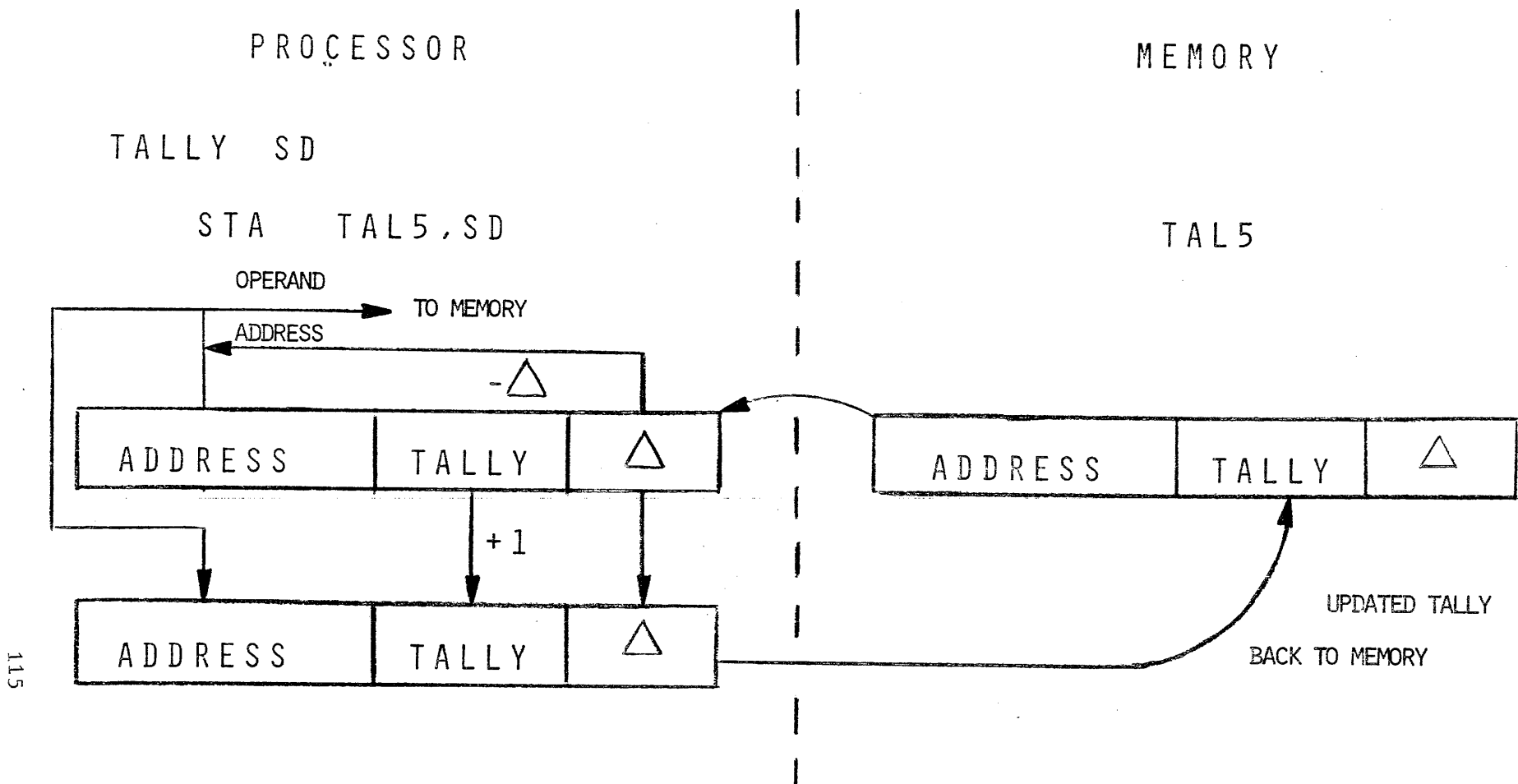
TAL4



1. FETCH TALLY INDIRECT WORD
2. USE OPERAND ADDRESS FOR INSTRUCTION EXECUTION.
3. ADD DELTA TO OPERAND ADDRESS AND RESTORE IN TALLY WORD.
4. SUBTRACT ONE (1) FROM TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.

ADD DELTA VARIATION (AD)  
FLOWCHART

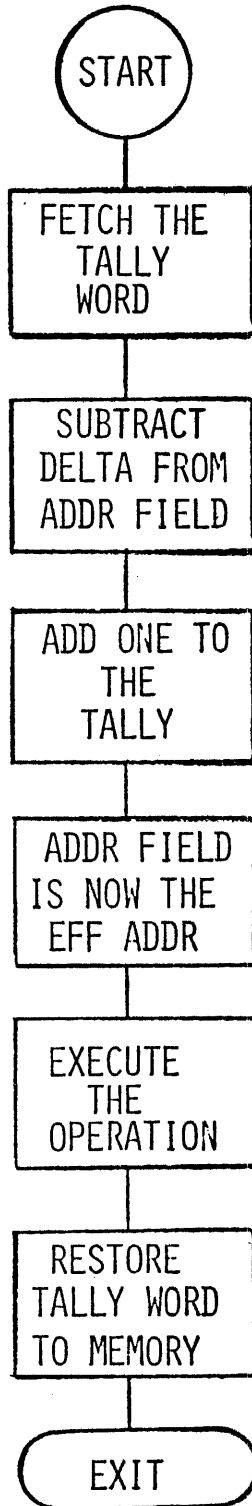




115

1. FETCH TALLY INDIRECT WORD
2. SUBTRACT DELTA FROM OPERAND ADDRESS AND USE.
3. RESTORE UPDATED ADDRESS IN TALLY WORD.
4. ADD ONE (1) TO TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.

SUBTRACT DELTA VARIATION (SD)  
FLOWCHART





ADD DELTA -AD- AND SUBTRACT DELTA -SD-

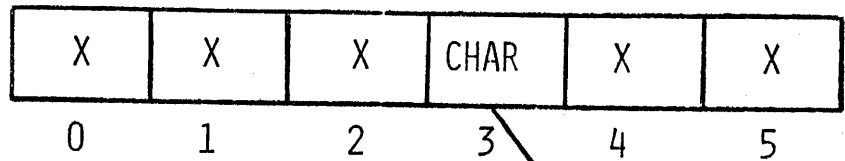
PROBLEM: REVERSE THE ORDER OF ONE TABLE INTO ANOTHER USING DOUBLE PRECISION COMMANDS.

1	8	1
		6
	:	
	:	
BEGIN	LDÄQ	TAL1,AD
	STAQ	TAL2,SD
	TTF	BEGIN
	:	
	:	
TAL1	TÄLLYD	TABLEA,4096,2
TAL2	TÄLLYD	TABLEB,,2
TABLEA	BSS	8192
TABLEB	BFS	8192

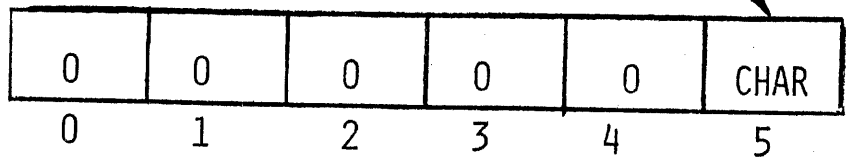
CHARACTER OPERATIONS

OPERAND FROM MEMORY  
SIX-BIT CHARACTER

OPERAND  
FROM  
MEMORY



TO  
PROCESSOR  
REGISTER  
A OR Q



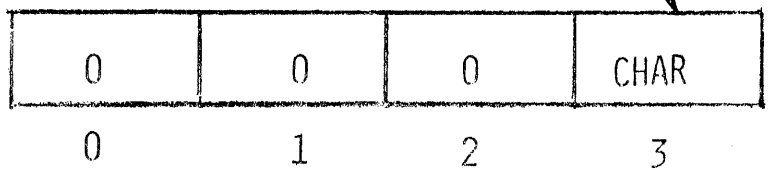
CHARACTER OPERATIONS

OPERAND FROM MEMORY  
NINE-BIT CHARACTER

OPERAND  
FROM  
MEMORY



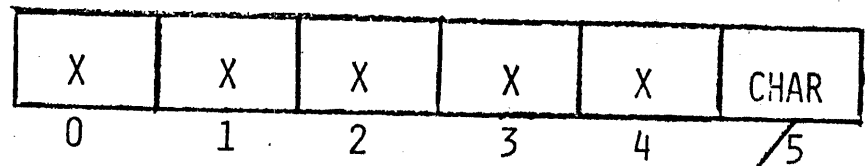
TO  
PROCESSOR  
REGISTER  
A OR Q



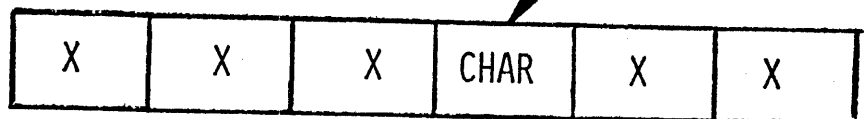
CHARACTER OPERATIONS

SIX-BIT CHARACTER

FROM  
A OR Q



TO  
MEMORY



PROCESSOR

MEMORY

TALLY CI

LDA TAL7, CI

TAL7

OPERAND  
ADDRESS → TO MEMORY

121

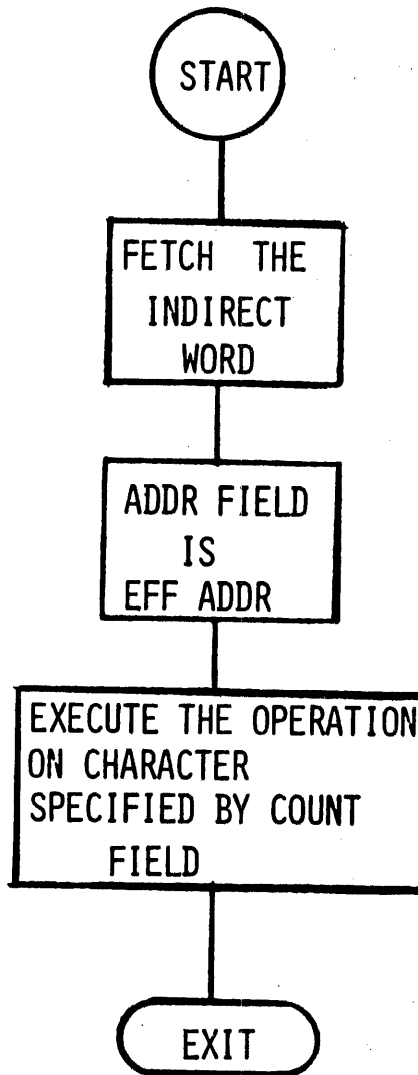


NOTE:

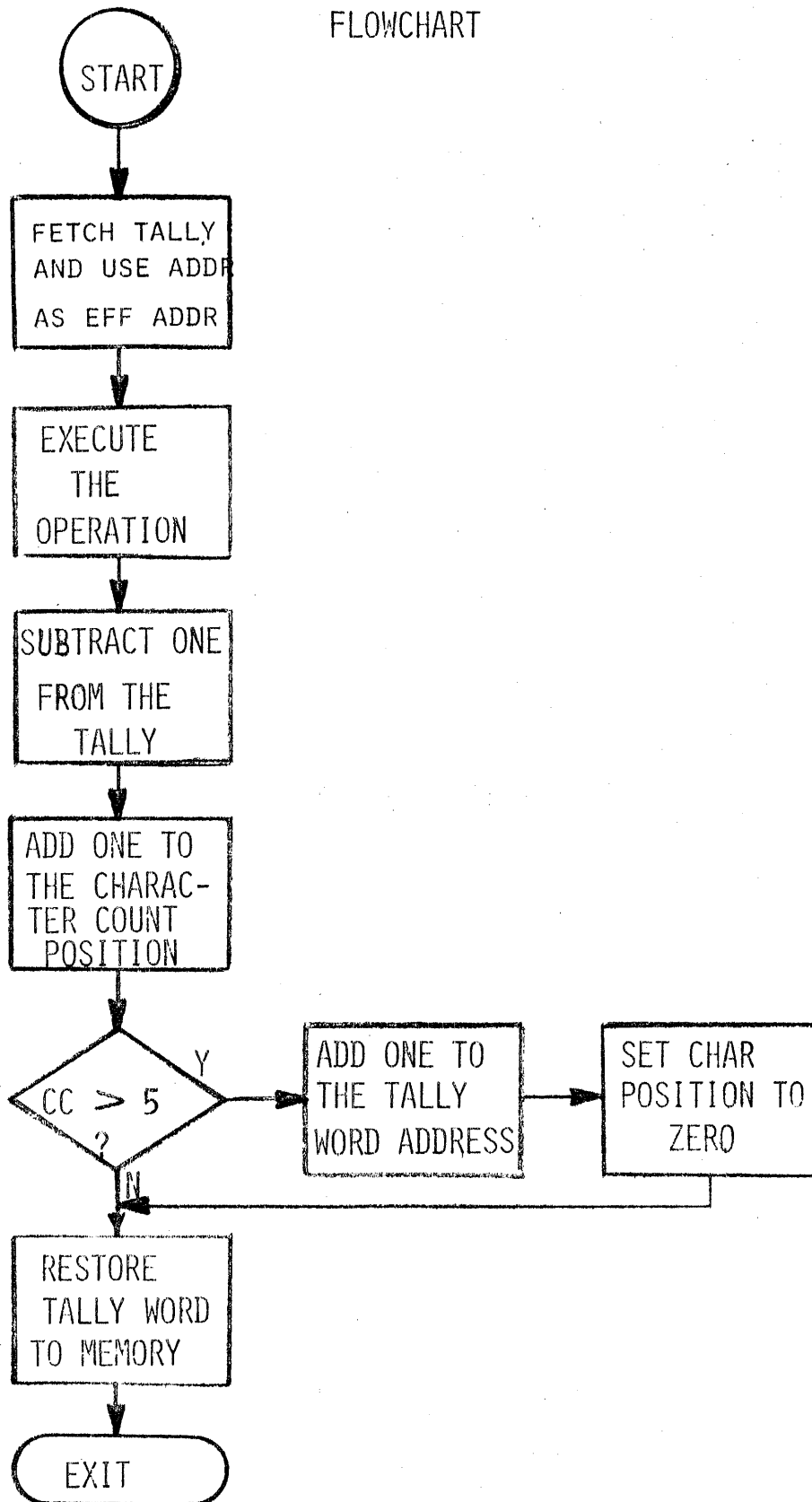
CI TALLY INDIRECT WORD NOT ALTERED IN MEMORY.

# CHARACTER FROM INDIRECT VARIATION (CI)

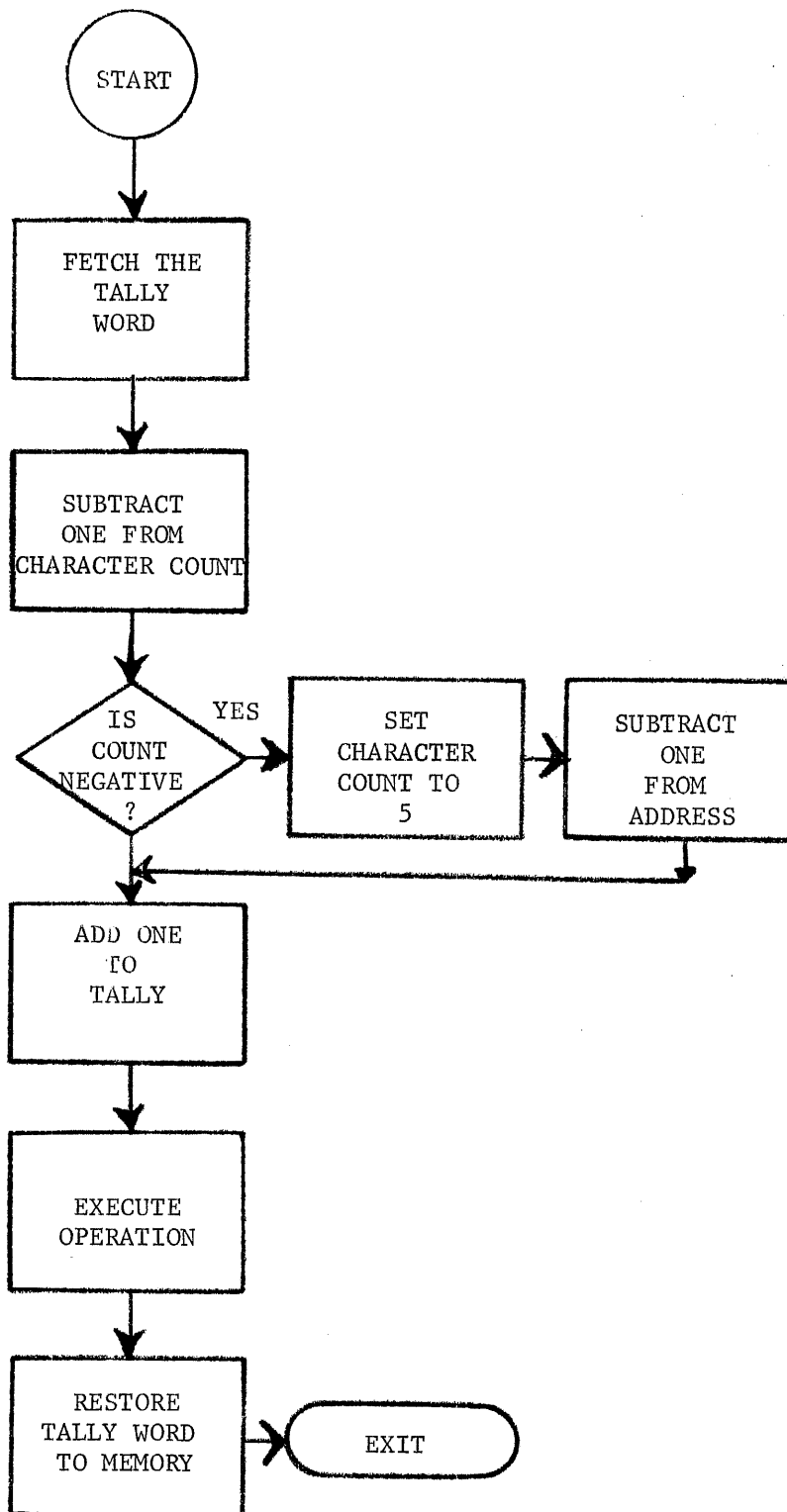
## FLOWCHART



SEQUENCE CHARACTER VARIATION (SC)  
FLOWCHART



# SEQUENCE CHARACTER REVERSE (SCR) FLOWCHART





REVERSING TABLE WITHIN ITSELF

CHARACTER BY CHARACTER

LDA	TAL1,SCR
LDQ	TAL2,CI
STQ	TAL1,CI
STA	TAL2,SC
TTF	*-4
.	
.	
.	

TAL1	TALLY	TABLE+200,0,0
TAL2	TALLY	TABLE,600,0
TABLE	BSS	200

-CI- AND -SC-

PROBLEM: GIVEN

DOUBLE-WORD    XXXXXX XX.XXX

INSERT \*'S IN PLACE OF LEADING ZEROS

		1
1	8	6
GO	LDQ	=3H00*,DL
	LDA	TALI,CI
	TNZ	*+3
	STQ'	TALI,SC
	TRA	GO+1
	.	
TALI	TALLY	WORD,,0

PROBLEM:

GIVEN  
DOUBLE-WORD    ~~XXXXXX~~ XXXXXX  
INSERT DEC PT AT IMPLIED LOC

·  
·  
·  
LDQ            =3H ~~bb.~~, DL  
LDA            TAL1, SC  
STA            TAL2, SC  
TTF            \*-2  
STQ            TAL2, CI

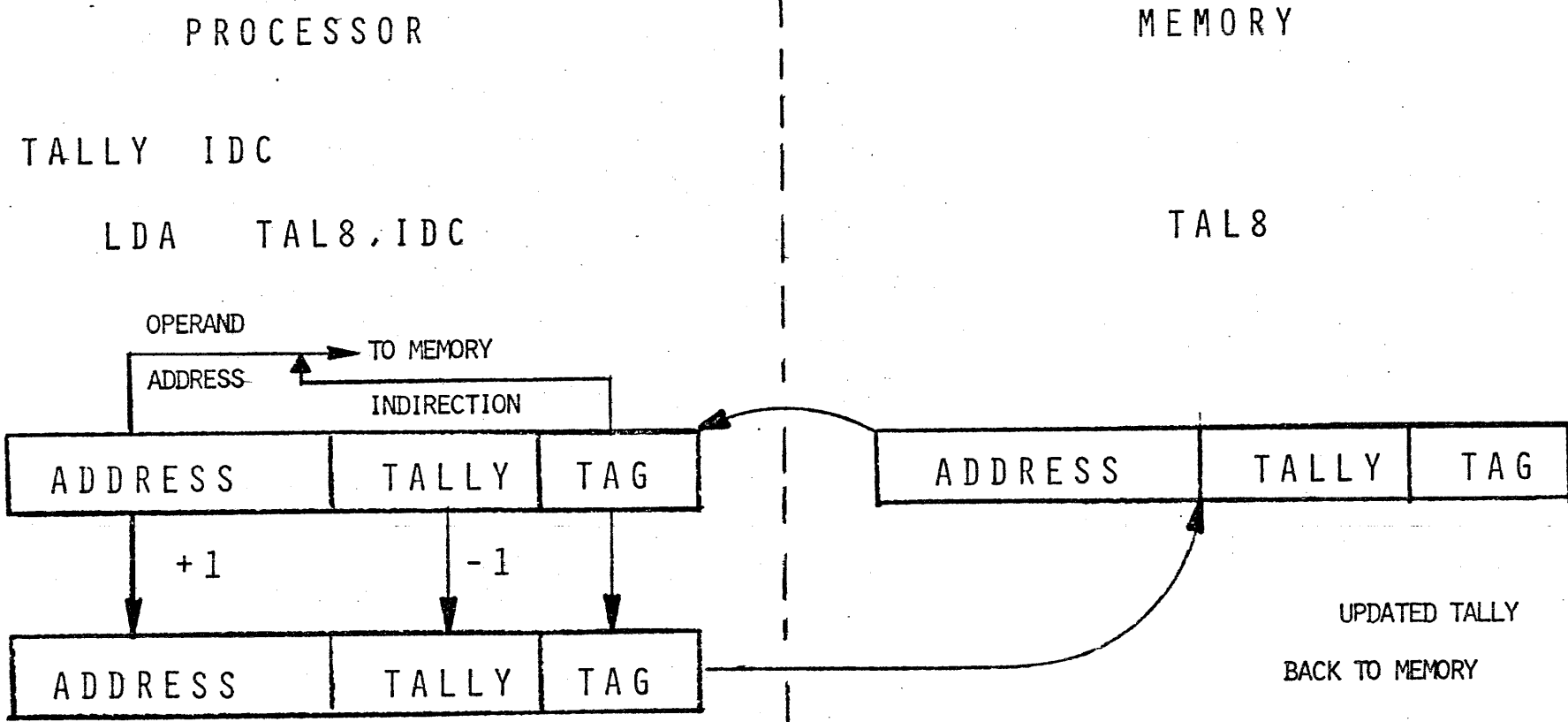
·  
·  
TAL1    TALLY    WORD, 8, 1  
TAL2    TALLY    WORD, 8, 0

# CHECK PROTECTION AND DECIMAL INSERTION

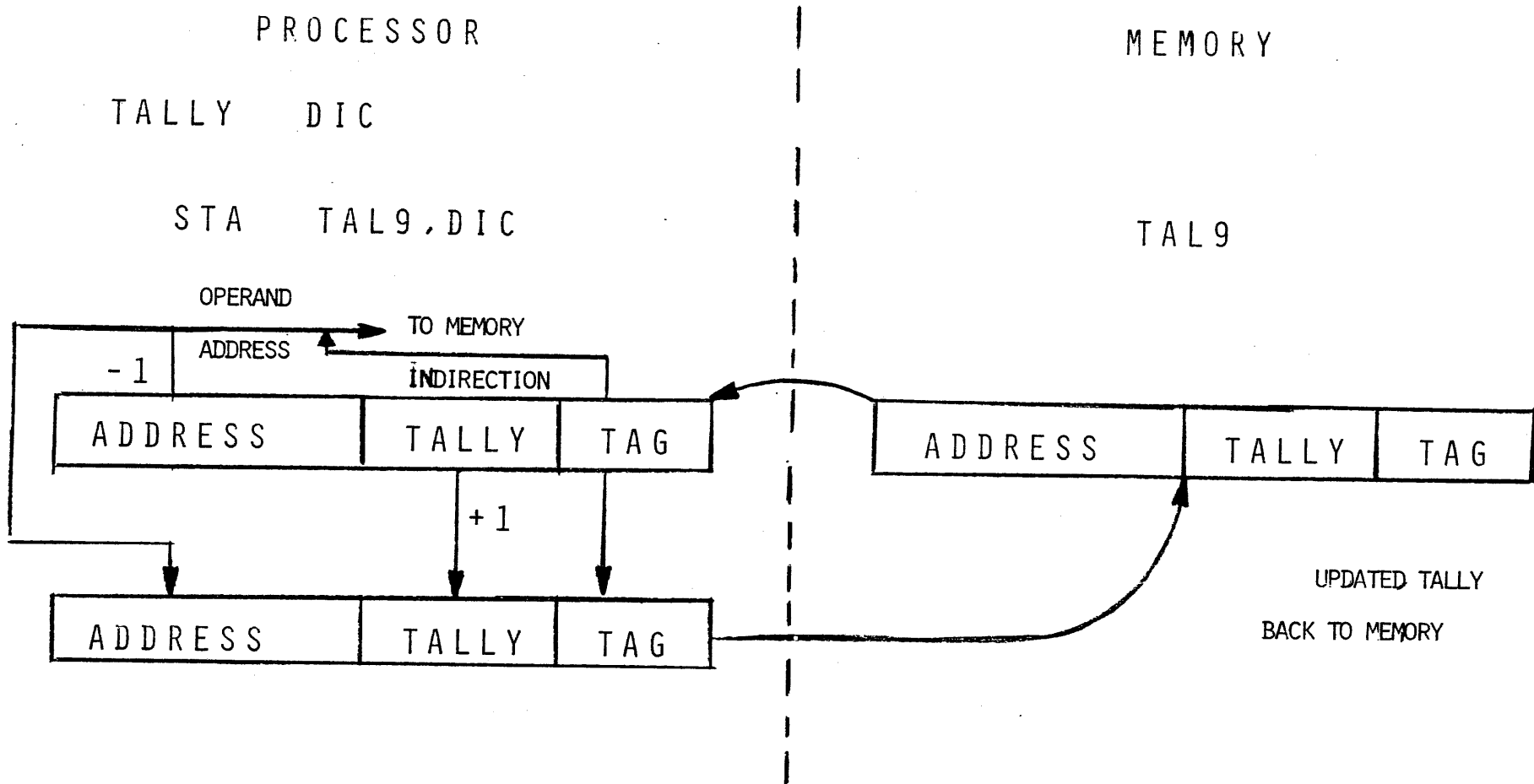
## EXAMPLES:

BEFORE            Ø000XX    XXXXXX    AFTER    \*\*\*XXX    XX.XXX  
 BEFORE            Ø00000    000XXX    AFTER    \*\*\*\* \*\*    \*\*.XXX

	1	8	16
		LDQ	=3HØ.* ,DL
		LDA	TAL1 ,SC
		TNZ	CHAR
		STQ	TAL2 ,SC
		TTF	-3 ,IC
		TRA	INSERT
		LDA	TAL1 ,SC
CHAR		STA	TAL2 ,SC
		TTF	*-2
INSERT		QRS	6
		STQ	TAL2 ,CI
		.	
		.	
TAL1		TALLY	WORD , 8 , 1
TAL2		TALLY	WORD , 8 , 0



1. FETCH TALLY INDIRECT WORD
2. TRAP OPERAND ADDRESS AND MODIFICATION FOR INSTRUCTION EXECUTION.
3. ADD ONE (1) TO OPERAND ADDRESS AND RESTORE IN TALLY WORD.
4. SUBTRACT ONE (1) FROM TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.
6. USE TRAPPED ADDRESS FOR FURTHER INDIRECTION IF INDICATED BY THE TAG FIELD.



128

1. FETCH TALLY INDIRECT WORD
2. SUBTRACT ONE (1) AND TRAP OPERAND ADDRESS AND MODIFICATION.
3. RESTORE UPDATED OPERAND ADDRESS IN TALLY WORD.
4. ADD ONE (1) TO TALLY AND RESTORE IN TALLY WORD.
5. RESTORE UPDATED TALLY INDIRECT WORD TO MEMORY.
6. USE TRAPPED ADDRESS FOR FURTHER INDIRECTION IF INDICATED BY THE TAG FIELD.

# REPEAT INSTRUCTION

## INSTRUCTION FORMAT

TALLY 1-256		EXIT CONDITIONS	OP CODE	I	DELTA 0-63
----------------	--	--------------------	---------	---	---------------

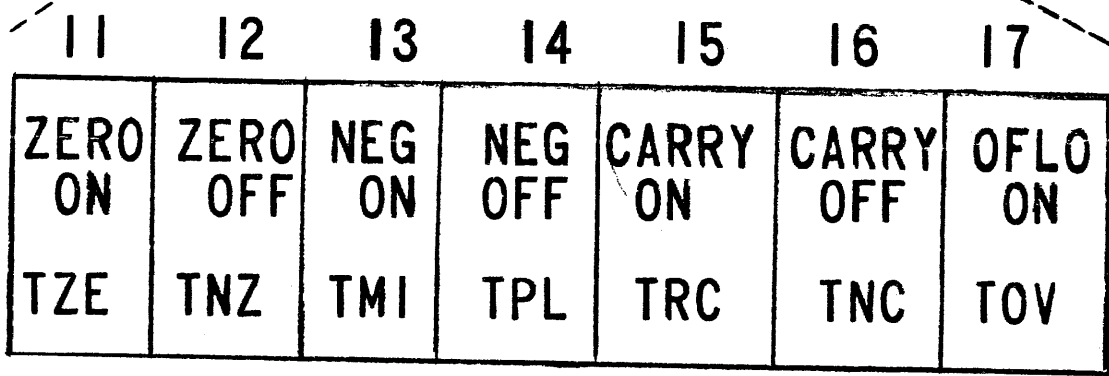
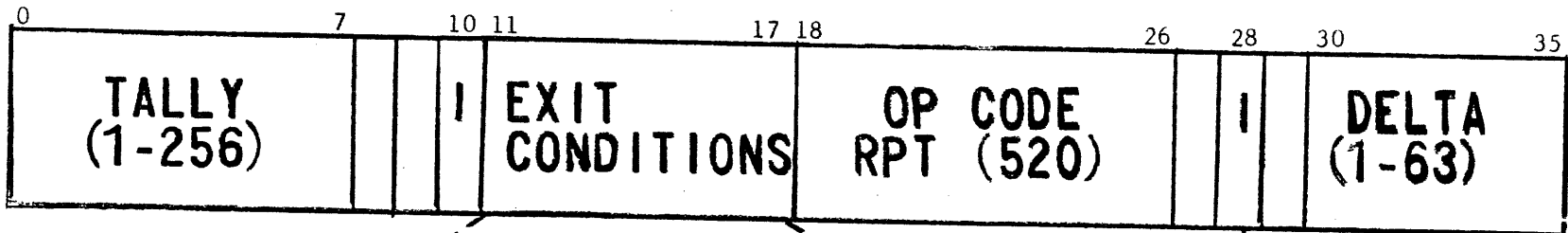
## CODING FORMAT

1	8	16
	RPT	TALLY, DELTA, EXIT CONDITIONS

## SUMMARY

1. REPEAT NEXT INSTRUCTION N TIMES.
2. INCREMENT OPERAND ADDRESS BY DELTA. *DELTA MUST BE POSITIVE*
3. EXIT PRIOR TO TALLY RUNOUT IF EXIT CONDITIONS MET.

# REPEAT INSTRUCTION FORMAT



INHIBIT INTERRUPTS

130



## REPEAT INSTRUCTION

### RULES:

REPEATED INSTRUCTION MUST BE MODIFIED  
BY INDEX REGISTER.

ONLY X1 THRU X7 MAY BE USED.

TALLY, EXIT CONDITIONS AUTOMATICALLY  
PLACED IN X0.

TALLY DECREMENTED IN X0.

### CODING EXAMPLE:

<u>1</u>	<u>8</u>	<u>16</u>
	LDA	KEY
	EAX4	TABLE
	RPT	64, 1, TZE
	CMPA	0, 4
	TZE	FOUND
	.	
	.	
TABLE	BSS	64
KEY	BSS	1

ADDRESS MODIFICATION  
OF  
REPEATED INSTRUCTION

FIRST TIME:

EFFECTIVE ADDRESS = TA + C(X<sub>N</sub>)

$$EA + \Delta \Rightarrow C(X_N)$$

N TIMES:

EFFECTIVE ADDRESS = C(X<sub>N</sub>)

$$EA + \Delta \Rightarrow C(X_N)$$

TA = TENTATIVE ADDRESS

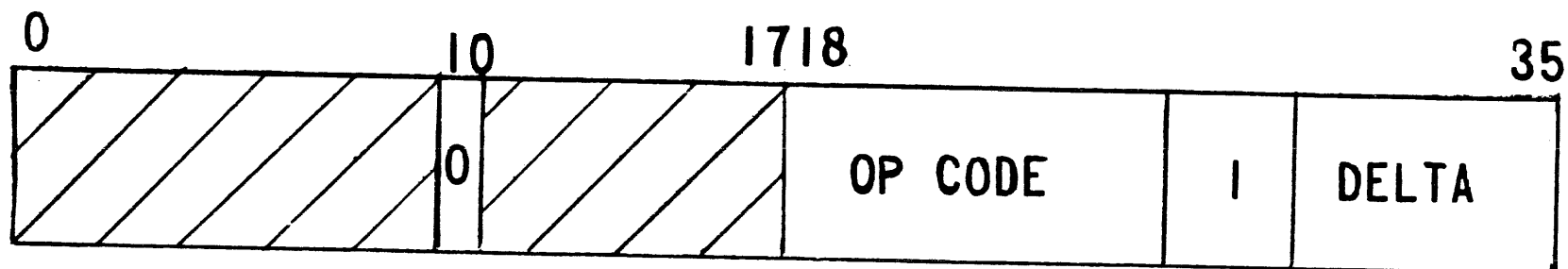
EA = EFFECTIVE ADDRESS

CODING EXAMPLE  
USING  
REPEAT INSTRUCTION

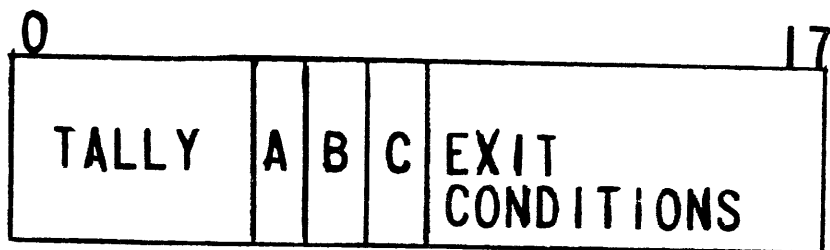
PROBLEM: SEARCH A TABLE OF 64 WORDS FOR NEGATIVE  
VALUES. STORE ZEROS IN EVERY NEGATIVE WORD.

```
GO      EAX7  TABLE
        RPT   64,1,TMI
        LDA   0,7
        TPL   DONE
        STZ   -1,7
        TRA   GO
DONE     XXX
        .
        .
TABLE   BSS   64
```

RPTX - RPDX



PROGRAMMER LOADS XO:



RPTX

DELTA

RPDX

, DELTA

CODING EXAMPLES

GO	EAX7	TABLE
	RPT	64,1,TMI
	LDA	0,7
	TPL	DONE
	STZ	-1,7
	TRA	GO

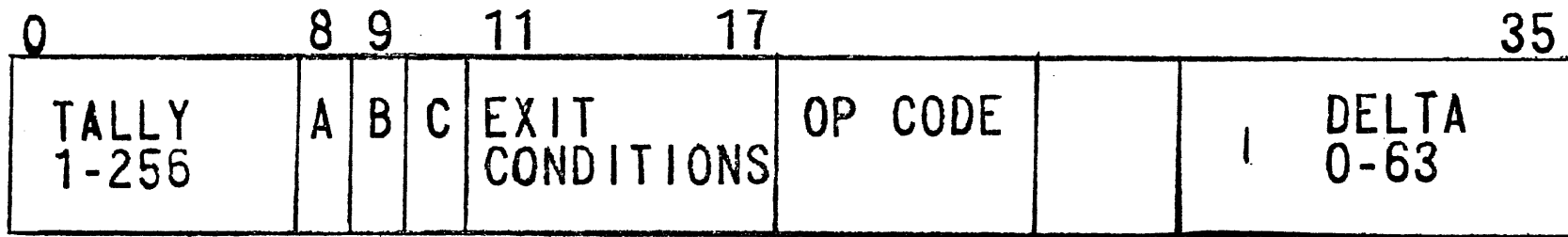
	EAX7	TABLE
	RPT	64,1,TMI
	LDA	,7
	TPL	DONE
BACK	STZ	-1,7
	CANXO	=0776000,DU
	TZE	DONE
	RPTX	,1
	LDA	,7
	TMI	BACK

DONE

.  
.

	EAX7	TABLE
GO	LDXO	GO
	RPTX	64,1,TMI
	LDA	0,7
	TPL	DONE
	STZ	-1,7
	CANXO	=0776000,DU
	TNZ	GO

# REPEAT DOUBLE FORMAT



RPD

RPDA

RPDB

**RPDA**     ADD DELTA ONLY TO FIRST REPEATED INSTRUCTION

**RPDB**     ADD DELTA ONLY TO SECOND REPEATED INSTRUCTION

REPEAT DOUBLE EXAMPLES

	EAX6	FROM
	EAX7	TO
	RPD	100,2
	LDAQ	0,6
	STAQ	0,7
	.	
	.	
FROM	BSS	200
TO	BSS	200

SAME INDEX REGISTER MAY NOT BE USED FOR BOTH INSTRUCTIONS

LDI	,DL
LDA	CARDIN
EAX2	CARDIN+2
EAX3	0
RPDA	22,1
ADLA	0,2
AWCA	=0,3
CMPA	CARDIN+1
TNZ	ERROR

## REPEAT LINK (RPL)

INSTRUCTION FORMAT:



CODING FORMAT:

RPL      TALLY, EXIT CONDITIONS

ADDRESS MODIFICATION:

FIRST TIME

$$\text{EFFECTIVE ADDRESS} = \text{TA} + \text{C}(X_n)$$

$$\text{EA} \Rightarrow \text{C}(X_n)$$

N TIMES

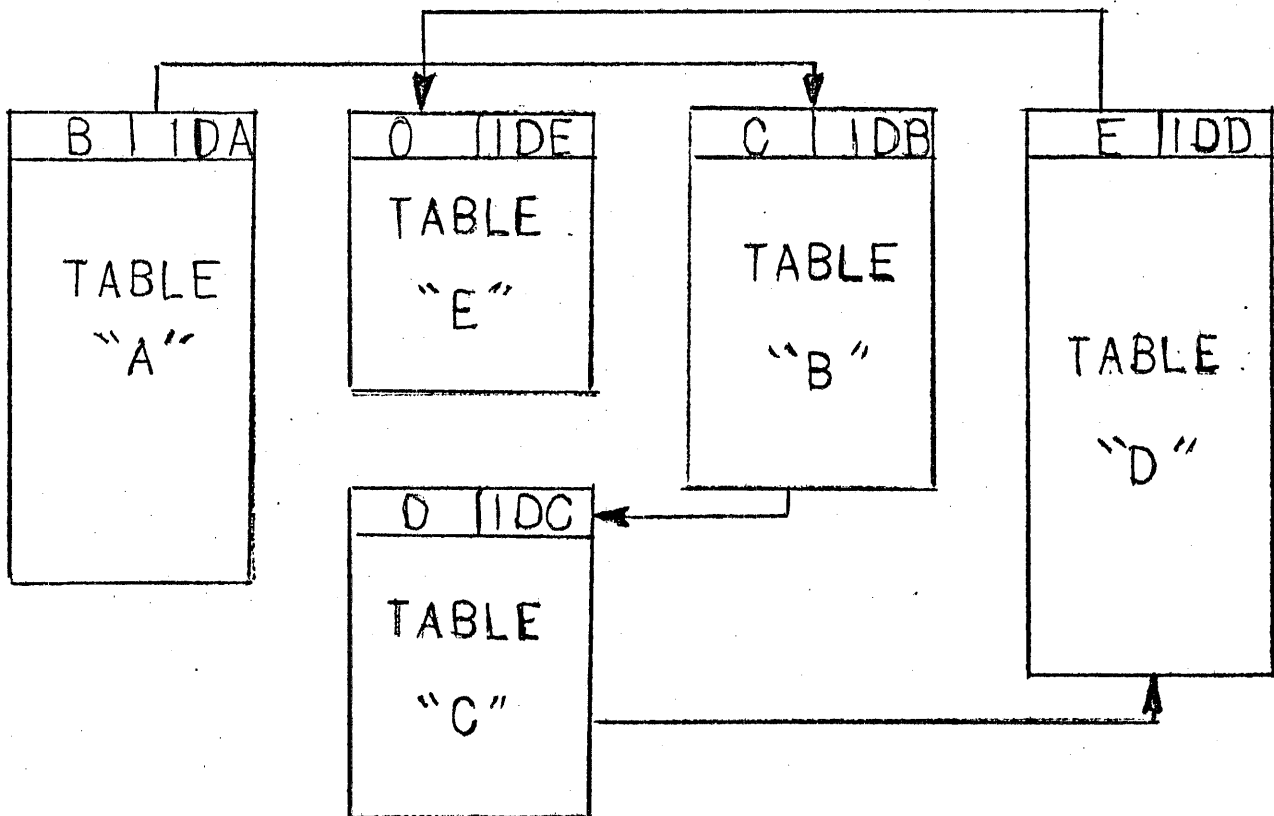
$$\text{NEW EFFECTIVE ADDRESS} = \text{C}(\text{EA}_{0\dots 17})$$

$$\text{C}(\text{NEA}_{0\dots 17}) \Rightarrow \text{C}(X_n)$$



# KEYWORD

0	17	18	35
POINTER TO NEXT TABLE		IDENTIFICATION OF TABLE LOOKED AT	

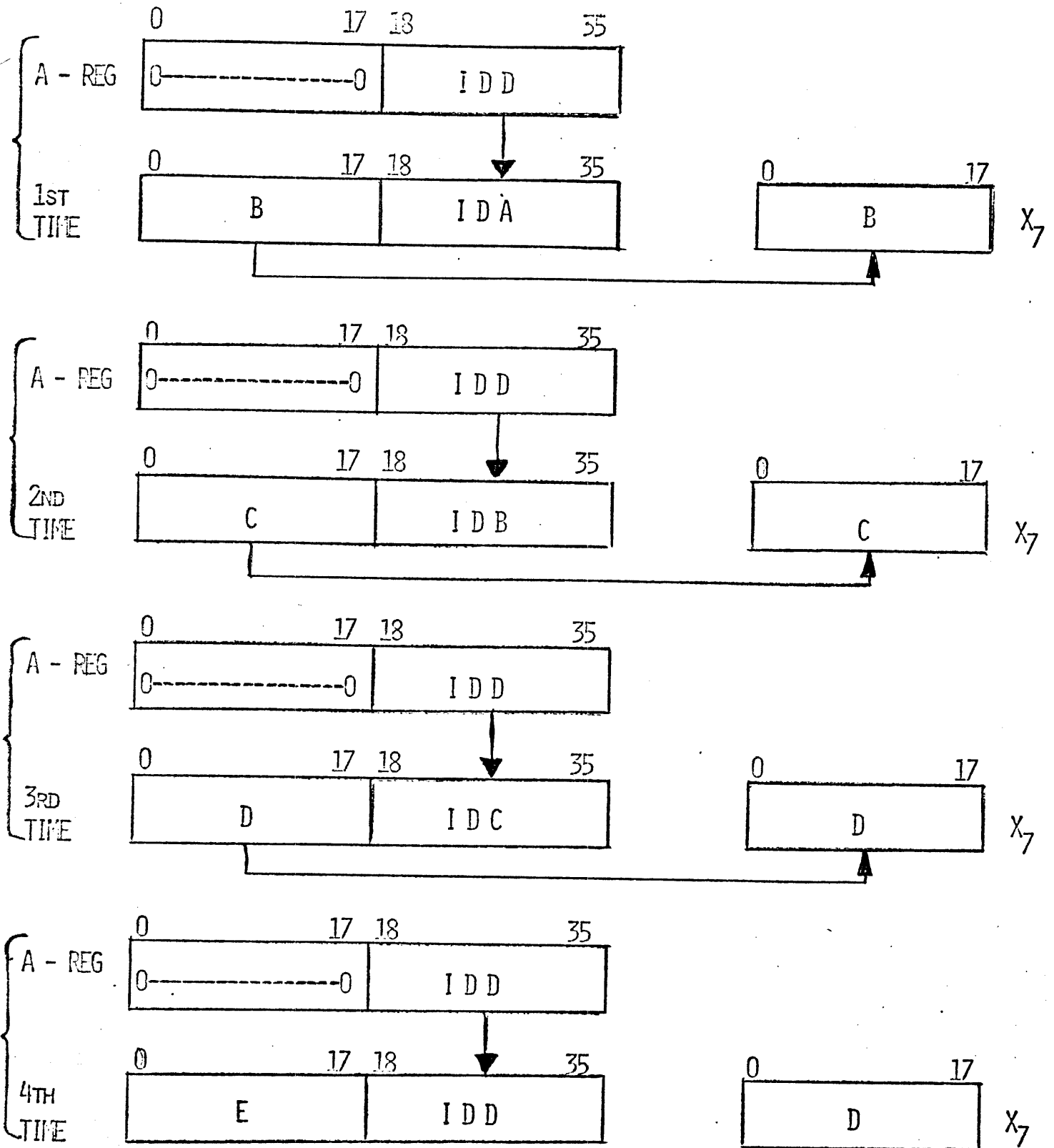


## RPL CODING

### EXAMPLE

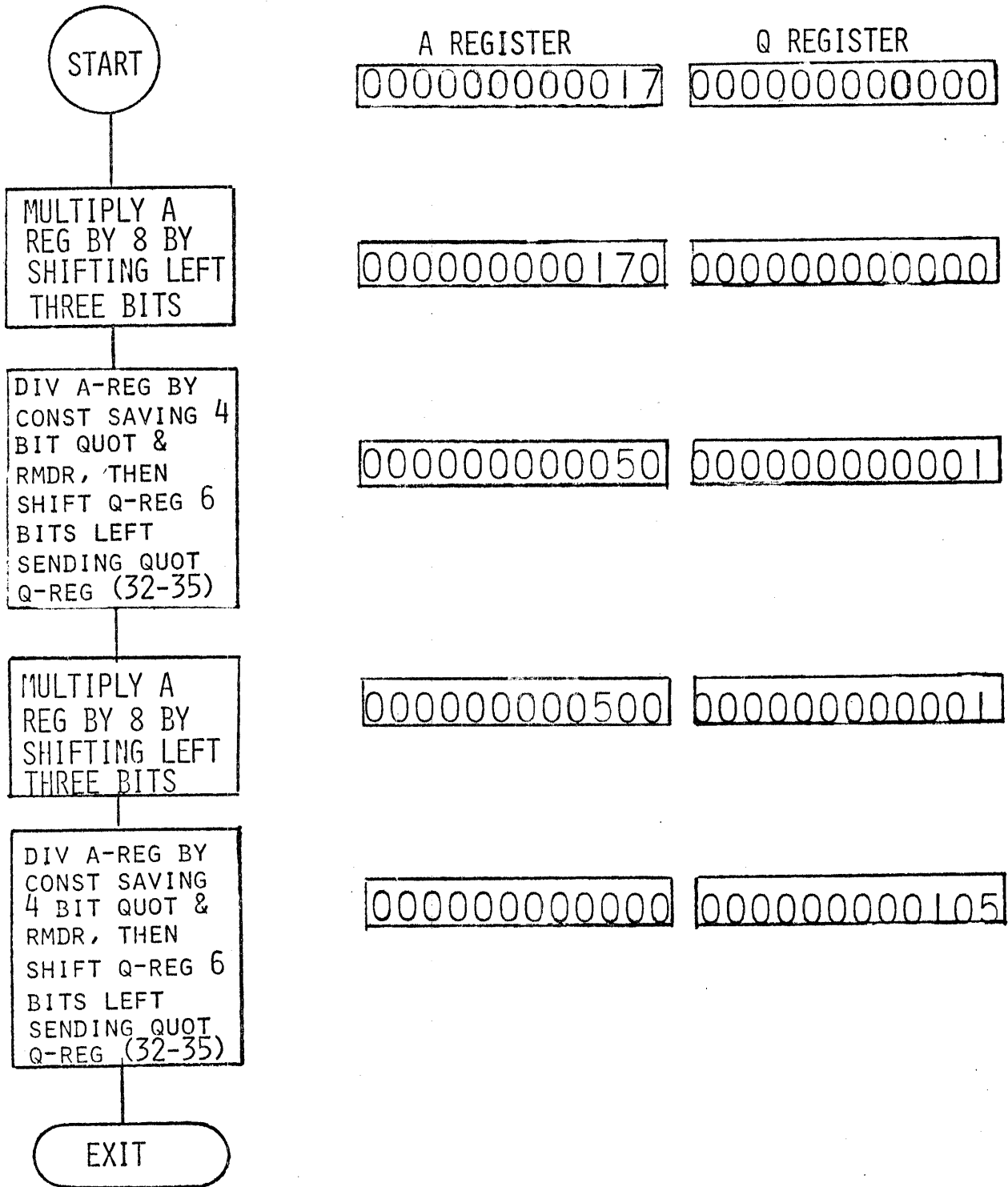
EAX7	A
LCQ	= 1, DU
LDA	= 3HIDD, DL
RPL	5, TZE
CMK	0, 7
TNZ	ERROR
.	
.	
.	

### EXECUTION OF RPL



BCD INSTRUCTION FLOWCHART

LDA =017  
 EAQ 0  
 BCD =80  
 BCD =64



BINARY TO BCD CONVERSION CONSTANTS

CONVERSION STEP	STARTING RANGE OF C (AR)	$10^{10-1}$	$10^{10-1}$	$10^{9-1}$	$10^{8-1}$	$10^{7-1}$	$10^{6-1}$	$10^{5-1}$	$10^{4-1}$	$10^{3-1}$	$10^{2-1}$	$10^{1-1}$
		$10^{10+1}$	$10^{9+1}$	$10^{8+1}$	$10^{7+1}$	$10^{6+1}$	$10^{5+1}$	$10^{4+1}$	$10^{3+1}$	$10^{2+1}$	$10^{1+1}$	
1	$8^1 \times 10^9$	$8 \times 10^8$	$8 \times 10^7$	$8 \times 10^6$	$8 \times 10^5$	$8 \times 10^4$	$8 \times 10^3$	$8 \times 10^2$	$8 \times 10^1$	8		
2	$8^2 \times 10^8$	$8^2 \times 10^7$	$8^2 \times 10^6$	$8^2 \times 10^5$	$8^2 \times 10^4$	$8^2 \times 10^3$	$8^2 \times 10^2$	$8^2 \times 10^1$	$8^2$			
3	$8^3 \times 10^7$	$8^3 \times 10^6$	$8^3 \times 10^5$	$8^3 \times 10^4$	$8^3 \times 10^3$	$8^3 \times 10^2$	$8^3 \times 10^1$	$8^3$				
4	$8^4 \times 10^6$	$8^4 \times 10^5$	$8^4 \times 10^4$	$8^4 \times 10^3$	$8^4 \times 10^2$	$8^4 \times 10^1$	$8^4$					
5	$8^5 \times 10^5$	$8^5 \times 10^4$	$8^5 \times 10^3$	$8^5 \times 10^2$	$8^5 \times 10^1$	$8^5$						
6	$8^6 \times 10^4$	$8^6 \times 10^3$	$8^6 \times 10^2$	$8^6 \times 10^1$	$8^6$							
7	$8^7 \times 10^3$	$8^7 \times 10^2$	$8^7 \times 10^1$	$8^7$								
8	$8^8 \times 10^2$	$8^8 \times 10^1$	$8^8$									
9	$8^9 \times 10^1$	$8^9$										
10	$8^{10}$											

# BCD INSTRUCTION

## CONVERT 1460 OCTAL TO 816 DECIMAL

A-REG

Q-REG

00 00 00 00 14 60 = 816

00 00 00 00 00 00

X 8  
÷ 800

ALS 3

00 00 00 01 46 00 = 6528

=8 R128

QLS 6

00 00 00 00 00 00

00 00 00 00 02 00 = 128

00 00 00 00 00 10

X 8  
÷ 640

ALS 3

00 00 00 00 20 00 = 1024

=1 R384

QLS 6

00 00 00 00 10 00

00 00 00 00 06 00 = 384

00 00 00 00 10 01

X 8  
÷ 512

ALS 3

00 00 00 00 60 00 = 3072

=6 R0

QLS 6

00 00 00 10 01 00

00 00 00 00 00 00

00 00 00 10 01 06  
= 8 1 6

143

## BCD INSTRUCTION

CONVERTING VALUES TO 999999

EAX2	0	PLACE ZEROS IN X2
LDA	X	LOAD ACCUMULATOR WITH VALUE TO BE CONVERTED
RPT	6,1	REPEAT 6 TIMES, INCREMENT BY 1
BCD	TAB,2	DIVIDE BY TAB, TAB+1, ETC.
STQ	Y	STORE CONVERTED NUMBER IN Y
	.	
	.	
	.	
TAB DEC	300000, 640000, 512000, 409600, 327680	
DEC	262144	

BCD EXAMPLE  
 CONVERTING MAXIMUM VALUES

	EAX7	TABLE	PLACE ADDRESS OF TABLE IN X7
	EAQ	0	ZERO OUT QR
	LDA	VALUE	BINARY VALUE TO BE CONVERTED
	RPT	4,1	CONVERT FIRST 4 DIGITS
	BCD	0,7	STEP THRU TABLE
	STQ	RESULT	STORE Q IN MEMORY
* MEMORY	NOW	CONTAINS	00XXXX
	RPT	6,1	CONVERT REMAINING VALUE
	BCD	0,7	X7 CONTAINS ADDRESS OF TABLE+4
	STQ	RESULT+1	
		.	
TABLE	DEC	8E9B35,64E8B35,512E7B35	
	DEC	4096E6B35,3276800000	
	DEC	2621440000,2097152000	
	DEC	1677721600,1342177280	
	DEC	1073741824	



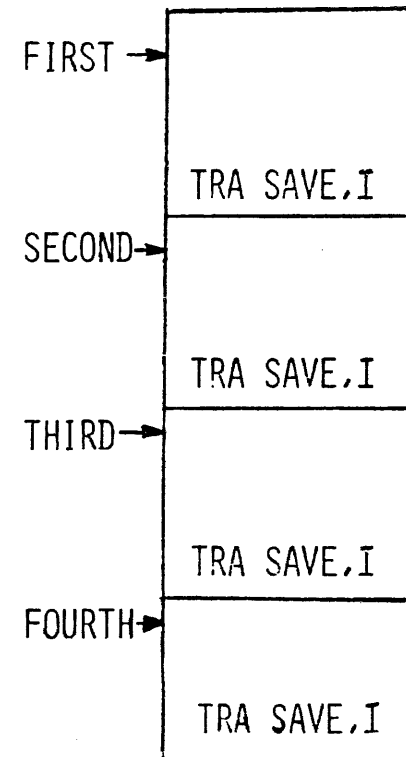
## XED CODING EXAMPLE

EXAMPLE: A PROGRAM IS MADE UP OF FOUR (4) DIVISIONS  
WHICH MAY BE ACCESSED AT DIFFERENT TIMES.

X7 INITIALIZED TO DETERMINE ENTRY.

```

XED      LOC.7
RE-ENTRY → XXX
          }
LOC  ESTC1  SAVE
      TRA   FIRST
      STC1  SAVE
      TRA   SECOND
      STC1  SAVE
      TRA   THIRD
      STC1  SAVE
      TRA   FOURTH
  
```



...ALSO LITERALS AND MACROS.

PREFACE

PROGRAM BREAK	2206
COMMON LENGTH	24
V COUNT BITS	5

PRIMARY SYMDEF ENTRY

.SMDEF	2001
.SAVE	2130

SECONDARY SYMDEF ENTRY

.EVN	2012
------	------

BLOCK	LENGTH
-------	--------

1 LABCOM	20
2 TALLYS	4

SYMREF

3 .SMREF	
----------	--

147

...ALSO LITERALS AND MACROS.

```

1      TTL      EXAMPLES OF PSUEDO OPERATIONS**
2      TTLS     ...ALSO LITERALS AND MACROS.
3      *****
4      *****
5      **
6      **                               THE FOLLOWING PROGRAM LISTING
7      **
8      **                               WILL SHOW MOST OF THE COMMONLY
9      **
10     **                               USED PSEUDO OPS IN A
11     **
12     **                               RELOCATABLE ASSEMBLY.
13     **
14     *****
15     *****
16     *
17     * THIS FIRST PSEUDO OP IS NECESSARY FOR ALL RELOCATABLE PROGRAMS IN ORDER
18     * TO PROVIDE AN ENTRY POINT FOR GELOAD. IF MORE THAN ONE SYMBOL APPEARS IN
19     * THE VARIABLE FIELD THE FIRST SYMBOLE WILL BE TAKEN AS THE ENTRY POINT.
20     *
21     *      SYMDEF  .SMDEF          PROVIDES AN ENTRY POINT INTO THE PROGRAM.
22     *      SYMDEF  -.EVN          A MINUS SIGN DENOTES A SECONDARY SYMDEF.
23     *
24     * THE PCC WILL ALLOW THE PRINTING OF ALL PSEUDO OPS FOR YOUR CONVENIENCE.
25     *
26     *      PCC      ON
27     *
28     * THE ORG PSEUDO OP WILL START THE PROGRAM OFF AT LOCATION 2000 OCTAL.
29     *
30     *      ORG      1024
31     *      .ORG.   LDA      .ORG.
32     *      .SMDEF  STA      .OPSYM
33     *      START   LDx7    .ZERO.
34     * THE EQU PSEUDO OP WILL EQUATE THE SYMBOLIC ADDRES OF .EQU WITH 512 DECIMAL.
35     *
36     *      .EQU.   EQU      512
37     *      LDA      .EQU.
38     *
39     * IN THIS EXAMPLE OF THE EQU PSEUDO OP, WE ARE EQUATING ONE SYMBOL.....
40     * TO A PREVIOUSLY DEFINED SYMBOL. HOWEVER, THIS SYMBOL MUST NOT BE A.....
41     * SYMDEF OR SYMREF.
42     *
43     *      STOP   EQU      START
44     *      LDA      START
45     *      LDA      STOP
46     *
47     * THE BOOL PSEUDO OP WILL EQUATE .BOOL. WITH 1200 OCTAL.
48     *
49     *      .BOOL.  BOOL    1200
50     *      STA      .BOOL.,6

```

```

          002000
002000 002000 2350 00 010
002001 002145 7550 00 010
002002 002065 2270 00 010

```

```

          001000
002003 001000 2350 00 000

```

```

          002002
002004 002002 2350 00 010
002005 002002 2350 00 010

```

```

          001200
002006 001200 7550 16 000

```

...ALSO LITERALS AND MACROS.

002007 002063 2350 00 010  
 002010 002204 2350 00 010

002011 000000011007 000  
 002012 002035 2350 00 010  
 002013 002015 7550 00 010

002014 000000011007 000  
 002015 002202 2350 00 010  
 002016 002063 7550 00 010

002017 000001710004 000  
 002020 002051 2350 00 010  
 002021 002036 0750 00 010  
 002022 002020 7550 00 010

002023 000000000300 000  
 002024 777777525252 000  
 002025 400000000004 000

002026 777777777777 000  
 002027 000000000140 000  
 002030 006600000000 000

51 \* THE NULL PSEUDO OP CAUSES THE LABEL TO ASSUME THE NEXT LOCATION COUNTER VALUE.  
 52 \*  
 53 .NULL. NULL  
 54 LDA .ARG.  
 55 LDA .E.L.. .E.L.. IS THE ERROR LINKAGE SYMBOL.  
 56 \*  
 57 \* THIS EVEN PSEUDO OP FORCES THE LOCATION COUNTER TO AN EVEN COUNT  
 58 \*  
 59 EVEN  
 60 .EVN LDA .VFD.  
 61 STA .ODD  
 62 \*  
 63 \* THE ODD PSEUDO OP FORCES THE LOCATION COUNTER TO AN ODD COUNT.  
 64 \*  
 65 ODD  
 66 .ODD LDA .USE.  
 67 STA .BFS.  
 68 \*  
 69 \* THE EIGHT PSEUDO OP FORCES THE LOCATION COUNTER TO AN EVEN MULTIPLE OF EIGHT.  
 70 \* NOTICE BITS 15 THRU 17, 6TH OCTAL CHAR, OF THE LOCATION COUNTER IS ALL ZEROS  
 71 \*  
 72 EIGHT  
 73 .EIGHT LDA .BSS.  
 74 ADA .ETC.  
 75 STA .EIGHT  
 76 \*  
 77 \* I HOPE YOU NOTICED THE NOP THAT THE ASSEMBLER INSERTED IN THE  
 78 \* INSTRUCTION SEQUENCE WHEN ENCOUNTERING THOSE LAST THREE PSEUDO OPS.  
 79 \*  
 80 \*  
 81 \* WHILE WE ARE AT IT, DID YOU NOTICE THAT THESE REMARKS IN NO WAY EFFECTED  
 82 \* THE LOCATION COUNTER, OR THE INSTRUCTIONS.  
 83 \*  
 84 \*  
 85 \*  
 86 \* THE OCT PSEUDO OP USED HERE WILL CREATE 3 OCTAL WORDS STARTING AT LOC. .OCT.  
 87 \*  
 88 .OCT. OCT 300,777777525252,-4  
 89 \* THIS DEC PSEUDO OP WILL CREATE THREE WORDS STARTING AT LOCATION .DEC. AFTER  
 90 \* FIRST CONVERTING FROM DECIMAL TO OCTAL.  
 91 \*  
 92 .DEC. DEC -1,1.2E1832,6.  
 93 \*

149

...ALSO LITERALS AND MACROS.

94 \* THE BCI PSEUDO OP WILL STORE A FOUR WORD PHRASE STARTING AT LOC. .BCI.

95 \*

002031 312043314225 000  
 002032 206330316220 000  
 002033 476225642446 000  
 002034 204647332020 000

96 .BCI. BCI 4,I LIKE THIS PSEUDO OP.

97 \* THE VFD PSEUDO OP WILL CREATE A WORD THAT IS BIT ORIENTED IN ITS EXPRESSION.

98 \*

002035 002031775225 010

99 .VFD. VFD 18/.BCI.,06/77,H6/-,3/2,3/5

100 \*

101 \* HERE IS A REAL BEAUT. A VFD USING ALGEBRAIC AND BOOLEAN OPERATORS.

102 \*

002036 432214612752 000

103 .ETC. VFD 4/16/2, 4 BITS OF 16 DIVIDED BY 2  
 104 ETC 3/2\*3, 3 BITS OF 2 TIMES 3  
 105 ETC 5/15+3, 5 BITS OF 15 PLUS 3  
 106 ETC 6/17-5, 6 BITS OF 17 MINUS 5  
 107 ETC 06//16, BOOLEAN 6 BITS OF 16 NOT  
 108 ETC 03/2\*3, BOOLEAN 3 BITS OF 2 AND 3  
 109 ETC 04/15+3, BOOLEAN 4 BITS OF 15 ORED TO 3  
 110 ETC 05/17-5, BOOLEAN 5 BITS OF 17 EX ORED TO 5

111 \*

112 \* BY THE WAY, THAT ALSO SHOWED THE USE OF THE ETC PSEUDO OP.

113 \*

114 \*.....

115 \*

116 \* THE FOLLOWING SHOWS A USE OF THE SET AND DUP PSEUDO OPS. IT WILL PROVIDE.....

117 \* CODING TO MOVE 5 WORDS FROM X TO Y.

118 \*

000000  
 002037 000005 2350 00 020  
 002040 000012 7550 00 020  
 000001  
 002041 000006 2350 00 020  
 002042 000013 7550 00 020  
 000002  
 002043 000007 2350 00 020  
 002044 000014 7550 00 020  
 000003  
 002045 000010 2350 00 020  
 002046 000015 7550 00 020  
 000004  
 002047 000011 2350 00 020  
 002050 000016 7550 00 020  
 000005

119 .SET. SET 0  
 120 DUP 3,5 DUP THE NEXT 3 CARDS, 5 TIMES.  
 121 LDA X+.SET.  
 122 STA Y+.SET.  
 123 .SET. SET .SET.+1  
 LDA X+.SET.  
 STA Y+.SET.  
 .SET. SET .SET.+1  
 LDA X+.SET.  
 STA Y+.SET.  
 .SET. SET .SET.+1  
 LDA X+.SET.  
 STA Y+.SET.  
 .SET. SET .SET.+1  
 LDA X+.SET.  
 STA Y+.SET.  
 .SET. SET .SET.+1

124 \*

125 \* DID YOU NOTICE THAT THE SET PSEUDO OP ALLOWED THE SAME SYMBOL IN THE LOCATION

126 \* FIELD TO BE RE DEFINED WITHOUT ERROR.

...ALSO LITERALS AND MACROS.

```

127      EJECT
128 * THE BSS PSEUDO OP WILL RESERVE 5 WORDS STARTING AT LOCATION .BSS.
129 *
002051 130 .BSS. BSS      5
131 *
132 * THIS PSEUDO OP WILL RESERVE 5 WORDS PRECEEDING LOCATION .BSF.
133 *
002063 134 .BFS. BFS      5
135 *
136 * THAT LAST PSEUDO OP IS NICE IF YOU WISH TO USE REVERSE SCANNING ADDRESS
137 * MODIFIERS SUCH AS DI TALLY OR SD TALLY.
138 *
139 * THIS PSEUDO OP WILL CREATE AN INSTRUCTION WORD WITH
140 * NO OP CODE. IT IS HANDY FOR CREATING ADDRESSES FOR IR OR RI MODIFICATION.
141 *
002063 142 .ARG. NULL
002063 143      ARG      .NULL.,7
002064 144      ARG      .DLIT.,1
145 *
146 * OR WE COULD CREATE TWO 18 BIT ADDRESSES IN ONE WORD BY USING THE
147 * FOLLOWING PSEUDO OP.
148 *
002065 149 .ZERO. ZERO    .OCT.,.DEC.
150 *
151 * BUT ON THE OTHER HAND IF YOU WOULD RATHER USE ABSOLUTE ADDRESSING RATHER
152 * THAN SYMBOLIC ADDRESSING, YOU CAN CODE IT THIS WAY.
153 *
002066 154      ZERO    1024,512
155 *
156 * OR IF YOU CANT MAKE UP YOUR MIND, YOU CAN USE BOTH SYMBOLIC AND ABSOLUTE.
157 *
002067 158      ZERO    1024,.ORG.      HA, HA. THEY'RE BOTH THE SAME ADDRESS.
159 *****
160 *
161 * HERE IS AN INSTRUCTIONAL LITERAL
162 *
002070 163 .MLIT. LDA      =MLDX1 5,DU
002120 164 *****
002120 165 *
002071 166 * HERE ARE A COUPLE EXAMPLES OF VARIABLE FIELD LITERALS.
002121 167 .VLIT. LDA      =V18/START,09/235,3/0,06/17
002072 168      LDA      =V10/512,02/2,H18/B2X,6/0
002122 169 *****
002073 170 *
171 .REF. NULL
172      REF      LNRSN      LIST NONREFERENCED SYMBOLS (EG: .REF.)
173 * NOTICE: THIS WILL CAUSE A COMPLETE REFERENCE LISTING OF ALL THE MME'S.
    
```

151

...ALSO LITERALS AND MACROS.

174 \* HERE ARE SOME EXAMPLES OF DECIMAL LITERALS.

```

175 *
002073 002104 2350 00 010 176 .DLIT. LDA      =10          DECIMAL INTEGER
002074 002105 2350 00 010 177      LDA      =-15         NEGATIVE DECIMAL INTEGER
002075 002106 2350 00 010 178      LDA      =6.0          SINGLE PRECISION FLOATING POINT
002076 002107 2350 00 010 179      LDA      =1.55E1       SINGLE PRECISION FLOATING POINT
002077 002110 2370 00 010 180      LDAQ     =5.12D2       DOUBLE PRECISION FLOATING POINT
002100 002112 2350 00 010 181      LDA      =5817         SINGLE PRECISION FIXED POINT
002101 002113 2350 00 010 182      LDA      =22.585        SINGLE PRECISION FIXED POINT
002102 002114 2350 00 010 183      LDA      =1.2E1B32      SINGLE PRECISION FIXED POINT
002103 002116 2350 00 010 184      LDA      =1.95D1B37     DOUBLE PRECISION FIXED POINT

```

185 \*\*\*\*\*

```

002104 002104 000000000012 000
002105 002105 777777777761 000
002106 002106 006600000000 000
002107 002107 010760000000 000
002110 002110 024400000000 000
002111 002111 000000000000 000
002112 002112 000005000000 000
002113 002113 264000000000 000
002114 002114 0000000000140 000
002115 002115 000000000000 000
002116 002116 000000000004 000
002117 002117 700000000000 000

```

186 .LIT. NULL  
187 LIT

CAUTION...NOTICE THE LOCATION COUNTER.

188 \*\*\*\*\*

```

189 *
002123 030000 7100 00 030 190 * IF I USE THE SYMREF PSEUDO OP, I CAN REFERENCE A SYMBOL WHICH DOES NOT.....
191 * APPEAR IN THIS ASSEMBLY, AND WILL NOT CAUSE AN ERROR.
192 *
193      SYMREF .SHREF
194      TRA   .SHREF
195 *

```

196 \* OF COURSE IT IS EXPECTED THAT THIS SAME SYMBOL WILL HAVE BEEN DEFINED.....  
197 \* BY A SYMDEF IN ANOTHER ASSEMBLY TO BE EXECUTED WITH THIS ONE.

198 \*\*\*\*\*

```

199 *
002124 002002 2352 00 010 200 * IF I NEED TO TURN BIT 28 ON, I WOULD USE THE FOLLOWING PSEUDO OP.
002125 002124 7552 00 010 201 *

```

```

202      INHIB  ON
203 .INHIB LDA  START
204      STA   .INHIB
205 *

```

206 \* TO TURN IT OFF, I MERELY DO THE FOLLOWING.

```

002126 002002 2350 00 010 207 *
002127 002124 7550 00 010 208      INHIB  OFF
209      LDA  START
210      STA  .INHIB
211 *

```

...ALSO LITERALS AND MACROS.

212 \* THE FOLLOWING IS AN EXAMPLE OF THE USE OF THE CALL, SAVE, AND RETURN PSEUDOS.  
 213 \* NOTE: THE SAVE AND RETURN PSEUDO OPS MUST APPEAR IN THE ASSEMBLY BEFORE....  
 214 \* THE CALL PSEUDO OP.  
 215 \*

					002130					
002130	002132710000		010							
002131	002204630000		010							
002132	002204754000		010							
002133	002204741000		010							
002134	002202 2350 00	010		217	LDA	.USE.		BODY OF THE SUBROUTINE		
002135	002137 7550 00	010		218	STA	.CALL		DITTO		
002136	002131710000	010		219	.RET.	RETURN	.SAVE			
				220	*					
				221	*	NOTE ** THE CALL PSEUDO OP MUST FOLLOW THE SAVE AND RETURN PSEUDO OPS.....				
				222	*	IF USED IN THE SAME ASSEMBLY.				
				223	*					
				224	*					
002137	002130701000	010		225	.CALL	CALL	.SAVE	THE SYMBOL .SAVE WILL APPEAR AS A SYMREF.		
002140	002142710000	010								
002141	002204000341	010								
002142	002104 6350 00	010		226	EAA	.LIT.		RETURN POINT INTO PROGRAM.		
				227	*					
				228	*****					
				229	*					
				230	*	HERE ARE TWO EXAMPLES OF FLOATABLE CODE.....				
				231	*					
002143	777637 2350 04	000		232	.FLOAT	LDA	START-*,IC	AS YOU CAN SEE, FLOATABLE CODE....		
002144	777636 2350 04	2002		233		LDA	START,\$	MAY BE CODED EITHER WAY.		
				234	*					
				235	*	SUPPOSE YOU WOULD PREFER TO USE THE MNEMONICS LDAR INSTEAD OF LDA.....				
				236	*	FOR THE LOAD THE A REGISTER INSTRUCTION. THEN YOU WOULD USE THE FOLLOWING.				
				237	*					
				238	LDAR	OPSYN	LDA			
				239	*					
				240	*	NOW LETS SEE WHAT HAPPENS WHEN WE USE IT.				
				241	*					
002145	002070 2350 00	010		242	.OPSYN	LDAR	.MLIT.			
002146	002071 2350 00	010		243		LDA	.VLIT.			
002147	002202 7010 00	010		244		TSX1	.USE.			
				245	*					
				246	*	WATCH ME CHANGE THE LOCATION COUNTER BY USING THE USE PSEUDO OP.				
				247	*					
				248		USE	NEXT	NEXT IS THE NAME OF MY NEW LOCATION CTR.		
002202	002143 2350 00	010		249	.USE.	LDA	.FLOAT			
002203	002136 7550 00	010		250		STA	.RET.			
				251	*					
				252	*	NOW WATCH ME PICK UP WHERE I LEFT OFF WITH THE ORIGINAL LOCATION COUNTER.				
				253	*					
				254		USE				
				255	*					

153



...ALSO LITERALS AND MACROS.

002150 000000 2350 00 020  
 002151 010000 0750 00 030  
 002152 000005 7550 00 020

256 \* SUPPOSE YOU WANT TO SHOW BLANK OR LABELLED COMMON AREAS, BELOW THE CODING.  
 257 LDA .BLOCK

258 ADA WM  
 259 STA X

260 \*  
 261 \* THE FOLLOWING BSS PSEUDO OPS WILL RESERVE AREAS IN BLANK COMMON.  
 262 \* NOTE\*\*\* INSTRUCTIONS OR DATA MAY NOT BE PLACED IN BLANK COMMON.  
 263 \*

000000  
 000000  
 000005  
 000012  
 000017

264 BLOCK THIS IS BLANK COMMON OF COURSE.  
 265 .BLOCK BSS 5  
 266 X BSS 5  
 267 Y BSS 5  
 268 Z BSS 5

000000  
 000000

271 BLOCK LABCOM AND THIS IS LABELED COMMON.  
 272 WM BSS 10  
 273 XX DEC 99  
 274 YY OCT 77  
 275 ZZ BCI 4, THIS IS LABELED COMMON.

000012 000000000143 000  
 000013 000000000077 000  
 000014 633031622031 000  
 000015 622043212225 000  
 000016 432524202346 000  
 000017 444446453320 000

276 \*  
 277 \* NOTICE THAT LABELED COMMON PERMITTED THE USE OF CONSTANTS.....  
 278 \* WITHIN THE LABELED COMMON AREA.  
 279 \*

002153

280 USE AGAIN I RETURN TO MY ORIGINAL COUNTER  
 281 \*

282 \* HERE ARE EXAMPLES OF THE FOUR TALLY PSEUDO OPS.  
 283 \*

002153 020000 2350 52 030  
 002154 020001 2360 52 030  
 002155 020002 7550 57 030  
 002156 020003 7560 53 030

284 \*  
 285 LDA .TALY.,SC  
 286 LDQ .TALB.,SC NOTE THE SC MOD IS USED FOR BYTE ALSO.  
 287 STA .TALC.,IDC IDC ALLOWS FURTHER INDIRECTION.  
 288 STQ .TALD.,AD  
 289 \*  
 290 \*

000000 010000 0012 02 030  
 000001 010012 0012 41 030  
 000002 010013 0012 77 030  
 000003 010014 0012 12 030

291 BLOCK TALLYS LABELED COMMON AGAIN.  
 292 .TALY. TALLY WM,10,2 TALLY USED FOR I, ID, DI, SC, & CI FORMS.  
 293 .TALB. TALLYB XX,10,1 NOTICE BIT 30 ON FOR BYTE TALLY.  
 294 .TALC. TALLYC YY,10,\*7 NOTICE IR MODIFICATION IN THE TAG FIELD.  
 295 .TALD. TALLYD ZZ,10,10

002157

296 \*  
 297 USE BACK TO THE ORIGINAL LOCATION CTR AGAIN.  
 298 \*\*\*\*\*

...ALSO LITERALS AND MACROS.

```

299      EJECT
300 *
301      REFMA ON          CREATES A SEPERATE MACRO SYMBOL TABLE.
302 *****
303 *
304 * THIS NEXT SEQUENCE OF INSTRUCTIONS WILL SERVE TO EXPLAIN THE USE OF MACROS.
305 * IT WILL ALSO SHOW THE USE OF THE CONDITIONAL PSEUDO OPS, CREATED SYMBOLS,
306 * IDRP AND THE PRINT MACRO EXPANSION ON/OFF PSEUDO OP.
307 *
308      PNC ON
309 *
310 *          TURN PNC ON TO SEE EXPANDED CODE
311 *
312 *****
313 *
314 HOLY  MACRO
315      LDX7 #1,DU        ARG #1 IS NR. OF WORDS TO BE MOVED.
316      LDX6 0,DU
317 #2   LDA #3,6        ARG #3 IS DATA ORIGIN.
318      STA #4,6        ARG #4 IS DATA DESTINATION.
319      IFG #1,1,3     IF ARG #1 GREATER THAN ONE, ASSEMBLE AND
320 *                               EXECUTE THE NEXT THREE INSTRUCTIONS.
321      ADX6 1,DU
322      SBX7 1,DU
323      TNZ #2          XFER BACK TO ARG #1 IF NOT ZERO.
324      ENDM HOLY
325 *
326 * ARGUMENT NUMBER 2 IS IMPLICITLY NULLED TO FORCE CREATED SYMBOLS.
327      HOLY 25,#,X,Y
          LDX7 25,DU
          LDX6 0,DU
          .001. LDA X,6
          STA Y,6
          IFG 25,1,3
          ADX6 1,DU
          SBX7 1,DU
          TNZ .001.

002157 000031 2270 03 000
002160 000000 2260 03 000
002161 000005 2350 16 020
002162 000012 7550 16 020

002163 000001 0660 03 000
002164 000001 1670 03 000
002165 002161 6010 00 010

          002166
002166 000001 2270 03 000
002167 000000 2260 03 000
002170 010012 2350 16 030
002171 010013 7550 16 030

          BIG
          LDA XX,6
          STA YY,6
          IFG 1,1,3

331 *
332 * LETS SEE THAT LAST ONE WITH PNC OFF THIS TIME:
333      PNC OFF
002172 334      HOLY 1,BIGGER,XX,YY
    
```

155

...ALSO LITERALS AND MACROS.

```

335      EJECT
336 *
337 * WATCH CAREFULLY NOW. HERE IS THE IDRP PSEUDO OP AT WORK. WITH THIS
338 * WE SHALL SEE THE ARG PSEUDO OP AGAIN.
339 DOIT  MACRO
340      TSX1  #1
341      IDRP  #2 REPEAT NEXT INST BY NUMBER OF SUB ARGS IN ARG#2.
342      ARG   #2 THIS IS THE ARG PSEUDO OP.
343      IDRP          THIS IDRP IS USED AS A DELIMITER.
344      ENDM  DOIT
345 *
346 * LETS TURN PMC ON AND SEE THIS MACRO EXPANDED.
347 *
348      PMC   ON
349 *
350      DOIT  START,(XX,YY,ZZ)
          TSX1  START
          IDRP  XX,YY,ZZ
          ARG   XX
          ARG   YY
          ARG   ZZ
          IDRP
351 *****
352 *
353 * TH-TH-THATS ALL FOLKS, BUT LET US NOT FORGET THE END CARD.
354 *

```

```

          002176
002176 002002 7010 00 010
002177 010012 0000 00 030
002200 010013 0000 00 030
002201 010014 0000 00 030

```

ERROR LINKAGE

```

002204 000000000000 000
002205 336244242526 000

```

2206 IS THE NEXT AVAILABLE LOCATION. GMAP VERSION JMPA/060970 JMPB/060970 JMPC/060970  
THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY

355

END

NO SYMBOL IN THE VARIABLE FIELD FOR RELOCABLE.

JMPA/060970 JMPB/060970 JMPC/060970

156

CTAL	SYMBOL	REFERENCES BY ALTER NO.		
2170	BIG	330	330	
2174	BIGGER	334	334	
10	GEBORT			
22	GECALL			
27	GECHEK			
16	GEENDC			
3	GEFADD			
12	GEFCON			
13	GEFELS			
7	GEFINI			
40	GEFRCE			
41	GEFSYE			
35	GEIDSE			
45	GEINFO			
1	GEINOS			
6	GELAPS			
37	GELBAR			
33	GELoop			
11	GEMORE			
25	GENREL			
43	GENEWS			
42	GEPRIO			
17	GERELC			
4	GERELS			
15	GERETS			
2	GEROAD			
31	GEROLL			
30	GEROUT			
24	GERSTR			
23	GESAVE			
14	GESETS			
5	GESNAP			
44	GESNUM			
20	GESPEC			
26	GESYOT			
21	GETIME			
32	GEUSER			
34	GEWAKE			
2161	.001.	327	327	
2063	.ARG.	142	54	142
2031	.BCI.	96	96	99
2063	.BFS.	134	67	134
0	.BLOCK	265	257	265
1200	.BOOL.	49	49	50
2051	.BSS.	130	73	130
2137	.CALL	225	218	225
2026	.DEC.	92	92	149
2073	.DLIT.	176	144	176
2020	.EIGHT	73	73	75
2204	.E.L..		55	216 225

157

OCTAL	SYMBOL	REFERENCES BY ALTER NO.									
36	.EMM										
1000	.EQU.	36	36	37							
2036	.ETC.	103	74	103							
2012	.EVN	60	22	60							
2143	.FLOAT	232	232	249							
2124	.INMIR	203	203	204	210						
2104	.LIT.	186	186	226							
2070	.MLIT.	163	163	242							
2007	.NULL.	53	53	143							
2023	.OCT.	88	88	149							
2015	.ODD	66	61	66							
2145	.OPSYN	242	32	242							
2000	.ORG.	31	31	158							
2073	.REF.	171	171								
2136	.RET.	219	219	250							
2130	.SAVE	216	216	219	225						
5	.SET.	123	119	121	122	123					
2001	.SMDEF	32	21	32							
3	.SMREF		193	194							
1	.TALB.	293	286	293							
2	.TALC.	294	287	294							
3	.TALD.	295	288	295							
0	.TALY.	292	285	292							
2202	.USE.	249	66	217	244	249					
2035	.VFD.	99	60	99							
2071	.VLIT.	167	167	243							
2065	.ZERO.	149	33	149							
2002	START	33	33	43	44	167	203	209	232	233	350
2002	STOP	43	43	45							
0	HW	272	258	272	292						
5	X	266	121	123	259	266	327				
12	XX	273	273	293	330	334	350				
12	Y	267	122	123	267	327					
13	YY	274	274	294	330	334	350				
17	Z	268	268								
14	ZZ	275	275	295	350						

158

96425 11 07-21-70 19.072 EXAMPLES OF PSEUDO OPERATIONS\*\*

PAGE 13

OCTAL SYMBOL REFERENCES BY ALTER NO.

MACRO REF TABLE BY ALTERS

2176	DOIT	339	350		
2157	HOLY	314	327	330	334

\*\* 17845 WORDS OF MEMORY WERE USED BY GMAP FOR THIS ASSEMBLY.

## MACROS

### MACRO PROTOTYPE:

<u>1</u>	<u>8</u>		
DOIT	MACRO		(HEADING)
	LDA	= 5	} (BODY)
	ADA	COUNT	
	STA	TOTAL	
	ENDM	DOIT	(TERMINATOR)

### MACRO EXPANSION CALL:

DOIT

MACRO  
WITH  
SUBSTITUTABLE ARGUMENTS

MACRO PROTOTYPE:

```
DOIT . MACRO
      LDA      #1
      ADA      #2
      STA      #3
      ENDM     DOIT
```

MACRO CALL:

```
DOIT      COUNT,=5,TOTAL
```



REFRESHING TALLY WORDS  
WITH MACRO CALL

RESTOR	MACRO
	EAX6 #2
	EAX7 #3
	RPD #1.1
	LDA 0.6
	STA 0.7
	ENDM RESTOR
	:
	:
	:
RESTOR	150,TALSAV, TALWRK

THIS EXAMPLE WILL RESTORE UP TO  
256 TALLY WORDS WITH ONE CALL

LOCATION FIELD SYMBOLS

<u>1</u>	<u>8</u>	<u>16</u>
HOLY	MACRO	
	EAX7	0
GO	LDA	#1.7
	STA	#2.7
	ADX7	1.DU
	CMPX7	#3.DU
	TNZ	GO
	ENDM	HOLY

NESTING OF MACROS

PLEASE	MACRO	
	TZE	#1
	TRC	#2
	ENDM	PLEASE

AGAIN	MACRO	
	CMPA	#1
	PLEASE	#2
	ENDM	AGAIN

DOIT	MACRO	
	LDA	#1
	AGAIN	#2
	STA	#3
	ENDM	DOIT

MACRO WITH CONDITIONAL PSEUDO OP

MACRO PROTOTYPE:

BINBCD	MACRO	
	EAX7	0
	LDA	#1
	EAQ	0
	IFE	#1,SEQBIN,3
	RPT	3,1
	BCD	TAB1,7
	STQ	SEQBCD
	INE	#1,SEQBIN,6
	RPT	4,1
	BCD	TAB,7
	STQ	#2
	RPT	6,1
	BCD	0,7
	STQ	#2+1
	ENDM	BINBCD

MACRO CALL:

BINBCD	EBIN,EBCD
BINBCD	FBIN,FBCD
BINBCD	SEQBIN

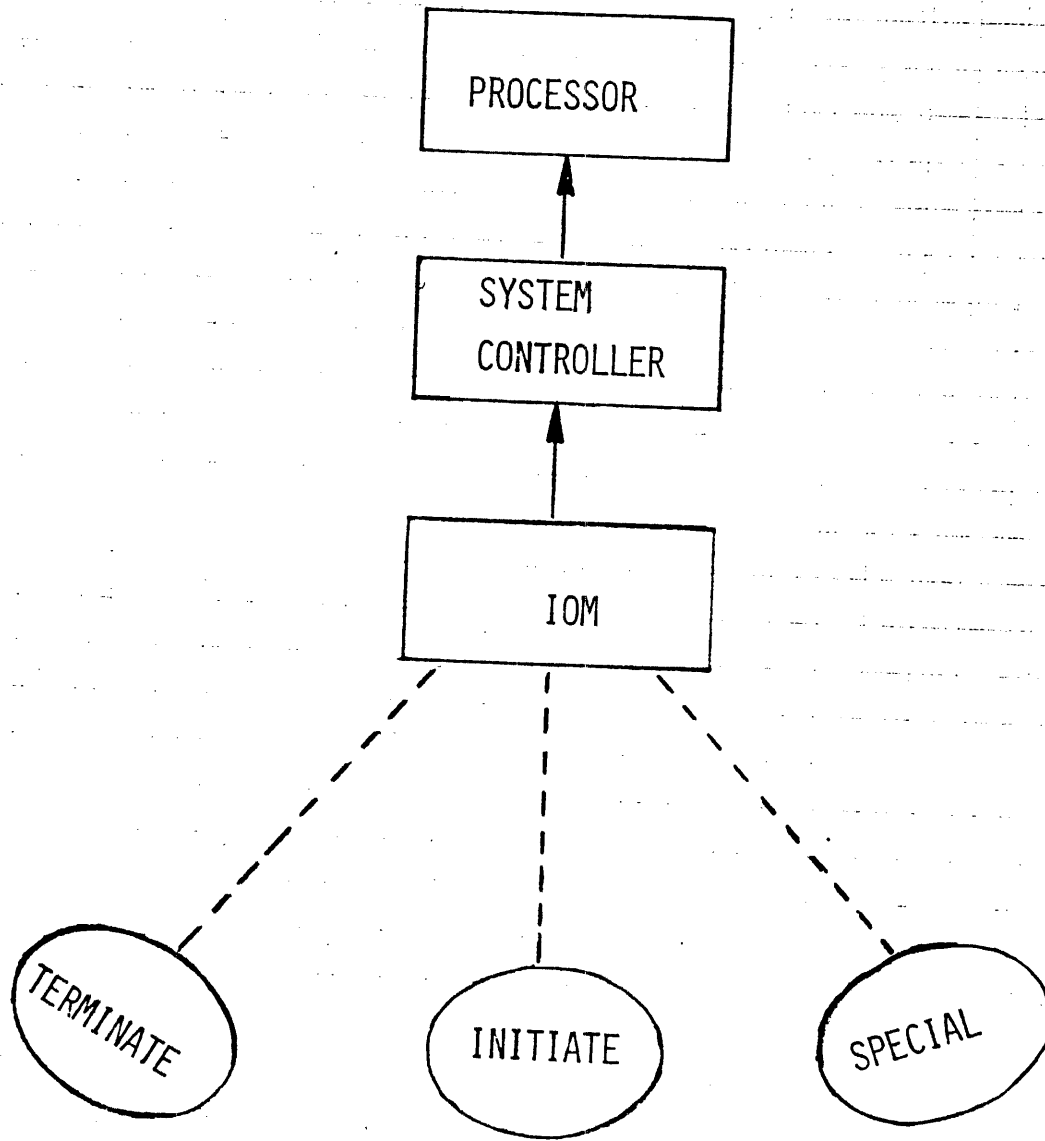
MACRO WITH  
INDEFINITE REPEAT

<u>1</u>	<u>8</u>	<u>16</u>
DOIT	MACRO	
	LDA	#1
	IDRP	#2
	ADA	#2
	IDRP	
	STA	#3
	ENDM	DOIT

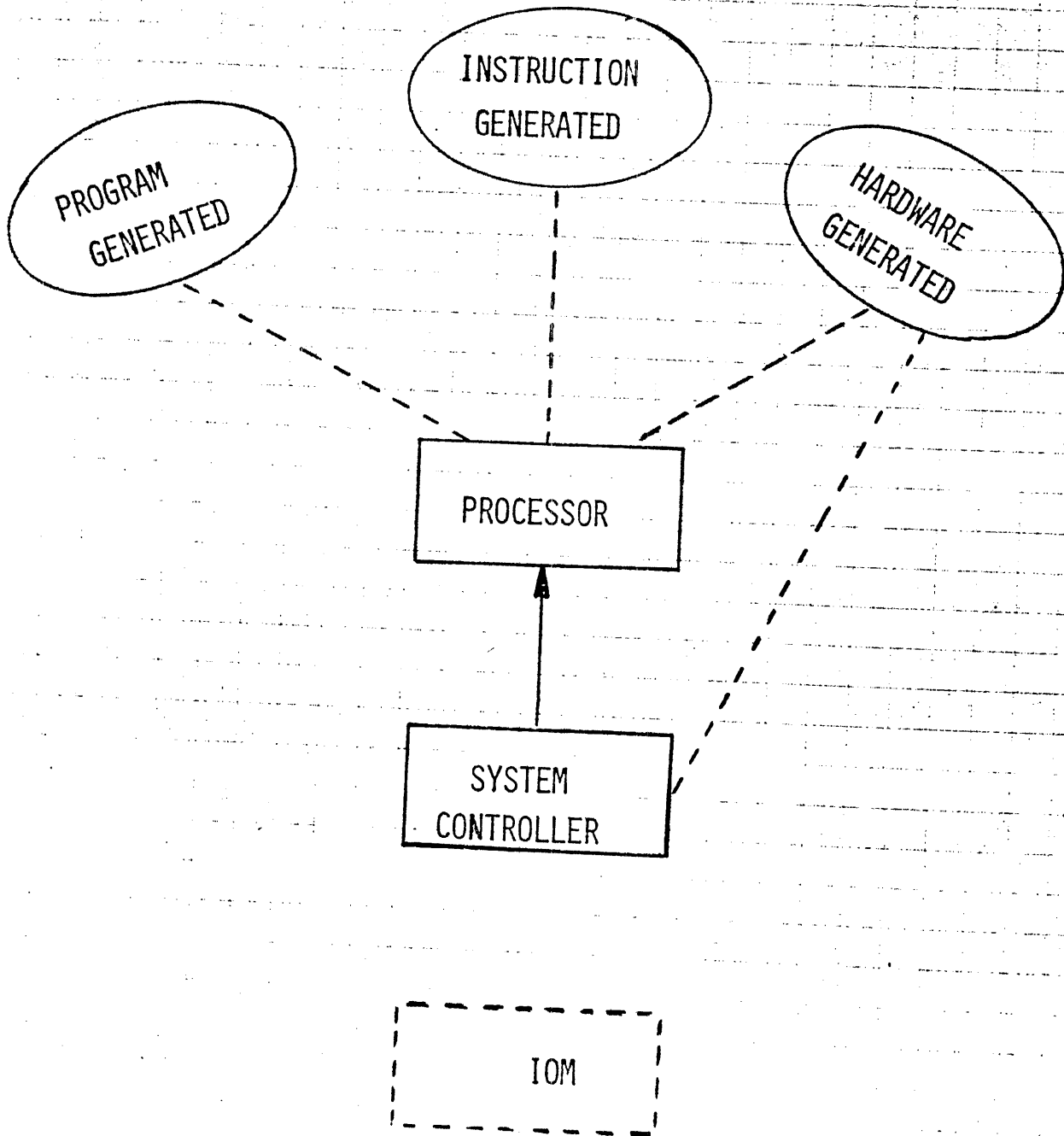
MACRO WITH SET  
AND INDEFINITE REPEAT

<u>1</u>	<u>8</u>	<u>16</u>
XFER	MACRO	
K	SET	0
	IDRP	#2
K	SET	K+1
	IDRP	
	TSX1	#1
	TRA	*+1+K
	IDRP	#2
	ARG	#2
	IDRP	
	ENDM	XFER

## EXTERNAL INTERRUPTS

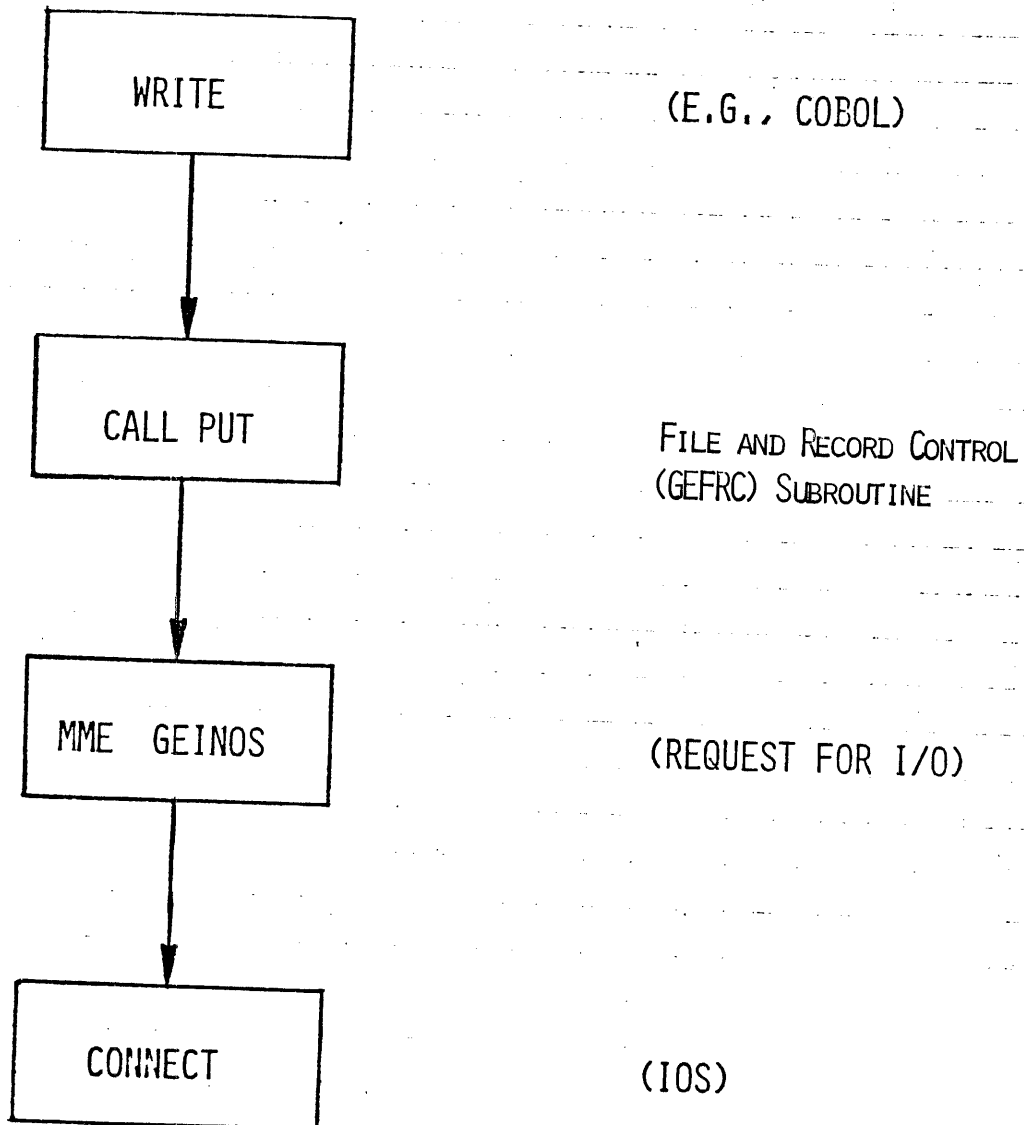


INTERNAL INTERRUPTS  
(FAULTS)

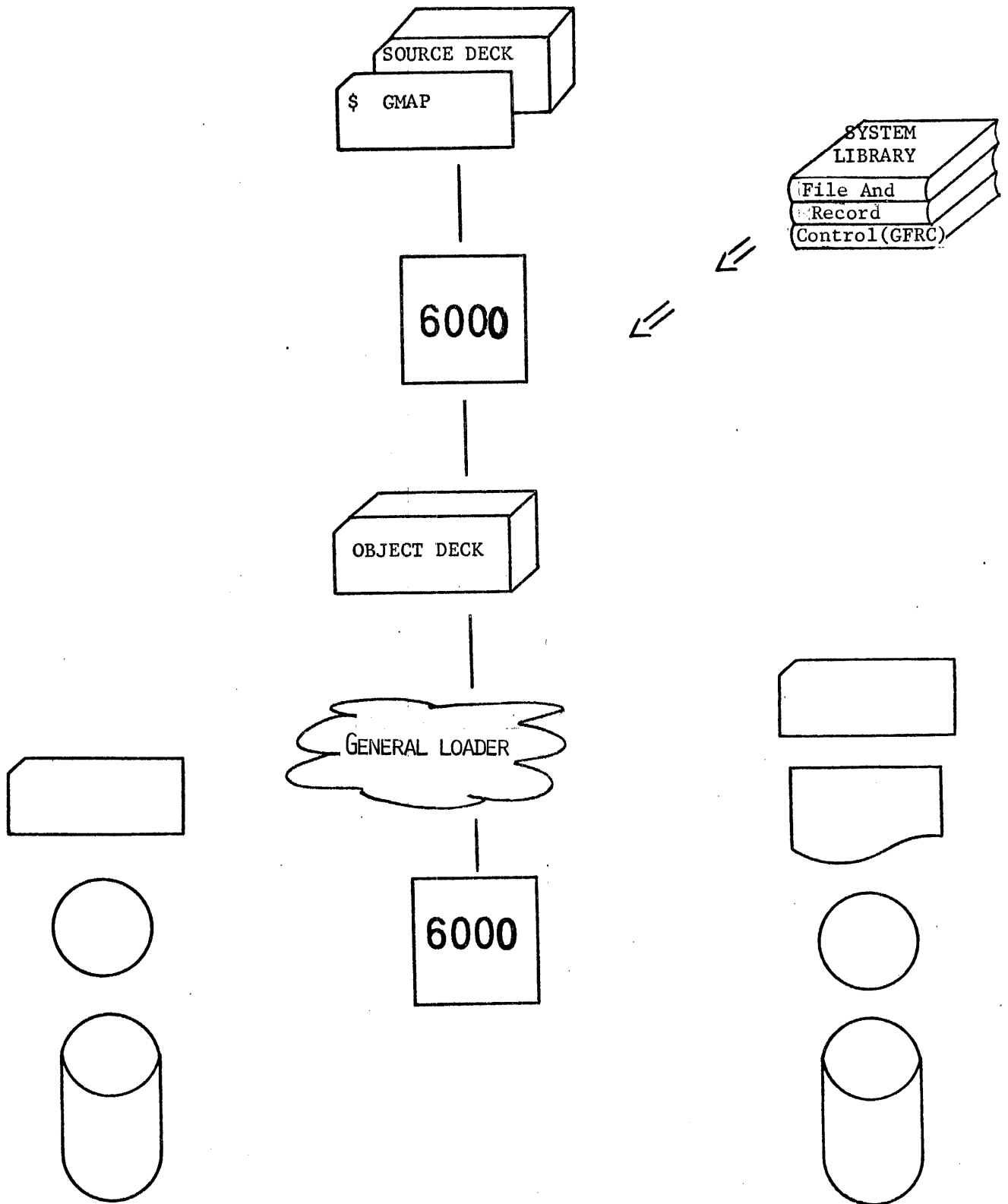




### LEVELS OF I/O

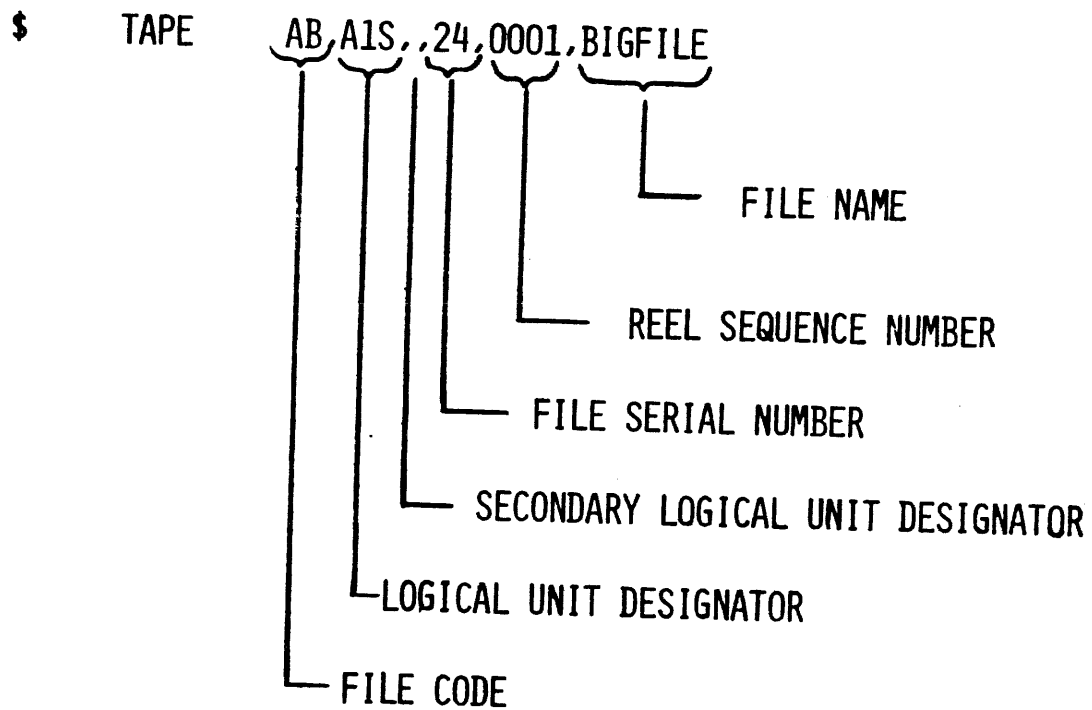


Pages 26-171 through  
26-182 have been  
deleted from this handbook.



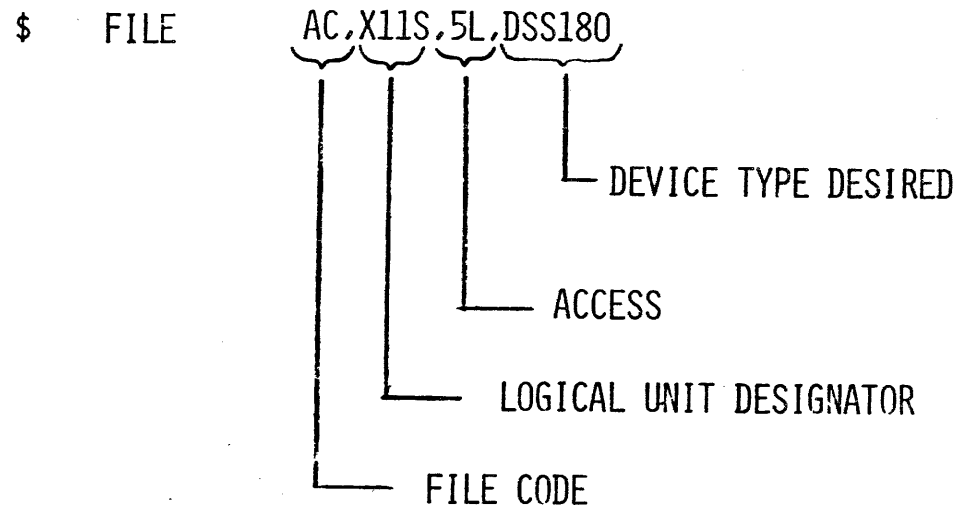
\$ TAPE FILE CARD

ASSIGN A TAPE UNIT TO THE PROGRAM.



# MASS STORAGE FILE CARD

ALLOCATE A TEMPORARY FILE ON SYSTEM MASS STORAGE.



## SYSOUT & DATA FILE CARDS

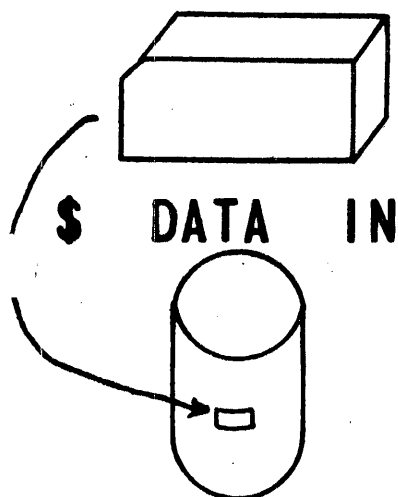
ASSIGN OUTPUT FILES TO SYSTEM OUTPUT MEDIA CONVERSION.

\$ SYSOUT AD  
└─ FILE CODE

WRITE FILES ONTO TEMPORARY LINKED SYSTEM STORAGE FOR INPUT INTO USER PROGRAM.

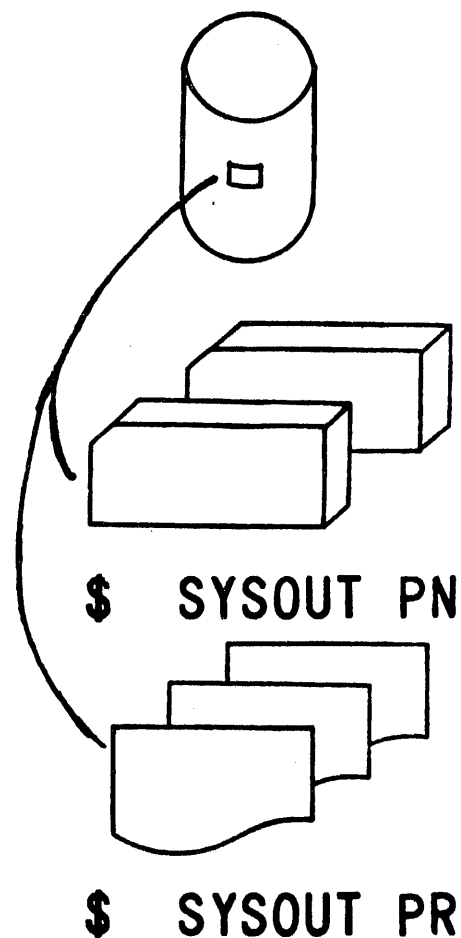
\$ DATA AE  
└─ FILE CODE

MEDIA CONVERSION

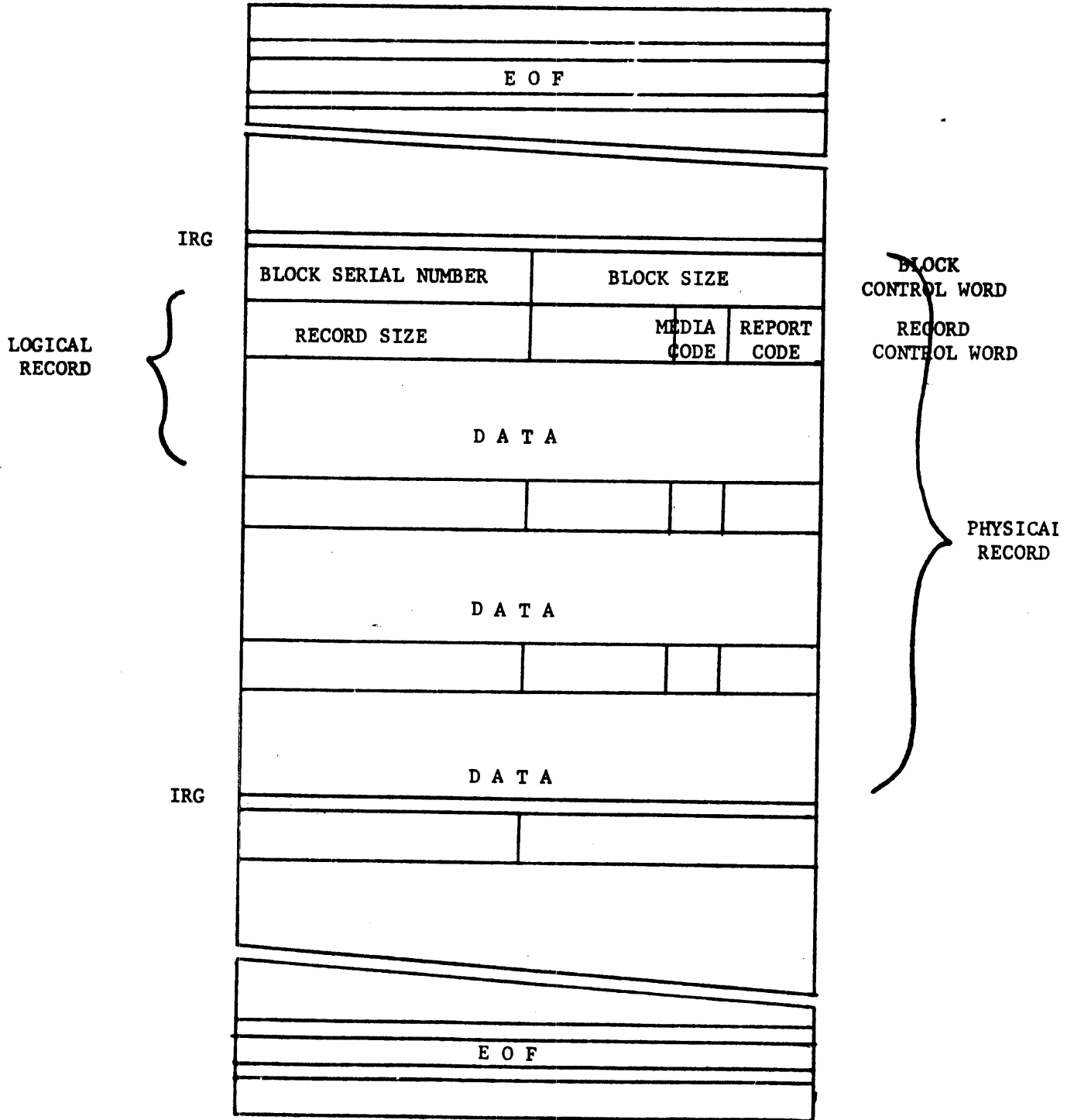


```

$ SNUMB
$ IDENT
$ GMAP
.
.
.
$ END
$ EXECUTE
$ DATA IN
.
$ SYSOUT PN
$ SYSOUT PR
$ ENDJOB
  
```

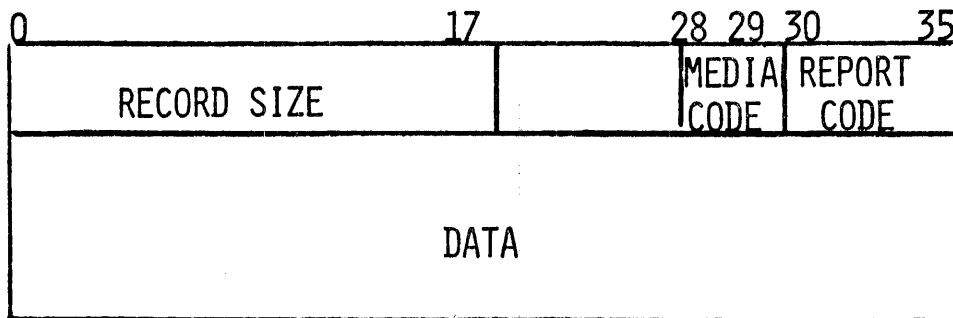


LOGICAL AND PHYSICAL RECORDS

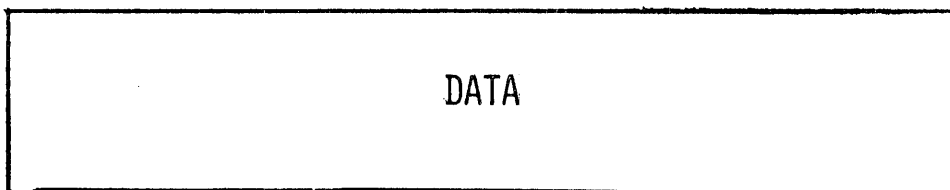




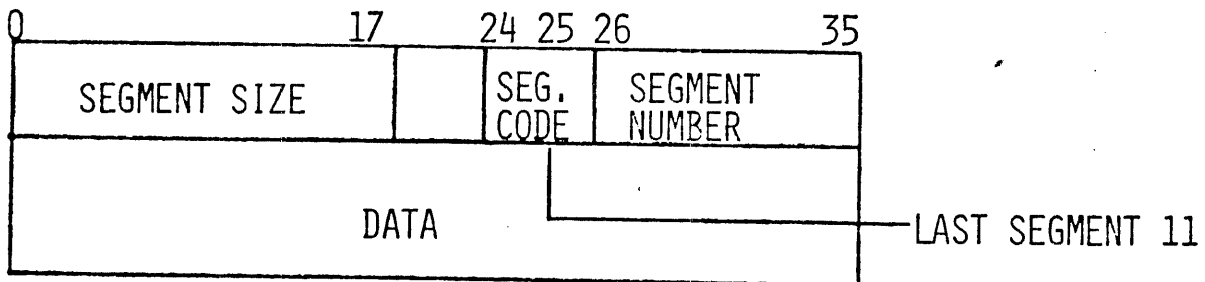
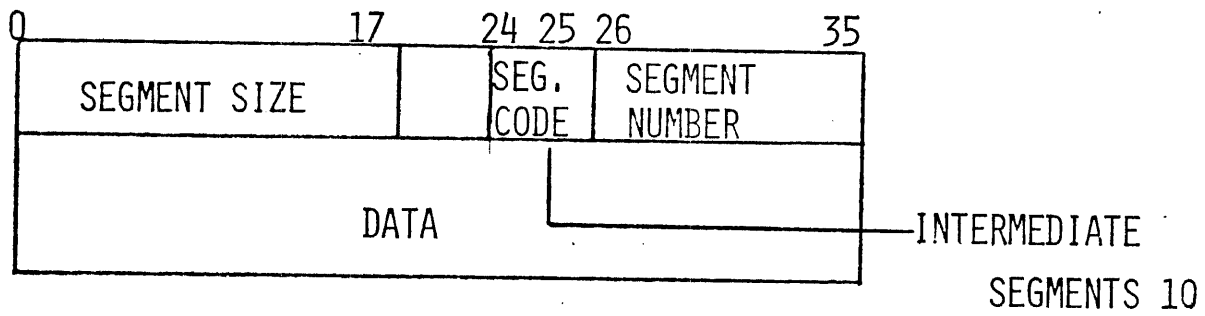
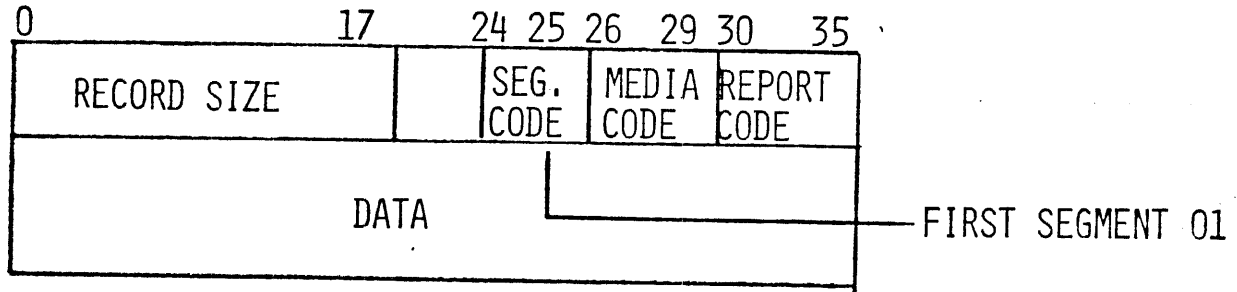
VARIABLE LENGTH RECORDS



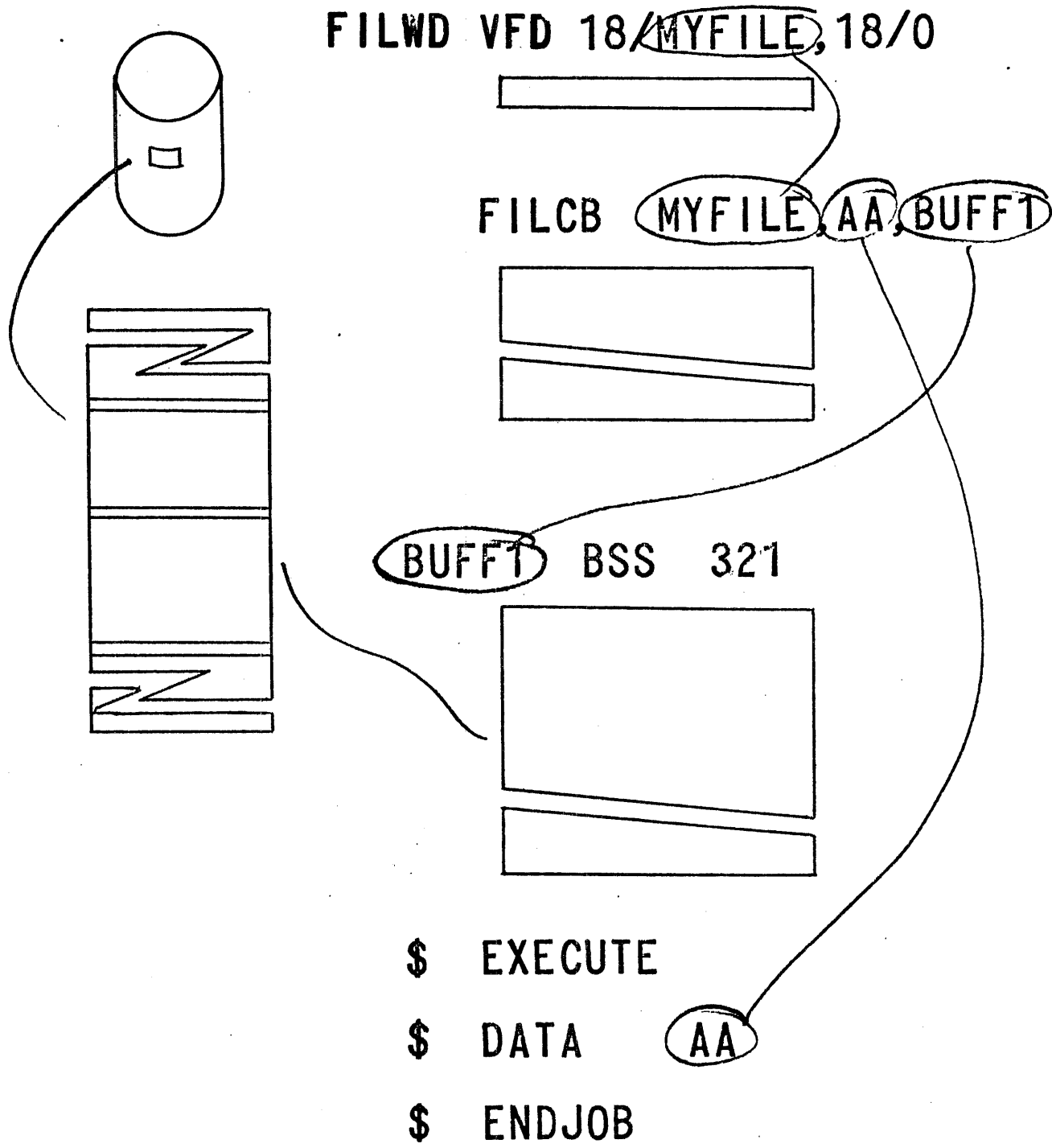
FIXED LENGTH RECORDS



PARTITIONED RECORDS

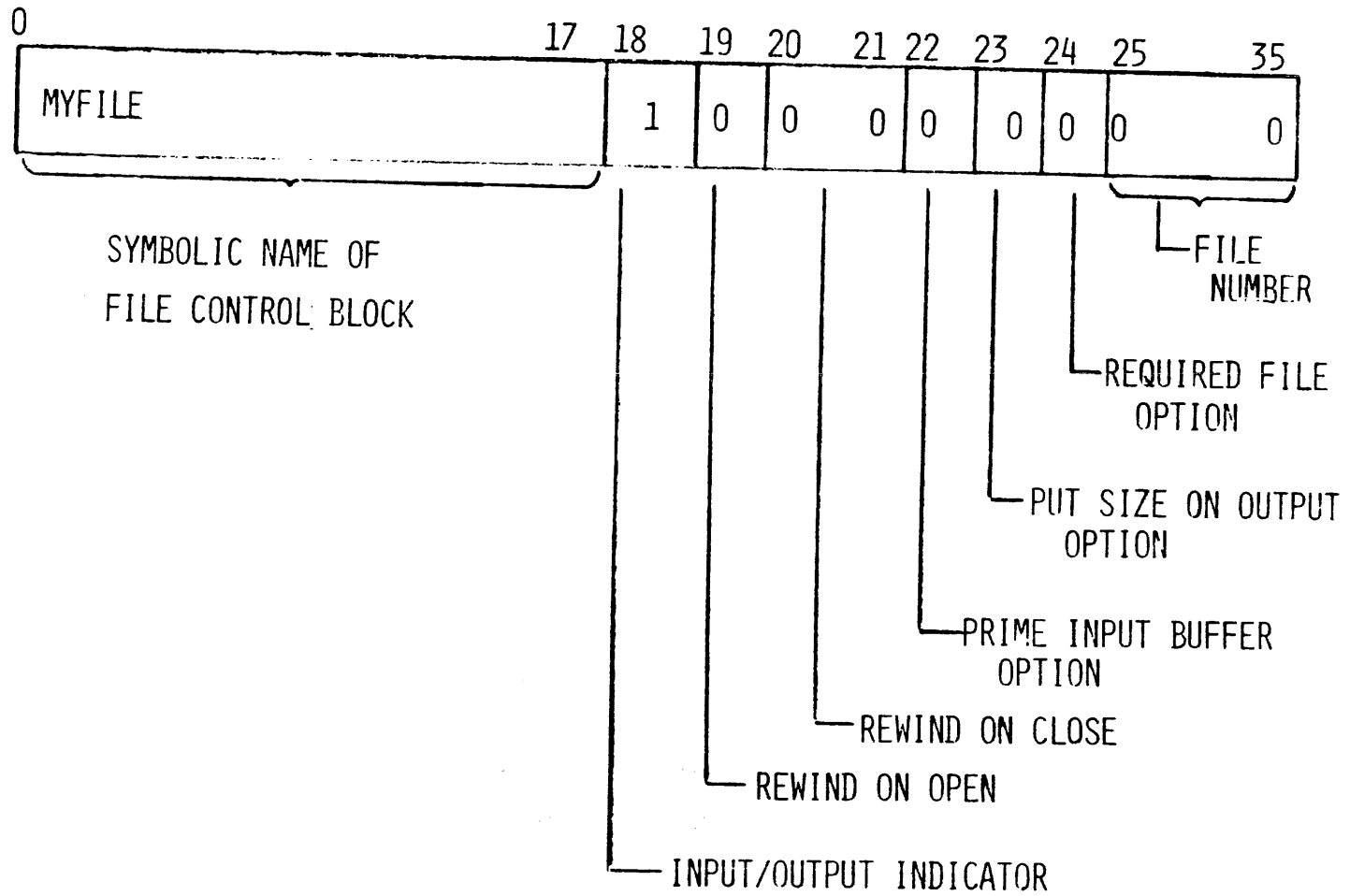


# \$ GMAP



# FILE DESIGNATOR WORD

VFD                    18/MYFILE,1/1,17/0



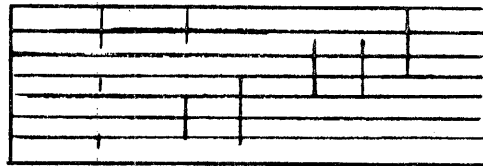
# OPEN FILES

CALL OPEN(AWD, 2)

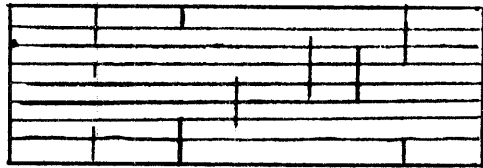
AWD

VFD 18/FILEAA, 18/0  
VFD 18/FILEBB, 1/1, 17/0

FILCB FILEAA, AA, BUFFA1, BUFFA2

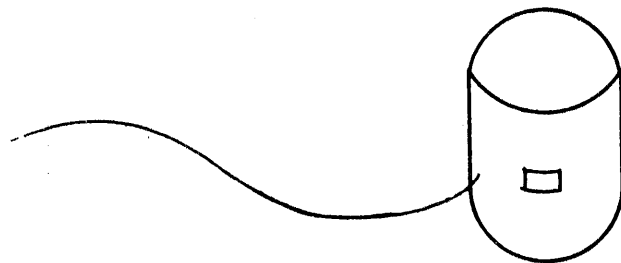
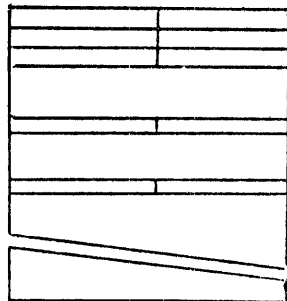


FILCB FILEBB, BB, BUFFB1



BUFFA1

BSS 321



\$ DATA AA

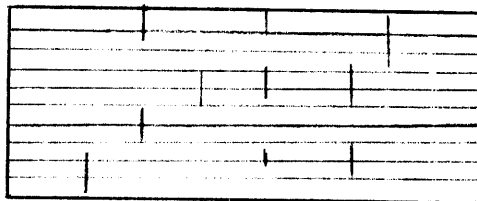
\$ TAPE BB

## CLOSE FILES

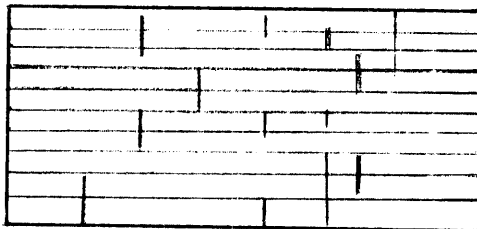
CALL CLOSE(AWD,2)

AWD VFD 18/FILEAA,18/0  
VFD 18/FILEBB,1/1,17/0

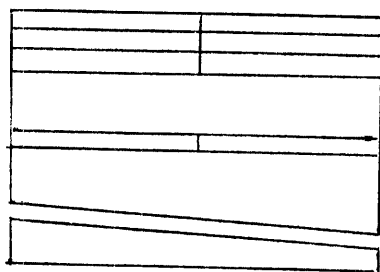
FILCB FILEAA,AA,BUFFA1,BUFFA2



FILCB FILEBB,BB,BUFFB1



BUFFB1 BSS 321



\$ DATA AA

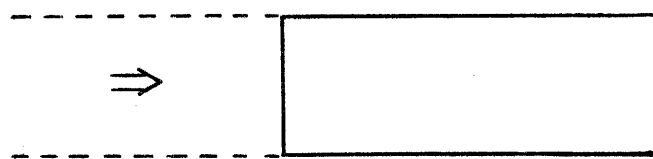
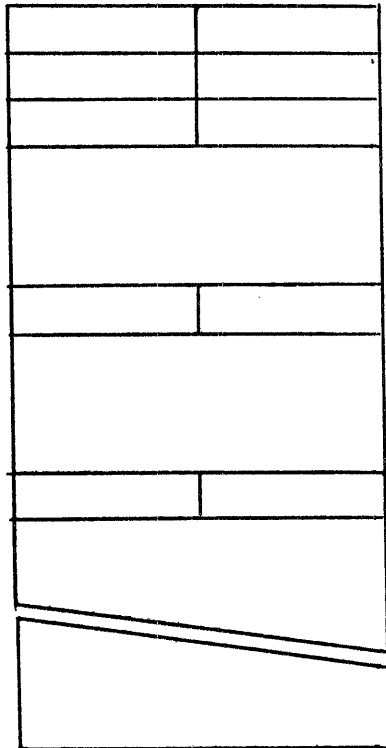
\$ TAPE BB

# GET LOGICAL RECORD

CALL GET (FILEAA, ENDAA, WSA)  
FILCB FILEAA, AA, BUFFA1, BUFFA2

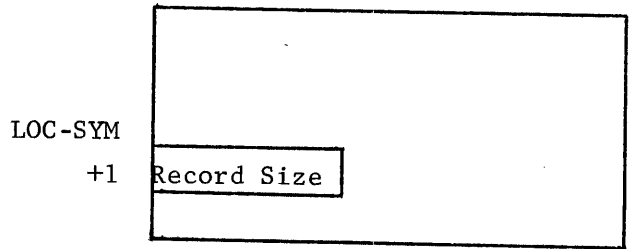
BUFFA1 BSS 321

WSA BSS 14

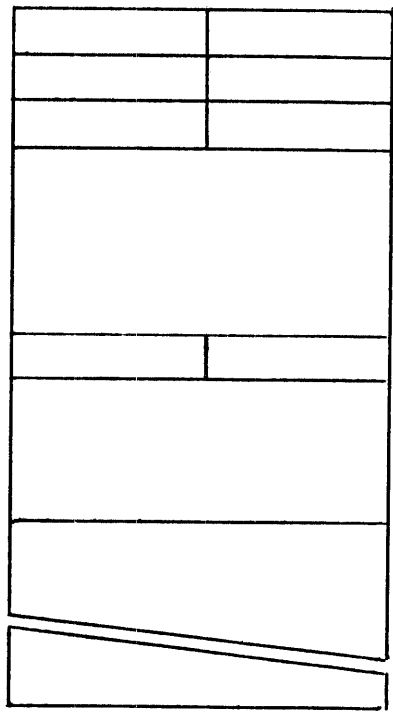


# WRITE LOGICAL RECORD

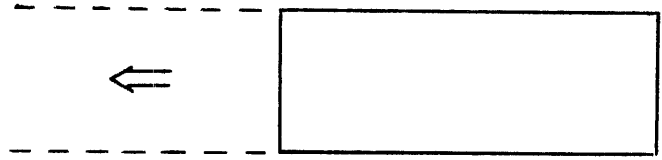
```
CALL PUT (FILEBB, WSA)  
FILCB (FILEBB, BB, BUFFB1)
```



BUFFB1 BSS 321



WSA BSS 14



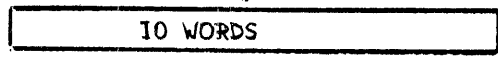
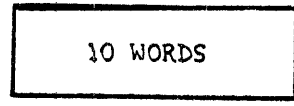


# INITIALIZE EDITING PARAMETERS

CALL IOEDIT (LLIST, 5)

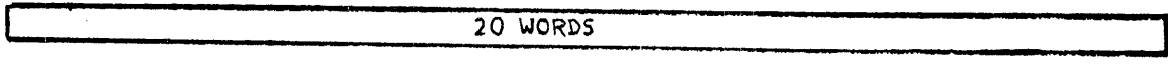
LLIST ZERO HEAD1, 1  
 ZERO HEAD2, 2  
 ZERO PCHCD, 3  
 ZERO RPCD, 7  
 ZERO PGNO, 5

HEAD1 BCI

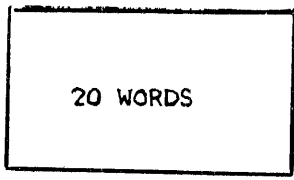


39686 01 03-16-70 19.268

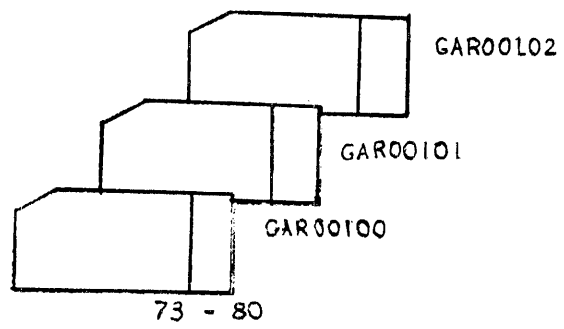
PAGE 00



HEAD2 BCI



PCHCD BCI 2, GAR00100



# WRITE LOGICAL RECORD

FILCB **OUTFL**, EE, OTBUFF

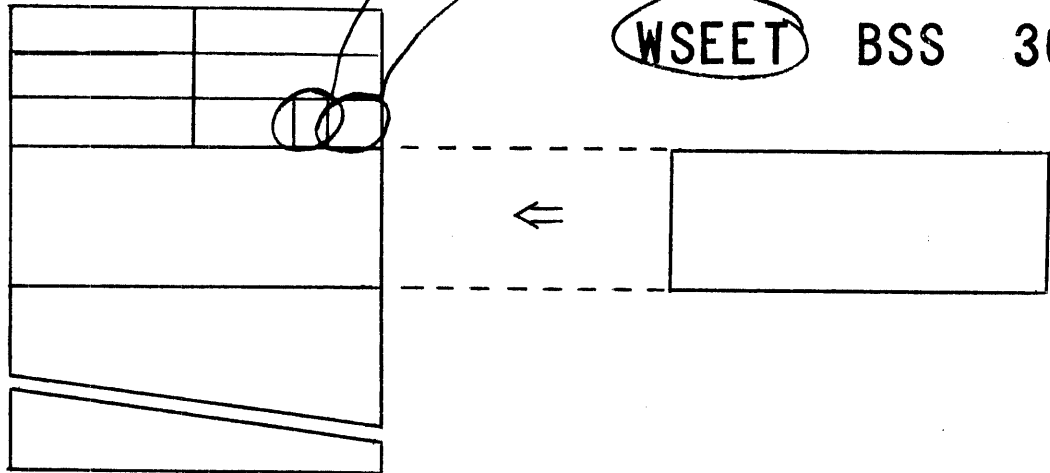
LOC-SYM

+1 Record Size

CALL WTREC (**OUTFL**, **WSEET**, **=3**, **=20**)

OTBUFF BSS 321

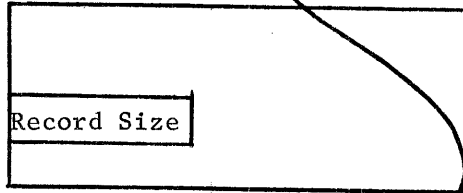
**WSEET** BSS 30



# WRITE LOGICAL RECORD

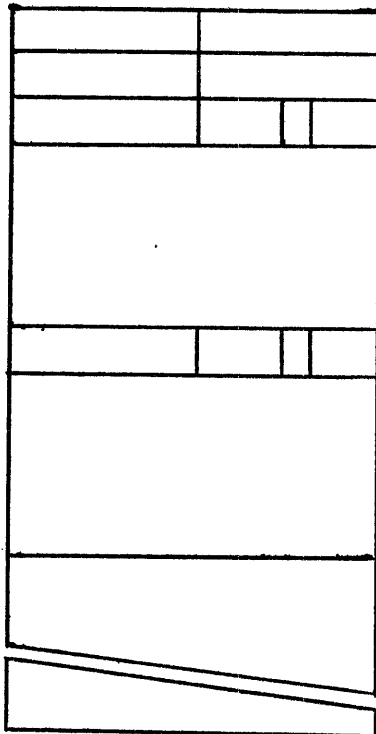
FILCB (FLFF), FF, BUFFF

LOC-SYM  
+1

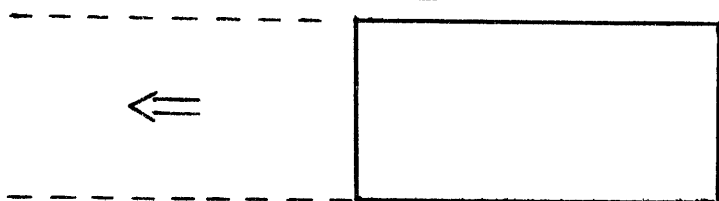


CALL PRINT ((FLFF), (FFWS), =2)

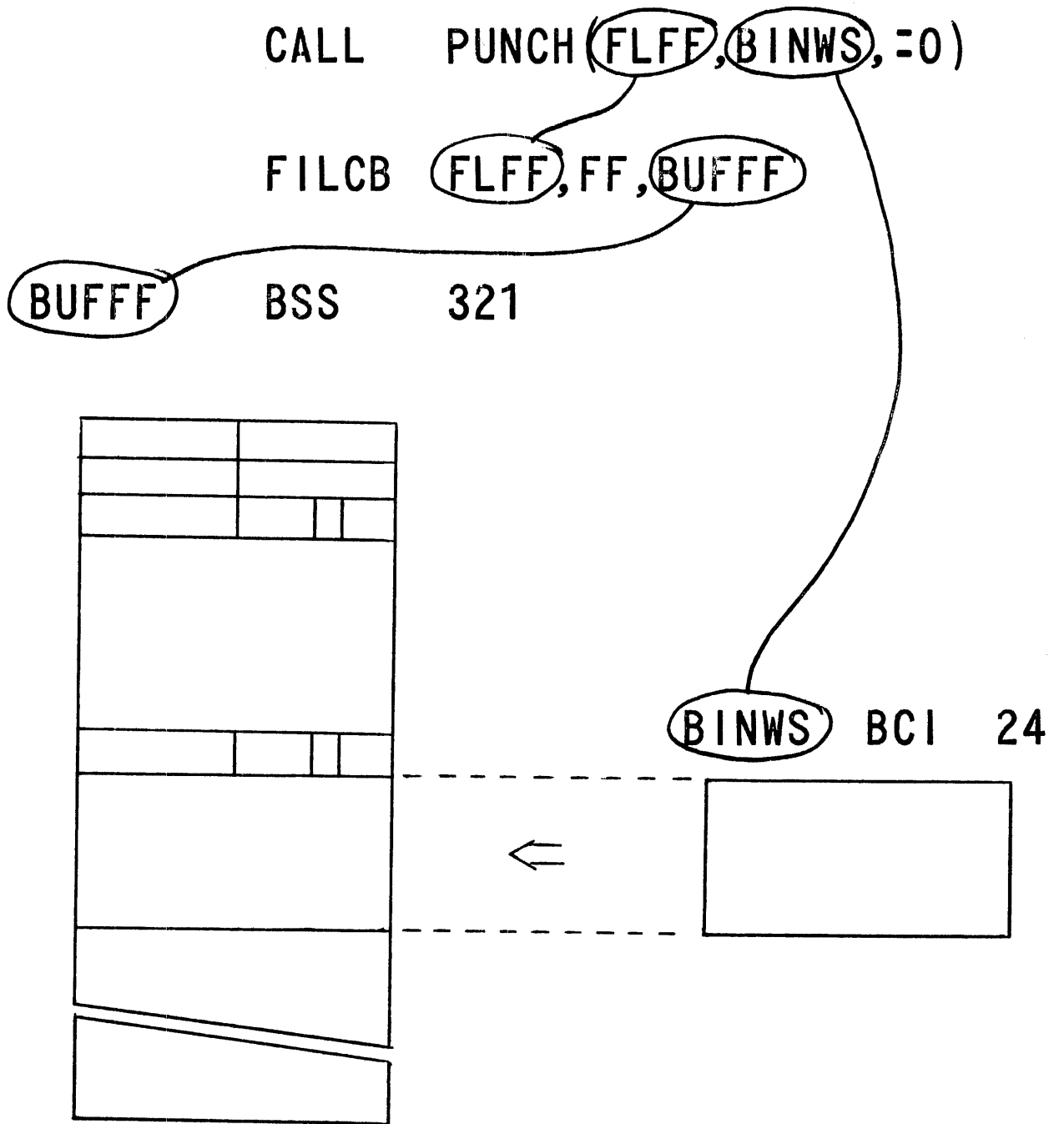
BUFFF          BSS          321



(FFWS) BCI 22



# WRITE LOGICAL RECORD



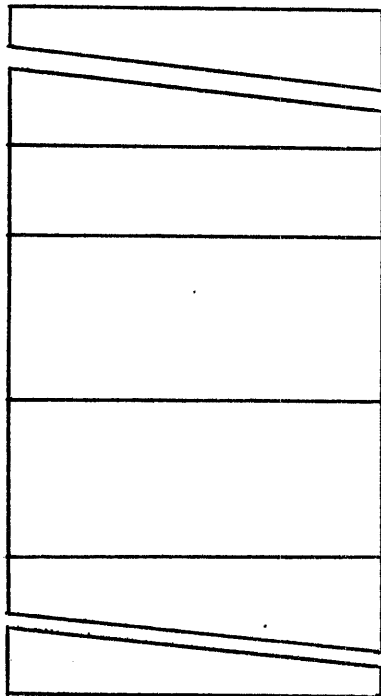
# PHYSICAL RECORD PROCESSING SEQUENTIAL FILES

FILCB (PHINS),SI

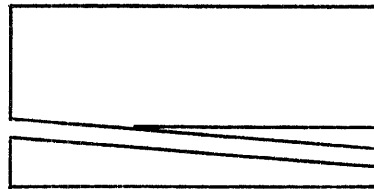
CALL READ((PHINS),(CNTA),INSCC)

(CNTA)

IOTP AREA3,25  
IOTP AREA1,50  
IOTD AREA2,50



AREA1 BSS 100



AREA2 BSS 50



AREA3 BSS 25



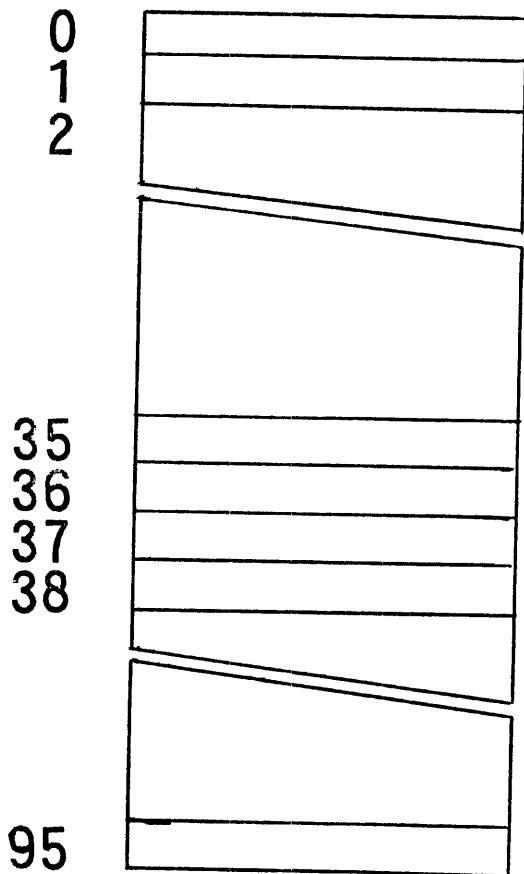
# PHYSICAL RECORD PROCESSING RANDOM FILES

FILCB PHINR, RI

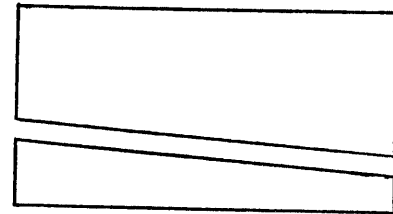
CALL READ(PHINR, CNTC)

CNTC

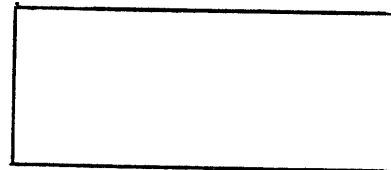
DEC 35  
 IOTP AREA8, 40  
 IOTP AREA7, 80  
 IOTD AREA9, 40



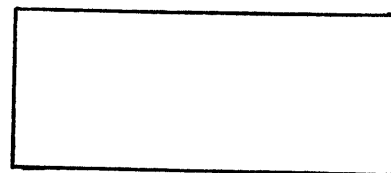
AREA7 BSS 80



AREA8 BSS 40



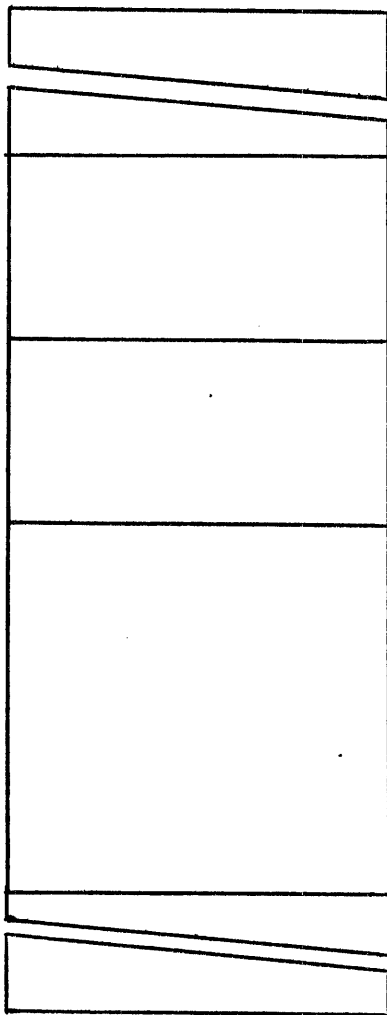
AREA9 BSS 40



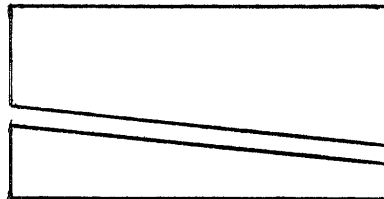
\$ DISC RI, X1S, 1R

# PHYSICAL RECORD PROCESSING SEQUENTIAL FILES

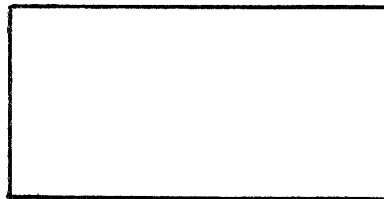
```
FILCB PHOTS,50  
CALL WRITE (PHOTS,CNTB)  
CNTB IOTP AREA5,25  
      IOTP AREA4,50  
      IOTD AREA6,25
```



AREA4 BSS 50



AREA5 BSS 25



AREA6 BSS 25



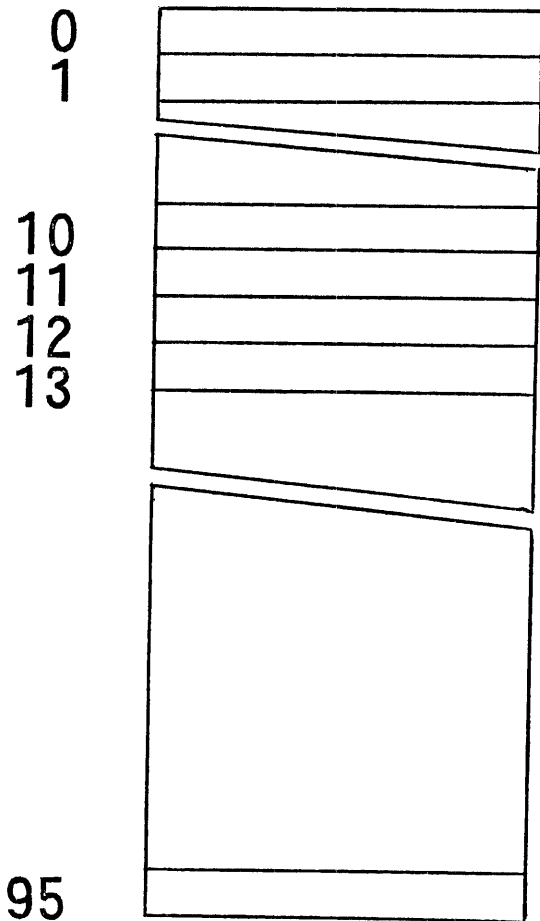
# PHYSICAL RECORD PROCESSING RANDOM FILES

FILCB PHOTR, R0

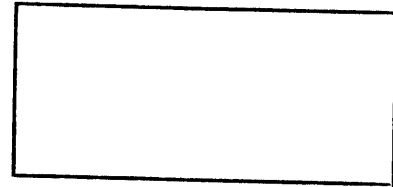
CALL WRITE (PHOTR, CNTD)

CNTD

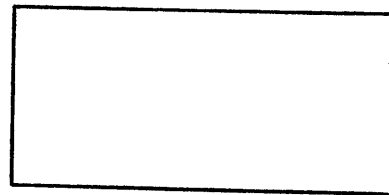
DEC 10  
IOTP AREA12, 80  
IOTP AREA10, 40  
IOTD AREA11, 40



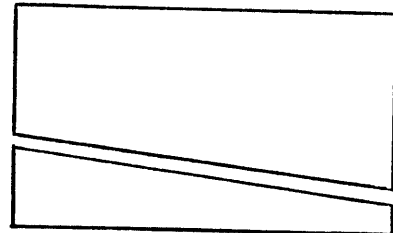
AREA10 BSS 40



AREA11 BSS 40



AREA12 BSS 80



95

\$ DISC R0, X1S, 1R



# FILE CONTROL BLOCK

## FILCB MYFILE,AA,BUFF1

-14	PREHEADER LABEL EXIT		
-13	POSTHEADER LABEL EXIT		
-12	PRETRAILER LABEL EXIT		
-11	POSTTRAILER LABEL EXIT		
-10	FIRST SIX CHARACTERS OF FILE NAME		
-9	LAST SIX CHARACTERS OF FILE NAME		
-8	RETENTION PERIOD		REEL SEQUENCE NUMBER
-7	FILE SERIAL NUMBER		
-6	BLOCK COUNT		FILE COUNT
-5	ERROR ROUTINE EXIT		
-4	PAT POINTER		FILE CODE
-3	IOS STATUS RETURN WORD		
-2	IOS STATUS RETURN WORD		
-1	FGB POINTER		PHYSICAL DEVICE ADDRESS
LOC-SYM	CURRENT RECORD INDEX		
+1	RECORD SIZE		
+2	LOCATION OF FIRST BUFFER		LOCATION OF SECOND BUFFER
+3	GEFRC WORKING STORAGE WORD		
+4	BLOCK SIZE		
+5	RECORD SIZE ROUTINE LOCATION		

4  
 3  
 1  
 2  
 3

### FILE DESIGNATOR WORD

FWD VFD 18/MYFILE,18/0

MYFILE								
--------	--	--	--	--	--	--	--	--

FILE CONTROL BLOCK MACRO EXAMPLE

000008 000000 0110 00 000

```

1      TTL      BASIC SA COURSE - GEFRC EXAMPLES
2      TTLS     FILE CONTROL BLOCK MACRO EXAMPLE
3      SYMDEF   GO
4      NOP

```

```

5 *
6 *
7 *     EXAMPLE OF COMPLETE FILE CONTROL
8 *     BLOCK MACRO-INSTRUCTION
9 *
10 *

```

000276	11.	JSE	COUNT	
000276	12	FILCB	MYFILE,	SYMBOLIC NAME OF FILE CONTROL BLOCK
000276	13	ETC	ZZ,	FILE CODE
000276	14	ETC	BUFF1,	FIRST BUFFER
000276	15	ETC	BUFF2,	SECOND BUFFER
000276	16	ETC	320,	MAXIMUM BLOCK SIZE
000276	17	ETC	0,	RECORD FORM
000276	18	ETC	:	RECORD SIZE
000276	19	ETC	:	NO BLOCK SERIAL NUMBERS
000276	20	ETC	:	ERROR ROUTINE
000276	21	ETC	:	NO STANDARD LABELS
000276	22	ETC	0,	RECORDING MODE
000276	23	ETC	0,	RECORDING DENSITY
000276	24	ETC	0,	MULTIFILE REEL
000276	25	ETC	0,	RETENTION PERIOD
000276	26	ETC	:	PREHEADER LABEL ROUTINE
000276	27	ETC	:	POSTHEADER LABEL ROUTINE
000276	28	ETC	:	PRETRAILER LABEL ROUTINE
000276	29	ETC	:	POSTTRAILER LABEL ROUTINE
000276	30	ETC	MYFILE,	FILENAME
000276	31	ETC	0,	GEPR OVERRIDE
000276	32	ETC	0	PARTITIONED RECORD

206

FILE DESIGNATOR WORD AND BUFFERS

```

35 *
36 *
37 *   EXAMPLE OF FILE DESIGNATOR WORD
38 *
39 *
40 ZZWD   VFD   18/MYFILE,   SYMBOLIC NAME OF FILE CONTROL BLOCK
41       ETC   1/1,         INPUT/OUTPUT INDICATOR
42       ETC   1/0,         REWIND ON OPEN
43       ETC   2/0,         REWIND ON CLOSE
44       ETC   1/0,         PRIME INPUT BUFFER OPTION
45       ETC   1/0,         PUT SIZE ON OUTPUT OPTION
46       ETC   1/0,         REQUIRED FILE OPTION
47       ETC   11/0        FILE POSITIONING VALUE

```

000315 000310400800 010

```

48 *
49 *
50 *   RESERVE BUFFER SPACE!
51 *
52 *   EACH BUFFER MUST BE ONE WORD LARGER THAN
53 *   THE PHYSICAL RECORD FOR A BUFFER CONTROL WORD
54 *   TO CONTROL THE DATA FLOW TO OR FROM THE BUFFER
55 *
56 *

```

008316  
001017

```

57 BUFF1 3SS   321   FIRST BUFFER
58 BUFF2 3SS   321   SECOND BUFFER

```

207

CALL: OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS.

61 \*  
 62 \*  
 63 \* THE FOLLOWING IS A DESCRIPTION OF A FILE CONTROL  
 64 \* BLOCK FOR A CARD FILE USING A S DATA AA  
 65 \* FILE CONTROL CARD.  
 66 \*  
 67 \* BECAUSE OF THE USE OF THE S DATA AA FILE  
 68 \* CONTROL CARD THIS FILE WILL BE IN STANDARD  
 69 \* SYSTEM FORMAT  
 70 \*

001520

71 \*  
 72 \* FILECB FILEAA,AA,BUFFA2,BUFFA2  
 73 \*  
 74 \*

75 \* FILE CONTROL BLOCK FOR OUTPUT FILE CODE BB  
 76 \* USING STANDARD SYSTEM FORMAT WITH ONE BUFFER  
 77 \* THIS IS A TAPE FILE USING A S TAPE BB  
 78 \* FILE CONTROL CARD  
 79 \*

001535

80 \*  
 81 \* FILECB FILEBB,BB,BUFFB1  
 82 \*  
 83 \*

84 \* FILE DESIGNATOR WORDS ASSOCIATED WITH FILEAA AND FILEBB  
 85 \*

001552 001530000000 010  
 001553 001545400000 010

86 \*  
 87 \* AWD: VFD 18/FILEAA,18/0  
 88 \* VFD 18/FILEBB,1/1,17/0  
 89 \*  
 90 \*

91 \* BUFFERS FOR FILEAA AND FILEBB  
 92 \*

001554  
 002255  
 002756

93 \*  
 94 \* BUFFA1 BSS 321 FIRST BUFFER FOR FILEAA  
 95 \* BUFFA2 BSS 321 SECOND BUFFER FOR FILEAA  
 96 \* BUFFB1 BSS 321 BUFFER FOR FILEBB  
 97 \*  
 98 \*

99 \* WORKING STORAGE AREA INTO WHICH GEFRG  
 100 \* WILL MOVE THE LOGICAL RECORD  
 101 \*

003457  
 000001

102 \*  
 103 \* WSA BSS 14  
 104 \* JSE

208

CALL OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

```

107 *
108 *
109 *   INITIALIZE TWO FILES BEGINNING AT A LIST
110 *   OF FILE DESIGNATOR WORDS LOCATED AT AWD
111 *
112 *
113   CALL OPEN(AWD,2)

000001 040000700000 030
000002 000006710000 010
000003 007210000161 010
000004 001552000000 010
000005 000002000000 000

```

CALL, OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

```

116 *
117 *
118 *      OBTAIN THE NEXT LOGICAL RECORD FROM THE
119 *      BUFFER OF FILEAA AND MOVE IT TO WORKING
120 *      STORAGE AREA WSA
121 *
122 *
123 *      CALL GET(FILEAA,ENDAA,WSA)

000006 010000701000 030
000007 000014710000 010
000010 007210000173 010
000011 001530000000 010
000012 000052000000 010
000013 003457000000 010

124 *
125 *
126 *      OBTAIN THE FIRST LOGICAL RECORD IN THE NEXT
127 *      PHYSICAL RECORD FROM FILEAA AND MOVE
128 *      IT TO WORKING STORAGE AREA WSA
129 *
130 *
131 *      CALL GETBK(FILEAA,ENDAA,WSA)

000014 100000701000 030
000015 000022710000 010
000016 007210000203 010
000017 001530000000 010
000020 000052000000 010
000021 003457000000 010
    
```

210

CALL: OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

134 \*  
 135 \*  
 136 \* TO INSERT THE NEXT LOGICAL RECORD INTO THE  
 137 \* NEXT AVAILABLE SPACE IN THE OUTPUT BUFFER  
 138 \*  
 139 \* THE SIZE OF THE LOGICAL RECORD MUST  
 140 \* BE PLACED IN WORD PLUS 1 OF THE  
 141 \* FILE CONTROL BLOCK WHEN CALL PUT MACRO IS USED  
 142 \*

143 \* SEVERAL METHODS CAN BE USED  
 144 \* TO PLACE A BINARY VALUE IN THE  
 145 \* RECORD SIZE OF THE FILE CONTROL BLOCK  
 146 \*

147 \* (1.) USING A LITERAL  
 148 \*  
 149 \*

000022 000016 2260 03 000  
 000023 001546 7360 00 010

150 LDX6 #14,DU  
 151 STX6 FILEBB+1  
 152 \*  
 153 \*

154 \* (2.) OR USING THE CONTENTS OF ANOTHER FIELD  
 155 \*  
 156 \*

000024 003475 2260 00 010  
 000025 001546 7360 00 010

157 LDX6 FOR  
 158 STX6 FILEBB+1  
 159 \*  
 160 \*

003475 010420202820 000  
 000026 000026

161 JSE COUNT  
 162 FOR VFD 18/14,18/0  
 163 JSE  
 164 \*

165 \*  
 166 \* INSERT THE NEXT LOGICAL RECORD FROM AREA WSA INTO  
 167 \* THE NEXT AVAILABLE SPACE OF THE OUTPUT BUFFER  
 168 \*  
 169 \*

000026 020000701000 030  
 000027 000033710000 010  
 000030 00721000252 010  
 000031 001545000000 010  
 000032 003457000000 010

170 CALL PJT(FILEBB,WSA)

171 \*  
 172 \*  
 173 \* TO INSERT THE NEXT LOGICAL RECORD AS THE FIRST LOGICAL  
 174 \* RECORD OF THE NEXT PHYSICAL RECORD FROM AREA WSA  
 175 \*  
 176 \*

000033 130000701000 030  
 000034 00004071000 010  
 000035 003457000000 010

177 CALL PJTBK(FILEBB,WSA)

211

39686 01 03-16-70 19,268

BASIC SA COURSE - GEFRC EXAMPLES

PAGE 8

CALL OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

000036	001545000000	010
000037	003457000000	010

212



CALL OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

```

180 *
181 *
182 * THIS SAME OPERATION COULD HAVE BEEN
183 * ACCOMPLISHED EASIER USING THE CALL COPY MACRO
184 *
185 *
000040 010000701000 030 186 CALL GET(FILEAA,ENDAA)
000041 000045710000 010
000042 007210000272 010
000043 001530000000 010
000044 000052000000 010
000045 030000701000 030 187 CALL COPY(FILEBB,FILEAA)
000046 000052710000 010
000047 007210000273 010
000050 001545000000 010
000051 001530000000 010

```

CALL OPEN, CLOSE, GET, PUT MACRO-INSTRUCTIONS

```

190 *
191 *
192 *   THIS IS THE END-OF-FILE ROUTINE WHERE
193 *   CONTROL WILL BE TRANSFERRED WHEN ALL
194 *   OF THE INPUT RECORDS HAVE BEEN PROCESSED
195 *   AND A 17 OCTAL END-OF-FILE MARK IS ENCOUNTERED
196 *
197 *   THE CALL CLOSE MACRO DISCONNECTS THE FILES
198 *   SPECIFIED BY THE FILE DESIGNATOR WORDS
199 *
200 *
000052 070000701000 030
000053 000057710000 010
000054 007210000311 010
000055 001552000000 010
000056 000002000000 000
000057 000007 0010.00 000
201 ENDA  CALL CLOSE(AWD,2)
202 MME  GEFINI

```

PROCESSING FIXED LENGTH RECORDS

```

205 *
206 *
207 *   THIS EXAMPLE SHOWS HOW TO PROCESS FIXED
208 *   LENGTH RECORDS AND MOVE LOGICAL RECORDS
209 *   FROM FILECC TO FILEDD.
210 *
211 *   FILE CONTROL BLOCK, FILE DESIGNATOR WORD,
212 *   BUFFER, AND WORKING STORAGE AREA FOR
213 *   INPUT FILECC USING A S TAPE CC
214 *   FILE CONTROL CARD
215 *
216 *
217 *           JSE      COUNT
218 *           FILECB   FILECC,CC,BUFFC1,,101,1,20
003513 003506000000 010 219 DWD   VFD      18/FILECC,18/0
           003476 220 BUFFC1 BSS      102      BUFFER FOR FILECC
           003514 221 WSC   BSS      20        WORKING STORAGE AREA
           003662
222 *
223 *
224 *   FILE CONTROL BLOCK, FILE DESIGNATOR WORD,
225 *   AND BUFFER FOR OUTPUT FILE DD USING
226 *   A S TAPE DD CONTROL CARD
227 *
228 *
229 *           FILECB   FILEDD,DD,BUFFD1,,201,1,20
003723 003716400000 010 230 DWD   VFD      18/FILEDD,1/1,17/0
           003724 231 BUFFD1 BSS      202      BUFFER FOR FILEDD
           000060 232                JSE

```

215

PROCESSING FIXED LENGTH RECORDS

235 \*  
 236 \*  
 237 \* THIS IS AN EXAMPLE OF CODING TO ESTABLISH  
 238 \* RECORD SIZE OF INPUT AND OUTPUT FILES  
 239 \* AND THE TRANSFER OF DATA TO AND FROM  
 240 \* WORKING STORAGE AREA WSC  
 241 \*  
 242 \*

000060 040000701000 030  
 000061 00006571000 010  
 000062 007210000363 010  
 000063 003513000000 010  
 000064 000001000000 000  
 000065 040000701000 030  
 000066 000072710000 010  
 000067 007210000364 010  
 000070 003723000000 010  
 000071 000001000000 000  
 000072 000024 2250 03 000  
 000073 003507 7450 00 010  
 000074 010000700000 030  
 000075 000102710000 010  
 000076 007210000367 010  
 000077 003506000000 010  
 000100 000111000000 010  
 000101 003662000000 010  
 000102 000024 2250 03 000  
 000103 003717 7450 00 010  
 000104 020000701000 030  
 000105 000111710000 010  
 000106 007210000372 010  
 000107 003716000000 010  
 000110 003662000000 010  
 000111 070000701000 030  
 000112 000116710000 010  
 000113 007210000373 010  
 000114 003513000000 010  
 000115 000001000000 000  
 000116 070000701000 030  
 000117 000123710000 010  
 000120 007210000374 010  
 000121 003723000000 010  
 000122 000001000000 000

243 CALL OPEN(CWD,1)  
  
 244 CALL OPEN(DWD,1)  
  
 245 LDX5 #20,DU PLACE RECORD SIZE OF  
 246 STX5 FILECC+1 INPUT FILE IN FCB  
 247 CALL GET(FILECC,ENDE,WSC)  
  
 248 LDX5 #20,DU PLACE RECORD SIZE OF  
 249 STX5 FILEDD+1 OUTPUT FILE IN FCB  
 250 CALL PUT(FILEDD,WSC)  
  
 251 ENDC CALL CLOSE(CWD,1)  
  
 252 CALL CLOSE(DWD,1)

216

CALL WTREC EXAMPLES

```

255 *
256 *
257 * THE CALL WTREC CAN BE USED TO CREATE A
258 * LOGICAL RECORD AND DIRECT IT TO ANY OUTPUT
259 * MEDIA DEPENDING ON THE DEVICE SPECIFIED
260 * ON THE FILE CONTROL CARD
261 *
262 * FILE CONTROL BLOCK, FILE DESIGNATOR WORD,
263 * AND BUFFER FOR OUTPUT FILE OUTFL
264 *
265 *
266 * USE COUNT
267 * FILCB OUTFL,EE,DTBUFF
268 *
269 * OTWD VFD 18/OUTFL,1/1,17/0
270 *
271 * DTBUFF 355 321 BUFFER FOR OUTFL
272 * USE
    
```

004236  
004236

004253 004244400000 010

004254  
000123

217

CALL WTREC EXAMPLES

275 \*  
 276 \*  
 277 \* THIS IS AN EXAMPLE USING CALL WTREC  
 278 \* TO CREATE A LOGICAL RECORD AND DIRECT  
 279 \* IT TO TAPE OR DISC DEPENDING ON THE  
 280 \* FILE CONTROL CARD  
 281 \*  
 282 \*

000123 000036 2350 03 000  
 000124 004247 7350 00 010  
 000125 150000701000 030  
 000126 000134710000 010  
 000127 007210000435 010  
 000130 004246000000 010  
 000131 004755000000 010  
 000132 007212000000 010  
 000133 007213000000 010

283 \_DA \*30,DU PLACE RECORD SIZE IN  
 284 STA OUTFL\*1 FILE CONTROL BLOCK  
 285 CALL WTREC(OUTFL,WSEET,\*5,\*20)

286 \*  
 287 \*  
 288 \* A LOGICAL RECORD WILL BE CREATED  
 289 \* USING THE CONTENTS OF THIS  
 290 \* FIELD  
 291 \*  
 292 \*

004755 293 JSE COUNT  
 004755 294 WSEET 3SS 30  
 000134 295 JSE

218

CALL WTREC EXAMPLES

298 \*  
 299 \*  
 300 \* THE FOLLOWING EXAMPLES CREATE A LOGICAL RECORD  
 301 \* USING THE CALL WTREC AND DIRECT THE RECORD  
 302 \* TO THE PRINTER OR CARD PUNCH THROUGH THE  
 303 \* USE OF S SYSOUT EE FILE CONTROL CARD  
 304 \*  
 305 \* THIS IS AN EXAMPLE USING CALL WTREC  
 306 \* TO PUNCH A BINARY CARD  
 307 \*

000134 000033 2350 03 000  
 000135 004247 7580 00 010  
 000136 150000701000 030  
 000137 000145710000 010  
 000140 007210000467 010  
 000141 004246000000 010  
 000142 005013000000 010  
 000143 007214000000 010  
 000144 005046000000 010

308 \*  
 309 LDA =27,DU PLACE THE RECORD SIZE IN  
 310 STA OUTFL+1 FILE CONTROL BLOCK  
 311 CALL WTREC(OUTFL,DTSTR,=1,REP22)

312 \*  
 313 \*  
 314 JSE COUNT  
 315 DTSTR SCI 9, THIS INFORMATION

005013 202063303162 000  
 005014 203145264651 000  
 005015 442163319645 000  
 005016 202020202020 000  
 005017 202020202020 000  
 005020 202020202020 000  
 005021 202020202020 000  
 005022 202020202020 000  
 005023 202020202020 000  
 005024 202066314343 000  
 005025 202225209764 000  
 005026 452330252420 000  
 005027 202020202020 000  
 005030 202020202020 000  
 005031 202020202020 000  
 005032 202020202020 000  
 005033 202020202020 000  
 005034 202020202020 000  
 005035 202031452022 000  
 005036 314521517020 000  
 005037 202020202020 000  
 005040 202020202020 000  
 005041 202020202020 000  
 005042 202020202020 000  
 005043 202020202020 000  
 005044 202020202020 000  
 005045 202020202020 000

316 SCI 9, WILL BE PUNCHED

317 SCI 9, IN BINARY

219

CALL NTREC EXAMPLES

- 318 \*
- 319 \*
- 320 \*     SYSOUT WILL SORT TOGETHER ALL RECORDS
- 321 \*     HAVING THE SAME REPORT CODE
- 322 \*
- 323 \*

005046 000000000022 000     324 REP22   JCT     000000000022     REPORT CODE OF 22  
                   000105                    325            USE



CALL WTREC EXAMPLES

328 \*  
 329 \*  
 330 \* THIS IS AN EXAMPLE USING CALL WTREC  
 331 \* TO PUNCH A HOLLERITH CARD  
 332 \*  
 333 \*

000145 000016 2350 03 000  
 000146 004247 7350 00 010  
 000147 150000705000 030  
 000150 000156710000 010  
 000151 007210000320 010  
 000152 004246000000 010  
 000153 00504700:000 010  
 000154 007215000000 010  
 000155 005046000000 010

334 LDA =14,DU  
 335 STA OUTFL+1  
 336 CALL WTREC(OUTFL, #SPUN, #2, REP22)

337 \*  
 338 \*  
 339 JSE COUNT  
 340 WSPUN SCI 9, THIS INFORMATION WILL BE

005047 202063303162 000  
 005050 203145264651 000  
 005051 442163311645 000  
 005052 206631434320 000  
 005053 222520204020 000  
 005054 202020202020 000  
 005055 202020202020 000  
 005056 202020204020 000  
 005057 202020202020 000  
 005060 202047645523 000  
 005061 302524203145 000  
 005062 203046435325 000  
 005063 513163306020 000  
 005064 202020204020 000  
 000156

341 SCI 5, PUNCHED IN HOLLERITH  
 342 JSE

221

CALL WTREC EXAMPLES

345 \*  
 346 \*  
 347 \* THIS IS AN EXAMPLE USING CALL WTREC  
 348 \* TO PRINT A LINE  
 349 \*  
 350 \*

000156 000025 2350 03 000  
 000157 004247 7850 00 010  
 000160 150000701000 030  
 000161 000167710000 010  
 000162 00721000,541 010  
 000163 004246000000 010  
 000164 00506500,000 010  
 000165 005112000000 010  
 000166 005046000000 010

351 LOA =21,DU MOVE RECORD SIZE INCLUDING  
 352 STA OUTFL\*1 THE SLEW CHARACTER TO THE FCB  
 353 CALL WTREC(OUTFL,PRWS,PRLIN,REP22)

354 \*  
 355 \*  
 356 JSE COUNT  
 357 PRWS 3CI 9, THIS INFORMATION

005065

005065 202063303162 000  
 005066 203145264651 000  
 005067 442163314645 000  
 005070 202020200020 000  
 005071 202020200020 000  
 005072 202020200020 000  
 005073 202020200020 000  
 005074 202020200020 000  
 005075 202020200020 000  
 005076 202066319343 000  
 005077 202225204751 000  
 005100 314563252420 000  
 005101 202020200020 000  
 005102 202020200020 000  
 005103 202020200020 000  
 005104 202020200020 000  
 005105 202020200020 000  
 005106 202020200020 000  
 005107 202020200020 000  
 005110 202020200020 000  
 005111 770400000000 000

358 3CI 9, WILL BE PRINTED

359 3CI 2,  
 360 OCT 770400000000 PRINTER SLEW CHARACTER

361 \*  
 362 \*  
 363 \* A MEDIA CODE OF 3 DIRECTS THIS  
 364 \* RECORD THROUGH SYSOJT TO THE PRINTER  
 365 \*

005112 000000 000003 000  
 366 \*  
 367 PRLIN ZERO 0,3 MEDIA CODE

222

CALL PRINT EXAMPLES

```

005130 005123400000 005113 010
          005131
          000167
    
```

```

000167 000026 2260 03 000
000170 005124 7460 00 010
000171 110000701000 030
000172 000177710000 010
000173 007210000600 010
000174 005123000000 010
000175 005632000000 010
000176 007215000000 010
    
```

```

          005632
005632 202063301162 000
005633 203145251651 000
005634 442163314645 000
005635 202020202020 000
005636 202020202020 000
005637 202020202020 000
005640 202020202020 000
005641 202020202020 000
005642 202020202020 000
005643 202056314343 000
005644 202225204751 000
005645 314563254420 000
005646 202020202020 000
005647 202020202020 000
005650 202020202020 000
005651 202020202020 000
005652 202020202020 000
005653 202020202020 000
005654 202020202020 000
005655 202020202020 000
005656 202020202020 000
005657 202020202020 000
          000177
    
```

```

370 *
371 *
372 * THIS IS AN EXAMPLE USING CALL PRINT
373 * TO WRITE A PRINT LINE
374 *
375 *
376 * FILCB FLFF,FF,BJFF
377 FFWD VFD 19/FLFF,1/1,17/0
378 BUFFF: BSS 321
379 * JSE
380 *
381 *
382 * LDX6 =22,DU
383 * STX6 FLFF+1
384 * CALL PRINT(FLFF,FFWS,#2)
    
```

```

385 *
386 *
387 * JSE COUNT
388 FFWS BCI 9, THIS INFORMATION
    
```

```

389 * BCI 9, WILL BE PRINTED
    
```

```

390 * BCI 4,
    
```

```

391 * JSE
    
```

223

CALL PUNCH EXAMPLES

394 \*  
 395 \*  
 396 \* THIS IS AN EXAMPLE USING CALL PUNCH  
 397 \* TO PUNCH BINARY CARDS  
 398 \*  
 399 \*  
 400 CALL PUNCH(FLFF,BINWS,#0)

000177 120000701000 030  
 000200 000205710000 010  
 000201 007210000620 010  
 000202 005123000000 010  
 000203 005660000000 010  
 000204 007216000000 010

005660

005660 202063303162 000  
 005661 203143253651 000  
 005662 442163311645 000  
 005663 202020205020 000  
 005664 202020202020 000  
 005665 202020202020 000  
 005666 202020204020 000  
 005667 202020204020 000  
 005670 202020204020 000  
 005671 202066311343 000  
 005672 202225204764 000  
 005673 452330255420 000  
 005674 202020204020 000  
 005675 202020204020 000  
 005676 202020207020 000  
 005677 202020202020 000  
 005700 202020204020 000  
 005701 202020202020 000  
 005702 202031454022 000  
 005703 314521517020 000  
 005704 202020202020 000  
 005705 202020202020 000  
 005706 202020202020 000  
 005707 202020204020 000

000205

401 JSE COUNT  
 402 BINWS SCI 9, THIS INFORMATION  
  
 403 SCI 9, WILL BE PUNCHED  
  
 404 SCI 6, IN BINARY  
  
 405 JSE

224

CALL PUNCH EXAMPLES

408 \*  
 409 \*  
 410 \* THIS IS AN EXAMPLE USING CALL PUNCH  
 411 \* TO PUNCH HOLLERITH CARDS  
 412 \*  
 413 \*  
 414 \* CALL PUNCH(FLFF,PJNWS,=1)

000205 120006701000 030  
 000206 000213710000 010  
 000207 007210000636 010  
 000210 005123000000 010  
 000211 005710000000 010  
 000212 007214000000 010

415 \*  
 416 \*  
 417 \* JSE COUNT  
 418 \* PJNWS 301 9, THIS INFORMATION WILL BE PUNCHED

005710 202063303162 000  
 005711 203145259651 000  
 005712 442163312645 000  
 005713 206631434320 000  
 005714 222520472445 000  
 005715 233025248020 000  
 005716 202020202020 000  
 005717 202020202020 000  
 005720 202020202020 000  
 005721 202031452030 000  
 005722 464343258131 000  
 005723 633020202020 000  
 000213

419 \* 301 3, IN HOLLERITH  
 420 \* JSE

225

CALL IOEDIT

423 \*  
 424 \*  
 425 \* THE CALL IOEDIT INITIALIZES PARAMETERS  
 426 \* THAT WILL BE USED AS EDITING FUNCTIONS.  
 427 \* WHEN EITHER CALL PRINT OR CALL PUNCH  
 428 \* ARE USED  
 429 \*  
 430 \*

000213	160000701000	030
000214	000220710000	010
000215	00721000657	010
000216	005724000000	010
000217	000000000000	000

431 CALL IOEDIT(LLIST,5)

432 \*  
 433 \*  
 434 \* LIST OF CONTROL PARAMETERS AND CODES  
 435 \*  
 436 \*

	005724	
005724	005731 000001	010
005725	005743 000002	010
005726	005767 000003	010
005727	005771 000007	010
005730	005772 000005	010

437	JSE	COUNT
438 LLIST	ZERO	HEAD1,1
439	ZERO	HEAD2,2
440	ZERO	PCHCD,3
441	ZERO	RPCD,7
442	ZERO	PGNO,5

443 \*  
 444 \*  
 445 \* TEN WORDS TO BE INSERTED IN PRINT POSITIONS  
 446 \* OF 31 - 90 OF THE FIRST LINE OF THE HEADING  
 447 \* WITH CALL PRINT  
 448 \*  
 449 \*

005731	202063303162	000
005732	203145264651	000
005733	442163314645	000
005734	206631434320	000
005735	222520202020	000
005736	202031452225	000
005737	516325242031	000
005740	452026313162	000
005741	632043314525	000
005742	202020206020	000

450 HEAD1 3CI 5, THIS INFORMATION WILL BE

451 3CI 5, INSERTED IN FIRST LINE

452 \*  
 453 \*  
 454 \* TWENTY WORDS TO BE USED AS THE SECOND  
 455 \* HEADING LINE OF EACH PAGE WITH CALL PRINT  
 456 \*  
 457 \*

005743	202063303162	000
005744	203145264651	000
005745	442163314645	000

458 HEAD2 3CI 9, THIS INFORMATION WILL

226

CALL: F0EDIT

005746 206631434320 000  
 005747 202020202020 000  
 005750 202020204020 000  
 005751 202020202020 000  
 005752 202020202020 000  
 005753 202020204020 000  
 005754 202022252047 000  
 005755 513145634524 000  
 005756 202162204330 000  
 005757 252020204020 000  
 005760 202020202020 000  
 005761 202020204020 000  
 005762 202020204020 000  
 005763 202020202020 000  
 005764 202020202020 000  
 005765 202062642220 000  
 005766 302521247145 000

459 . 3CI 9, BE PRINTED AS THE

460 3CI 2, SUB HEADING

461 \*  
 462 \*  
 463 \* THIS WILL BE PUNCHED IN COLS, 73 - 80  
 464 \* OF EACH CARD WITH THE CALL PUNCH  
 465 \* AND EACH CARD WILL BE SEQUENCED  
 466 \* NUMBERED BEGINNING WITH 00100  
 467 \*  
 468 \*

005767 272151000001 000  
 005770 000020204020 000

469 PCHCD 3CI 2,GAR00100

470 \*  
 471 \*  
 472 \* THIS IS USED TO CHANGE THE REPORT  
 473 \* CODE WITH THE CALL PRINT  
 474 \* OPTAL 74 IS ASSUMED BY SYSOUT  
 475 \*  
 476 \*

005771 000000000024 000

477 RPCD DEC 20

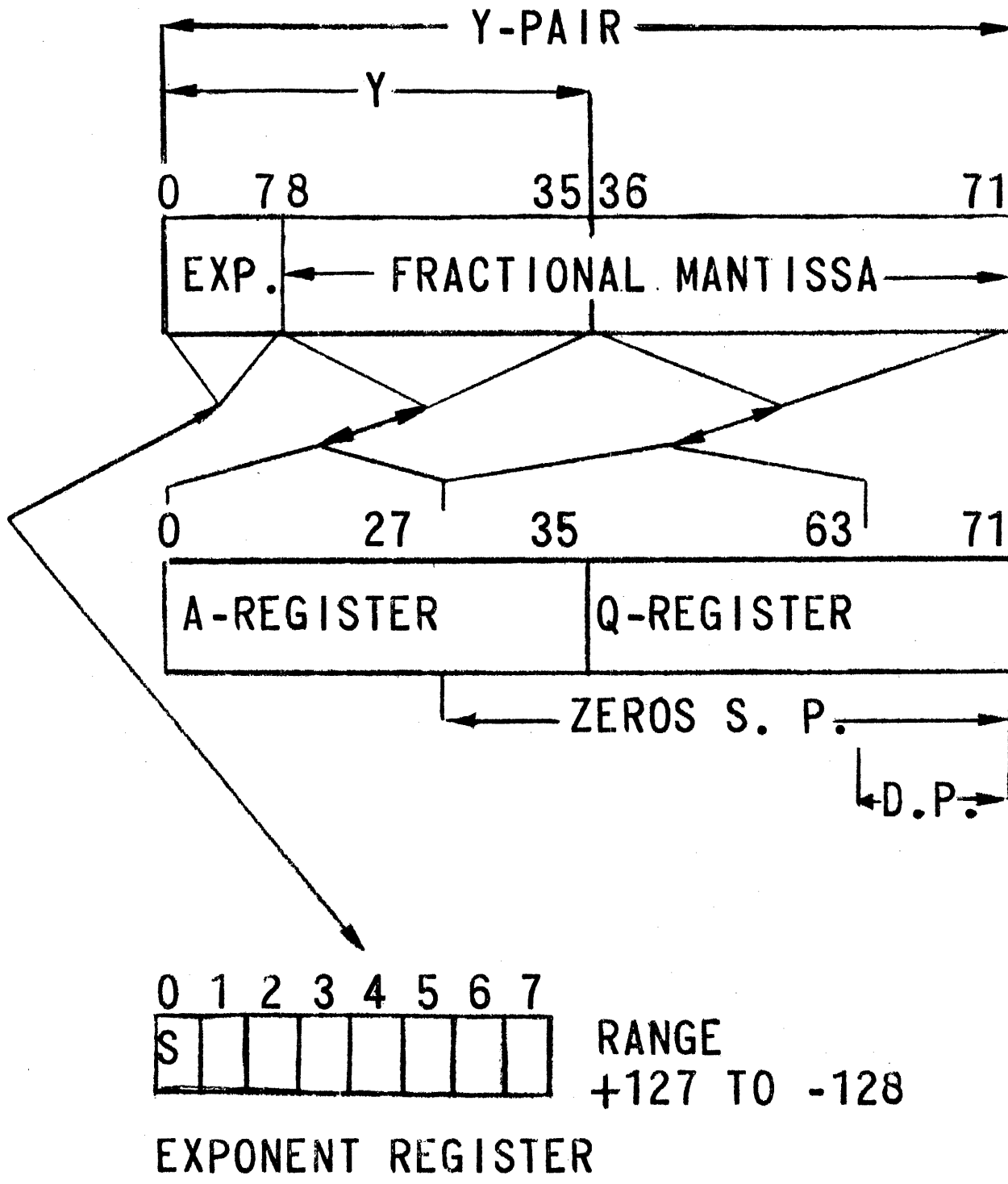
478 \*  
 479 \*  
 480 \* THE PAGES OF THE OUTPUT REPORT WILL  
 481 \* BE NUMBERED BEGINNING WITH THIS VALUE.  
 482 \*  
 483 \*

005772 000000010000 000

484 PGNO 3CI 1,000100

227

# FLOATING LOAD/STORE OPERATIONS





## FLOATING LOAD/STORE

FLD

FST

DFLD

DFST

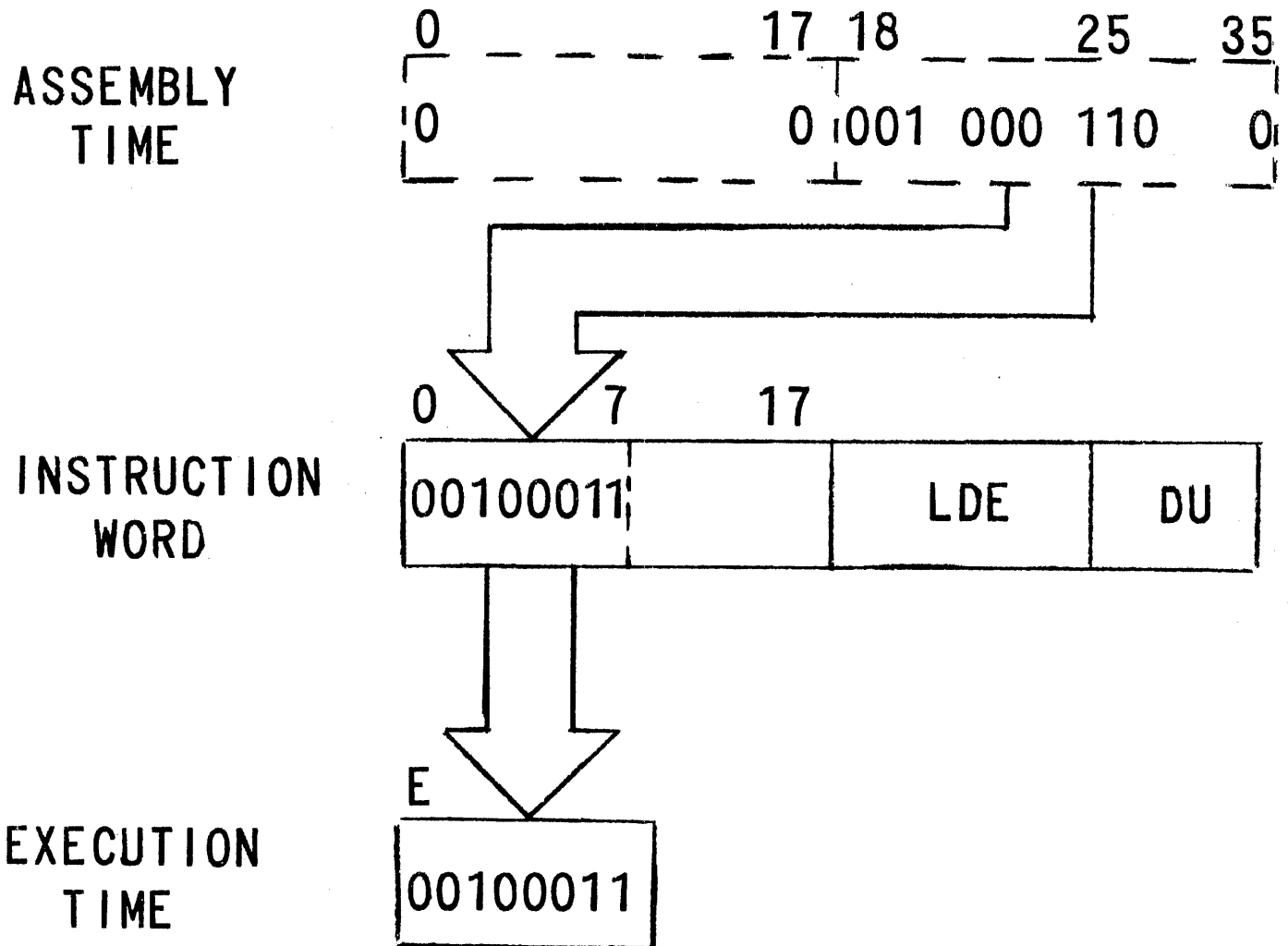
LDE

STE

FSTR

## LOADING EXPONENT REGISTER

EXAMPLE: LDE = 35B25, DU



## FLOATING POINT ARITHMETIC

FAD

FSB

DFAD

DFSB

ADE

UFA

UFS

DUFA

DUFS

FNO

FNEG

FLOATING POINT  
MULTIPLY AND DIVIDE

FMP

FDV

DFMP

DFDV

UFM

FDI

DUFM

DFDI

## FLOATING POINT COMPARE

FCMP	Y
DFCMP	Y-PAIR
FCMG	Y
DFCMG	Y-PAIR

# FLOATING POINT

ASSEMBLY NOTATION	FLOATING POINT MEMORY WORD											OCTAL	
	S	EXPONENT	S	MANTISSA									
= .1	1	1111101	0	110	011	001	100	110	011	001	100	110	772631463146
= .2	1	1111110	0	110	011	001	100	110	011	001	100	110	774631463146
= .3	1	1111111	0	100	110	011	001	100	110	011	001	101	776463146315
= .4	1	1111111	0	110	011	001	100	110	011	001	100	110	776631463146
= .5	0	0000000	0	100	000	000	000	000	000	000	000	000	000400000000
= .6	0	0000000	0	100	110	011	001	100	000	011	001	101	000463146315
= .7	0	0000000	0	101	100	110	011	001	100	110	011	010	000546314632
= .8	0	0000000	0	110	011	001	100	110	011	001	100	110	000631463146
= .9	0	0000000	0	111	001	100	110	011	001	100	110	011	000714631463
= .10	1	1111101	0	110	011	001	100	110	011	001	100	110	772631463146
= 0.	1	0000000	0	000	000	000	000	000	000	000	000	000	400000000000
= 1.	0	0000001	0	100	000	000	000	000	000	000	000	000	002400000000
= 2.	0	0000010	0	100	000	000	000	000	000	000	000	000	004400000000
= 3.	0	0000010	0	110	000	000	000	000	000	000	000	000	004600000000
= 4.	0	0000011	0	100	000	000	000	000	000	000	000	000	006400000000
= 5.	0	0000011	0	101	000	000	000	000	000	000	000	000	006500000000

# FLOATING POINT

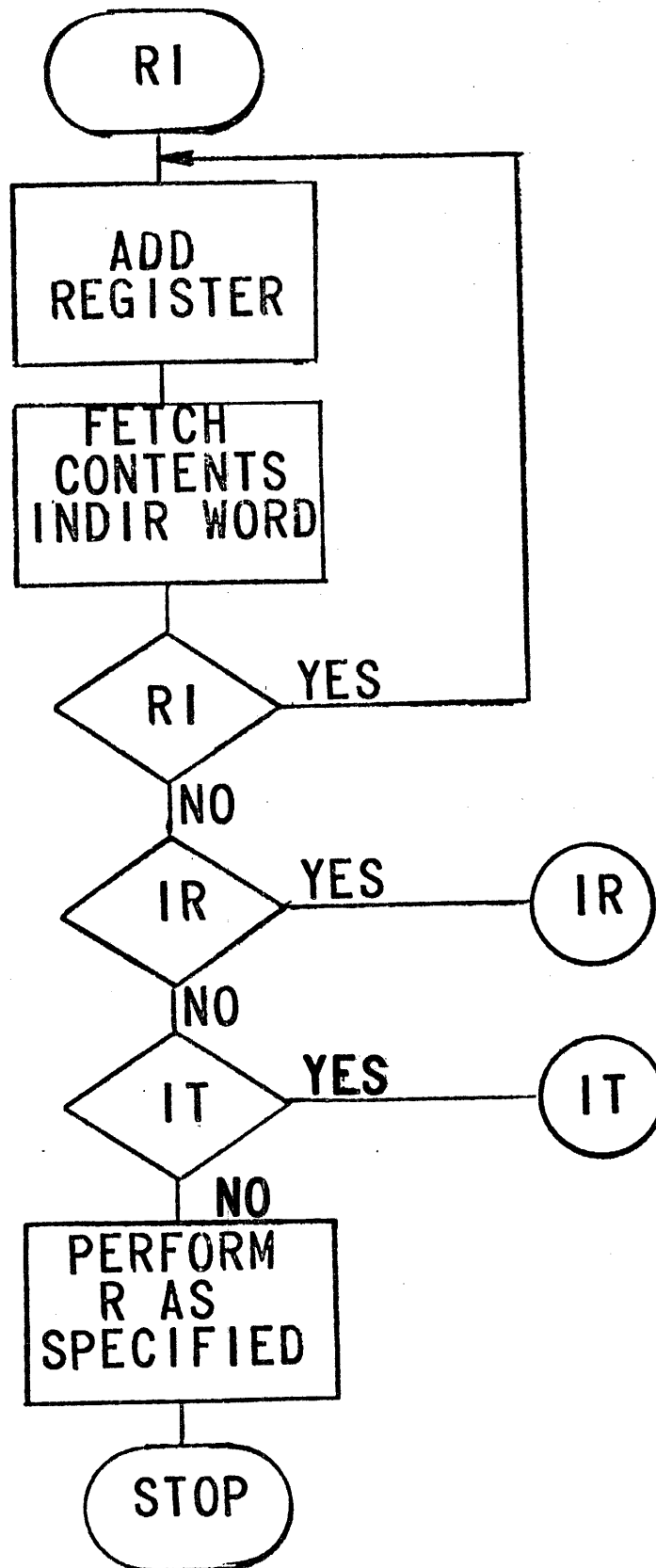
ASSEMBLY NOTATION	FLOATING POINT MEMORY WORD												OCTAL		
	S	EXPONENT	S	MANTISSA											
= -1.	0	0000000	1	000	000	000	000	000	000	000	000	000	000	000	001000000000
= -2.	0	0000001	1	000	000	000	000	000	000	000	000	000	000	000	003000000000
= -3.	0	0000010	1	010	000	000	000	000	000	000	000	000	000	000	005200000000
= -4.	0	0000010	1	000	000	000	000	000	000	000	000	000	000	000	005000000000
= -5.	0	0000011	1	011	000	000	000	000	000	000	000	000	000	000	007300000000
= -.1	1	1111101	1	001	100	110	011	001	100	110	011	010	010	010	773146314632
= -.2	1	1111110	1	001	100	110	011	001	100	110	011	010	010	010	775146314632
= -.3	1	1111111	1	011	001	100	110	011	001	100	110	011	011	011	777314631463
= -.4	1	1111111	1	001	100	110	011	001	100	110	011	010	010	010	777146314632
= -.5	1	1111111	1	000	000	000	000	000	000	000	000	000	000	000	777000000000

# FLOATING POINT

ASSEMBLY NOTATION	FLOATING POINT MEMORY WORD										OCTAL		
	EXONENT	MANTISSA/UNNORMALIZED											
= 1.55E1	0   0 0 0 0 1 1 0	0	0 0 1	1 1 1	1 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 1 4 1 7 4 0 0 0 0 0 0
		MANTISSA/NORMALIZED											
	0   0 0 0 0 1 0 0	0	1 1 1	1 1 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 1 0 7 6 0 0 0 0 0 0 0
= 1.55E-1	1   1 1 1 1 1 1 0	0	1 0 0	1 1 1	1 0 1	0 1 1	1 0 0	0 0 1	0 1 0	0 0 1	1 1 1	0 0 0	7 7 4 4 7 5 3 4 1 2 1 7
= 5.12D2	0   0 0 0 1 0 1 0	0	1 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 2 4 4 0 0 0 0 0 0 0 0
	0 0 0 0 0 0 0 0	0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0 0 0 0 0 0 0 0 0 0	
= 5.2D-1	0   0 0 0 0 0 0 0	0	1 0 0	0 0 0	1 1 0	0 0 1	0 0 1	0 0 1	1 0 1	1 1 0	1 0 0	0 0 0 4 0 6 1 1 1 5 6 4	
= 12345.12345	0   0 0 0 1 1 0 0	0	1 1 0	0 0 0	0 0 1	1 1 0	0 1 0	0 0 1	1 1 1	1 1 0	0 1 1	0 3 4 6 0 1 6 2 1 7 6 3	
= 320. DU	0   0 0 0 1 0 0 1	0	1 0 1	0 0 0	0 0 0	TRUNCATED @ BIT 17						0 0 2 5 0 0	



# REGISTER THEN INDIRECT



LDA 3000,3\* C(X3)=50

3000	LDA	3*
------	-----	----

TEMP. EFF. ADDR. =  $3000 + C(X3) = 3050$

(3050) XXX 4000

4000	XXX	
------	-----	--

RI MODIFICATION

LDQ

4000,0\* C(X0)=100

4000	LDQ	0*
------	-----	----

(4100) XXX 4200,1\* C(X1)=200

4200	XXX	1*
------	-----	----

(4400) XXX 4600,2\* C(X2)=300

4600	XXX	2*
------	-----	----

(4900) XXX 5000,1 C(X1)=(STILL)200

5000	XXX	1
------	-----	---

RI MODIFICATION

## REGISTER INDIRECT (RI)

PERFORM THE REGISTER MODIFICATION TO OBTAIN THE ADDRESS OF THE INDIRECT WORD.

IF THE INDIRECT WORD SPECIFIES FURTHER INDIRECTION, THEN FETCH THE NEXT INDIRECT WORD.

CEASE INDIRECTION WHEN THE CANDIDATE FOR THE ACTUAL EFFECTIVE ADDRESS IS EITHER:

1. NOT INDEXED AT ALL
2. INDEXED BY A REGISTER (R) TYPE
3. INDEXED BY AN INDIRECT THEN TALLY (IT) TYPE

THE VARIATIONS DU AND DL OF REGISTER (R) MODIFICATION CANNOT BE USED WITH (RI) MODIFICATION.

WHEN WORKING WITH (RI) WE USE THE:

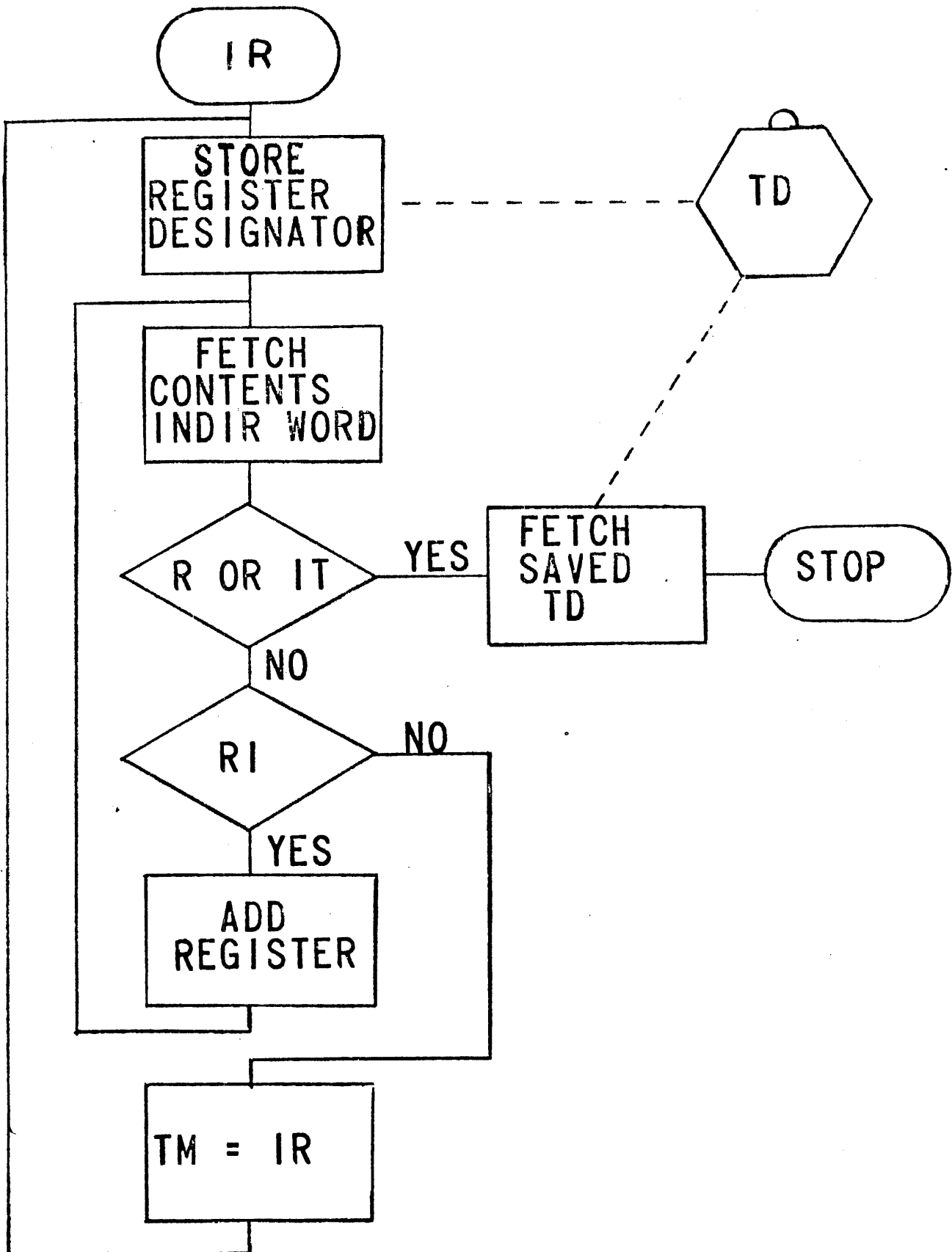
INDEX REGISTERS	$X_N$ ( $n = 0--7$ )
A-REGISTER	(UPPER AND LOWER PORTIONS)
Q-REGISTER	(UPPER AND LOWER PORTIONS)
INSTRUCTION COUNTER	IC

ALGORITHMS:

REGISTER AND INDIRECT (RI)

<u>PERMISSIBLE REGISTERS</u>		<u>EFFECTIVE ADDRESS</u>	<u>CODING FORMAT</u>
Xn	yi	$y + C(Xn)$ 0-17	Y, 0-7*
AU	yi	$y + C(A)$ 0-17	Y, AU*
AL	yi	$y + C(A)$ 18-35	Y, AL*
QU	yi	$y + C(Q)$ 0-17	Y, QU*
QL	yi	$y + C(Q)$ 18-35	Y, QL*
IC	yi	$y + C(IC)$ 0-17	Y, IC*
N	yi	y	Y, N* or Y, *

# INDIRECT THEN REGISTER



LDA 3000, \*5 X5 SAVED

3000	LDA	*5
------	-----	----

(3000) XXX 1000, \*7 X5 NOW REPLACED BY X7

1000	XXX	*7
------	-----	----

(1000) XXX 200 CEASE INDIRECTION AND MODIFY  
USING X7

200	XXX	
-----	-----	--

IR MODIFICATION

## I N D I R E C T   R E G I S T E R   ( I R )

REGISTER SPECIFIED IS PLACED IN A SAVE-STORE AREA.

ADDRESS IS USED TO LOCATE THE INDIRECT WORD.

PROCESS IS CONTINUED UNTIL THE CANDIDATE FOR THE ACTUAL EFFECTIVE ADDRESS IS EITHER;

1. NOT INDEXED AT ALL
2. INDEXED BY A REGISTER (R) TYPE
3. INDEXED BY AN INDIRECT THEN TALLY (IT) TYPE

THE VARIATIONS DU AND DL OF REGISTER (R) MODIFICATION WITH (IR) MODIFICATION.

WHEN THE SEARCH FOR THE ACTUAL EFFECTIVE ADDRESS IS TERMINATED, THE LAST MODIFICATION PERFORMED IS DEPENDENT UPON THE CONTENTS OF THE SAVE-STORE AREA.

WHEN WORKING WITH (IR) WE USE THE:

INDEX REGISTER	$X_N$ (N = 0 - 7)
A-REGISTER	(UPPER AND LOWER PORTIONS)
Q-REGISTER	(UPPER AND LOWER PORTIONS)
INSTRUCTION COUNTER	IC



ALGORITHMS:

INDIRECT THEN REGISTER (IR)

<u>PERMISSIBLE REGISTERS</u>	<u>EFFECTIVE ADDRESS</u>	<u>CODING FORMAT</u>
Xn	$y_i = y$ (SAVE C(Xn)0-17)	Y,*0-7
AU	$y_i = y$ (SAVE C(AU)0-17)	Y,*AU
AL	$y_i = y$ (SAVE C(AL)18-35)	Y,*AL
QU	$y_i = y$ (SAVE C(QU)0-17)	Y,*QU
QL	$y_i = y$ (SAVE C(QL)18-35)	Y,*QL
IC	$y_i = y$ (SAVE C(IC)0-17)	Y,*IC
N	$y_i = y$ (SAVE NO REGISTER)	Y,*N

LDA

500,\*IC      IC SAVED

	500	LDA	*IC
--	-----	-----	-----

(500) XXX 300,\*2      IC IS REPLACED BY      X2

	300	XXX	*2
--	-----	-----	----

(300) XXX 2000,3\* C(X3)=1000. USE X3; X2  
 STILL SAVED.

	2000	XXX	3*
--	------	-----	----

(3000) XXX 200,4      USE X2; X4 IGNORED.

	200	XXX	4
--	-----	-----	---

### IR AND RI MODIFICATION

EXAMPLE 1

FIRST  
SECOND+C(A)0-17

LDA  
STA

SECOND, AU\*  
THIRD, i

THIRD+C(XI)0-17

EXAMPLE 2

NONE  
ONCE

MPY  
ARG

ONCE, \*  
TWICE, QU

TWICE+C(QU)

EXAMPLE 3

A

LDA  
ARG

A, \*QL  
B

B+C(Q)18-35

EXAMPLE 4

B	LDA	B, *3
C	ARG	C, *5
D	ARG	D, *QU
	ARG	E, 7

$E + C(Q)_{0-17}$

EXAMPLE 5

A	LDA	A, *3
B+C(X5)	ARG	B, 5*
	ARG	C, 1C

$C + C(X3)$

EXAMPLE 6

Z	LDA	Z, *3
	STA	B, DU*

NOT VALID

EXAMPLE 7

A+C(X3)  
 B  
 C  
 D+C(X1)  
 E

LDA  
 STA  
 LDQ  
 STQ  
 ARG  
 ARG

A, 3\*  
 B, \*3  
 C, \*N  
 D, 1\*  
 E, \*  
 F, 3

F

EXAMPLE 8

START  
 A+C(X7)  
 B+C(X4)  
 C+C(X5)

LDA  
 ARG  
 ARG  
 ARG

A, 7\*  
 B, 4\*  
 C, 5\*  
 D, \*

1 D  
 2 D  
 3 D  
 E

ARG  
 ARG  
 ARG

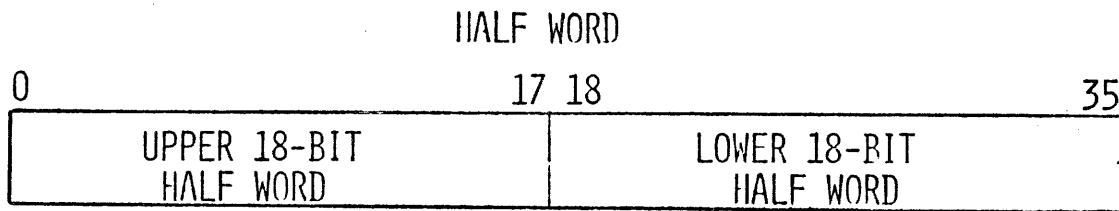
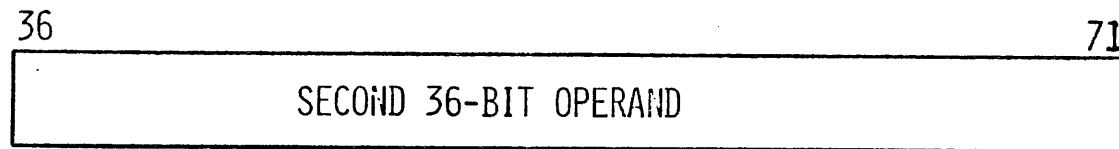
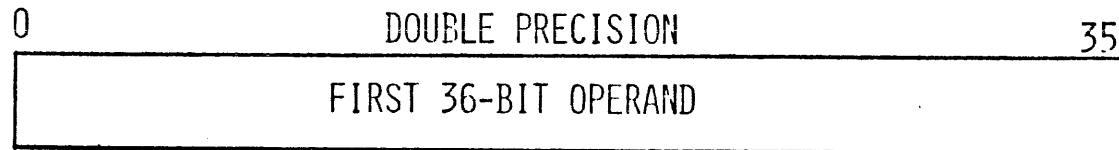
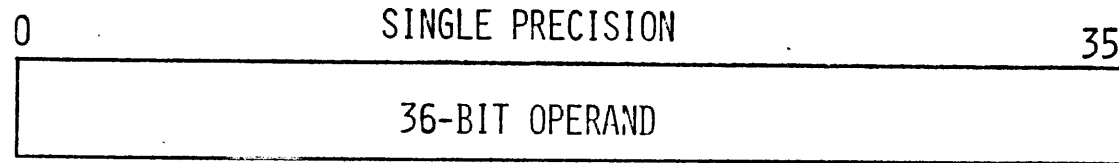
E  
 E, 3  
 E, ID

TALLY

F, 100

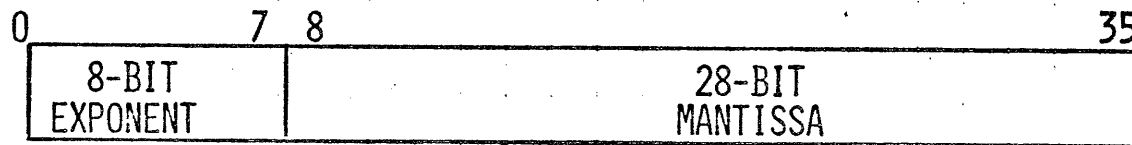
1 E  
 2 E + C(X3)  
 3 F

## FIXED-POINT DATA FORMATS

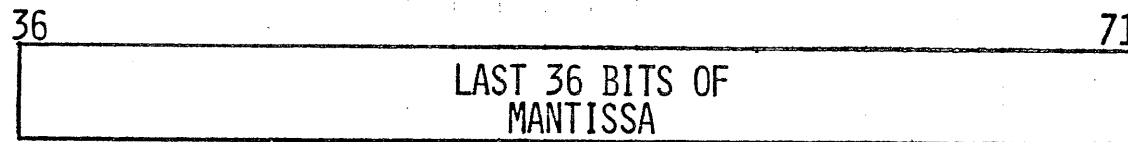
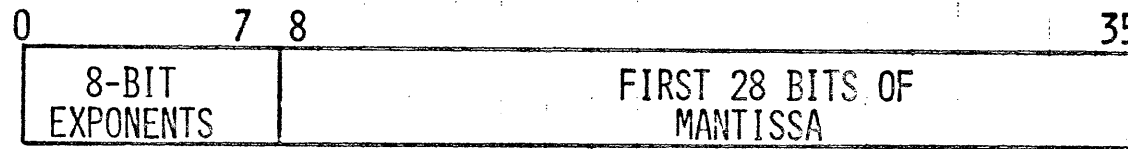


# FLOATING-POINT DATA FORMATS

## SINGLE PRECISION



## DOUBLE PRECISION



# ALPHANUMERIC DATA FORMATS

## 6-BIT FORMAT

0	5 6	11 12	17 18	23 24	29 30	35
CHARACTER	CHARACTER	CHARACTER	CHARACTER	CHARACTER	CHARACTER	
0	1	2	3	4	5	

## 9-BIT FORMAT

0	8 9	17 18	26 27	35
CHARACTER	CHARACTER	CHARACTER	CHARACTER	
0	1	2	3	



## DECIMAL FORMATS FOR EIS

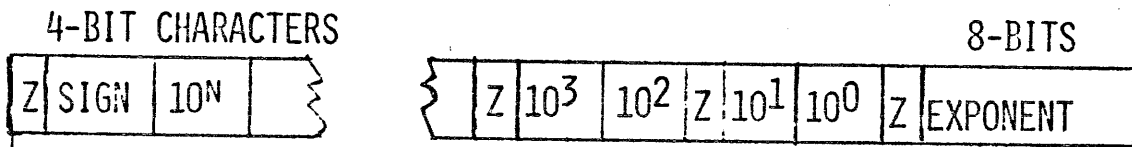
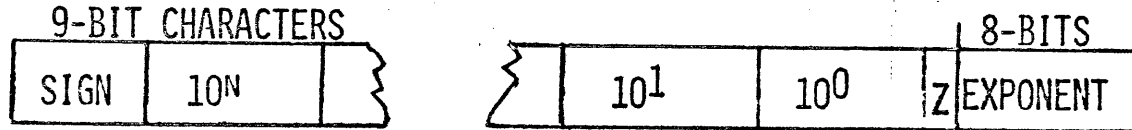
0	1	4	5	8	9	10	13	14	17	18	19	22	23	26	27	28	31	32	35
Z	0	1	Z	2	3	Z	4	5	Z	6	7								

PACKED DECIMAL (4-BIT)

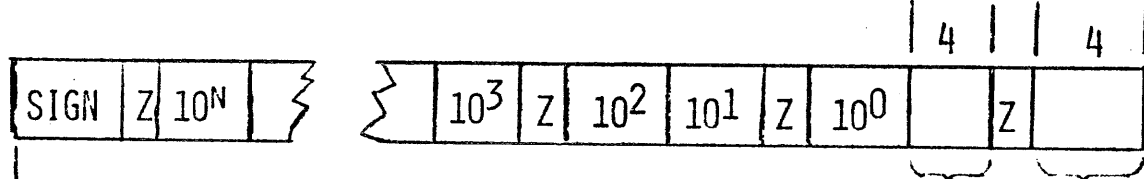
0	1	8	9	10	17	18	19	26	27	28	35
Z	0	Z	1	Z	2	Z	3				

ASCII (9-BIT)

# FLOATING-POINT DECIMAL FORMATS



→ EVEN (ODD) CHARACTER BOUNDARY, N ODD (EVEN)



→ ODD (EVEN) CHARACTER BOUNDARY, N ODD (EVEN)

EXONENT

# DECIMAL SIGN POSITION FORMATS

	PLUS	MINUS
4-BIT	$13_8$	$15_8$
9-BIT	$53_8$	$55_8$

253.1-253.1

	000(1) <sup>1</sup>	040(1)	100(1)	140(1)	200(1)	240(1)	300(1)	340(1)
000 001 002 003 004 005 006 007			MLR MRL		AD2D SB2D		MVN BTD CMPN DTB	
010 011 012 013 014 015 015 017			CMPC		MP2D DV2D			
020 021 022 023 024 025 026 027	MVE  MVNE	CSL CSR  SZTL SZTR CMPB	SCD SCDR  SCM SCMR	MVT  TCT TCTR	AD3D SB3D  MP3D DV3D			
030 031 032 033 034 035 036 037								
<sup>1</sup> Octal representation of the operation code consists of three octal digits corresponding to instruction bit positions 18-26 and a zero (0) or one (1) for instruction bit position 27 (the EIS bit).								

Figure 1-2. Operation Code Map for the Extended Instructions

	400(1) <sup>1</sup>	440(1)	500(1)	540(1)	600(1)	640(1)	700(1)	740(1)
000			A9BD	ARA0	TRTN	ARN0		SAR0
001			A6BD	ARA1	TRTF	ARN1		SAR1
002		SAREG	A4BD	ARA2		ARN2		SAR2
003			ABD	ARA3		ARN3		SAR3
004				ARA4	TMOZ	ARN4		SAR4
005				ARA5	TPNZ	ARN5		SAR5
006		SPL		ARA6	TTN	ARN6		SAR6
007			AWD	ARA7		ARN7		SAR7
010								
011								
012								
013								
014								
015								
016								
017								
020			S9BD	AAR0		NAR0		LAR0
021			S6BD	AAR1		NAR1		LAR1
022		LAREG	S4BD	AAR2		NAR2		LAR2
023			SBD	AAR3		NAR3		LAR3
024				AAR4		NAR4		LAR4
025				AAR5		NAR5		LAR5
026		LPL		AAR6		NAR6		LAR6
027			SWD	AAR7		NAR7		LAR7
030								
031								
032								
034								
035								
036								
037								

<sup>1</sup> Octal representation of the operation code consists of three octal digits corresponding to instruction bit positions 18-26 and a zero(0) or one (1) for instruction bit position 27 (the EIS bit).

Figure 1-2 (cont). Operation Code Map for the Extended Instructions

EIS MULTI-WORD INSTRUCTIONS

VARIABLE FIELD	OP CODE		MF 1
1ST OPERAND DESCRIPTOR			
2ND OPERAND DESCRIPTOR			
3RD OPERAND DESCRIPTOR			

253.4

# SINGLE WORD INSTRUCTIONS

Special Format

0 2 3

17 18

27 29

35

--	--	--	--	--

Normal Format

0

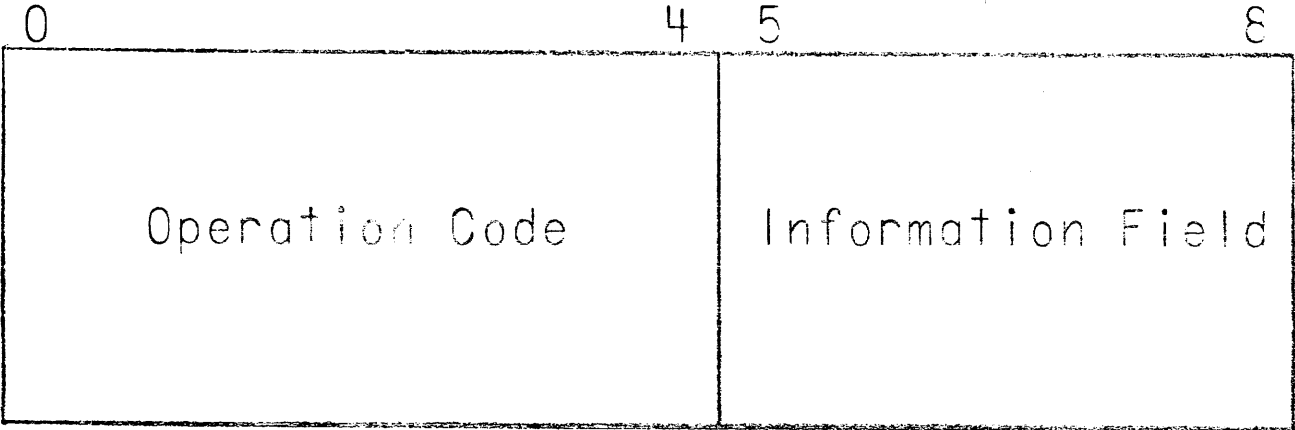
17 18

27 29

35

--	--	--	--

MICRO OPERATIONS

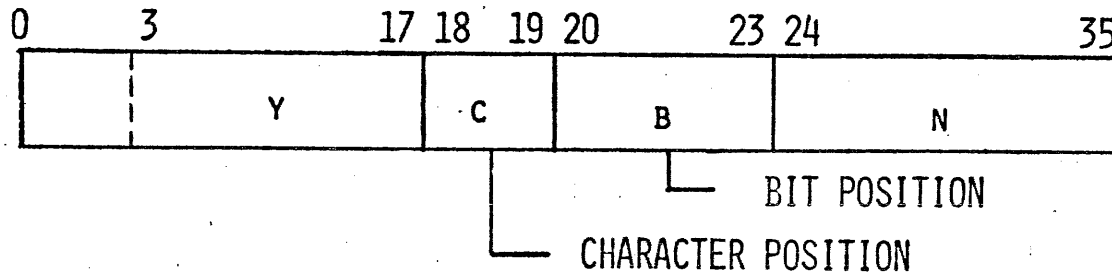


253.6

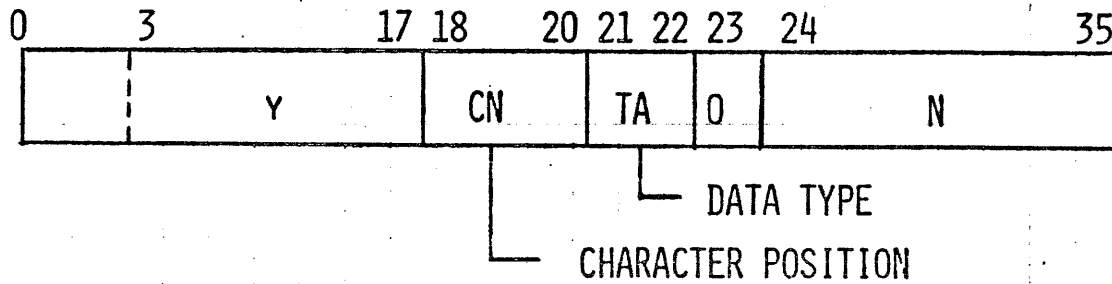


# OPERAND DESCRIPTORS

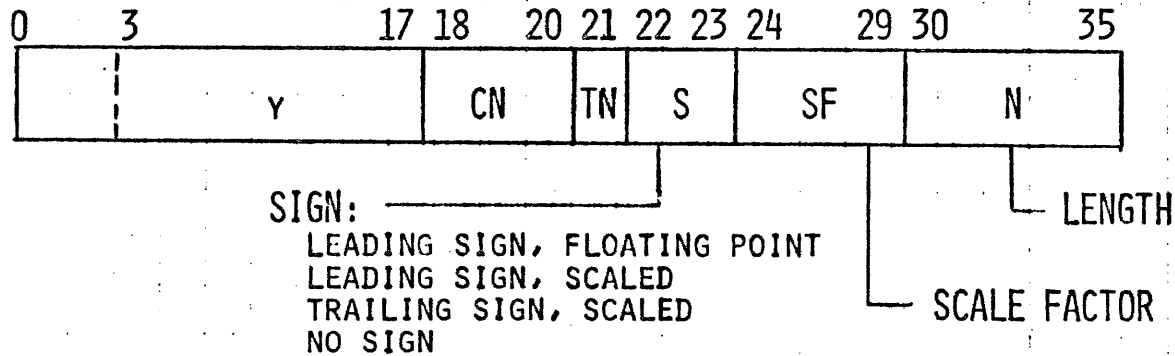
## BIT STRING



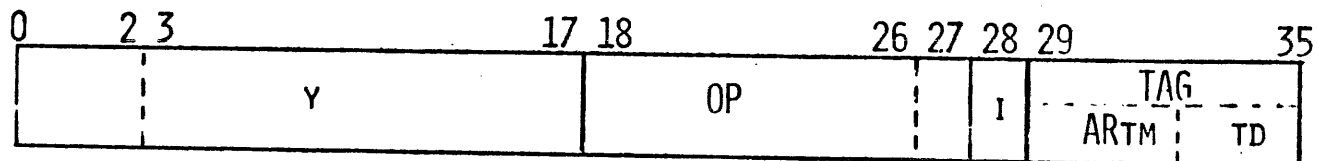
## ALPHANUMERIC



## NUMERIC



## EIS ADDRESS MODIFICATION

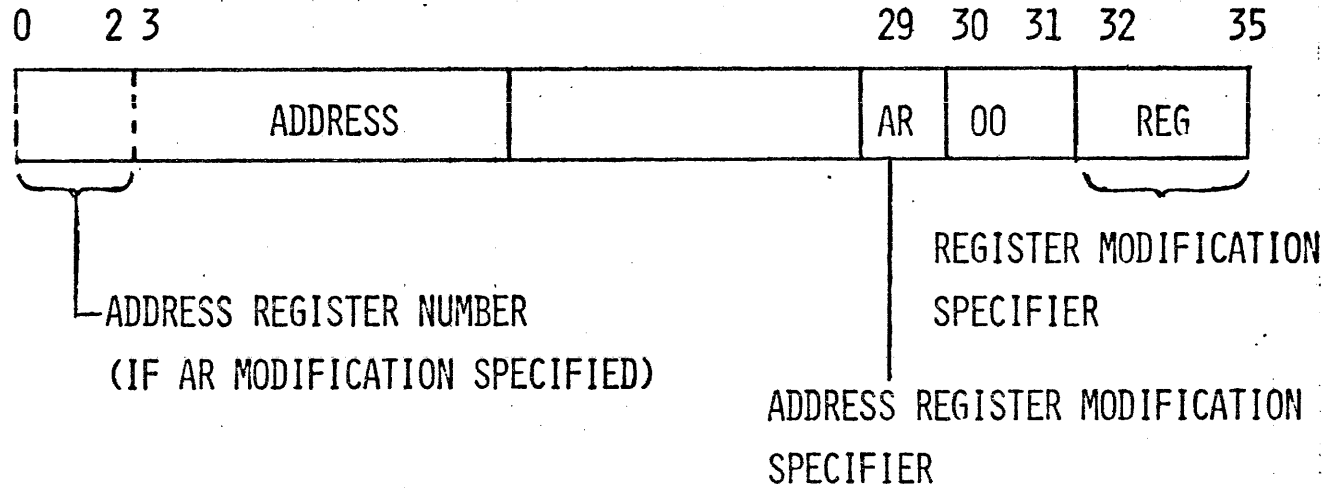


1	1	1	4
AR	RL	ID	REG

NUMBER OF BITS  
SYMBOL

- AR      ADDRESS REGISTER SPECIFIER
- RL      REGISTER OR LENGTH
- ID      INDIRECT OPERAND DESCRIPTOR
- REG     ADDRESS MODIFICATION REGISTER

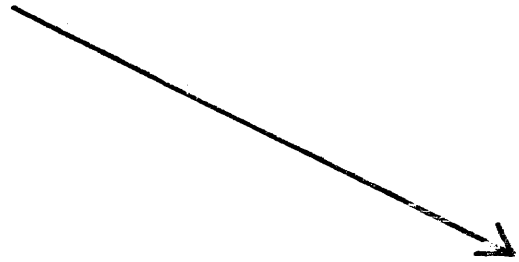
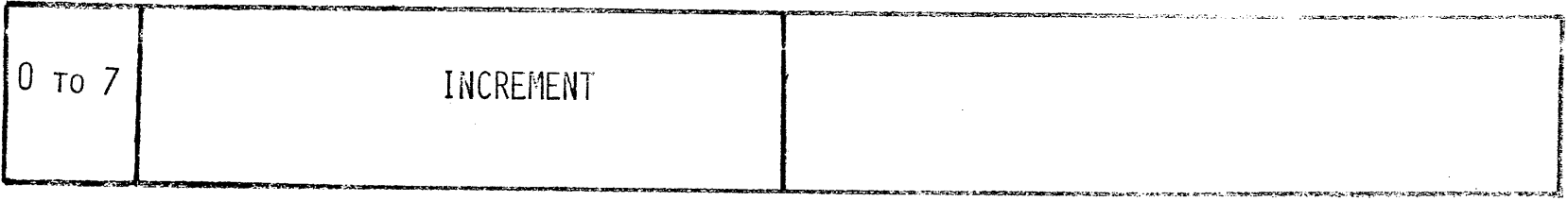
INDIRECT WORD



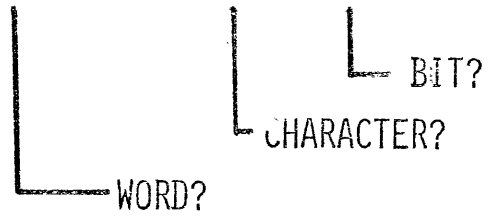
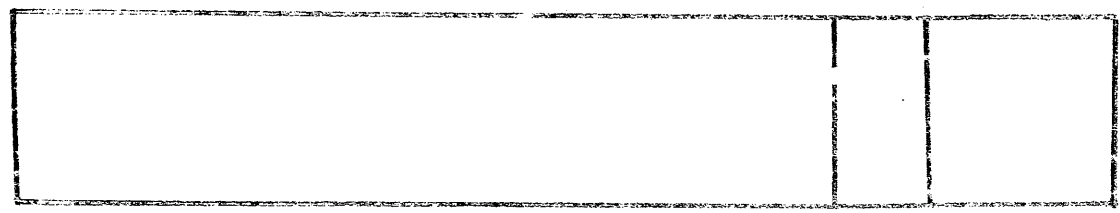
AR AND REG FIELDS IDENTICAL IN FUNCTION WITH CORRESPONDING MODIFICATION FIELDS IN INSTRUCTION.

# ADDRESS REGISTERS

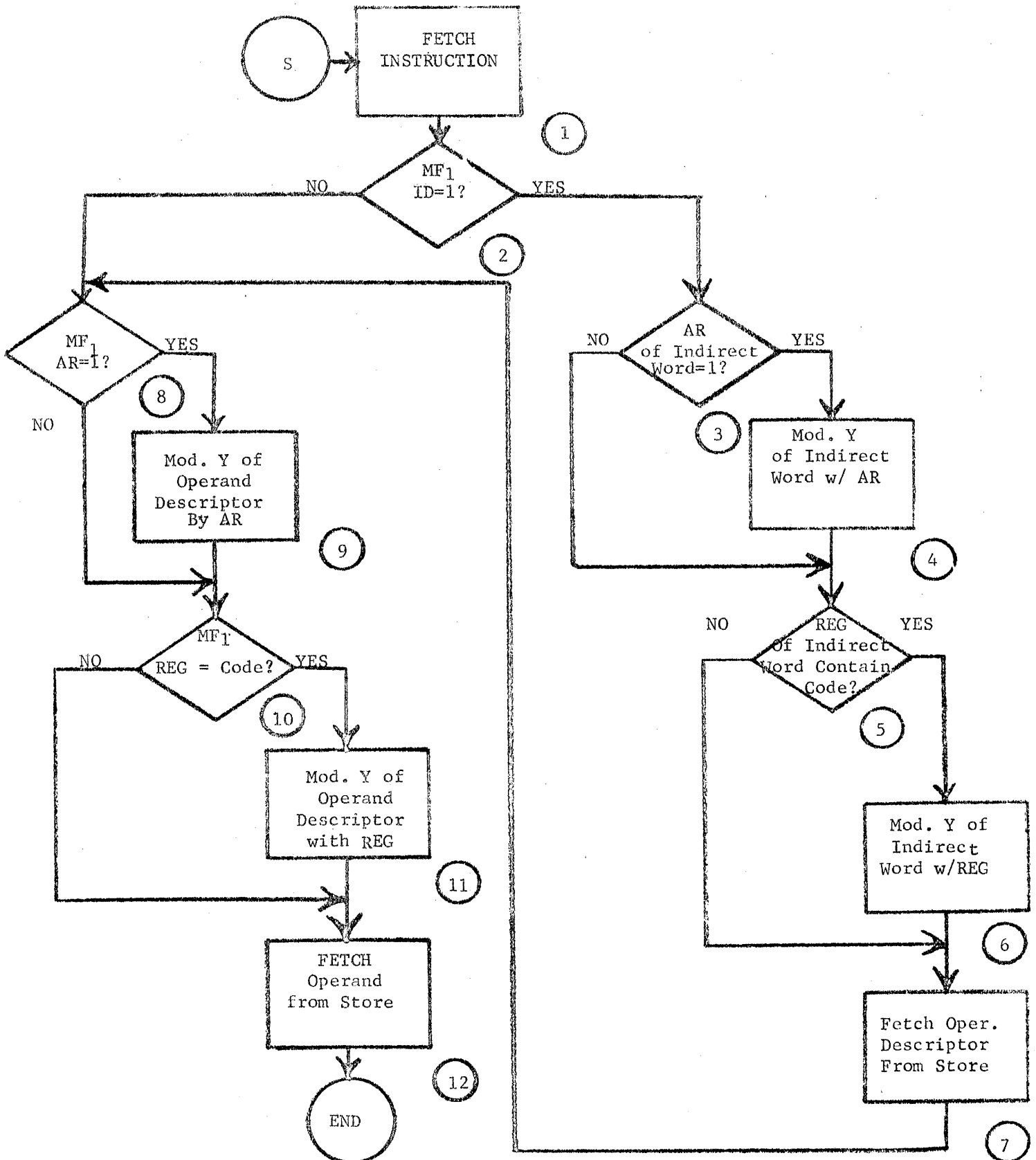
ADDRESS FIELD



ADDRESS REGISTER (24 BITS) 0 TO 7



253.91



SS 46264 ENTERED FIN635 AT 12,971 FROM CD RDR 0-06-00

```

0001 $ SNLMB 46264
0002 $ IDENT 4817,-TRNG-RGK,J999,C959100,8177
0003 $ GMAP NDECK
0004 $ LPEATE GINA1
0006 $ GMAP NDECK
0007 $ LPEATE GINA2
0020 $ GMAP NDECK
0021 $ LPEATE GINA3
0026 $ EXECUTE DUMP
0027 $ LIMITS ,5K,,1000
0028 $ SYSOUT A1
0029 $ SYSOUT A2
0030 $ DATA A3
0031 $ ENDJOB
  
```

TOTAL CARD COUNT THIS JOB = 000115

```

* BEGIN ACTIVITY -01- GMAP 10/12/71 SW=211400000000
* NORMAL TERMINATION AT 002420 INDICATORS 5020
* NORMAL TERMINATION AT 002420 INDICATORS 5020
* NORMAL TERMINATION AT 002420 INDICATORS 4020
  
```

254

START	STCP	LINES	PROC	I/O	IU	MEMORY	
12.974	12.982	755	0,0011	0,003	5	24K	
		LIMIT 12446	LIMIT 0,0992	LIMIT	CU 5	M=7 807	
LAPSE	0,009	FC D TYPE	BUSY	IP/AT	FP/RT	IS/RC MS/BE	ADDRESS L#/T#
		G* R D167 *	248	U	3	1 6	0-00=08 255
		A* R D167 *	117	U	1	1 4	0-00=08 255
		P* SYOUT					
		K* SYOUT					
		C* SYOUT					
		*1 R D167	1967	U	0	4 4	0-00=02 633
		B* S D167	207	U	4	2 2	0-00=03 123

LIST 755 LINES

```

* BEGIN ACTIVITY -02- GELOAD 10/12/71 SW=400000000000
* USERS P=ME GEBART AT 011717 INDICATORS 3020
  
```

START	STCP	LINES	PROC	I/O	IU	MEMORY	
12.983	12.990	550	0,0003	0,002	5	5K	
		LIMIT 1000	LIMIT 0,0500	LIMIT	CU 5	M=7 276	
LAPSE	0,007	FC D TYPE	BUSY	IP/AT	FP/RT	IS/RC MS/BE	ADDRESS L#/T#
		B* R D167	177	4	4	2 2	0-00=03 123
		A3 R D167 *	50	U	1	1 1	0-00=08 256
		R* R D167 *	56	U	0	1 1	0-00=08 256
		A1 SYOUT					
		A2 SYOUT					
		P* SYOUT					
		L* R D167	2963	U	0	28 28R	0-00=01 385

LIST 540 LINES  
 CK 10 CARDS

PREFACE

PROGRAM BREAK	36
COMMON LENGTH	J
V OCLAT BITS	5

PRIMARY SYMDEF ENTRY

START 0

SECONDARY SYMDEF ENTRY

BLOCK	LENGTH
-------	--------

SYMDEF

- 1 CALC
- 2 EDIT
- 3 INI
- 4 FLCAT
- 5 INPLT
- 6 TORCE
- 7 TORIN
- 10 GUTFLT
- 11 WRAPLP

			1	TTL	SUBPROGRAM NO. 1 - DRIVER
			2	SYNDEF	START
000000	030000701000	030	3	START CALL	INIT
000001	000003710000	010			
000002	000034000003	010			
000003	050000701000	030	4	READ CALL	INPLT
000004	000006710000	010			
000005	000034000004	010			
000006	070000701000	030	5	SKIP CALL	TCBIN
000007	000011710000	010			
000010	000034000005	010			
000011	040000701000	030	6	CALL	FLOAT
000012	000014710000	010			
000013	000034000006	010			
000014	010000701000	030	7	CALL	CALC
000015	000017710000	010			
000016	000034000007	010			
000017	000000701000	030	8	CALL	TCBCD
000020	000022710000	010			
000021	000034000010	010			
000022	020000701000	030	9	CALL	EDIT
000023	000025710000	010			
000024	000034000011	010			
000025	100000701000	030	10	CALL	CLPUT
000026	000030710000	010			
000027	000034000012	010			
000030	000033 7100 00	010	11	TRA	READ
000031	110000701000	030	12	CALL	WRAPUP
000032	000034710000	010			
000033	000034000014	010			

ERRCR LINKAGE

000034	000000000000	000
000035	626321516320	000

13 ENL

36 IS THE NEXT AVAILABLE LOCATION, GMAP VERSION J\*PA/030271 J\*PE/030271 J\*PC/030271  
THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY

256



OCTAL	SYMBOL	REFERENCES BY ALTER NO.,										
1	CALC		7									
2	EDIT		9									
4	FLOAT		6									
3	INIT		3									
5	INPUT		4									
34	.E.L.		3	4	5	6	7	8	9	10	12	
10	CLTPLT		10									
3	READ	4	4	11								
0	START	3	2	3								
6	TCBCC		8									
7	TCBIN		5									
11	WRAPLP		12									

\*\* 16189 WORDS OF MEMORY WERE USED BY GMAP FOR THIS ASSEMBLY,

46264 01 10-12-71 12.977

SUBPROGRAM NO. 2 - INPUT/OUTPUT SERVICES

3: 1

INITIALIZATION SUBROUTINE

PREFACE

PROGRAM BREAK 339  
COMMON LENGTH 1703  
V COUNT BITS 5

PRIMARY SYMDEF ENTRY

INIT 0  
INFLY 166  
OUTFLT 201  
AAFLP 300

SECONDARY SYMDEF ENTRY

BLOCK LENGTH

1 IO,WRK 64

SYNDEF

2 GET  
3 OPEN  
4 CLOSE  
5 PUNCH

258

INITIALIZATION SUBROUTINE

```

1      TTL      SUBPROGRAM NO. 2 - INPUT/OUTPUT SUBROUTINES
2      TTLS     INITIALIZATION SUBROUTINE
4 INIT  SAVE

000000
000000 000002710000 010
000001 0000315630000 010
000002 0000315754000 010
000003 0000315741000 010
000004 000000701000 030 5      CALL      CFEN(FUNOT,1)
000005 000011710000 010
000006 0000315000005 010
000007 0000143000000 010
000010 0000110000000 000
000011 000000701000 030 6      CALL      CFEN(FRNOT,1)
000012 000016710000 010
000013 0000315000006 010
000014 0000164000000 010
000015 0000010000000 000
000016 000000701000 030 7      CALL      CFEN(READIN,1)
000017 000023710000 010
000020 0000315000007 010
000021 0000165000000 010
000022 0000010000000 000
000023 000016 2260 03 000 8      LDX6      14,CU
000024 0000157 7460 00 010 9      STX6      CRDFIL+1
000025 0000125 7460 00 010 10     STX6      FLNFIL+1
000026 000026 2260 03 000 11     LDX6      22,CU
000027 0000142 7460 00 010 12     STX6      PRVFIL+1
000030 000021 0010 00 000 13     MME      GETIME
000031 0000262 7550 00 010 14     STA      TIME1
000032 000006 7320 00 000 15     GRS      6
000033 0000320 5060 00 010 16     DIV      =60000      CONVERT TO MINUTES
000034 0000321 5060 00 010 17     DIV      =60      CONVERT TO HOURS
000035 0000217 7550 00 010 18     STA      MINS
000036 000014 1160 07 000 19     CMPD     =12,DL
000037 000042 6000 00 010 20     TZE     P7
000040 000045 6040 00 010 21     TMI     A7
000041 000014 1760 07 000 22     SBC     =12,DL
000042 0000322 2350 00 010 23 P7   LDA     =P,P,
000043 0000275 7550 00 010 24     STA     TIME2+1
000044 000047 7100 00 010 25     TRA     **3
000045 0000323 2350 00 010 26 A7   LDA     =P,A,P,
000046 0000275 7550 00 010 27     STA     TIME2+1
000047 0000220 7560 00 010 28     STC     PCURS
000050 000000 6360 00 000 29     EAC     0
000051 000000 2260 03 000 30     LDX6     ,CU
000052 0000270 2350 00 010 31     LDA     PCURS
000053 014200 5202 01 000 32     RPT     6,1
000054 0000222 5050 16 010 33     BCC     TAB,6
000055 000011 1160 07 000 34     CMPD     =3H009,DL
000056 000060 6030 00 010 35     TRC     **2

```

259

INITIALIZATION SUBROUTINE

000057	000324	2760	00	010	36	ORC	=C202C00
000060	000325	2760	00	010	37	ORC	=C200C00
000061	000221	7560	00	010	38	STL	HRSACE
000062	000000	2260	03	000	39	LDB6	,DJ
000063	000217	2350	00	010	40	LDA	MINS
000064	014200	5202	01	000	41	RPI	6.1
000065	000222	5050	16	010	42	BCL	TAB,6
000066	000215	7560	00	010	43	STC	CKMINS
000067	000326	2760	00	010	44	ORC	=C330C00
000070	000322	7360	00	000	45	QLS	1E
000071	000221	2350	00	010	46	LDA	HRSBCE
000072	000022	7330	00	000	47	LRS	1E
000073	000274	7560	00	010	48	STC	TIME2
000074	000262	2350	00	010	49	LDA	TIME1
000075	000216	7550	00	010	50	STA	CKTIME
000076	000014	7330	00	000	51	SHIFTQ LRS	12
000077	000006	7320	00	000	52	QRS	6
000100	000327	2760	00	010	53	ORC	=C610C00000000
000101	000014	7330	00	000	54	LRS	12
000102	000006	7320	00	000	55	QRS	6
000103	000327	2760	00	010	56	ORC	=C610C00000000
000104	000330	1150	00	010	57	CMFA	=C100
000105	000111	6040	00	010	58	THI	ALESS
000106	000331	2750	00	010	59	GRA	=C202C20200000
000107	000262	7570	00	010	60	STAQ	TIME1
000110	000113	7100	00	010	61	TRA	*+3
000111	000332	2750	00	010	62	ALESS GRA	=C202C20202000
000112	000107	7100	00	010	63	RA	*-3
000113	000001710000			010	64	RETURN	INIT
	000114				65	RELOCB	PRNFIL,A1,PRNEUF
000114	000000000000			000		VFL	R18/,8/0
000115	000000000000			000		DEC	0
000116	000000000000			000		VFL	1E/0,1/0,3/,2/0,1/,11/0
000117	000000000400			000		VFL	1E/,5/0,1/,1/,1/,2/1,8/0
000120	000000002101			000		VFL	24/0,12/A1
000121	000000000000			000		DEC	0,0,0
000122	000000000000			000			
000123	000000000000			000			
000124	000000000000			000		PUNFIL VFL	1E/0,2/,2/,1/,13/0
000125	000000 000000			000		ZERO	,0
000126	000000 000000			022		ZERO	PLNBUF,PUNBUF
000127	000000 000000			000		ZERO	0
000130	000500 000000			000		ZERO	320,0
	000131				56	FILCB	PRNFIL,A2,PRNEUF
000131	000000000000			000		VFL	R18/,18/0
000132	000000000000			000		DEC	0
000133	000000000000			000		VFL	1E/0,1/0,3/,2/0,1/,11/0
000134	000000000400			000		VFL	1E/,5/0,1/,1/,1/,2/1,8/0
000135	000000002102			000		VFL	24/0,12/A2
000136	000000000000			000		DEC	0,0,0

260

INITIALIZATION SUBROUTINE

000137	000000000000	000			
000140	000000000000	000			
000141	000000000000	000	PRNFIL	VFL	18/0,2/,2/,1/,13/0
000142	000000 000000	000		ZERO	,C
000143	000501 000501	022		ZERO	PRNBUF,PRNBUF
000144	000000 000000	000		ZERO	0
000145	000500 000000	000		ZERO	320,0
	000146		67	FILCB	CRDFIL,A3,RDBUF1
000146	000000000000	000		VFL	R18/,18/P
000147	000000000000	000		DEC	0
000150	000000000000	000		VFL	18/0,1/0,3/,2/0,1/,11/0
000151	000000000400	000		VFL	18/,5/0,1/,1/,1/,2/1,8/0
000152	000000002103	000		VFL	24/0,1,12/A3
000153	000000000000	000		DEC	0,0,0
000154	000000000000	000			
000155	000000000000	000			
000156	000000000000	000	CRDFIL	VFL	18/0,2/,2/,1/,13/0
000157	000000 000000	000		ZERO	,C
000160	001202 001202	022		ZERO	RDBUF1,RDBUF1
000161	000000 000000	000		ZERO	0
000162	000500 000000	000		ZERO	320,0
000163	000124400000	010	68	PUNOT	VFL 18/PUNFIL,1/1,17/0
000164	000141400000	010	69	PRNOT	VFL 18/PRNFIL,1/1,17/0
000165	000156200000	010	70	READIN	VFL 18/CRDFIL,2/1
	000000		71	FLCCK	
	000000		72	PUNBUF	BSS 321
	000501		73	PRNBUF	BSS 321
	001202		74	RDBUF1	BSS 321
	000166		75	USE	



BETTER TRY FOR INPUT

000246	272025246423	000			
000247	216331464520	000			
000250	274421472023	000			
000251	466451622520	000			
000252	770200000000	000	94	OCT	770200000000
000253	000000011007	000			
000254	202020202020	000	95	WR2 EBCI	6,
000255	202020202020	000			
000256	202020202020	000			
000257	202020202020	000			
000260	202020202020	000			
000261	202020202020	000			
000262	202020202020	000	96	TIME1 BCI	1,
000263	202020202020	000	97	BCI	9,
000264	202020202020	000			
000265	202020202020	000			
000266	202020202020	000			
000267	202020202020	000			
000270	202020202020	000			
000271	202020202020	000			
000272	202020202020	000			
000273	202020202020	000			
000274	202020202020	000	98	TIME2 BCI	1,
000275	204673234346	000	99	BCI	2, C:CLCK
000276	234220202020	000			
000277	770400000000	000	100	OCT	770400000000
	000300		101	WRAPUP SAVE	
000300	000302710000	010			
000301	000315630000	010			
000302	000315754000	010			
000303	000315741000	010			
000304	000305 7100 00	010	102	ENDCRD TRA	**1
000305	040000701000	030	103	CALL	GLCSE(PUNQT,3)
000306	000312710000	010			
000307	000315000147	010			
000310	000163000000	010			
000311	000033000000	000			
000312	000334 2360 00	010	104	LDC	=3HRGK,DL
000313	000010 0010 00	000	105	MME	GEBCRT
000314	000301710000	010	106	RETURN	WRAPUP
	000000		107	BLCK	IC,WRK
000000	202020202020	000	108	RDWRK EBCI	5, 8 2 2 1
000001	201020202002	000			
000002	202020202002	000			
000003	202020202020	000			
000004	202020202001	000			
	000005		109	BSS	9
000016	202020202020	000	110	PRTOUT CCT	202020202020
000017	202020000000	000	111	PRSEQ OCT	202020000000
000020	202020202020	000	112	OCT	202020202020

263

BETTER TRY FOR INPUT

000021	202020202020	000	113 PRF	BCI	3,	
000022	202020202020	000				
000023	202020202020	000				
000024	202020202020	000	114 PRF	BCI	3,	
000025	202020202020	000				
000026	202020202020	000				
000027	202020202020	000	115 PRVAR	BCI	6,	
000030	202020202020	000				
000031	202020202020	000				
000032	202020202020	000				
000033	202020202020	000				
000034	202020202020	000				
000035	202020202020	000	116	BCI	6,	
000036	202020202020	000				
000037	202020202020	000				
000040	202020202020	000				
000041	202020202020	000				
000042	202020202020	000				
000043	770200000000	000	117	OCT	770200000000	
	000044		118	PUNOUT	BSS	0
000044	202020202020	000	119	PUE	BCI	2,
000045	202020202020	000				
000046	202020202020	000	120	PUF	BCI	2,
000047	202020202020	000				
000050	202020202020	000	121	PUSED	BCI	1,
000051	202020202020	000	122	PUVAR	BCI	9,
000052	202020202020	000				
000053	202020202020	000				
000054	202020202020	000				
000055	202020202020	000				
000056	202020202020	000				
000057	202020202020	000				
000060	202020202020	000				
000061	202020202020	000				
	000062		123	7SGGG	EBSS	2

ERRCR LIKAGE

000315	000000000000	000
000316	314531632020	000

LITERALS

000320	000000165140	000
000321	000000000074	000
000322	204733443320	000
000323	202133443320	000
000324	000000202000	000
000325	000000200000	000
000326	000000330000	000

264



BETTER TRY FOR INPUT

000327	610000000000	000
000330	000000000100	000
000331	202020200000	000
000332	202020202000	000
000333	000000000001	000
000334	512742202020	000

124                    ENC  
 335 IS THE NEXT AVAILABLE LOCATION, GMAP VERSION JMPA/C30271 JPPE/U30271 JMPC/O30271  
 THERE WERE NO WARNING FLAGS IN THE ABOVE ASSEMBLY

265

OCTAL	SYMBOL	REFERENCES BY	ALTER NO.										
45	A7	26	21	26									
111	ALESS	62	58	62									
215	CKMINS	84	43	84									
216	CKTIME	85	50	85									
4	CLOSE		103										
156	CRDFIL	67	9	67	70	78							
304	ENDCRD	102	78	102									
10	GEBORT		105										
2	GET		78										
21	GETIME		13										
220	HCURS	87	28	31	87								
221	HRSBCD	88	38	46	88								
0	INIT	4	4	64									
166	INPUT	77	77	79									
315	.F.I..		4	5	6	7	77	78	80	81	101	103	
217	MINS	86	18	40	86								
3	OPEN		5	6	7								
201	CLTPLT	80	80	82									
42	F7	23	20	23									
501	PRNRLF	73	66	73									
141	PRNFIL	66	12	66	69								
164	PRNOT	69	6	69									
0	FLNRLF	72	65	72									
5	FLNCF		81										
124	FLNFIL	65	10	65	68	81							
163	FLNOT	68	5	68	103								
44	FLNOLT	118	81	118									
1202	RDBUF1	74	67	74									
0	REWRK	108	78	108									
165	READIN	70	7	70									
222	TAB	89	33	42	89								
262	TIME1	96	14	49	60	96							
274	TIME2	98	24	27	48	98							
300	WRAPLP	101	101	106									

266

\*\* 18259 WORDS OF MEMORY WERE USED BY GMAP FOR THIS ASSEMBLY.

FLQAT SUBROUTINE

PREFACE

PROGRAM BREAK	265
COMMCA LENGTH	0
V COLAT BITS	5

PRIMARY SYMDEF ENTRY

FLQAT	0
CALC	102
TOBIN	146
TOBCC	211
EDIT	223

SECONDARY SYMDEF ENTRY

BLOCK	LENGTH
1 IO,WRK	62
SYMDEF	

267

FLOAT SUBROUTINE

000000	000000	000000	010	1	TTL	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000001	000260630000	000000	010	2	TTL	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000002	000260754000	000000	010	3	AGAIN	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000003	000260741000	000000	010	4	AGAIN	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000004	000101 2350 00	000000	010	5	LD	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000005	000100 7550 00	000000	010	6	LD	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000006	000004 2260 03	000000	000	7	LDX6	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000007	000010 2270 03	000000	000	8	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000010	000011 6170 00	000000	010	9	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000011	000022 2350 16	000000	010	10	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000012	000000 2360 07	000000	000	11	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000013	106000 4110 03	000000	000	12	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000014	000000 5730 00	000000	000	13	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000015	000026 4570 17	000000	010	14	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000016	777777 0660 03	000000	000	15	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000017	000002 1670 03	000000	000	16	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000020	000100 7530 53	000000	010	17	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000021	000010 6010 00	000000	010	18	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000022	000001710000	000000	010	19	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000023	0000000000010	000000	000	20	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000024	0000000000002	000000	000	21	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000025	0000000000002	000000	000	22	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000026	0000000000001	000000	000	23	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000027	000000011007	000000	000	24	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000030	000000	000	25	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000032	000000	000	26	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000034	000000	000	27	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000036	000000	000	28	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000040	000000	000	29	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
	000040	0004 10	010	30	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000100	000040 0004 10	000000	010	31	LDX7	SLBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES	
000101	000040 0004 10	000000	010				

268

CALCULATE SUBROUTINE

```

000102 000104710000 010 33 CALC SAVE
000103 000260630000 010
000104 000260754000 010
000105 000260741000 010
000106 000030 4330 00 010 34 DFLD AFLCAT
000107 000036 4630 00 010 35 DFMP DFPCAT
000110 000034 5670 00 010 36 DFV CFPCAT
000111 000032 4770 00 010 37 DFAD BFPCAT
000112 000262 4630 00 010 38 DFMP =1D3
000113 216000 4350 03 000 39 UFA =71825,DU
000114 000141 7560 00 010 40 STQ EBIN
000115 000121 6040 00 010 41 TMI EMINUS
000116 000020 2350 07 000 42 LDA =C20,CL
000117 000144 7550 00 010 43 STORE STA ESIGN
000120 000125 7100 00 010 44 TRA DCNE
000121 000052 2350 07 000 45 EMINUS LDA =C52,CL
000122 000117 7100 00 010 46 TRA STORE
000123 000052 2350 07 000 47 FMINUS LDA =C52,CL
000124 000136 7100 00 010 48 TRA STORE1
000125 000030 4330 00 010 49 DONE DFLD AFLCAT
000126 000032 5770 00 010 50 DFSB BFPCAT
000127 000034 5670 00 010 51 DFV CFPCAT
000130 000036 5770 00 010 52 DFSB DFPCAT
000131 000262 4630 00 010 53 DFMP =1D3
000132 216000 4350 03 000 54 UFA =71825,DU
000133 000142 7560 00 010 55 STQ EBIN
000134 000123 6040 00 010 56 TMI FMINUS
000135 000020 2350 07 000 57 LDA =C20,CL
000136 000145 7550 00 010 58 STORE1 STA FSIGN
000137 000143 0540 00 010 59 AOS SEGBIN
000140 000103710000 010 60 RETURN CALC
000141 000000000000 000 61 EBIN DEC 0
000142 000000000000 000 62 FBIN DEC 0
000143 000000000000 000 63 SEGBIN DEC 0
000144 000000000000 000 64 ESIGN DEC 0
000145 000000000000 000 65 FSIGN DEC 0

```

LET,S CONVERT TO BINARY

000146	000150710000	010	000146	67	TORIN	SAVE	
000147	000260630000	010					
000150	000260754000	010					
000151	000260741000	010					
000152	000210 2350 00	010	68	LDA	SVTAL3		
000153	000207 7550 00	010	69	STA	TAL3		
000154	000000 2260 03	000	70	LDX6	,CU		
000155	010000 2370 00	030	71	RTNX	LDQA	REWRK	
000156	000014 7370 00	000	72		LLS	12	
000157	000014 7320 00	000	73		GRS	12	
000160	010000 7570 00	030	74	STAO	REWRK		
000161	000172 7070 00	010	75	TSX7	CONVRT		
000162	000174 7160 16	010	76	XEC	STORE2,6		
000163	000001 0660 03	000	77	ADX6	1,DL		
000164	000161 7100 00	010	78	TRA	*-3		
000165	010005 2350 00	030	79	AROUND	LDA	REWRK*5	
000166	000202 3750 00	010	80		ANA	MASKA	
000167	000200 2270 03	010	81		LDX7	CLT,DL	
000170	000200 6270 00	010	82		EAX7	OLT	
000171	000173 7100 00	010	83		TRA	CONVRT*1	
000172	000202 3770 00	010	84	CONVRT	ANAG	MASKA	
000173	000000 7100 17	000	85		TRA	0,7	
000174	010001 2350 00	030	86	STORE2	LDA	REWRK*1	
000175	010002 2350 00	030	87		LDA	REWRK*2	
000176	010004 2350 00	030	88		LDA	REWRK*4	
000177	000165 7100 00	010	89		TRA	AROUND	
000200	000147710000	010	90	OUT	RETURN	TCBIN	
000201	000000011007	000					
000202	171717171717	000	91	MASKA	ECCT	171717171717	
000203	000000000000	000	92		OCI	000000000000	
	000204		93	MYWRK	BSS	1	
000205	000204 0006 00	010	94	TAL1	TALLY	MYWRK,6,0	
000206	000204 0006 00	010	95	TAL2	TALLY	MYWRK,6,0	
000207	000023 0004 00	010	96	TAL3	TALLY	AEIN,4	
000210	000023 0004 00	010	97	SVTAL3	TALLY	AEIN,4	

270

NOW HOW ABOUT BCD!

```

000211 000213710000 010
000212 000260630000 010
000213 000260754000 010
000214 000260741000 010
000215 000000 2270 03 000 100 LDX7 ,CU
000216 000143 2350 00 010 101 LDA SEQBIN
000217 006200 5202 01 000 102 RPT 3,1
000220 000000 5050 17 000 103 BCD CCNTB,7
000221 000257 7560 00 010 104 STC SEQBDC
000222 000212710000 010 105 RETURN TCBCD

```

0

271

EDIT SUBROUTINE

```

107 DOIT MACRO
108 LDAQ #1
109 LLS 12
110 STA #1
111 LLS 6
112 GRS 6
113 GRG =C3 1000000
114 LRS 6
115 GRG #2
116 STG #3
117 ENDM DCIT
118 MAC1 MACRO
119 LDA =MTNZ #6
120 STA #5
121 LDG =C202020202020
122 #4 LDA #1,SC
123 #5 TNZ #6
124 STG #2,SC
125 STG #3,SC
126 TRA #4
127 #6 LDG =MTRA #7
128 STG #5
129 #7 STA #2,SC
130 STA #3,SC
131 TTF #4
132 ENDM MAC1
133 EDIT SAVE
    
```

```

000223 000225710000 000223 010
000224 000260630000 010
000225 000260754000 010
000226 000260741000 010
000227 000252 2370 00 010
000230 000252 7370 00 000
000231 000252 7550 00 010
000232 000006 7370 00 000
000233 000006 7320 00 000
000234 000264 2760 00 010
000235 000006 7330 00 000
000236 000144 2760 00 010
000237 000253 7560 00 010
000240 000224710000 000240 010
000251 000252 138 EB CD EBSS 2
000254 000254 139 FB CD BSS 2
000256 000256 140 DU MM Y BSS 1
000257 000257 141 SE QB CD BSS 1
000000 000000 142 BL CK IC,WRK 1
000001 201020202002 000 000
000001 201020202002 000
    
```

```

134 DOIT FECD,ESIGN,FB CD+1
LDAQ FECD
LLS 12
STA FECD
LLS 6
GRS 6
GRG =C330C000000000
LRS 6
GRG ESIGN
STG FECD+1
DOIT FECD,FSIGN,FB CD+1
RETURN ECIT
138 EB CD EBSS 2
139 FB CD BSS 2
140 DU MM Y BSS 1
141 SE QB CD BSS 1
142 BL CK IC,WRK 1
143 RD WRK EB CI 5, 8 2 2 1
    
```

272



EDIT SUBROUTINE

000002	202020202002	000			
000003	202020202020	000			
000004	202020202001	000			
	000005		144	BSS	9
000016	202020202020	000	145	PRTOUT	OCT 202020202020
000017	202020000000	000	146	PRSEQ	CCT 202020000000
000020	202020202020	000	147		OCT 202020202020
000021	202020202020	000	148	PRE	BCI 3,
000022	202020202020	000			
000023	202020202020	000			
000024	202020202020	000	149	PRF	BCI 3,
000025	202020202020	000			
000026	202020202020	000			
000027	202020202020	000	150	PRVAR	BCI 6,
000030	202020202020	000			
000031	202020202020	000			
000032	202020202020	000			
000033	202020202020	000			
000034	202020202020	000			
000035	202020202020	000	151		BCI 6,
000036	202020202020	000			
000037	202020202020	000			
000040	202020202020	000			
000041	202020202020	000			
000042	202020202020	000			
000043	770200000000	000	152		OCT 770200000000
	000044		153	PUNOUT	BSS 0
000044	202020202020	000	154	PUE	BCI 2,
000045	202020202020	000			
000046	202020202020	000	155	PUF	BCI 2,
000047	202020202020	000			
000050	202020202020	000	156	PUSEQ	BCI 1,
000051	202020202020	000	157	PUVAR	BCI 9,
000052	202020202020	000			
000053	202020202020	000			
000054	202020202020	000			
000055	202020202020	000			
000056	202020202020	000			
000057	202020202020	000			
000060	202020202020	000			
000061	202020202020	000			

ERROR LINKAGE

000260	000000000000	000
000261	264346216320	000

LITERALS

000262	024764000000	000
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46264 01 10-12-71 12,980

SUBPROGRAM NO. 3 - DATA MANIPULATION SUBROUTINES

PAGE 8

EDIT SUBROUTINE

000263 000000000000 000  
000264 330000000000 000

265 IS THE NEXT AVAILABLE LOCATION, 158 END  
THERE WERE 1 WARNING FLAGS IN THE ABOVE ASSEMBLY GMAP VERSION JHPA/030271 JPPE/030271 JHPC/030271  
ON PAGE NO. 5

274

OCTAL	SYMBOL	REFERENCES BY ALTER NO.					
23	ABIN	20	10	20	96	97	
30	AFLOAT	24	14	24	34	49	
10	AGAIN	9	9	18			
145	AROUND	79	79	89			
32	EFLOAT	25	25	37	50		
102	CALC	33	33	60			
34	CFLOAT	26	26	36	51		
172	CCNVRT	84	75	83	84		
36	DFLOAT	27	27	35	52		
125	LCNE	49	44	49			
252	ERCD	138	134	138			
141	EBIN	61	40	61			
223	EDIT	133	133	137			
121	EMINLS	45	41	45			
144	ESIGN	64	43	64	134		
254	FFCD	139	136	139			
142	FRIN	62	55	62			
0	FLOAT	4	4	19			
123	FMINLS	47	47	56			
145	FSIGN	65	58	65	136		
260	E.L.	4	4	33	67	99	133
202	MASKA	91	80	84	91		
204	MYWRK	93	93	94	95		
200	CLT	90	81	82	90		
0	RCWRK	143	71	74	79	86	87 88 143
100	REGTLY	30	6	17	30		
40	SAVREG	29	29	30	31		
257	SEQRCD	141	104	141			
143	SEQRIN	63	59	63	101		
136	STORE1	58	48	58			
174	STORE2	86	76	86			
117	STORE	43	43	46			
210	SVTAL3	97	68	97			
207	TAL3	96	69	96			
101	TLYREG	31	5	31			
211	TCHCL	99	99	105			
146	TCHIN	67	67	90			

\*\* 18357 WORDS OF MEMORY WERE USED BY GHAP FOR THIS ASSEMBLY,

275

5264 02 10-12-71 12,983

PAGE 1

ORIGIA 062170 ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION

SUBPROGRAMS INCLUDED IN DECK,

011742	101271	START	011742				
011404	101271	INIT	011404	INPUT	011572	CUTPLT	011605
	ELOCK COMMON	IO,WRK	011320			WRAPUP	011704
011030	101271	FLOAT	011030	CALC	011132	TOBIN	011176
	ELOCK COMMON	IO,WRK	011320			TOBCD	011241
						EDIT	011253

SUBPROGRAMS OBTAINED FROM SYSTEM LIBRARY,

010752	021271	,SETU	010760	,FRSIG	010752		
010744	073069	,FLTPR	010744				
010632	101970	,GPNCH	010632	,GAPNC	010632	FUNCH	010692
010562	073069	,GWTRC	010562	,GAWTH	010562	WTREC	010562
010242	101970	,GIL01	010422	,GILLB	010242		
010126	101970	,GEDIT	010126	,GE062	010221	,GE063	010222
		,GE066	010226	,GE067	010227	,GAEDI	010126
		,GE071	010232	,GE072	010233	EDATE	010234
007410	062470	,GGTBK	007410	GETBK	007410	,GGET	007412
		,GAGET	007412			,GE064	010223
		,GOPNR	007402	,GCLSR	007402	,GE068	010230
007402	073069	,GCOPY	006656	COPY	006656	ETIME	010235
006656	100370	PUT	006664	,GACOP	006656	GET	007412
		,GOPEN	006114	,GAOPE	006114	,GE065	010225
006114	010970	,GCLSE	005430	,GACLS	005430	,GE069	010231
005430	020371	CLOSE	005430	,GR185	005534	IOEDIT	010126
		,GBNRY	005412			,GAGTB	007410
005412	101970	,GR200	005220	,GGETH	007402		
005220	101970	,GBCD	005172	,GPTBK	006661	,GPUTR	007402
005172	101970	,GR250	005120	,GAPYB	006661	PUTBK	006661
005120	101970	,GR225	005042	,GAPEN	006664	,GAPUT	006664
005042	101970	,GR275	004536	CPEN	006114	,GPR67	007357
004536	062470	,GR960	004521	,GR185	005534		
004514		,GR377	004400			,GR186	005625
004344	101970	,GR990	004312	,GR385	004337	,GR178	005541
004312	101970	,GR980	004206	,GR991	004333		
004206	101970	,GR999	004216	15AUG5	004330	,GR37X	004417
		,GINHD	003357	,GR979	004304		
003352		,GOVRL	003360	,GR99X	004212	,GR984	004250
		,GINID	003350			,GR985	004304
003350	101970			,GOUTH	003856	,GINTL	003355
				,GLREA	003444	,GOUTL	003354
						PREVS	003403

276

ALLOCATED CORE	RANGE	SIZE
OBJECT PROGRAM	000000 THRU 011777	012000
RELOCATABLE	003350 THRU 011777	006430
COMMON	000100 THRU 002002	001703
S	SYSOUT A1	
S	SYSOUT A2	

ORIGIN 062170 ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION ENTRY LOCATION

S DATA A3
SK. IS THE MINIMUM MEMCRY NEEDED TO LOAD THIS ACTIVITY

EXECUTION PROGRAM ENTERED AT C11742

EI 011740236000 OI 000010001000 IC 011717 IR 302001 ER 107 AR 000000000000 QR 512742202020 TR 00007755
BA 230012 X0 000000 X1 011712 X2 000000 X3 000000 X4 000000 X5 000000 X6 000000 X7 000003

Table with 10 columns of alphanumeric data, likely representing memory addresses and values. The data is organized in rows, with some rows starting with a letter (e.g., 'S', 'E', 'B', 'O', 'C', 'I', 'R', 'A', 'Q', 'T', 'R') and others with numbers. The values are mostly 0s and 1s, with some larger numbers interspersed.

277

001600	0000000000	0000000000	0000000000	0024000000	0000000000	0000000000	0000000000	0000000000
001610	0000000000	0366262000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001620	0367344400	0044000000	0447502200	0305670000	0000000000	0000000000	0000000000	0000000000
001630	0000016500	0547234601	4527442320	0007000000	0000000000	0000000000	0000000000	0000000000
001640	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001650	4527442325	0007000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001660	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001670	0024000000	0524470012	0347051000	0000000000	0000000000	0000000000	0000000000	0000000000
001700	0000000000	0346375000	0000024300	0024000000	0000024300	0420065000	0264540000	03640006000
001710	0364047400	0000000000	0000000000	0000000000	0000000000	0000000000	00110007000	00240000000
001720	0000025100	0547234601	4527442320	0011000000	0000000000	0000000000	0000000000	0000000000
001730	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001740	4527442325	0011000000	0024000000	0024651756	0000000000	0000000000	0000000000	0000000000
001750	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
001760	0024000000	0525561540	0364312000	0000000000	0000000000	0000000000	0000000000	0000000000
001770	0000000000	0000000000	0000024300	0547234601	0000000000	0000000000	0000000000	0000000000
002000	0364321400	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
002010	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
003370	2725204730	0003000706	0034150110	0034150110	0034150110	0034150110	0034150110	0034150110
003380	0033657100	0000022300	0008002240	0000022500	0000022500	0042036300	0042036300	0042036300
003390	0033627440	0033637450	0047002360	0034157520	0034157520	0034262230	0000022240	0034466250
003400	0034446000	0034037550	0000220010	0000220020	0034440000	0034440000	0034440000	0033771000
003410	0034146000	0000270010	0000010110	0034254500	0033617100	0033617100	3327432247	0000000000
003420	0000000000	0000000000	0000000000	0045210000	0033500240	0000000000	0000000000	0000000000
003430	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000	0000000000
004200	0000000000	0000000000	0010020400	0000000000	0000000000	0000000000	0000000000	0000000000
004210	0043357520	7777756360	0043367560	0043042360	0042447440	0000062240	0000062240	0043002020
004220	0042236070	0045217000	0127264110	0043337560	0042462350	0043317550	0042447100	0043317560
004230	0000046750	0042366000	0043337560	0000041750	0000227310	0050712240	0000007100	0043337100
004240	0043337550	0000041750	0201400000	0214140000	1101140000	1101140000	3105140000	1611140000
004250	0000140000	0204100000	0310100000	1101160000	1101160000	1202160000	3005200000	1405020000
004260	0100100000	0204100000	0310100000	1501020000	1501020000	1405020000	1405020000	1405020000
004270	0000000000	0404020000	0314020000	2500000000	3101060000	7700007777	0000000000	0000000000
004300	2500040024	3101040024	2500000000	0043157100	0023002200	0043416350	0043173400	0043417410
004310	0000000000	3327511110	0043157100	0043316010	7777722350	3400003750	0043116000	0000167100
004320	7777732350	0400003150	0043316010	3400000000	0115560115	2500000000	0115560130	0115570000
004330	7777757550	0000010010	3400000000	3327511110	0000000000	0000022350	0000022100	0044157410
004340	0000707100	0050755000	0044047420	0044047420	0000012210	0044060100	7777752350	1703003750
004350	0000007410	0044067400	0000002350	0017003750	0000006100	0043670100	0000062210	00440071000
004360	0043670010	0000002350	0000020610	0000020610	0000020610	7777752350	0000023750	00440060000
004370	0043670010	0043670000	0044067100	0044067100	0043440000	0000000000	0000000000	0000000000
004400	0044057410	7777737010	0044156000	0045217000	0044327100	0000027100	0044327100	0000007410
004410	0000002210	0000002210	0000027100	0044327100	0044327100	0000027100	0044327100	0000001100
004420	0044207100	0000027100	0044347440	0044377040	0000022400	7777732360	0044062200	0000003700
004430	7777753410	0000027100	0044347440	0044377040	0000022400	7777732360	0044062200	0000003700
004440	0045077410	0000002350	0000014735	0042167160	0045032360	0042022104	7777723501	00440601000
004450	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004460	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004470	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004480	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004490	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004500	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004510	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004520	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004530	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004540	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004550	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004560	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004570	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004580	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004590	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004600	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004610	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004620	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
004630	0045077410	0000002350	0000014735	0045032360	0045032360	0045032360	7777723501	00440601000
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010000	400000220003	001000235007	77777375012	007506600000	726235012	004306750000	004312/01000	01001710000
010010	010122000401	400000220003	004306235000	007507710000	000200235007	77777325012	00001220003	777772040012
010020	010023710000	020000235007	77777325012	011577221003	007/66710000	004521700000	012725032020	004521700000
010030	002725632020	004344700000	007414000000	000000000000	010113/10000	777775235012	010041901000	000017001000
010040	010035710000	170000375003	00773601000	777776220012	000003740012	010000710000	000007350100	00170035007
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010060	010023221000	007431700000	777773220012	010066601000	004272700000	042725632020	00002236012	00002276000
010070	000002756012	004344700000	007414000000	000001000000	00002223012	000001220013	777772740012	777773235012
010100	001000375007	007572600000	000002236012	000022776000	000002756012	010111742000	005042/01000	010112710000
010110	010122000475	000000000000	007572710000	004405220000	000006100003	010000601000	010021/10000	000000000000
010120	011320000000	0011010306010	011710502000	332727632242	000000004200	000000000000	010132/10000	000000222003
010130	000010223003	010237630000	010237754000	010237741000	010237741000	010130743000	000002222011	000003223011
010140	010201600000	000021001000	010234755000	341000036007	702000036007	000044737001	005172/01000	010151710000
010150	010237000030	010235752007	000006736000	010235752000	00000235012	000022735000	010175000000	000007115003
010160	010162600000	010175603000	000000235012	010163710025	010173710000	010202710000	010207/10000	010207710000
010170	010212710000	010214710000	010216710000	010221755000	010233054000	000001123003	010201000000	000001002003
010200	010154710000	010127710000	010222755000	010174710000	010225751070	010230054000	010175/10000	010226751070
010210	010211054000	010175710000	010223751070	010175710000	010227751070	010175710000	000000235001	010232755000
010220	010175710000	000000000000	000000000000	010224000000	000000000000	000000000000	000000000000	000000000000
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010250	010325741000	000003220011	010353600000	000000222003	010225221000	000000235011	000001236011	010452117012
010260	010312600000	010366700000	010225221000	000000235011	000001236011	010432757012	010432757012	010422757000
010270	010424377000	010424377012	010424377000	010442177000	010432355012	010433356012	010432237012	000030773000
010300	010436117012	010310600000	005412701000	010305710000	010200000047	000001076007	010432756012	010431710000
010310	010432450012	010325710000	010432235012	010432254012	0051/2701000	010317710000	010500000057	010436117012
010320	010422601000	010432450012	000030737000	010426277012	010422757007	000000621000	000002235011	010346601000
010330	010422235003	001000275007	010444755000	000004235011	000004235007	010445755000	010444235052	010457236005
010340	000006737000	010445755052	000006737000	010445755052	010336607000	010243710000	000004222011	010422235000
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010380	010456450000	010372607000	010456236000	000006402007	000022736000	010407752070	010446237000	000000737000
010390	010424757000	000010236007	010456176000	000006402007	000022736000	010417752070	010450237000	000000773000
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010450	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010460	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010470	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010480	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010490	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010500	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010510	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010520	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010530	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010540	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010550	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010560	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010570	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010580	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010590	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010600	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010610	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010620	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010630	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010640	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010650	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010660	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
010670	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
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010750	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
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010770	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
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011010	011012710000	011022000047	000000220003	000000222003	000000223003	000000224003	000000225003	000000226003
011020	011032710000	011032710000	011310630000	011310741000	011310741000	011310741000	011310741000	011310741000
011030	011041617000	011052235016	000000236007	100000411003	000000573000	011056457017	777777066003	000002167003
011040	011130753053	011040601000	011034710000	000000000010	000000000002	000000000002	000000000001	000000011007
011050	010400000000	000000000000	004400000000	000000000000	000000000000	000000000000	000000000000	000000000000
011060	010400011754	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
011070	011400011754	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
011080	011400011754	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000	000000000000
011090	011130000010	011070000410	011134710000	011310630000	011310741000	011310741000	011000433000	011066463000
011100	011064567000	011062477000	011312463000	210000435003	011171756000	011151604000	000020235007	011174755000
011110	011155710000	000052235007	011147710000	000052235007	011166710000	011060453000	011002577000	011064567000
011120	011066577000	011312463000	216000435003	011172756000	011153604000	000020235007	011175755000	011173054000
011130	011133710000	000000013560	000000003720	000000000012	000000000020	000000000020	011200710000	011310630000
011140	011310741000	011310741000	011240235000	011237755000	0000000226003	011320237000	000014737000	000014732000
011150	011320757000	011222707000	011224716016	000001066003	011211710000	011325235000	011232375000	011230227003
011160	011230627000	011223710000	011232377000	000000710017	011321235000	011322235000	011324235000	011215710000
011170	011177710000	000000011007	171717171717	000000000000	000000000000	012340000000	011234000000	011053000400
011180	0110530000400	011243710000	011310630000	011310741000	011310741000	000000227003	011173235000	006200520201
011190	000000505017	011307756000	011242710000	011255710000	011310630000	011310741000	011310741000	011302237000
011200	000014737000	011302755000	000000673700	000000673200	011314276000	000000673000	011174276000	011303756000
011210	011304237000	000014737000	011304735000	000000673700	000000673200	011314276000	000000673300	011175276000
011220	011305756000	011254710000	203320235033	203320202020	203320332033	203320202020	000000000000	003720171717
011230	011765502000	264346216320	024764000000	000000000000	330000000000	000000000000	000000000000	000000000000
011240	000000071011	000020010203	202020202003	202020202020	202020202020	632562632062	214447432520	632545525220
011250	234645275121	636443216331	464562202646	512027256363	314527203025	512520202020	202020202020	202020202020
011260	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020
011270	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020
011280	006114701000	011415710000	011721000005	011567000000	000001000000	006114701000	011422710000	011721000006
011290	011570000000	000001000000	006114701000	011427710000	011721000007	000001000000	000001000000	000016226003
011300	011563746000	011531746000	000026226003	011546746000	000021001000	011666755000	000000673200	011724506000
011310	011725506000	011623755000	000014116007	011446000000	011916040000	000014176007	011726235000	011701755000
011320	011453710000	011727235000	011701755000	011624756000	000000636000	000000226003	011624235000	014200520201
011330	011626505016	000011116007	011464603000	011730276000	011731276000	011625756000	000000226003	011623250000
011340	014200520201	011626505016	011621756000	011732276000	000022736000	011625235000	000022733000	011700756000
011350	011666235000	011622755000	000014733000	000006732000	011733276000	000014733000	000006732000	011733276000
011360	011734115000	011515604000	011735275000	011666757000	011736275000	011736275000	011405131000	011405131000
011370	000000000000	000000000000	000001000001	000000004000	000000002101	400000000000	000000000000	000000000000
011380	000027200200	000016000000	000100000100	000500000000	000000000000	000000000000	000000000000	000000000000
011390	000000004040	000000002102	400000000000	000000000000	000000000000	000000000000	000000000000	000010000001
011400	000500000000	000500000000	011553000001	000500036005	000001000001	000000000400	000000002103	401700000000
011410	002003040000	000000100000	000000000000	000000000000	001302001302	001533000000	000500000000	011530400000
011420	011545400000	011562200000	011574710000	011721630000	011721754000	011721741000	007412701000	011604710000
011430	011721000116	011562000000	011710000000	011320000000	011573710000	011607710000	011721630000	011721754000
011440	011721741000	010632701000	011617710000	011721000121	011530000000	011364000000	011737000000	011606710000
011450	000000000000	000000000511	010001020701	000000000073	000000000014	000000200102	000003032400	000002342000
011460	000001750000	000001440000	000001200000	000001000000	000000000030	772020202020	202020202020	202020202020
011470	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	304645257006
011480	254343204421	514225633145	272025246423	216331464520	274421472023	466451622520	770200000000	000000011007
011490	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020200100	610102610701
011500	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020	202020202020
011510	200102330511	204733443320	234220202020	770400000000	011706710000	011721630000	011721754000	011721741000
011520	011711710000	005430701000	011716710000	01172100147	011267000000	000003000000	011740236000	000010001000
011530	011705710000	011746502000	314531632020	000000000000	000000165140	000000000074	204733443320	202133443320
011540	000000702000	000000200000	000000330000	010000000000	000000000100	202020200000	202020202000	000000000001
011550	512742202020	000000000000	011404701000	011745710000	011745710000	011572701000	011700710000	011776000004
011560	011176701000	011753710000	011776000005	011030701000	011756710000	011776000006	011327010000	011761710000
011570	011776000007	011241701000	011764710000	011776000010	011253701000	011767710000	011776000011	011605701000
011580	011772710000	011776000012	011745710000	011704701000	011776710000	011776000014	000000000000	626321516320

UPPER SSA PLUS SLAVE PREFIX

777000M	226003777700	227030000110	011717302001	000010000000	006203000200	340300000000	340100000000	006346102200
777010	1334006661	001440227752	777212250000	000010000000	24202200	000175000001	000000000000	000075410000



COMMON SYSTEM FILES

FILE CODE	NAME	REMARKS
A*	ALTER FILE	Created when System Input (GIN) reads \$ UPDATE card. \$ ALTER cards and source cards put on A*.
B*	OBJECT PROGRAM FILE	Created by General Loader (GLOAD) when \$ EXECUTE included in source deck and no fatal error occurred during compilation. (General Loader loads from B* rather than from R* for compile-and-execute option)
C*	BINARY DECK FILE	Created when binary deck option (\$ GMAP DECK) is specified. Goes to SYSOUT--report code: 76 . 8
G*	GMAP SOURCE FILE	System Input (GIN) creates G* when it sees \$ GMAP. COBOL & FORTRAN create G* as output file. Used as input to GMAP Assembler.
H*	PROGRAM LINK FILE	Used for temporary storage of programs that are called into and out of memory.
I*	DATA STORAGE FILE	Used as storage area for input data required by User Programs.
K*	COMPRESSED DECK FILE	Created when COMDK option specified in language processor control card (\$ GMAP COMDK).
L*	SYSTEM SUBROUTINE LIBRARY FILE	Appropriate System Subroutines loaded as required.
P*	SYSTEM OUTPUT FILE	Collection of all Systems Output.
R*	LOADER INPUT FILE	Normal Loader Input File. Includes directives to General Loader (GLOAD) as well as binary programs. \$SOURCE directive generated by system and placed on R* for compile-and-execute jobs instructs General Loader to load from B* rather than R*.

SERIES 6000

GMAP PROGRAMMING

COURSE 605

EXERCISES

<u>EXERCISE NO.</u>	<u>TITLE</u>	<u>PAGE</u>
1	Binary and Octal Representation	287
2	Data Movement and Arithmetic Operations	288
3	Data Representation and Shift Operations	289
4	Indirect then Tally Address Modification	290
5	Miscellaneous Instructions	291
6	Pseudo Operations	292
7	Desk Debug Exercises	293
8	Macro Operations	294
9	File and Record Control (GFRC) Exercise No. 1	295
10	File and Record Control (GFRC) Exercise No. 2	297

PROBLEMS

<u>PROBLEM NO.</u>	<u>TITLE</u>	<u>PAGE</u>
1	Workshop Problem	299
2	Sumbit Class Problem	310
3	Optional Problem No. 2	310
	<u>Workshop Performance Chart</u>	311

## BINARY AND OCTAL REPRESENTATION

1. Do the following number conversions:

a.  $(285)_{10} = ( \quad )_8 = ( \quad )_2$

b.  $(.75)_{10} = ( \quad )_8 = ( \quad )_2$

c.  $(65)_8 = ( \quad )_{10} = ( \quad )_2$

d.  $(.101)_2 = ( \quad )_8 = ( \quad )_{10}$

e.  $(1101)_2 = ( \quad )_8 = ( \quad )_{10}$

f.  $(.31)_8 = ( \quad )_{10} = ( \quad )_2$

g.  $(1011011)_2 = ( \quad )_8 = ( \quad )_{10}$

h.  $(8)_{10} = ( \quad )_8 = ( \quad )_2$

2. Do the following arithmetic operations:

a.  $(1011)_2 + (111)_2 = ( \quad )_2$

b.  $(23)_8 + (57)_8 = ( \quad )_8$

c.  $(1110)_2 - (11)_2 = ( \quad )_2$

d.  $(6307)_8 - (1436)_8 = ( \quad )_2$

e.  $(111011)_2 - (11001)_2 = ( \quad )_2$

f.  $(110)_2 + (11)_2 + (101)_2 + (111)_2 = ( \quad )_2$

## DATA MOVEMENT AND ARITHMETIC OPERATIONS

1. Move 150 words from "Table A" to "Table B." Check each word and:
  - a. If negative, add one to "NEG" counter.
  - b. If zero, add one to "ZERO" counter.

Update your instruction word addresses without using register modification.

2. Repeat the problem for Step #1, except reverse the order of the Table and use register address modification.
3. Show the coding to complement a negative number back into its true form. Do not use the "NEG" instruction.
4. Show the coding to perform the following calculation:

$$\frac{(A \cdot B) - C}{D} + E = F$$

Note: If D equal to zero, store zero's in "F."



## DATA REPRESENTATION AND SHIFT OPERATIONS

1. Create the following values in memory by means of pseudo operations:
  - A.  $1492_{10}$
  - B.  $3466_8$
  - C.  $1903_{10}$  in upper 18 bits
  - D.  $770077_8$  in upper 18 bits
  - E.  $3.1416_{10}$
  - F.  $7.9 \times 10^3$
  - G.  $83.5 \times 10^9$  in double precision
  - H.  $183 \times 10^{-3}$  scaled to Bit 24
  - I. 6 bits of BCD X, 6 bits octal 72, 6 bits of  $28_{10}$ , 18 bits of address XRAY
  - J. The statement "THREE-D SYSTEM"
2. Shift the binary number in memory location "Place" three binary places to the left and store in "New"
  - A. Question: What effect does this operation have on this value?
3. Add "A" (= 14.73B23) to "B" (= 6.097B27) and store in Location "C"
4. Show the control cards and deck setup necessary to perform the following jobs:
  - A. GMAP assembly and execute
  - B. GMAP and FORTRAN assembly and execute
  - C. Binary object deck and execute

## INDIRECT THEN TALLY ADDRESS MODIFICATION

1. Code Problems 1 and 2 of Exercise #2 using Tally operations.
2. There are ten (10) words of 6-bit BCD characters beginning at Location "TABLEA." Completely reverse the characters so that the first character of the first word becomes the last characters of the last word and so on. Store result in TABLEB.

Write the GMAP coding to do the following:

3. Cause a fault trap to occur.
4. Load A with B indirect.
5. Move 5 characters starting with character position 3 in TAB to PRTOUT starting in character position 5. Use tally modification.
6. Move a 50 word table (TAB1) to TAB2 using tallies.
7. Do part 6 using LDAQ, STAQ.
8. Give the major difference between CI and SC modification.

## MISCELLANEOUS INSTRUCTIONS

1. Code Problem #1 of Exercise #2 using a Repeat type instruction.
2. Convert a binary value in Location "BIN" to BCD and store in "BCDNR."  
The maximum size of the binary number will not exceed 7 BCD digits when converted.
3. A physical record of 1000 words starts at Location "WORD" and is composed of 10-word logical records. If the first word of a logical record is negative, the logical record is to be moved to a second table starting at Location "WORK."
4. Write the GMAP coding to do the following:
  - a. Move a 50 word table using indexing from TAB1 to TAB2.
  - b. Do part "a" using RPD.
  - c. Find the data record with a key of 2A $\frac{1}{2}$  in a 50 record chain starting at MASCHN.
  - d. Count all numbers  $\langle 0$  or  $\rangle 50$  in table TAB which is 100 words long.
5. Show the contents of the instruction word.
  - a.           RPT           10,1
  - b.           RPDA          25,2,TZE,TMI

PSEUDO OPERATIONS

List the pseudo-operation which will do the following:

1. Reserve 500 words of core with the first word labeled A1.
2. Reserve 500 words of core with word 501 labeled A2.
3. Program documentation
4. Page title
5. Termination of assembly
6. Creates entry points
7. Used to begin a subroutine
8. Specifies labeled or blank common mode.

PROBLEM		Find the Coding Errors only and describe them in the Comments Field					
PROGRAMMER		Do not concern yourself with possible programming logic errors.				PAGE	OF
LOCATION	OPERATION	ADDRESS, MODIFIER		COMMENTS		IDENTIFICATION	
1 2	6 7 8	14 15 16	32	72 73	80		
	ORG	2358					
END	LDA	GOOD					
	STA	.OCT					
	ANA	ONE,X7					
GO	BOOL	2286					
	RPD	29,3,TZE,TRA					
	LDAQ	REG,6					
	CMPO	.OCT					
	TTF	IC-2					
	ANDQ	HI					
	TZE	HALT					
HELP	LDX	2,0,DU					
	ARL	24,7					
	ASQ	BYE,DU					
GOOD	SREG	REG					
A=3	TMI	.HELP					
HALT	MME	GESTOP					
	ORG	712345					
GOOD	BCI	4,I hope you got this correct.					
REG	BSS	8					
BYE	ZERO	HELP,					
HI	DEC	66724					
OCT	OCT	123456787654					
	END	END					

293

## MACRO OPERATIONS

1. Write a conditional assembly MACRO to move a variable number of words from one location to another. (Range 1-4095 words to be moved.) Also provide the option to either check and count ZERO and NEGATIVE words, or to ignore them. (Use the conditional PSEUDO OPS.)
2. Write a MACRO to move n words from argument #2 to argument #3. N is argument #1. Write an example of the CALL.
3. Write a MACRO to: swap N words from two given locations. Write an example of the CALL.

1. Write the coding for the File Control Block, File Designator

Word, and Buffers for an input tape file with the following parameters:

Fixed length records	
Record size	60 words
Block size	300 words
Two buffers	
File Code	AB
Standard labels	
Multireel file	
File name	TAPE-FILE
Location of File Control Block	TPFILE
Location of File Designator Word	FILEWD

2. Write the coding for the File Control Block, File Designator Word, Buffers and Working Storage Area for an input BCD card file with the following parameters:

File Code	I*
System Standard Format	
One Buffer	
Location of File Control Block	CDFILE
Location of File Designator Word	CARDWD

3. Give the effective address of the following examples:

a.

	LDA	A,*
	}	
A	LDQ	B,*5
	}	
B	LDX5	C,1

b.

	LDA	*+1,I
	LDQ	*-1,*

3.

c.		LDX1	B,1*
		⋮	
	B+C(X <sub>1</sub> )	STX1	C,*1
		⋮	
	C	ADX1	D,*5
		⋮	
	D	SBX1	10,DU
		⋮	
	1(+C(X <sub>5</sub> ))	ADX1	15,DU

4. Form the one's complement of ABLE in BAKER.

5. Turn on bits 1, 13-15, and 27 and turn off all others in SAM.



1. Write the coding for the File Control Block, File Designator Word, Buffer, and Working Storage Area for an output card file with the following parameters:
  - File Code P\*
  - Standard System Format
  - One Buffer
  - Location of File Control Block PUNCD
  - Location of File Designator Word PDWD
  
2. Using the card file described in No. 1 above, write the following File and Record Control (GFRC) CALL's:
  - a. OPEN
  - b. CLOSE
  
3. Write the coding for the File Control Block, File Designator Word, Buffer for an output tape file with the following parameters:
  - File Code T0
  - Variable Length Records
  - Record Size                    50 words
  - Block Size                    200 words
  - Two Buffers
  - Location of File Control Block OTFILE
  - Location of File Designator Word OTWD
  
4. Using the tape file described in No. 3 above, write the following File and Record Control (GFRC) CALL's.
  - a. OPEN
  - b. CLOSE
  - c. PUT
  
5. Using CALL WTREC, write the coding to punch a binary card and other required parameters.
  
6. Using CALL WTREC, write the coding to punch a Hollerith card and any other required parameters.
  
7. Using CALL WTREC, write the coding and any other required parameters to print a 20-word line.

8. Using CALL WTREC, write the coding and any other required parameters to create a tape record of 15 words.
9. Using CALL PUNCH, write the coding and any other required parameters to punch a binary card.
10. Using CALL PUNCH, write the coding and any other parameters to punch a Hollerith card.
11. Using CALL PRINT, write the coding and any other parameters to write a 22-word line. Skip to the top of page after printing.
12. Write the IOEDIT control parameters and codes to insert 30 characters in the first line of the heading.
13. Write the IOEDIT control parameters and codes to print a sub-heading.
14. Write the IOEDIT control parameters and codes to insert a sequence number in punched cards beginning with 00000100.

GMAP WORKSHOP PROBLEM  
(Optional)

PROBLEM DEFINITION

The GMAP Workshop problem consists of reading an input file of data cards, performing calculations on fields contained in these cards and printing the results on a report.

The input cards have four data fields labeled A, B, C & D respectively and a text field. The numeric input data from fields A, B, C & D must be converted from BCD values to binary values for calculations. After performing the calculations on these four fields, the results should be converted to BCD for output. The text field is then moved intact to the output areas.

Print line images should be edited by inserting decimal points and suppressing leading zeroes. These results should be formatted as specified.

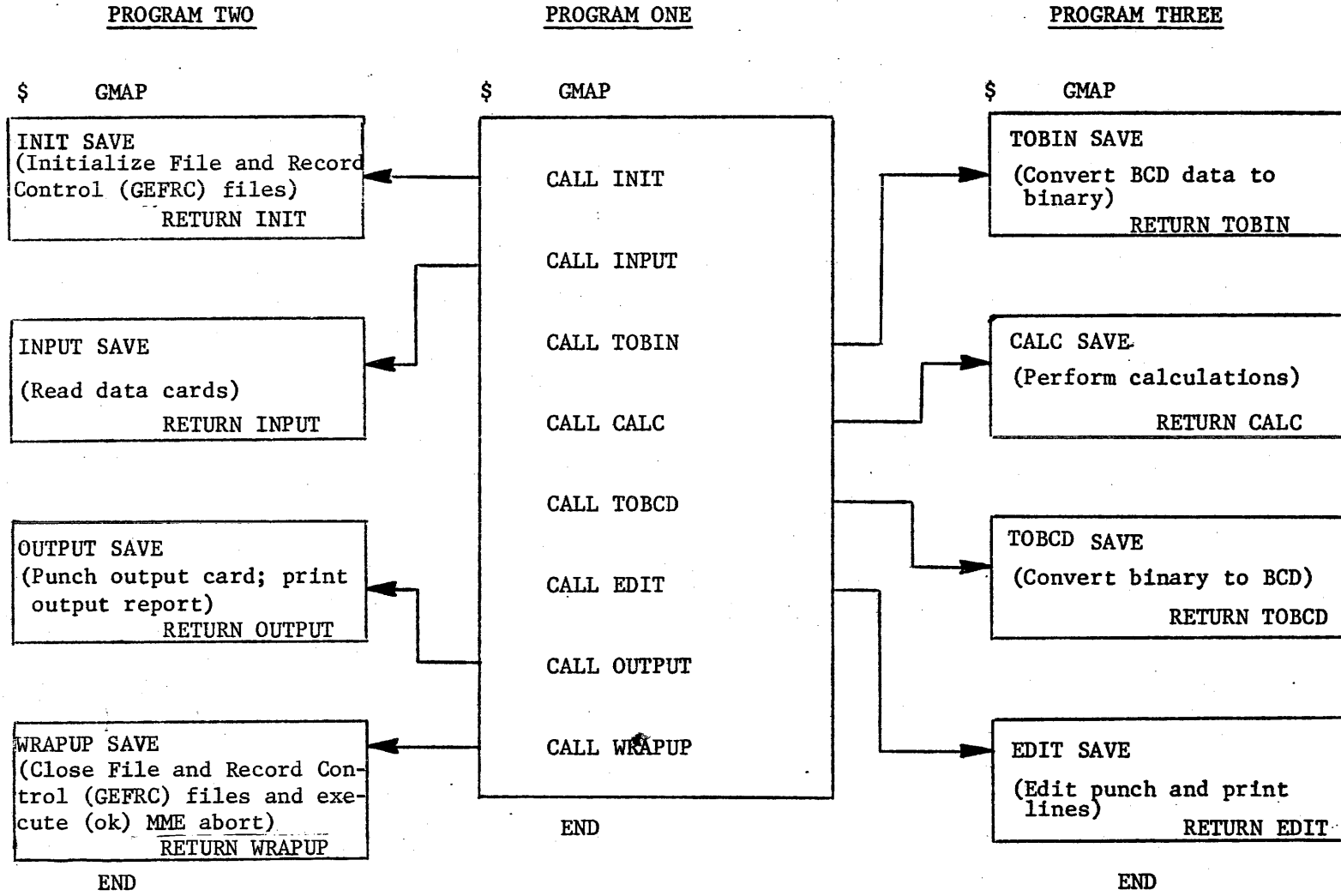
The program solution consists of three subprograms which form one program. Sub-program ONE is the main or executive routine that consists solely of calls to the subroutine modules contained in subprogram TWO and subprogram THREE. The sequence of the calls inserted into subprogram ONE determines the sequential order of processing at execution time. This process is repeated until all data cards have been processed and the job is terminated.

GMAP COURSE

WORKSHOP PROBLEM JOB STACK  
(Assignment No. 1)

```
$          SNUMB
$          IDENT
$          GMAP          NDECK          (program one)
          .
          .
          .          (source cards)
          END
$          GMAP          NDECK          (program two)
INIT       .
INPUT      .          (source cards)
OUTPUT     .
WRAPUP     .
          END
$          GMAP          NDECK          (program three)
TOBIN      .
          .
          .          (source cards)
          .
CALC       .
TOBCD     .
EDIT      .
          END
$          EXECUTE      DUMP
$          LIMITS      ,5K,,1000
$          ENDJOB
***EOF
```

GMAP WORKSHOP ASSIGNMENT

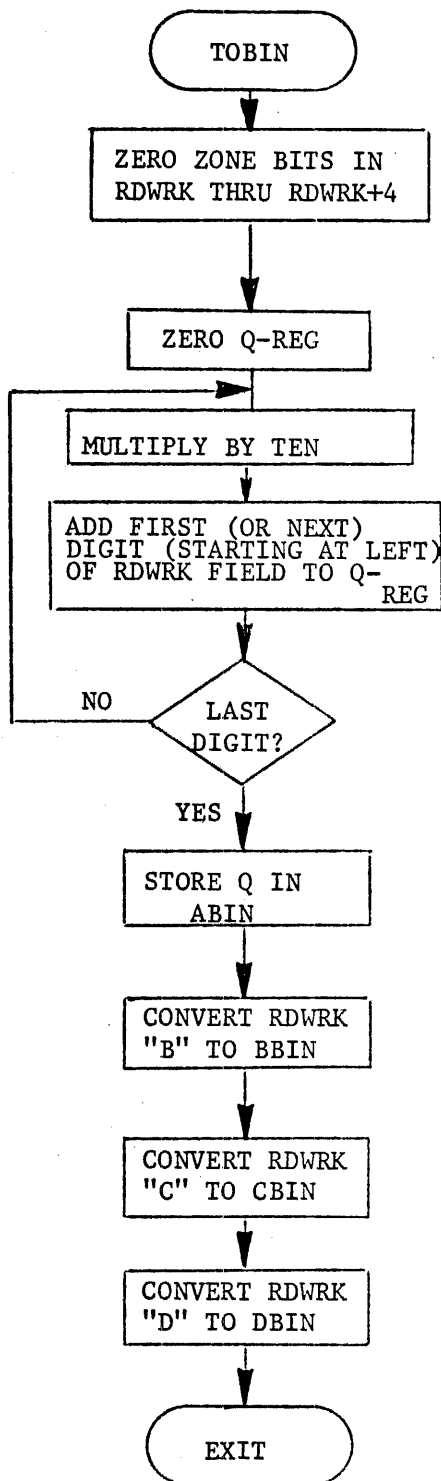


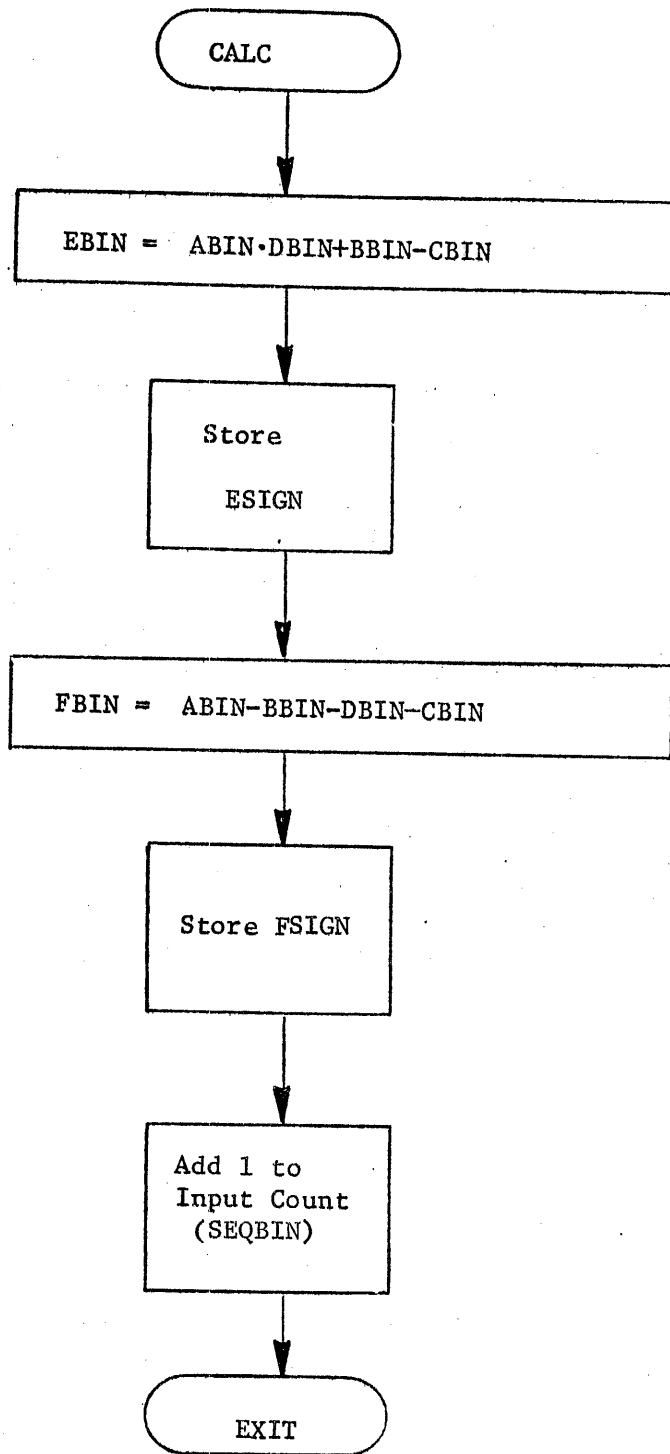
101

GMAP COURSE  
CLASS PROBLEM

ASSIGNMENT NO. 2

RDWRK contains the input  
card image.

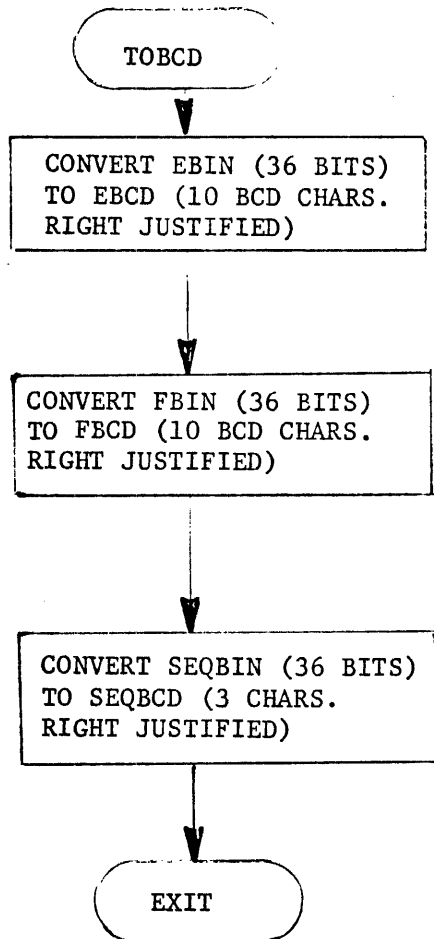




ASSIGNMENT NO. 3

1. Assume **EBIN**, **FBIN**, and **SEQBIN** single precision fixed point binary numbers with a binary point to right of Bit 35.
2. **ESIGN** and **FSIGN** will contain the sign of **E** and **F**, respectively.

GMAP COURSE  
CLASS PROBLEM



ASSIGNMENT NO. 4

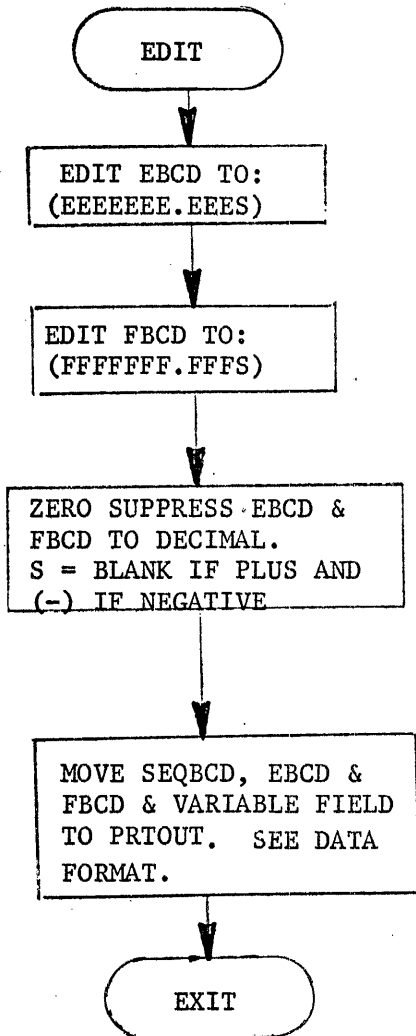
1. EBCD and FBCD are double precision words.
2. SEQBCD is a single precision word.



GMAP COURSE  
CLASS PROBLEM

ASSIGNMENT NO. 5

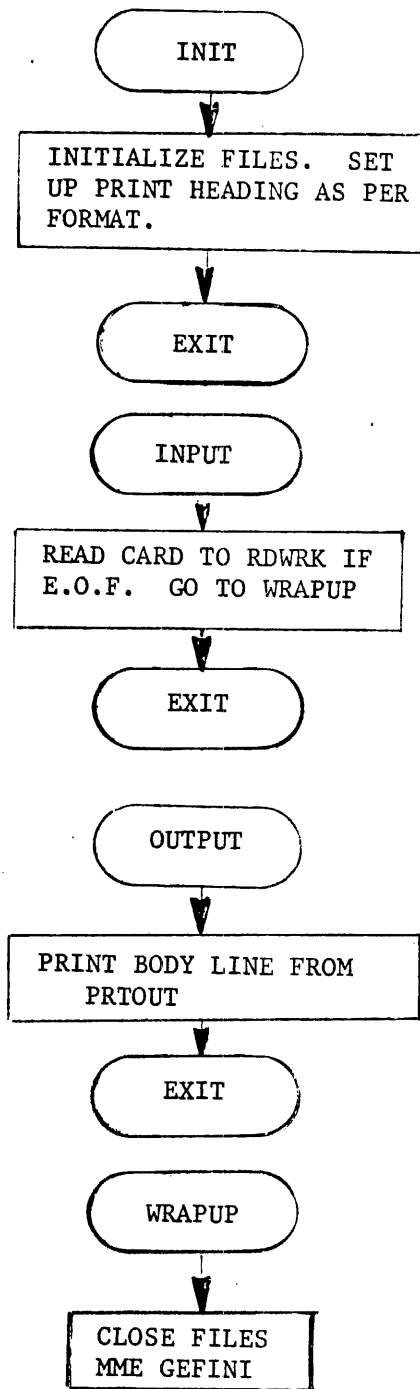
PRTOUT located in labeled common  
IØ.WRK.



GMAP COURSE  
CLASS PROBLEM

ASSIGNMENT NO. 6

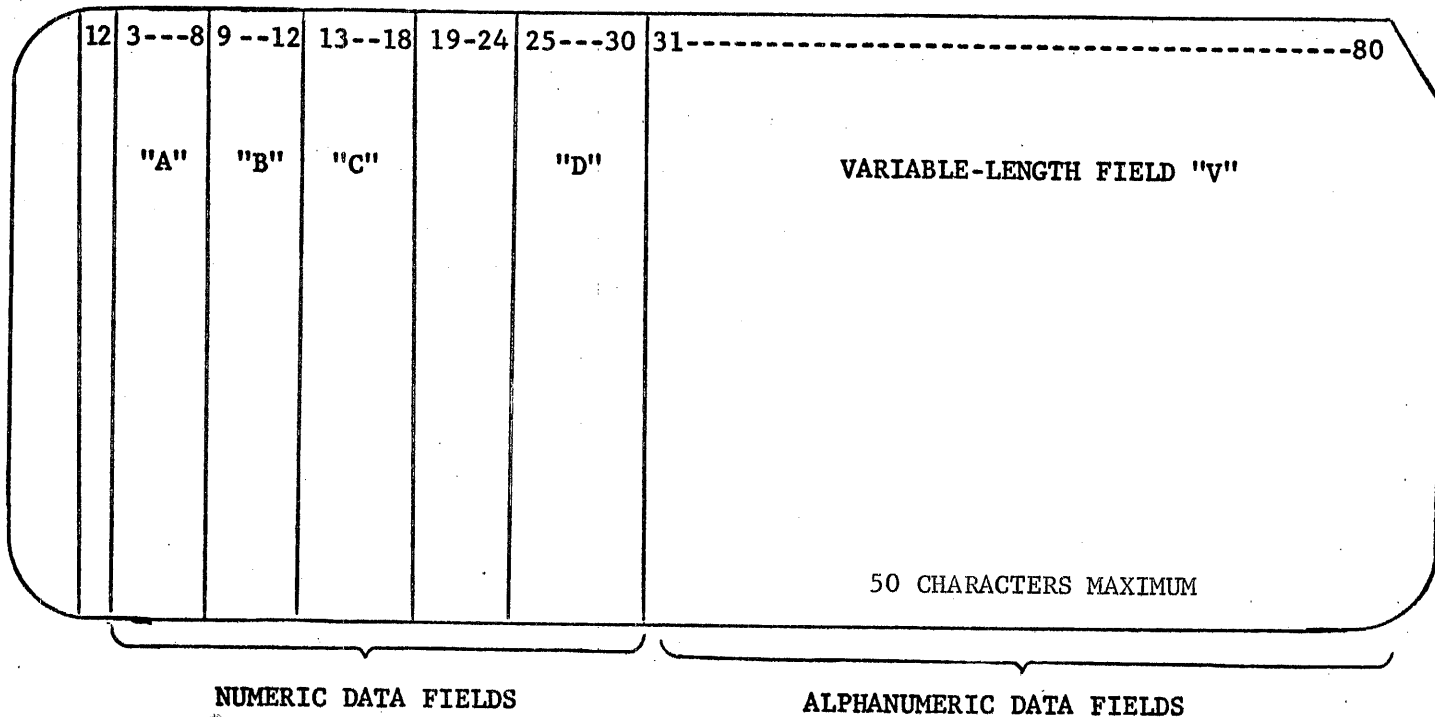
Perform all I/O functions  
using File and Record Control (GEFRC).



GMAP COURSE  
CLASS PROBLEM

INPUT ITEM DESCRIPTION

Input Card Format



Description

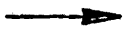
1. Input item is in BCD.
2. Numeric data fields contain positive integers; field "C" does not contain zero.
3. Each numeric data field is right-justified within its own area. It does not contain leading zeroes.
4. Variable-length field "V" contains some type of alphanumeric descriptive material. If present, field "V" begins with Column 31.

GMAP COURSE  
CLASS PROBLEM

Printed Report Format and Description

The printed report will consist of the following:

1. Heading line - Any desired information
2. Sub-heading line - Any desired information
3. Body lines - The body line should be double-spaced. The print positions for each of the fields in the body line is as follows:

<u>Char. position</u>	<u>Field</u>
1 - 9	Blank
10 - 12	Seq. No.
13 - 18	Blank
19 - 30	EBCD
31 - 36	Blank
37 - 48	FBCD
49 - 54	Blank
55 	Variable Field

GMAP COURSE  
CLASS PROBLEM

SAMPLE PRINTED REPORT

Heading Line

CLASS PROBLEM - JOHN SMITH

Sub-heading Line

AUG. 14, 1972

2:30 P.M.

Body Lines

1	2345.123-	2222.123	FIRST OUTPUT
2	2.000	9999999.000-	EXAMPLE
3	12653.070	1.000	THIS MAY BE 50 CHAR.
4.	.099-	234.000-	NO DECIMAL POINT USED
etc.	etc.	etc.	etc.

Sequence No.

Field "v"

Field "E"

Field "F"

OPTIONAL PROBLEM NO. 1

SUMBIT CLASS PROBLEM

1. BLANK THE PRINT AREA USING INDEXING.
2. USING FILE AND RECORD CONTROL (GFRC)
  - a. OPEN THE FILES
  - b. USE CALL GET AND MOVE THE RECORD TO WORKING STORAGE.
  - c. EOF ENCOUNTERED CLOSE FILES.
  - d. USE MME GEFINI.
3. CHECK THE FIRST WORD OF WORKING STORAGE FOR CODE SUMBIT.
4. CODE NOT EQUAL TO SUMBIT.
  - a. MOVE THE RECORD TO THE PRINT AREA.  
(USE THE DOUBLE REPEAT FOR THE MOVE)
  - b. MOVE AN ERROR MESSAGE TO THE PRINT AREA.
  - c. USE CALL PRINT TO PRINT THE RECORD.
5. CODE EQUAL TO SUMBIT.
  - a. COUNT THE BITS SET TO ONE IN WORDS 3 AND 4.
  - b. MOVE THE RECORD TO THE PRINT AREA (USING TALLYS).
  - c. CONVERT THE SUM OF BITS FROM WORDS 3 AND 4 TO BCD.
  - d. MOVE BCD SUM TO PRINT AREA.
  - e. PRINT THE VALID RECORD WITH THE SUM USING CALL PRINT.

OPTIONAL PROBLEM NO. 2

Given values A, B, and C (all values 1)

- IF  $A = B = C$  then  $X = A + B + C$
- IF  $A = B$  or  $B = C$  or  $A = C$  then add the two equal values and multiply by the third = Y
- IF  $A \neq B \neq C$  then divide largest by smallest and subtract middle = Z.

GMAP COURSE

WORKSHOP PERFORMANCE CHART

NAME: \_\_\_\_\_

CLASS NO.: \_\_\_\_\_

WORKSHOP PROBLEM

	<u>CODED</u>	<u>DEBUGGED</u>
TOBIN	_____	_____
CALC	_____	_____
TOBCD	_____	_____
EDIT	_____	_____
INIT	_____	_____
INPUT	_____	_____
OUTPUT	_____	_____
WRAPUP	_____	_____
OPTIONAL PROBLEM NO. 2	_____	_____
OPTIONAL PROBLEM NO. 3	_____	_____

EXERCISES :

#1	_____	#6	_____
#2	_____	#7	_____
#3	_____	#8	_____
#4	_____	#9	_____
#5	_____	#10	_____

MACRO	_____	BOOL	_____	SYMDEF	_____
INE/IFE/IFL/IFG	_____	OCT	_____	SYMREF	_____
IDRP	_____	VFD	_____	EQU	_____
RPT	_____	DEC	_____	BLOCK	_____
PMC	_____	BCI	_____	USE	_____
TTL/TTLS	_____	ARG	_____	SET	_____

SERIES 6000

GMAP PROGRAMMING

COURSE 605

QUIZZES AND FINAL EXAM

<u>QUIZ NO.</u>	<u>TITLE</u>	<u>PAGE</u>
1	Overview and Data Quiz	313
2	Instruction Counter	315
3	Tally Operations	316
4	Miscellaneous Operations	317
5	General Quiz	318
6	Macro Operations	322
7	Effective Address and File and Record Control (GFRC) Quiz	323
8	File and Record Control (GFRC) Examination	324
9	GMAP Quiz 9	326
10	GMAP Quiz 10	335
	Final Examination	341



OVERVIEW AND DATA QUIZ

1. Name the three component modules of the Series 6000.

\_\_\_\_\_, \_\_\_\_\_, \_\_\_\_\_.

2. Name six peripheral devices available with the Series 6000.

\_\_\_\_\_, \_\_\_\_\_, \_\_\_\_\_, \_\_\_\_\_,  
 \_\_\_\_\_, \_\_\_\_\_.

3. Name the two processor modes: \_\_\_\_\_ and

\_\_\_\_\_.

4. Name five processor registers excluding index registers.

\_\_\_\_\_, \_\_\_\_\_, \_\_\_\_\_,  
 \_\_\_\_\_, \_\_\_\_\_.

5. Maximum memory size on the Series 6000 is \_\_\_\_\_ K.



INSTRUCTION COUNTER

Fill in missing information is it would appear after the execution of each instruction in the order of execution.

<u>OCTAL LOCATION</u>		<u>INSTRUCTION</u>	<u>MEMORY OR REGISTER OCTAL CONTENTS AFTER EXECUTION</u>
000500	START	LDX5 5,DU LDX7 -7,DU LDX2 -7,DL LDA 2,DU	X5= _____ X7= _____ X2= _____ A= _____
000601	BETA	TRA ALPHA	IC= _____
000602		STC1 SAVE1	SAVE1= _____
000603		TSX3 TEST	X3= _____
000604		STC2 SAVE2	SAVE2= _____
000605		TRA J	IC= _____
000606		TRA *+1	IC= _____
000700	ALPHA	LDI RGR	IR= _____
000701		STI SAVE1	SAVE1= _____
000702		LDX2 RGR	X2= _____
000703		TRA BETA+1	IC= _____
000704		RGR OCT 000200404000	
000705		SAVE1 ZERO	
000706		TEST	
000707			
000710		TRA 0,3	IC= _____
000711	SAVE2	ZERO 2,5	
000712	J	RET SAVE2	IC= _____

TALLY OPERATIONS

1. Sixty characters are stored in ten words in memory beginning at location DATA. Store the characters, one character to a word, in right justified form with leading zeros in a sixty-word table beginning at location SPRED.
2. You are given a table of 100 one-word values stored in memory beginning at MAYBZ. Move all nonzero values to a table starting at NOZER. Put the number of nonzero values you have found into memory cell NOZER-1.
3. Write the octal representation of the value resulting from each of the following pseudo operations:

- |           |           |
|-----------|-----------|
| a. TALLY  | 1024,48   |
| b. TALLYD | 384,40,4  |
| c. TALLY  | 2048,18,3 |

MISCELLANEOUS OPERATIONS

1. TRANS contains a one digit BCD code in right justified form.  
Write the coding which will test the code for a range of 0 thru 9.  
If not 0 thru 9, branch to ERROR, If it is one of the digits 0 thru 9, branch to UPDATE.
2. Location BRANCH contains a TRA 0. If location CODE contains a 1, set the instruction at BRANCH to transfer to RETURN. If CODE contains a 3, set the instruction to transfer to CALC.
3. A one word field called CODE normally contains only positive BCD numeric characters. Check the field for the presence of non-numeric characters. If any are found, branch to ERROR.
4. Set the 800 words of memory beginning at location BUILD to zero.
5. Sum the 100 words stored in TABLE and store the sum in SIGMA.

GENERAL QUIZ

## SECTION I

This section of the quiz consists of True-False items, each of which has a value of one (1) point. Enter your answer for each item on this sheet, making sure that you check the correct line for the corresponding test item.

- \_\_\_\_\_ 1. The BLOCK pseudo-op may be used to assemble data and/or instructions into blank common.
- \_\_\_\_\_ 2. The TZE instruction will transfer and turn off the zero indicator if it is on.
- \_\_\_\_\_ 3. The expression (Y-pair) means any two sequential memory locations that begin at an even address.
- \_\_\_\_\_ 4. The contents of an index register may be incremented or decremented.
- \_\_\_\_\_ 5. LDX 3,4 will load X3 with contents of Location 4.
- \_\_\_\_\_ 6. LDX4 3,DL will load X4 with zeros.

## SECTION II

This section of the quiz consists of multiple choice items, each of which has a value of two (2) points. Each wrong answer will deduct two points from the total. Enter your answer in the A, B, C, or D column on this sheet, making sure that you check the correct line for the corresponding test item.

NOTE! There is only one (1) correct answer for each item in this section. Answer sheets which contain more than one answer per item will be considered to be wrong!

1. At assembly time--

LDA =MLDX1Ø5,DU

- \_\_\_\_\_ A. Will generate an entry in the literal pool.
- \_\_\_\_\_ B. The LDA is an instruction literal.
- \_\_\_\_\_ C. The LDA is a direct operand instruction.
- \_\_\_\_\_ D. Invalid coding.

2. LDA =MTRAPLOC,5

The above coding is an example of:

- A. Direct upper (DU) address modification.
  - B. An instruction literal.
  - C. An instruction literal, but the ",5" is illegal.
  - D. An illegal operand.
3. Given the following coding example, what will be the contents of the A and Q registers after execution of the LDAQ instruction?

000062	LDAQ	TABLE2
000063	TRA	OUT
000064	TABLE1	DEC 5
000065	DEC	6
000066	DEC	7
000067	TABLE2	DEC 1
000070	DEC	2
000071	DEC	3

- A. A = 6, Q = 7
  - B. A = 7, Q = 1
  - C. A = 1, Q = 2
  - D. A = 2, Q = 3
4. Indicate the coding which will store characters 2, 4, and 5 from the Q register to the same character positions of location X.
- A. STCQ X,13
  - B. STCQ X,26
  - C. STCQ X,31
  - D. None of the above.
5. TABLE OCT -43

The above line of coding will cause which of the following octal configurations to be generated?

- A. 100000000043
- B. 777777777735
- C. 777777777743
- D. 400000000043

6. LDA 5,DU

In this instruction, the number five is:

- A. Found in bits 18 - 35 of the instruction itself.
- B. Found in bits 0 - 17 of the instruction itself.
- C. Memory location five.
- D. Memory location five as modified by the DU directive.

7. LDA ,DU

The above instruction will cause which of the following octal contents to appear in bits 0 - 17 of the instruction?

- A. 000023
- B. 000000
- C. 235003
- D. 235007

8. STX2 0,DL

What, if anything, is wrong with this instruction?

- A. Nothing is wrong with this coding.
- B. The DL should be changed to a DU.
- C. The zero operand is illegal in slave mode.
- D. Direct type address modification is not allowed with a STX2.

### SECTION III

This section of the quiz consists of completion type items, each of which has a value of three (3) points. Each wrong answer will deduct three points from the total.

**NOTE!** Each answer must be entirely correct to receive credit. No partial credit will be given (1 or 2 points) or items which are "almost" correct.

All answers are brief, and should be entered in the space provided on this sheet.

1. TSX1 30113

Prior to the execution of the above instruction, the IC contains value 1122<sub>8</sub> and the 30113 is an octal number. What will the IC contain (in octal) after execution?



2. Referring to the information and conditions set forth in Item (Question) #1, what will be the octal contents of index register one after execution?
- 

3. Show the binary contents of a literal generated by: = 1.5B7
-

MACRO OPERATIONS

1. Write a MACRO procedure to:
  - a. Clear N words
  - b. Move N words from a given location to another given location.
  - c. Swap N words from two given locations.
  
2. Write an example for the CALL of each of the above examples.

## EFFECTIVE ADDRESS AND GFRC QUIZ

1. Give the effective addresses of the following instructions.

- |    |                            |       |
|----|----------------------------|-------|
| a. | LDA                        | C1,*  |
|    | C1 LDQ                     | B5,*3 |
|    | B5 LDX5                    | A,1   |
| b. | LDA                        | *+1,I |
|    | LDQ                        | *-1,* |
| c. | LDX1                       | B,1*  |
|    | B+C(X1) STX1               | A,*1  |
|    | A ADX1                     | F,*5  |
|    | F SBX1                     | 10,DU |
|    | 10+C(X <sub>5</sub> ) ADX1 | 15,DU |

2. Reverse the bits in the upper half of CHAR. That is

if  $\text{CHAR}_i = 1$  turn it off

if  $\text{CHAR}_i = 0$  turn it on

3. Show the coding to step through TAB until you find a zero word using index registers.

4. Define the following:

- a. File Control Block
- b. File Designator Word
- c. Buffer

## FILE AND RECORD CONTROL (GFRC) EXAMINATION

The following statements are either true or false. Write in the blank on the left a T for true or F for false.

- \_\_\_\_\_ 1. A file must be opened before it can be read from or written on.
- \_\_\_\_\_ 2. The user may open all his files with the use of one OPEN command.
- \_\_\_\_\_ 3. If the user tires to OPEN a file that is already open, he will be aborted.
- \_\_\_\_\_ 4. If the OPEN routine determines that a file has been assigned to SYSOUT, but the file control block for the file does not indicate standard systems format, the user will be aborted.
- \_\_\_\_\_ 5. After executing a GET, the current record index (word 0 of the file control block) contains the location of the first data word of the requested logical record.
- \_\_\_\_\_ 6. When executing a GET, the EOF exit will be taken any time that any file mark is found.
- \_\_\_\_\_ 7. The user can obtain the previous logical record processed by using the GETBK command.
- \_\_\_\_\_ 8. The PUT command must be used any time the user desires to move a logical record from the input buffer to working storage.
- \_\_\_\_\_ 9. The PUTBK command can be used if the programmer desires to put his logical record at the beginning of the next buffer, regardless whether or not the current buffer is full.
- \_\_\_\_\_ 10. CALL PRINT always requires a CALL IOEDIT to have been previously executed.
- \_\_\_\_\_ 11. Every time a CALL GET or CALL PUT is executed, the physical device (tape, for example) will be moved.
- \_\_\_\_\_ 12. All the information in a file control block is stored there by the user via the FILCB macro.
- \_\_\_\_\_ 13. File and Record Control (GFRC) subroutines are ~~loaded~~ into memory by General Loader (GLOAD).

- \_\_\_\_\_ 14. File and Record Control (GFRC) does not use GCOS.
- \_\_\_\_\_ 15. The subset of File and Record Control (GFRC) used by the user resides in a special area within.
- \_\_\_\_\_ 16. Standard magnetic tape labels are 14 words long.
- \_\_\_\_\_ 17. Random disc/drum files must be processed using logical record processing commands.
- \_\_\_\_\_ 18. In the standard system format a block serial number will exist as the first word of each block.
- \_\_\_\_\_ 19. When output is to be stacked on SYSOUT, the user must allow for two extra words in the buffer(s).
- \_\_\_\_\_ 20. The only type of records File and Record Control (GFRC) can handle are variable length records.
- \_\_\_\_\_ 21. One file control block must be provided for each file used in the program.
- \_\_\_\_\_ 22. Mixed density is not allowed within a magnetic tape file processed by File and Record Control (GFRC).
- \_\_\_\_\_ 23. The OPEN and CLOSE routines obtain their orders from file designator words.
- \_\_\_\_\_ 24. When buffers are used in conjunction with File and Record Control (GFRC) logical record processing commands, each buffer must be one word larger than the largest physical record it is to hold.
- \_\_\_\_\_ 25. When using the command READ, the user must provide his own list of control words.

To the left of each function listed in column "A" write the number of the item listed in column "B" that performs this function.

"A"	"B"
_____ Primary communication between the user program and File and Record Control (GFRC) subroutines	1. GETBK
_____ Provides information to the OPEN and CLOSE routines	2. One Word greater than physical record
_____ Whenever the logical record blocking/deblocking facilities of File and Record Control (GFRC) are used, the input/output buffers must meet this requirement	3. Standard Labels
_____ Used by GCOS to assign physical devices to the various files used by the program	4. File Control Blocks
_____ System macro-instruction used to define file control blocks	5. CALL
_____ Pseudo-operation commonly used to define file designator words	6. GET
_____ Used to initialize files so that they may be properly accessed by other File and Record Control (GFRC) functions	7. PUT
_____ Used to obtain the next logical input record from a designated input file	8. PUTBK
_____ Used to move a logical record from working storage into an output buffer	9. COPY
_____ To initiate reading of a physical record with the user providing the list of control words (DCW's)	10. READ
	11. File designator word
	12. File Control Cards
	13. FILCB
	14. OCT
	15. OPEN
	16. VFD
	17. RDREC

The following statements are followed by a series of choices, only one of which is correct. Read each item carefully and mark the correct answer.

1. The command used to initialize the edit functions, such as those used when printing or punching is:

\_\_\_\_\_ OPEN  
\_\_\_\_\_ IOEDIT  
\_\_\_\_\_ PRINT  
\_\_\_\_\_ PUNCH

2. The File and Record Control (GFRC) command with which the user can either allocate space within a buffer for a logical record and, if desired, move that logical record into the buffer is:

\_\_\_\_\_ GET  
\_\_\_\_\_ PUT  
\_\_\_\_\_ GETBK  
\_\_\_\_\_ PUTSZ

3. The pseudo-operation used in conjunction with all File and Record Control (GFRC) commands is:

\_\_\_\_\_ ERLK  
\_\_\_\_\_ IDRP  
\_\_\_\_\_ SAVE  
\_\_\_\_\_ CALL

4. The File and Record Control (GFRC) command used to disconnect files on which no further activity is to be performed is:

\_\_\_\_\_ CLOSE  
\_\_\_\_\_ PUTBK  
\_\_\_\_\_ RELSE  
\_\_\_\_\_ SETOUT

5. The File and Record Control (GFRC) command used to set a currently open file to an output file is:

\_\_\_\_\_ CLOSE  
\_\_\_\_\_ PUT  
\_\_\_\_\_ SETIN  
\_\_\_\_\_ PUTBK  
\_\_\_\_\_ SETOUT

6. The File and Record Control (GFRC) command that may be used to obtain the next logical input record from a file is:

\_\_\_\_\_ GET  
\_\_\_\_\_ PUT  
\_\_\_\_\_ CLOSE  
\_\_\_\_\_ RELSE

7. For disk and drum two types of files are provided:
- Variable- and fixed-length
  - File Control Block and File Designator Word
  - Linked and Random
  - Letter and Nail
8. In the standard system format data blocks (physical records) may be variable in length up to a maximum of:
- 320 bits
  - 321 words
  - 320 characters
  - 320 words
9. If in standard system format only one of the five media named below may use header and trailer labels. Indicate which.
- Card Reader
  - Magnetic Tape
  - Paper Tape
  - Disk/Drum
  - Card Punch
10. The maximum block (physical record) size allowed with File and Record Control (GFRC) is:
- 320 words
  - 1024 words
  - 4095 characters
  - 4095 words



1. Define the following terms:
  - a. Physical Record:
  - b. Logical Record:
  - c. File Control Block:
  - d. File Designator Word:
  - e. Buffer:
  - f. File:
2. What File and Record Control (GFRC) commands obtain information from the file designator words?
3. Why are there report codes and media codes in SYSOUT records?

4. Why must buffers that are used with logical records processing be one word larger than the largest physical record?
5. What are the types of records that can be handled by File and Record Control GFRC?
6. What are the characteristics of Standard System Format?
7. List the significant capabilities that are provided when Standard System Format is used.
8. What features does File and Record Control (GFRC) provide the user?

## SECTION I

## GMAP QUIZ 9

This section of the quiz consists of True-False items, each of which has a value of one (1) point. Enter your answer for each item on this sheet, making sure that you check the correct line for the corresponding test item.

- \_\_\_\_\_ 1. The following coding will convert one binary word to seven (7) BCD characters.

```

LDX1    0,DU
LDA     X      (X is some binary number)
RPT     6,1
BCD     PUB,1  (PUB is the start of the
               conversion table)

STQ     DATA
BCD     0,1
QLS     30
STQ     DATA+1

```

- \_\_\_\_\_ 2. The following is a valid literal expression: =12H JUMP HOME.

\_\_\_\_\_ 3. STCA LOC,40 will affect bit positions 18-23.

\_\_\_\_\_ 4. STCA LOC,04 will affect bit positions 18-23.

\_\_\_\_\_ 5. HERE TRA \*\*,5 will transfer to location HERE plus the value in X5.

\_\_\_\_\_ 6. LDX4 =1B18,DU will turn on bit zero in X4.

## SECTION II

This section of the quiz consists of multiple choice items, each of which has a value of two (2) points. Each wrong answer will deduct two points from the total. Enter your answer in the A, B, C, or D column on this sheet, making sure that you check the correct line for the corresponding test item.

**NOTE!** There is only one (1) correct answer for each item in this section. Answer sheets which contain more than one answer per item will be considered to be wrong!

1. What is meant by the expression =1B17?

- A. A one in bit position seventeen.
- B. A type of airplane.
- C. A one followed by seventeen zero bits.
- C. A one followed by seventeen zeros.

2. In a floating-point number in memory the binary point of the mantissa is:

- A. Following bit 0
- B. Following bit 7
- C. Following bit 8
- D. Following bit 35

3. LDA =.5B3,DU

The above line of coding will cause which of the following octal configurations to be generated?

- A. 500000235003
- B. 400000235003
- C. 200000235003
- D. 000000235003

4. We wish to move fifty words from table A to table B. The two tables were established as follows:

A	EBSS	50
B	EBSS	50

Which of the following coding examples (A or B) will do the job?

- |                             |      |      |                             |      |        |
|-----------------------------|------|------|-----------------------------|------|--------|
| <input type="checkbox"/> A. | LDX3 | A,DU | <input type="checkbox"/> B. | LDX7 | 50,DU  |
|                             | LDX4 | B,DU |                             | LDAQ | A-50,7 |
|                             | RPD  | 25,2 |                             | STAQ | B-50,7 |
|                             | LDAQ | 0,3  |                             | SBX7 | 2,DU   |
|                             | STAQ | 0,4  |                             | TNZ  | *-3    |

5. =32.7B7

This literal represents a/an:

- A. Floating point number
- B. Fixed point number
- C. Integer
- D. Octal literal

6. When using the DEC pseudo-op, what over-rides all other designations in determining whether the constant will be fixed or floating?

- A. The letter B
- B. The letter D
- C. The letter E
- D. A decimal point

7. A floating point number in memory has the following octal contents:  $000260000000_8$ . How many shift operations will be required to normalize this value?

- A. 9
- B. 6
- C. 3
- D. 1

### SECTION III

This section of the quiz consists of completion type items, each of which has a value of three (3) points. Each wrong answer will deduct three points from the total.

**NOTE!** Each answer must be entirely correct to receive credit. No partial credit will be given (1 or 2 points) or items which are "almost correct."

All answers are brief, and should be entered in the space provided on the following sheet.

1. How many times will the instruction at X be executed?

```
LDX2    0,DU
RPT     14,1,TMI
X       LDA    Z,2
      ---
      ---
      ---
Z       DEC    0,-1,-2,-3,-4,-5
```

2. The number  $013461000000_8$  is in core at location X. Show the octal contents of the E and A registers after execution of a FLD X.
3. Is the mantissa of the number in Item (Question) #2 positive or negative?



2. GOOD LDA ALPHA,\*N  
ALPHA LDQ BETA,\*5  
BETA CMPA STAR,DU

In the above program, the effective address of the LDA instruction is:

- A. ALPHA
- B. STAR+C(X5)
- C. BETA+C(X5)
- D. ALPHA+C(X5)

3. What is the effective address of the following coding sequence?

	LDA	W,*
W	---	X,6*
X+C(X6)	---	Y,7*
Y+C(X7)	---	Z

- A. W
- B. X+C(X6)
- C. Y+C(X7)
- D. Z

4. The instruction 000026735011<sub>8</sub> is contained in memory location BETA. Its representation on a symbolic coding form would be:

- A. BETA ALS 22,1
- B. BETA ALS 26,1
- C. BETA ALR 22,1\*
- D. BETA ALR 26,1\*

5. What is the maximum number of levels of indirection permitted?

- A. Less than 63
- B. 63
- C. 64
- D. Time limit consideration only.



6. LDQ PLACE,\*

The above coding example employs which type of address modification?

- \_\_\_\_\_ A. IR
- \_\_\_\_\_ B. RI
- \_\_\_\_\_ C. IT
- \_\_\_\_\_ D. CI

### SECTION III

This section of the quiz consists of completion type items, each of which has a value of three (3) points. Each wrong answer will deduct three points from the total.

NOTE! Each answer must be entirely correct to receive credit. No partial credit will be given (1 or 2 points) or items which are "almost" correct.

All answers are brief, and should be entered in the space provided on this sheet.

1. LDX3 0,1\*

Prior to the execution of the above instruction, the contents of the various registers and memory cells are as follows:

(ALL ARE IN OCTAL)

	X1	000177
	X3	002207
	177	001406000000
contents	307	001114000000
of core	1406	000307000000
locations	1760	000014000000
	2207	001760000000

What will be the octal contents of index register three after execution?

2. Referring to the information and conditions set forth in Item (Question) #1, what will be the octal contents of index register one be after execution?

```
3. DUMMY  MACRO
    K      SET      0
          IDRP     #2
    K      SET      K+1
          IDRP
          TRA      *+K
          ENDM     DUMMY
```

If we use the above MACRO with this calling sequence:

```
DUMMY  ABLE,(2,3,4,5,6,7),BAKER
```

What would be the actual value of K when the transfer instruction is built by the assembler?

GMAP EXAM

SECTION ONE

This section of the exam consists of twenty True-False items, each of which has a value of one (1) point. Each wrong answer will deduct one point from the total of twenty possible points for this section. Enter your answer for each item on the answer sheet provided, making sure that you check the correct line for the corresponding test item.

1. The slave prefix area can be accessed by a program in the slave mode.
2. The slave service area can be accessed by a program in the slave mode.
3. The BLOCK pseudo-op may be used to assemble data and/or instructions into blank common.
4. The TZE instruction will transfer and turn off the zero indicator if it is on.
5. The following coding will convert one binary word to seven (7) BCD characters.

LDX1	0,DU	
LDA	X	(X is some binary number)
RPT	6,1	
BCD	PUB,1	} (PUB is the start of the conversion table)
STQ	DATA	
BCD	PUB,1	
QLS	30	
STQ	DATA+1	

6. The following is a valid literal expression: =12H JUMP HOME.
7. If the letter Ø is coded in Column 7 of a source deck card, a NØP instruction may be inserted into the program by the assembler.
8. For the following coding the effective transfer address depends upon the contents of Index Register four (X4).

	TRA	TALLYX, I
	---	-----
	---	-----
TALLYX	TALLYC	ADDRS,12,*4

9. The tally run-out indicator is not affected by using IT type modification.
10. The instruction: LDAQ Y will not execute if Y is an odd address.
11. The expression (Y-pair) means any two sequential memory locations that begin at an even address.

12. The contents of an index register may be incremented or decremented.
13. STCA LOC,40 will affect bit positions 18-23.
14. STCA LOC,04 will affect bit positions 18-23.
15. STBA LOC,40 will affect bit positions 0-8.
16. STBA LOC,02 will affect bit positions 24-29.
17. HERE TRA \*\*,5 will transfer to location HERE plus the value in X5.
18. LDX 3,4 will load X3 with bits 0... 17 of location 4.
19. LDX4 3,DL will load X4 with zeros.
20. LDX4 =1B18,DU will put a bit in X4.

Go on to SECTION TWO of the test at this time.

GMAP EXAM

SECTION TWO

This section of the exam consists of twenty-five multiple choice items, each of which has a value of two (2) points. Each wrong answer will deduct two points from the total of fifty possible points for this section. Enter your answer in the A, B, C, or D column on the answer sheet, making sure that you check the correct line for the corresponding test item.

NOTE! There is only one (1) correct answer for each item in this section. Answer sheets which contain more than one answer per item will be considered to be wrong!

21. Which of the following coding examples represents IR type modification?

- A. ADR,N\*
- B. ADR,2\*
- C. ADR,\*
- D. ADR,\*N

22. What is meant by the expression =1B17?

- A. a one in bit position seventeen.
- B. a type of airplane.
- C. a one followed by seventeen zero bits.
- D. a one followed by seventeen zeros.

23. Given the following MACRO prototype:

```
MAC    MACRO
      LDA    #1
      ADA    #2
      STA    #3
      ENDM   MAC
```

Assuming that all symbols have been defined, which of the following examples contains improper coding to use the above MACRO?

- A. PLEASE MAC =20DL,=30DL,CAT
- B. GO MAC (ABLE,4),CAT,DOG
- C. GET MAC GOOD,BAD,INDIFF
- D. HELP MAC (,4),=45,UNABLE

24. At assembly time--

```
LDA    =MLDX1#5,DU
```

- A. will generate an entry in the literal pool.
- B. The LDA is an instruction literal.
- C. The LDA is a direct operand instruction.
- D. Invalid coding.

25. In a floating-point number in memory the binary point of the mantissa is:
- A. following bit 0
  - B. following bit 7
  - C. following bit 8
  - D. following bit 35

26. The following coding is used to go to a subroutine called "DØIT" with the indicated normal return:

```
TSX1  DØIT
ARG   ALPHA
ARG   BETA
STA   BØØL          (this is the normal return)
```

The correct coding at the end of subroutine DØIT which will return us back to the normal return is:

- A. TRA BETA,1
  - B. TRA 2,1
  - C. TRA BETA+1
  - D. TRA TSX1+3
27. LDA =MTRAYLOC,5

The above coding is an example of:

- A. Direct upper (DU) address modification.
- B. An instruction literal.
- C. An instruction literal, but the ",5" is illegal.
- D. An illegal operand.

28. The decimal value of the following floating point number contained in a core dump is:

770700000000<sub>8</sub>

- A. 5.4687
- B. 0.54687
- C. 0.054687
- D. 0.0054687

29. LDA =.5B3,DU

The above line of coding will cause which of the following octal configurations to be generated?

- A. 500000235003
- B. 400000235003
- C. 200000235003
- D. 000000235003

30. In a floating-point literal, why would you use a D instead of an E or a decimal point?

- A. To indicate that this is a decimal literal.
- B. To cause a literal table dump if the program aborts.
- C. To obtain a double-precision number.
- D. To assemble the literal directly into the operand of the instruction.

31. GOOD LDA ALPHA,\*N  
 ALPHA LDQ BETA,\*5  
 BETA CMPA STAR,DU

In the above program, the effective address of the LDA instruction is:

- A. ALPHA
- B. STAR+C(X5)
- C. BETA+C(X5)
- D. ALPHA+C(X5)

32. What is the effective address of the following coding sequence?

```

      LDA    W,*
W     ---   X,6*
X+C(X6)--- Y,7*
Y+C(X7)--- Z

```

- A. W
- B. X+C(X6)
- C. Y+C(X7)
- D. Z

33. The instruction 000026735011<sub>8</sub> is contained in memory location BETA. Its representation on a symbolic coding form would be:

- A. BETA ALS 22,1
- B. BETA ALS 26,1
- C. BETA ALR 22,1\*
- D. BETA ALR 26,1\*

34. Given the following coding example, what will be the contents of the A and Q registers after execution of the LDAQ instruction?

```

000062                LDAQ  TABLE2
000063                TRA   OUT
000064                TABLE1 DEC 5
000065                DEC   6
000066                DEC   7
000067                TABLE2 DEC 1
000070                DEC   2
000071                DEC   3

```

- A. A = 6, Q = 7
- B. A = 7, Q = 1
- C. A = 1, Q = 2
- D. A = 2, Q = 3

35. Indicate the coding which will store characters 2, 4, and 5 from the Q register to the same character positions of location X.

- A. STCQ X,13
- B. STCQ X,26
- C. STCQ X,31
- D. None of the above.

36. We wish to move fifty words from table A to table B. The two tables were established as follows:

A	EBSS	50
B	EBSS	50

Which of the following coding examples (A or B) will do the job?

A.	LDX3	A,DU	B.	LDX7	50,DU
	LDX4	B,DU		LDAQ	A-50,7
	RPD	25,2		STAQ	B-50,7
	LDAQ	0,3		SBX7	2,DU
	STAQ	0,4		TNZ	*-3

37. TABLE OCT -43

The above line of coding will cause which of the following octal configurations to be generated?

A.	100000000043
B.	777777777735
C.	777777777743
D.	400000000043

38. LDA 5,DU

In this instruction, the number five is:

A.	found in bits 18 - 35 of the instruction itself.
B.	found in bits 0 - 17 of the instruction itself.
C.	memory location five.
D.	memory location five as modified by the DU directive.

39. LDA ,DU

The above instruction will cause which of the following octal contents to appear in bits 0 - 17 of the instruction?

A.	000023
B.	000000
C.	235003
D.	235007

40. STX2 0,DL

What, if anything, is wrong with this instruction?

A.	Nothing is wrong with this coding.
B.	The DL should be changed to a DU.
C.	The zero operand is illegal in slave mode.
D.	Direct type address modification is not allowed with a STX2.



41. What is the maximum number of levels of indirection permitted?

- A. Less than 63
- B. 63
- C. 64
- D. Time limit consideration only.

42. =32.7B7

This literal represents a/an:

- A. floating point number
- B. fixed point number
- C. integer
- D. octal literal

43. When using the DEC pseudo-op, what over-rides all other designations in determining whether the constant will be fixed or floating?

- A. The letter B
- B. The letter D
- C. The letter E
- D. A decimal point

44. A floating point number in memory has the following octal contents: 000260000000<sub>8</sub> How many shift operations will be required to normalize this value?

- A. 9
- B. 6
- C. 3
- D. 1

45. LDQ PLACE,\*

The above coding example employs which type of address modification?

- A. IR
- B. RI
- C. IT
- D. CI

Go on to SECTION THREE of the test at this time

GMAP EXAM

SECTION THREE

This section of the exam consists of ten completion type items, each of which has a value of three (3) points. Each wrong answer will deduct three points from the total of thirty possible points for this section.

NOTE! Each answer must be entirely correct to receive credit. No partial credit will be given (1 or 2 points) for items which are "almost" correct.

All answers are brief, and should be entered in the space provided on the answer sheet.

46. TSX1 30113

Prior to the execution of the above instruction, the IC contains the value 1122<sub>8</sub> and the 30113 is an octal number. What will the IC contain (in octal) after execution?

47. Referring to the information and conditions set forth in Item (Question) #46, what will be the octal contents of index register one after execution?

48. How many times will the instruction at X be executed?

```

LDX2 0,DU
RPT 14,1,TMI
X LDA Z,2
---
---
---
Z DEC 0,-1,-2,-3,-4,-5
    
```

49. The number 013461000000<sub>8</sub> is in core at location X. Show the octal contents of the E and A registers after execution of a FLD X.

50. Is the mantissa of the number in Item (Question) #49 positive or negative?

51. LDX3 0,1\*

Prior to the execution of the above instruction, the contents of the various registers and memory cells are as follows:

(ALL ARE IN OCTAL)

	X1	000177
	X3	002207
contents of core locations	{	177 001406000000
		307 001114000000
		1406 000307000000
		1760 000014000000
		2207 001760000000

What will be the octal contents of index register three after execution?

52. Referring to the information and conditions set forth in Item (Question) #51, what will be the octal contents of index register one be after execution?

53. DUMMY MACRO  
 K SET 0  
 IDRП #2  
 K SET K+1  
 IDRП  
 TRA \*+K  
 ENDM DUMMY

If we use the above MACRO with this calling sequence:

DUMMY ABLE, (2,3,4,5,6,7), BAKER

What would be the actual value of K when the transfer instruction is built by the assembler:

54. Show the binary contents of a literal generated by: =1.5B7
55. Show the octal equivalent of the number generated in Item (Question) #54.

BEFORE TURNING IN YOUR ANSWER SHEET:

1. Be sure that your name and class number appear on the sheet.
2. Be sure that you have not forgotten to answer any items on the exam.

Please retain this exam question sheet for use during the review.

There are a total of 100 points available on this exam. It should be mentioned that your performance in this course, however, is not determined solely from the numerical grade achieved on this exam. The grade is used, along with several other factors, to contribute to a detailed personal evaluation of your work in this course.

GMAP EXAM ANSWER SHEET

Student's Name \_\_\_\_\_

Student's Class Number \_\_\_\_\_

SECTION ONE

ITEM	TRUE	FALSE
1		
2		
#		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		

SECTION TWO

ITEM	A	B	C	D
21				
22				
23				
24				
25				
26				
27				
28				
29				
30				
31				
32				
33				
34				
35				
36				
37				
38				
39				
40				
41				
42				
43				
44				
45				

SECTION THREE

ITEM	ANSWER
46	_____
	IC
47	_____
	X1
48	_____
49	_____
	E                      A
50	_____
51	_____
	X3
52	_____
	X1
53	_____
54	_____
	(binary)
55	_____
	(octal)

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