

# 3100 CLASS SCHEDULE

MONDAY

8:30 TO 4:30 TOLLAND

3100 OVERVIEW AND CPU THEORY

TUESDAY

NOON TO 5:00 TOLLAND

INTERPOLATOR THEORY

6:00 TO 9:00 MAIN PLANT

OPERATIONS

WEDNESDAY

NOON TO 4:00 TOLLAND

DEH INTERFACE, PARALLEL,  
POWER SUPPLY, MTA

5:00 TO 9:00 MAIN PLANT

DIAGNOSTICS

THURSDAY

NOON TO 4:00 TOLLAND

INSTRUCTION SET / GSI BUG

5:00 TO 9:00 MAIN PLANT

GSI BUG

FRIDAY

8:00 TO END TOLLAND

SASI, XEBEC - TEST

SWITCHES AND JUMPERS  
DEFINITIONS AND SETTINGS

---

CPU BOARD

Momentary: Abort switch (red) - program defined  
Reset switch (black) - hardwired

Dip Switches: Used to run on board diagnostics.  
(bank of 8) All 8 switches are program defined.

	<u>ON</u>	<u>OFF</u>
Switch 1	Boot from floppy at program load time	Start selected diagnostic
	OR	
	To exit a running diagnostic	

Switches 2 thru 8 Diagnostic Program Defined

INTERPOLATOR

IC's 73 and 75 (DIP switches - 2 banks of 8)	Software configu- ration switches
IC 76 (DIP switches - 1 bank of 2)	Motor Pulse Width Switches OFF=narrow pulse ON=wider pulse
IC 8 (DIP switches - 1 bank of 4)	Interpolator selection  All off=Board 0

SASI BOARD

Jumpers 1 thru 8 are located near IC11.

Jumper	2	>	Base Address
	4		
	6		Interrupt request level 3

XEBEC FLOPPY CONTROLLER

Jumper 8 to 1

MAG. TAPE CONTROLLER

Jumper 7 to 8 on J2	Interrupt Request Level 1	
Jumper 1 to 2 on J3	>	Base Address
3 to 4		
5 to 6		
7 to 8		

VIEWPOINT TERMINAL

Switch S1: 3,5,7,8 are up

PARALLEL INTERFACE

Jumper:

3 on JP1
2,3,4 on JP2

# CPU RAM MEMORY LAYOUT

IC 95	ADDRESS RANGE	IC 67
BET 15	20000 - 3FFFF	BET 8

IC 94	ADDRESS RANGE	IC 66
BET 15	0 - 1FFFF	BET 8

IC 93	ADDRESS RANGE	IC 65
BET 7	20000 - 3FFFF	BET 0

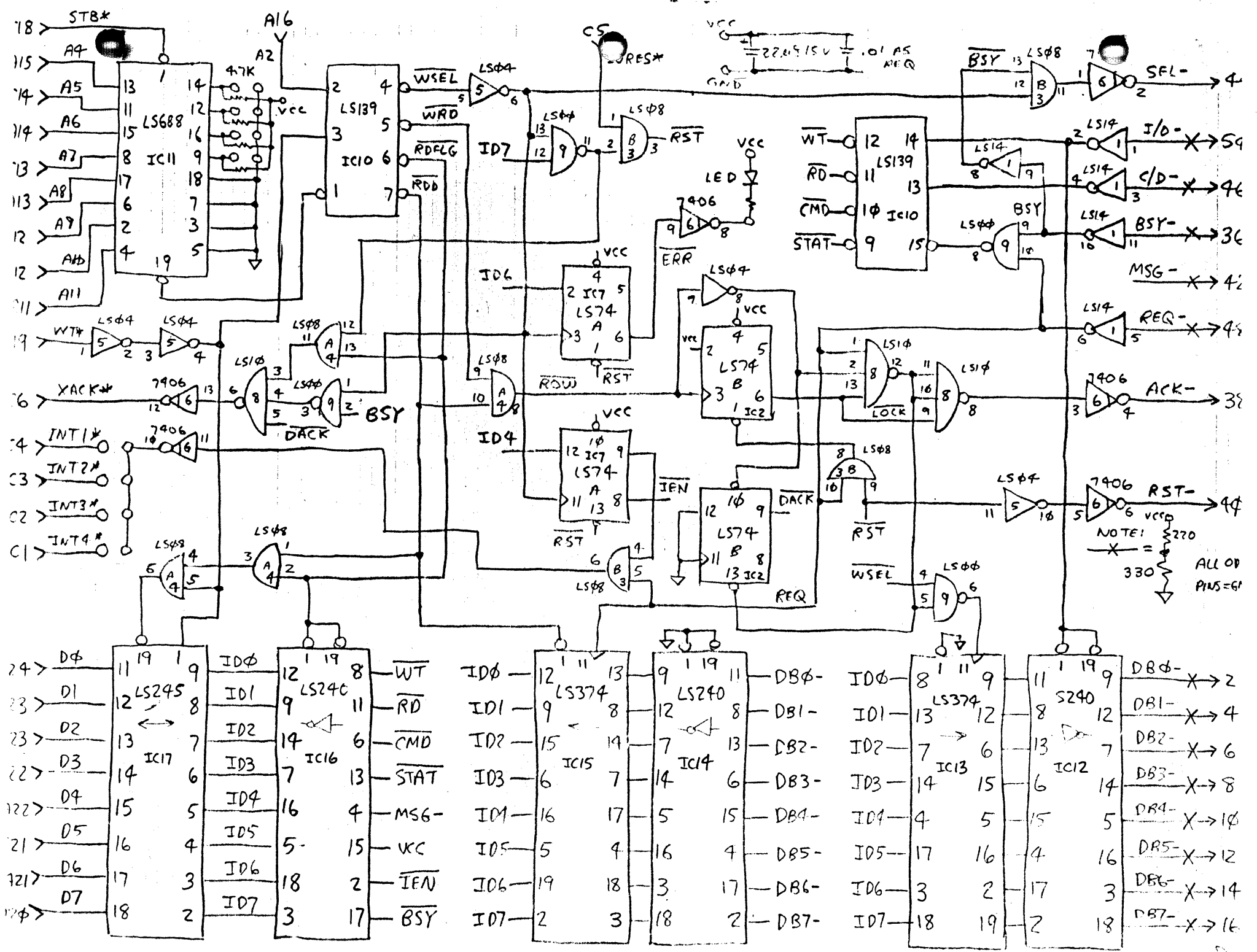
IC 92	ADDRESS RANGE	IC 64
BET 7	0 - 1FFFF	BET 0

↓  
FRONT

### 3100 Control PC Boards

August 30, 1983

<p><b>Standard</b></p>	<ul style="list-style-type: none"> <li>- GNC-1 CPU</li> <li>- GEC-1 INTRP</li> <li>- GTP-206 Backplane</li> <li>- GTP-215 CTL Panel</li> <li>- XEBEC S-1401 Floppy</li> <li>- GVM-1 (OWL) SASI (Floppy)</li> </ul>	<ul style="list-style-type: none"> <li>Released <span style="border: 1px solid black; border-radius: 50%; padding: 2px;">3</span></li> <li>Rel. 9/9</li> <li>Released</li> <li>Released <span style="border: 1px solid black; border-radius: 50%; padding: 2px;">5</span></li> <li>Purch.</li> <li>Rel. 9/2</li> </ul>
<p><b>Variable</b> (One required)</p>	<ul style="list-style-type: none"> <li>- GTP-222 OEHI &amp; OEHC</li> <li><i>To Be Done</i> - GTP-2xx OEHF</li> <li><i>OPT team</i> - GTP-2xx 35</li> <li>- GTP-2xx <i>DECentral</i> Film Cutter</li> </ul>	<ul style="list-style-type: none"> <li>Rel. 9/2 <i>RELEASED 9/1</i></li> <li>Rel. -</li> <li>Rel. -</li> <li>Rel. -</li> </ul>
<p><b>Options</b></p>	<ul style="list-style-type: none"> <li>- Motorola MVME435 Mag Tape</li> <li>- OWL GVM-2 Parallel</li> <li>- GTP-2xx <i>NEXT</i> Quick Look</li> <li>- Motorola MVME410 Dual 16 bit</li> <li>- GTP-2xx IBM Series 1</li> </ul>	<ul style="list-style-type: none"> <li>Stock</li> <li>Rel. - <i>NOT RELEASED</i> Protos 6/24</li> <li>Rel. -</li> <li>Purch. Used w/PPT and Series 1</li> <li>Rel. - Mounted on connector panel</li> </ul>



GND - A1 → A10, A7, 18, 19, 24, 25, 31, 32, C11, 20, 25, 31, 32

VCC - B29, 30, B29, 30

VME SAST VCC BOARD

3X 3  
1/16

NOTE!  
X = 330  
ALL OP PINS = GND

## IC IDENTIFICATION

### CPU

74LS138 - 1 of 8 Binary Decoder  
74LS148 - Priority Encoder (Binary Out)  
74LS155 - Dual 1 to 4 Line Demultiplexers  
74LS161 - Synchronous 4 Bit Counter  
74LS164 - 8 Bit Parallel-out Shift Register  
74LS175 - Quad D-Type Flip-Flops w/Common Clock and Clear  
74LS191 - Synchronous Up/Down Counters  
74LS244 - Octal Tri-State Line Drivers  
74LS245 - Octal Tri-State Bus Transceiver  
74LS367 - Six Tri-State Buffers  
74LS368 - Six Tri-State Inverting Buffers  
74LS373 - Octal Tri-State D-Type Transparent Latches (Register)  
555 - Timer

### INTERPOLATOR

7442 - BCD to Decimal Decoder  
7497 - 6 Bit Binary Rate Multipliers (BRM)  
74LS123 - One Shot  
74LS136 - Quad 2 Input Exclusive-OR w/open Collector Outputs  
74LS138 - 1 of 8 Binary Decoder  
74LS139 - Dual 2 to 4 Line Decoder  
74LS157 - Quad 2 to 1 Line Data Selectors/Multiplexers  
74LS161 - Synchronous 4 Bit Counter  
74LS175 - Quad D-Type Flip-Flops  
74LS191 - Up/Down Counter  
74LS240 - Octal Tri-State Buffers  
74LS273 - Octal D-Type Latches  
74LS283 - 4 Bit Binary Adder w/Look Ahead Carry  
74LS393 - Dual 4 Bit Binary Counter w/Individual Clocks  
  
14490 - Hex Switch Debounce Circuits  
26LS31 - Quad Differential Drivers

INTERRUPT AUTO VECTOR

Level

7	Abort Switch
6	DMA
5	Plotter Port
4	Duart
3	I/O Channel 1 (TAPE)
2	I/O Channel 2 (PARALLEL, PAPER TAPE)
1	I/O Channel 3 (FLOPPY)

MEMORY MAP

<u>Address Range</u>	<u>Assigned to</u>
00000 thru 3FFFF	RAM Memory (256KB)
40000 thru 7FFFF	Peripherals
80000 thru BFFFF	EPROM (16 Bit, Only 16KB is used)

PERIPHERAL BASE ADDRESSES

40000 thru 41FFF	I/O Channel (D0-D7) 4KB on Byte Boundary
40000	Mag Tape
40060	Floppy
40100	Parallel (16 Bit)
42000 thru 43FFF	I/O Channel (Redundant)
44000 thru 47FFF	DMA Port (D0-D15)
48000 thru 4BFFF	Duart Port (D0-D7)
4C000 thru 4EFFF	Plotter Port (D0-D15)
50000 thru 53FFF	Diagnostic Port (D0-D7)

### DMA PORT

<u>R/W to Address</u>	<u>Control Signal</u>	<u>Data Bits</u>	<u>Function</u>
W to \$44000	$\overline{\text{LPS}}$ -Load Port Select (Interpolator Board selection)	D0,D1,D2  D3  D4-D15	Selects 1 of 8 ports for DMA transfer. Controls A17 during DMA transfer. DMA buffer is lower (0) or upper (1) 128KB of RAM. N/A
W to \$44002	$\overline{\text{LBP}}$ Load Buffer Pointer	D0-D15	Correspond to A1→A16 Pointer can be set to any even address in RAM above \$4000. Area below \$4000 is hardware inhibited.
W to \$44004	$\overline{\text{LBC}}$ Load Buffer Counter	D0-D15	Correspond to the number of single word transfers. (64KW maximum)
W to \$44006	$\overline{\text{STARTD}}$ Enables DMA Transfer	D0-D15	N/A
R to \$44004	$\overline{\text{RBC}}$ Read Buffer Counter	D0-D15	Can be read <u>any time</u> . Reading clears DMAIRQ.

During DMA interrupt service, the buffer counter must be read to determine transfer completion (zero count) or a fault condition (non-zero).

DMA will be disabled on each  $\overline{\text{DMAIRQ}}$ .  $\overline{\text{DMAIRQ}}$  is generated on the completion or fault. A fault condition occurs during an access of RAM below \$4000 or trying to access non existing RAM. In both cases DMA will time out.



WRITING TO PLOTTER PORT

|A10-A13 Contain Port Select Code|

<u>R/W to Address</u>	<u>Control Signal</u>	<u>Data Bits</u>	<u>Function</u>
W to 4CXX0	$\overline{\text{LDM}}$ Load M Register	D0-D15	See Next Page
W to 4CXX2	$\overline{\text{LDR}}$ Load R Register	D0-D15	Preset to 1/2 Ratio
W to 4CXX4	$\overline{\text{LDD}}$ Load D Register	D0-D15	Distance Word
W to 4CXX6	$\overline{\text{LDC1}}$ Load Control 1	D0=1 D4=1 D5=1 D6 D7=1 Other Bits	Clears Start/Stop Status Bit 0, non latched Turns error LED on, latched Turns start LED on, latched Not used Enables DMA transfer to selected device N/A
W to 4CXX8	$\overline{\text{LDC2}}$ Load Control 2	D0-D7 D8-D15	Motor Pulse Clock Value Halt Clock Value
W to 4CXXA	Generates Clear	N/A	Write to this address clears device
W to 4CXXC W to 4CXXE	Reserved to control special photohead functions.		

M WORD FOR 3100

15	14	13	12	11	10	9	8	7	-	0
NMV (Delay)	Tool Select	2WD. Xfer	MME (45°)	PUD	MJA	B Sign	A Sign	Velocity		

Ratio is sent on a 3 word transfer; this word is used until the next 3 word transfer is given.

If Bit 13 is set, this is a 2 word transfer (M,D) otherwise, transfer 3 words (M,R,D).

If Bit 14 is set, load tool registers with M0-M7; decode M8,M9 for tool select.

<u>M9</u>	<u>M8</u>	
0	0	- Pen Select
0	1	- Aperture Select
1	0	- Reserved
1	1	- Reserved

<u>Pen Selection</u>																													
<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Velocity</td> <td style="text-align: center;">0-</td> </tr> <tr> <td style="text-align: center;"><u>Bits</u></td> <td style="text-align: center;">1-Pen 1</td> </tr> <tr> <td style="text-align: center;">M2,M1,M0</td> <td style="text-align: center;">2-Pen 2</td> </tr> <tr> <td></td> <td style="text-align: center;">3-Pen 3</td> </tr> <tr> <td></td> <td style="text-align: center;">4-Pen 4</td> </tr> <tr> <td></td> <td style="text-align: center;">5-Pen 5</td> </tr> <tr> <td></td> <td style="text-align: center;">6-Pen 6</td> </tr> <tr> <td></td> <td style="text-align: center;">7-SHTOP</td> </tr> </table>	Velocity	0-	<u>Bits</u>	1-Pen 1	M2,M1,M0	2-Pen 2		3-Pen 3		4-Pen 4		5-Pen 5		6-Pen 6		7-SHTOP		<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Velocity</td> <td></td> </tr> <tr> <td style="text-align: center;"><u>Bits</u></td> <td></td> </tr> <tr> <td style="text-align: center;">M3,M2</td> <td rowspan="2" style="font-size: 2em; vertical-align: middle;">}</td> </tr> <tr> <td style="text-align: center;">M1,M0</td> </tr> <tr> <td></td> <td style="text-align: center;">0 thru 12 - CP0 thru CP11</td> </tr> <tr> <td></td> <td style="text-align: center;">M4 = Side Select</td> </tr> </table>	Velocity		<u>Bits</u>		M3,M2	}	M1,M0		0 thru 12 - CP0 thru CP11		M4 = Side Select
Velocity	0-																												
<u>Bits</u>	1-Pen 1																												
M2,M1,M0	2-Pen 2																												
	3-Pen 3																												
	4-Pen 4																												
	5-Pen 5																												
	6-Pen 6																												
	7-SHTOP																												
Velocity																													
<u>Bits</u>																													
M3,M2	}																												
M1,M0																													
	0 thru 12 - CP0 thru CP11																												
	M4 = Side Select																												

Bit 15 (NMV) means no move. Setting this Bit will disable motor pulses. Executes delay based on velocity and distance bits value.

Bit 12 (MME) means min/max equal. Setting this Bit will generate a 45° line.

READING FROM PLOTTER PORT

<u>R/W to Address</u>				<u>Control Signal</u>				<u>Data Bits</u>				<u>Function</u>			
R to 4CXX0				$\overline{\text{RDW1}}$				D0-D15				Read Status Word			
				Read Word 1											
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
+X Slew	-X Slew	+Y Slew	-Y Slew	Plotter on X And Y Limit Switches	-	-	-	Buffer Is Empty	-	No Photo Head Mode	-	-	ESP Fault	Lamp Fault	Start/Stop Switch

Condition is true when specified Bit=1.

Generates IRQ

R to 4CXX2                       $\overline{\text{RDW2}}$                       D0-D15                      Read Configuration Switches (see next page)

Read Word 2

R to 4CXX4                       $\overline{\text{RDW3}}$                       D0-D15                      Read Status Word and clear IRQ

Read Word 3

R to 4CXX6  
R to 4CXX8  
R to 4CXXA

} Reserved for OEH status.

Note: A10 thru A13 contain port select code for systems containing multiple interpolators (ports).

OEH I MODULE

Module has redundant 6 Bit M Register.

D0-D4 For aperture select  
D11 - To detect pen down move

Bits 14 and 9 = 1, and Bit 8 = 0 will load module M register when XFB=1.

After aperture select has been initiated and the attempt to open the shutter follows it (M11=1), the circuit generates wait signal to interpolator. This prohibits the transfer and execution of the next command set (M,R,D) until aperture select is done. However, if the shutter is not going to be opened, the plotter can be moved while aperture select is in progress.

### 3100 POWER UP SEQUENCE

(Applies to proms marked GSI bug 05 only)

After the reset or program load switch is depressed, the bank of 8 DIP switches on the CPU board are read in.

Then:

- A) If switches 2,3,6,7 are on and 1,4,5,8 are off, we will enter "GSIBUG".
- B) If switch 1 is off and all the rest are on, the "Extended Memory Test" will begin to run.

Note: See "Memtest" description for operating instructions.

- C) Any other switch setting will cause:

1st step A "Power up memory test" to run for 7 seconds (approx.)  
When the test starts, the start LED on the control box will turn on. If the test completes successfully, the start LED will be turned off. If an error is detected, the error LED on the control box will turn on and an error message will be displayed in the LEDs on the CPU board. (See "Memtest" description for details.) Then the program will stop (loop).

2nd step If the memory test runs successfully, then the "boot" program will issue the internal and RAM diagnostic commands to the floppy controller before attempting to read from the disc. If these two commands or any other commands do not complete successfully, the controller error code will be displayed in LEDs on the CPU board, the error LEDs on the "SASI" board and the control box will turn on. Then the "boot" program will start from the beginning again (keep trying).

#### FATAL ERROR

A fatal error in the "boot" program occurs when we get an error during the "request sense status" command. We issue this command after an error is detected during another command to get the error code. A fatal error will turn on the "SASI" board and control box error LEDs and then the program will stop. At this point, the only option is to depress reset or program load to start the power up sequence from the beginning.

3rd step The "boot" program reads in our software starting at track 1 and sector 1. If an error occurs during this process it will be handled as described above in step 2.

## Xebec Controller (S-1401C) Error Codes

The following are the error codes and descriptions that apply when this controller drives the Tandon TM-100 5¼ inch floppy drive on the 3100.

If an error occurs while booting, the error code will be displayed in the LEDs on the CPU board, provided a fatal error does not occur (see first page).

EXAMPLE: Refer to last page.

### HEX CODE

14      The most significant digit, 1, will be displayed on LED bank 3 in binary. LED #5 is the LSB and LED #8 is the MSB.

         The least significant digit, 4, will be displayed on LED bank 2 in binary. LED #1 is the LSB and LED #4 is the MSB.

### HEX CODE      DEFINITION

00	The controller detected no error during the execution of the previous operation.
06	After stepping maximum number of cylinders, controller did not received track 00 signal from drive.
10	An error was detected in the CRC of the target ID zone.
11	Write fault.
12	A write was attempted on a write protected floppy diskette.
14	Sector Not Found: The controller found the correct cylinder and head, but not the target sector.
15	Seek Error: The controller detected an incorrect cylinder or track, or both.
16	Format track timeout error.
17	Format track not complete.
19	Two sided error.
1A	Wrong data mark found.
1D	Lost data in FDC.
1E	Data CRC error detected.

HEX CODE    DEFINITION

1F	FDC busy error. The LSI FDC chip was busy executing a command. This is a timeout or chip reset failure.
20	Invalid Command: The controller has received an invalid command from the host.
21	Illegal Disk Address. The controller detected an address that is not within the limits of the drive characteristics.
22	Something in the 8 bytes of data which specify the drive is not permissible.
23	Invalid interleave.
30	RAM Error: The controller detected a data error during the RAM sector-buffer diagnostic.
31	Program Memory Checksum Error: During its internal diagnostic, the controller detected a program-memory checksum error.

## MEMTEST

## Overview

This is the memory test for the RAM (currently 256KB) on the 3100 CPU board. This program resides in prom on the CPU board and uses RAM ONLY to output error messages to the CRT. ERROR MESSAGES OUTPUT TO THE CRT USE RAM FOR A PRINT BUFFER. THE AREA OF RAM USED IS BELOW ADDRESS \$800. THEREFORE, IF LOWER MEMORY (\$0-\$20000) IS "DEAD", OUTPUTTING AN ERROR MESSAGE TO THE CRT WON'T WORK. IN THIS INSTANCE, THE ERROR CODE DISPLAYED IN THE LEDS ON THE CPU BOARD MUST BE USED.

During one complete pass, this program writes and verifies 4 different data patterns, and verifies RAM addressing twice. One pass takes about 25 seconds.

A portion of "Ramtest" is also run on initial power up. Locations \$0000 thru \$3FFFE are tested by writing and verifying test pattern \$0000, then one pass of the address test is run. If successful, the start LED on the control box will turn on when the test is started and off when it ends, then the system will boot. Upon detecting an error, the control box error LED will light and if switch 2 is on, an error message will be output to the CRT. REGARDLESS OF SWITCH 2'S POSITION, AN ERROR CODE WILL BE DISPLAYED IN THE LEDS ON THE CPU BOARD.

NOTE: The power up ramtest takes about 6 seconds start to finish.

## Datatest

This test is specifically designed to test 64K RAM chips. The program writes the test pattern to all of memory first then goes back and compares what was written to what it reads. This sequence is done to force refreshes between the write and read portions of the test. The four test patterns used are:

\$0000  
\$FFFF  
\$5555  
\$AAAA

The last two test patterns will be complimented before being written to a memory location (except for the first location and every 257th location). This results in a true checkerboard pattern within a RAM array of 256 by 256.



## Address Test

A "march pattern" test is run immediately after each of the first two test pattern data tests: \$0000 and \$FFFF.

The "march pattern" test "walks" A 1 or A 0 bit thru a RAM chip array previously filled with bits of the opposite polarity, insuring along the way that every location in the array answers to it's own address only.

-----  
CPU Board Switch Assignments

Switch 1    OFF    Starts extended memory test.  
              ON    Stops extended memory test.

Switch 2    ON    Error messages output to CRT as well as LEDS.  
              OFF    Error messages output to LEDS only and stop testing.

Switch 8 has meaning only when error messages are being output to the CRT (switch 2 "on").

Switch 8    ON    Halt on single bit error and wait for start switch before testing next location.  
              OFF    Display errors but do not halt.

-----  
CPU Board LEDS

5-8    ON    = Error in upper 128KB  
5-8    OFF    = Error in lower 128KB

1-4 = Failing bit in binary  
      LED 1 is the lsb  
      LED 4 is the msb

(see last page)

## Control Box LEDS

Start = turns on when testing begins  
          turns off when testing ends

Error = turns on when error detected

-----  
CONTROL BOX SWITCHES

The start switch is used after an error has been detected to restart testing at the failing location plus 2 only when switch 2 is "on" (outputting error messages to CRT).

## OPERATING INSTRUCTIONS

1. Set CPU board switch 1 "off" to start the extended test.
2. Set CPU board switch 8 and 2 to desired position.
3. Depress program load switch. If the test is running, the control box start LED should be lit.

When you want to stop the test, turn switch 1 "on", the test will then complete the current pass, turn off the start LED and stop.

## Displaying Errors

If switch 2 is off:

Upon detecting an error, the program will turn on the control box error LED and display the failing bit and address range in the CPU Leds as described above. If more than one bit is found to be failing, the first failing bit will be displayed and the display will then flash on and off. In either case, testing will stop.

If switch 2 is on:

Error messages will be output to the CRT in the following format.

Failed at: (address) Wrote: (test pattern) Read:(data read back)

If switch 8 and 2 are "on":

The start switch will cause testing to resume at the failing address +2.

If switch 8 is "off" and switch 2 is "on":

The error will be displayed and testing will resume at the failing address +2.

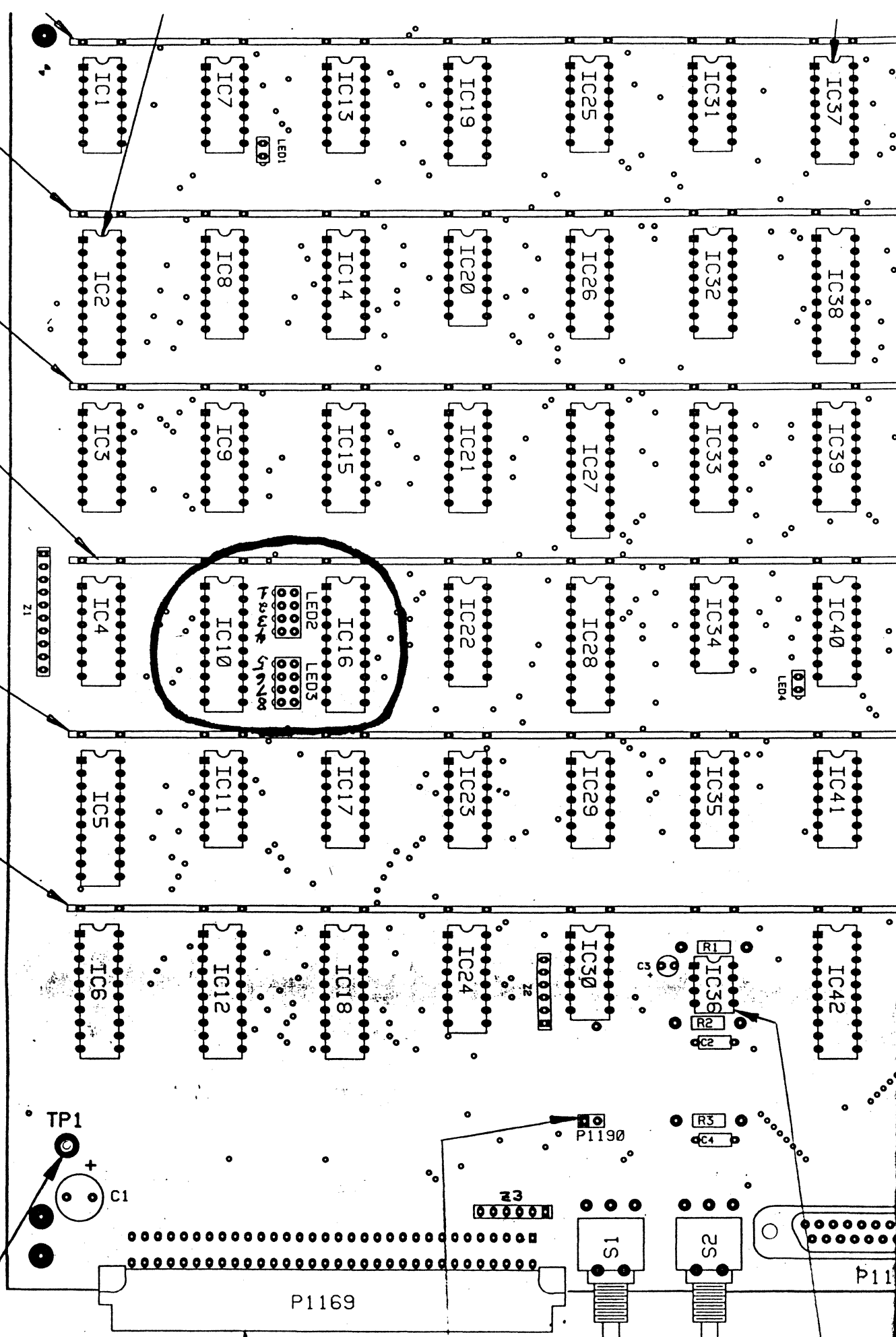
REF (58)

REF (58)

REF (58)

REF (58)

REF (58)



PAGE 4

E

D

## INTERPOLATOR DIAGNOSTICS

Presently Interpolator Diagnostics allow the user to input MRD words directly to the interpolator to test different hardware functions. Future release will include the ability to test DMA transfers to the interpolator.

The program allows the input of a group of MRD words with the ability to repeat up to 2,147,483,647 times.

Input data format is in hex.

	MMMM	RRRR	DDDD
Ex1	00FF	0001	8000
	<u>controls velocity</u>		

Ex1 sets M word velocity to max  
Ratio = 1  
Distance =32768

To interpret MRD transfer results decimal display counters are connected to PIN 2 (XMP+) and PIN 21 (YMP+) of the plotter connector. Make sure the interpolator is not halted before starting a MRD word transfer (halt led off).

ALL TESTING IS PERFORMED WITH THE TABLE DISCONNECTED\*

### RATIO EQUATION

For specific calculation of major and minor pluses use the following equation

$$\text{Ratio} = \frac{\text{Minor} \times 65536 - 32768}{\text{major}}$$

Ex. 2 If D word=10 what ratio needed to generate 1 minor plus?  
 $R = \frac{1 \times 65535 - 32768}{10}$   
R=3276.8 or 3277 = CCD hex

MAX VELOCITY

The interpolator diagnostic can be used to calculate maximum velocity. Results of this test are compared to know acceptable data for the table in question.

Ex3 For 41 Table - find velocity on axis  
known  $\Delta t = 154.2 \mu s$  @ stepsize (ss)  $\Delta x = .0005$

THEN V41  
calculated

$$= \frac{.0005 \text{ In/step}}{154.2 \times 10^{-6} \text{ sec/step}}$$

$$= 3.24 \text{ in/sec}$$

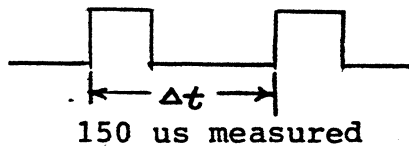
or  
 $3.24 \text{ in/sec} \times 60 \text{ sec/min} = \underline{194.4 \text{ in/min}}$

SO INPUT    MMMM    RRRR    DDDD  
              00FF    XXXX    XXXX  
              max velocity

Repeat as necessary to read pluse width

MEASURE  $\Delta t$  WITH SCOPE

Ex.



measured  $\Delta t = 7.5 \text{ div} (20 \mu s/\text{div})$   
 $= 150 \mu s$

THEN V41 =  
measured

$$= \frac{.0005 \text{ in/step}}{150 \times 10^{-6} \text{ sec/step}}$$

$$= 3.33 \text{ in/sec or } 3.33 \text{ in/sec} \times 60 \text{ sec/min} = \underline{200 \text{ in/min}}$$

OR SIMPLY COMPARE  $\Delta t$  known  $\forall$   $\Delta t$  meas.  $154.2 \mu s \cong 150 \mu s$

INTERPOLATOR TEST DATA

HEX M WORD	HEX R WORD	HEX D WORD	DEL. MAJOR PLUSES	DEL. MINOR PLUSES
00FF	0001	8000	32768	1
000F	0001	8000	32768	1 HO HUM
00FF	8000	0001	1	1
00FF	00F9	8000	32768	125
00FF	00FA	8000	32768	125
00FF	00FB	8000	32768	126
00FF	00FE	8000	32768	126
00FF	8000	00FA	250	125
00FF	FFFF	8000	32768	32768
00FF	8000	FFFF	65535	32768
00FF	OCCD	000A	10	1
00FF	0002	4000	6384	1
00FF	01F4	FFFF	65535	500
00FF	0800	00FF	255	8

DIRECT MOVE INTERPOLATOR

STOP RAMP CLOCK 41 TABLE DIVIDE BY CLOCK SPEED

MOVE.W #SE76A SET CLOCK CONTROL REGISTER  
MOVE.W #0,\$4C00A CLEAR DEVICE (CAUSES HALT)

BSR WAITBUFF WAIT FOR BUFFER EMPTY  
MOVE.W D1 \$4C000 M-WORD  
MOVE.W D2 \$4C002 R-WORD  
MOVE.W D3,\$4C004 D-WORD

SELF BRAS SELF

Routine to wait for plotter buffer to be empty. When outputting moves directly to the plotter(such as slew or initialization) we must make sure the previous command has been accepted by the plotter.

WAIT BUFF MOVE.W \$4C000,D7  
TST.B D7 is buffer empty bit set.  
BPL.S WAIT BUFF

## FLOPPY TEST

### COMMANDS:

RD Read sector from floppy  
WR Write sector to floppy  
FM Format diskette  
DA Specify data to be written (see "DA" below)  
UN Specify unit number (0)  
HD Specify head (0 or 1)  
TR Specify track number (0 thru 79)  
SC Specify sector number (0 thru 15)  
CB Compare read data buffer to data previously written  
DT Disc test-perform all functions of the controller  
DE Disc exerciser-read random sectors and verify contents  
(verifies data written during "DT")  
DR Read sector 0 on every track  
IN Perform on board internal processor diagnostics  
RA Perform on board RAM diagnostics  
\* Specify number of repetitions  
+ Increment last specified value after each operation  
- Decrement last specified value after each operation  
  
EX Exit floppy diagnostic

### DATA FORMATS (DA) Specify data to be written

0 All 0's  
1 All 1's  
2 0 thru 9 over and over  
3 A thru Z over and over  
4 Any character typed in by user  
5 Text typed in by user  
6 Track and sector numbers over and over

### EXAMPLES:

Unit X	Head X	Track X	Sector X	
WRDA0				Will write all 0's
WRDA4T				Will write all T's
RDCB*3				Will read previously written data and compare buffers in memory 3 times.
RDUN0HD1TR44SC3				Will read sector 3 on track 44, unit 0, head 1



MOVE.B NXT\_SEC,DO  
BRA RETURN,I

Get next sector pointer  
Return

Error Table

\*  
\*  
\*

ERROR TBL

DC.B	0	00 - No error
DC.B	-6	01 - Undefined
DC.B	-19	02 - No seek complete signal
DC.B	-6	03 - Undefined
DC.B	-19	04 - Not ready (after select)
DC.B	-6	05 - Undefined
DC.B	-19	06 - Track 00 not found
DC.B	-16	07 - Door open on drive
DC.B	-19	08 - No head loaded
DC.B	-6	09 - Undefined
DC.B	-6	0A - Undefined
DC.B	-6	0B - Undefined
DC.B	-6	0C - Undefined
DC.B	-6	0D - Undefined
DC.B	-6	0E - Undefined
DC.B	-6	0F - Undefined
DC.B	-14	10 - ID CRC error
DC.B	-14	11 - Write fault
DC.B	-5	12 - Write protect
DC.B	-6	13 - Undefined
DC.B	-14	14 - Sector not found
DC.B	-17	15 - Seek error
DC.B	-14	16 - Format track timeout error
DC.B	-14	17 - Format track not complete

1.30SYS : 15.

.FDSKDVR .SA 12/02/83 15:43:26

ISK DRIVER -----

DC.B	-6	18 - Undefined
DC.B	-17	19 - Two sided error
DC.B	-14	1A - Wrong data mark found
DC.B	-18	1B - Transfer length error
DC.B	-6	1C - Undefined
DC.B	-18	1D - Lost data in FDC
DC.B	-14	1E - Data CRC error
DC.B	-18	1F - FDC busy error
DC.B	-17	20 - Invalid command
DC.B	-17	21 - Illegal disk address
DC.B	-17	22 - Configure data error
DC.B	-17	23 - Invalid interleave
DC.B	-6	24 - Undefined
DC.B	-6	25 - Undefined
DC.B	-6	26 - Undefined
DC.B	-6	27 - Undefined
DC.B	-6	28 - Undefined
DC.B	-6	29 - Undefined
DC.B	-6	2A - Undefined
DC.B	-6	2B - Undefined
DC.B	-6	2C - Undefined
DC.B	-6	2D - Undefined
DC.B	-6	2E - Undefined
DC.B	-6	2F - Undefined
DC.B	-20	30 - RAM error
DC.B	-20	31 - Program memory checksum error

## Magnetic Tape Diagnostics

### Quick Data Test

This test is entered by typing "3" on the main menu then "1" on the tape diagnostics submenu. This test will rewind the tape drive to the load point and then write a specified byte of data, a specified number of times per record, and a specified number of records in the file. The program will then rewind the drive and read the data back comparing it to what was written. The program provides for modifications of the default parameters which will not change unless the user specifically changes them or the user exits the Quick data test submenu to the magnetic tape test main menu. The default parameters and their ranges are:

Name	Default value	Range
Data byte	-127	-127 ... 127
#of bytes per record	256	0... 1024
# of records per file	20	0... 250

Typing a value outside these values will result in the default value.

The quick data test provides error messages and gives the operation which was being attempted when the error occurred.

#### Examples:

- 1) unit not on line  
error during rewind operation
- 2) No write ring detected error during mag tape write operation
- 3) Data read does not match data written error during data compare

If no errors occur the message will read:

No errors in quick data test

Using the quick data test is as simple as selecting "1" from the Quick data test submenu and pressing return. To alter the parameters type "2" at this level, the program will then prompt you for the data byte, followed by the prompt for the number of bytes per record, and finally the number of records per file. After entering the number of records the test will automatically run. Repeated tests using the same parameters can be run by typing "1" followed by a return. In each prompt the range of acceptable values is displayed. Typing a value outside the accepted range will cause the default value to be used.

Examples:

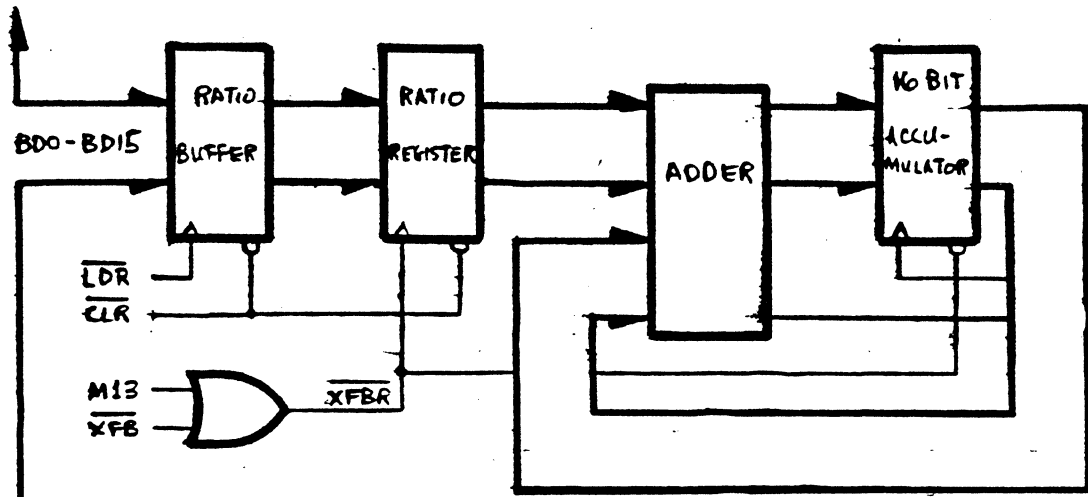
- 1) Quick data test
    1. Run test with present parameter values
    2. Alter parameter values
  99. Exit
- Please enter a menu choice 1

This will cause the program/test to run with the present parameter values. These will be the default values or the user inputted values.

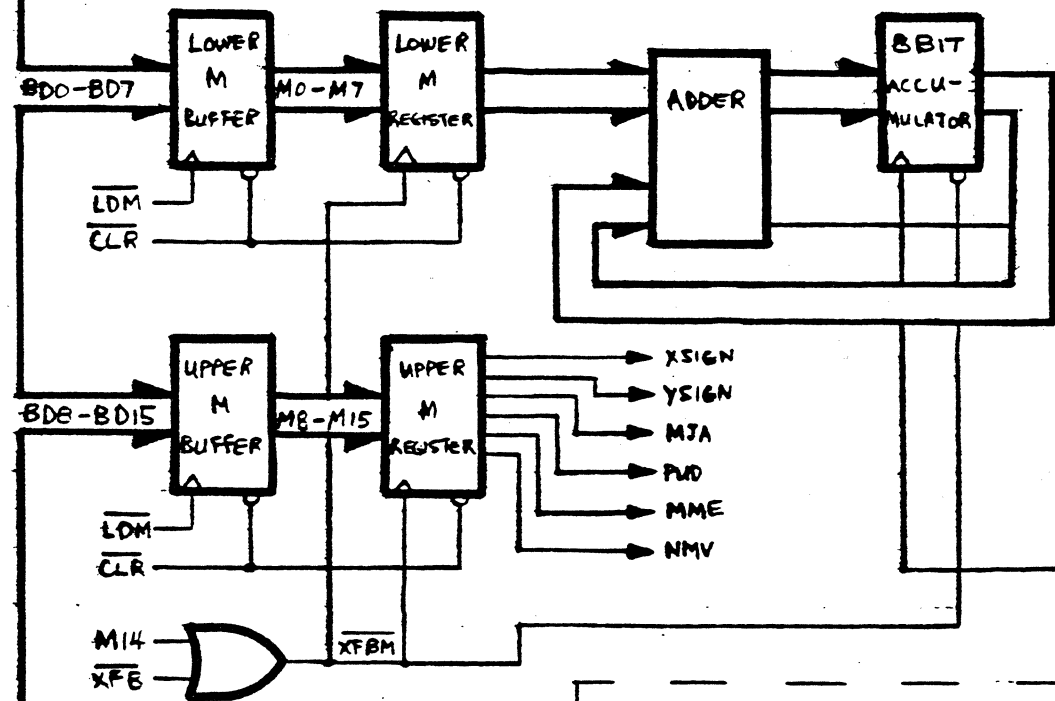
- 2) Quick Data test
    1. Run the test with present parameter values
    2. Alter parameter values
  99. Exit
- Please enter a menu choice 2
- Type in the data byte (-127...127)
- 1
- Type in the number of bytes per record (...1024)
- 1024
- Type in the number of records in the file (....250)
- 200

This sequence will cause the system to write and verify 200 records of 1024 bytes each with each byte being equal to -1.

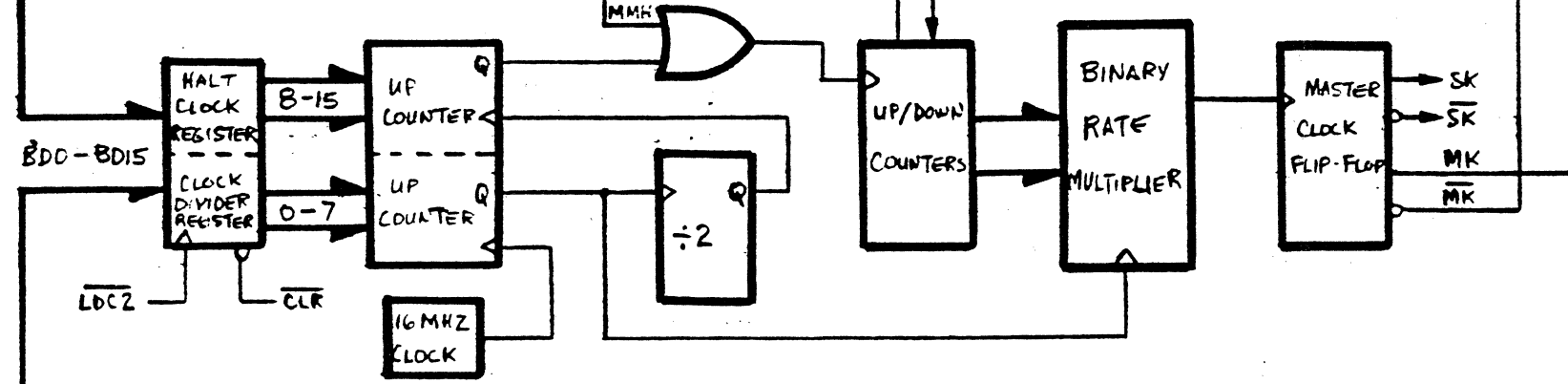
### RATIO (R WORD)



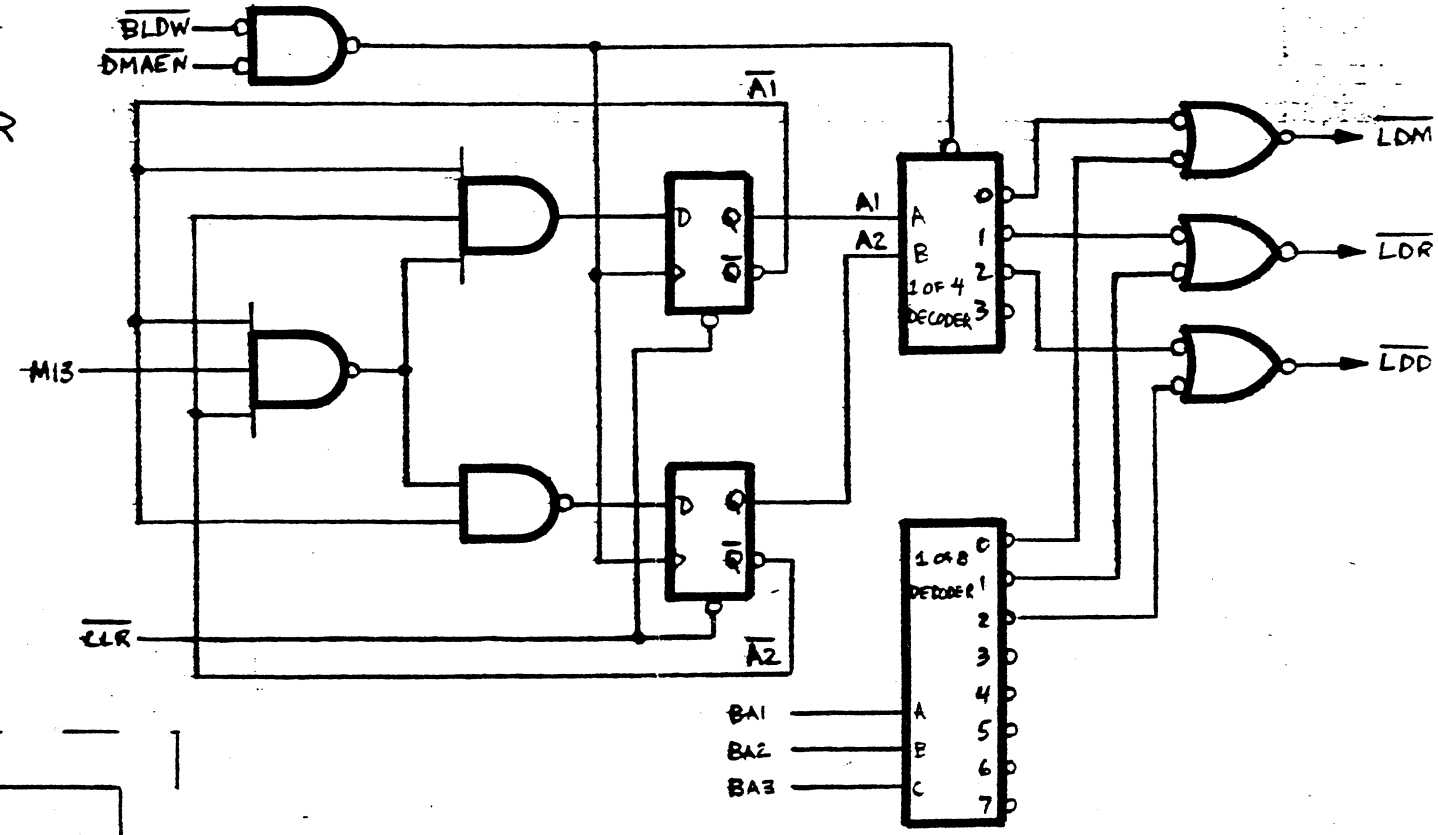
### VELOCITY (M WORD)



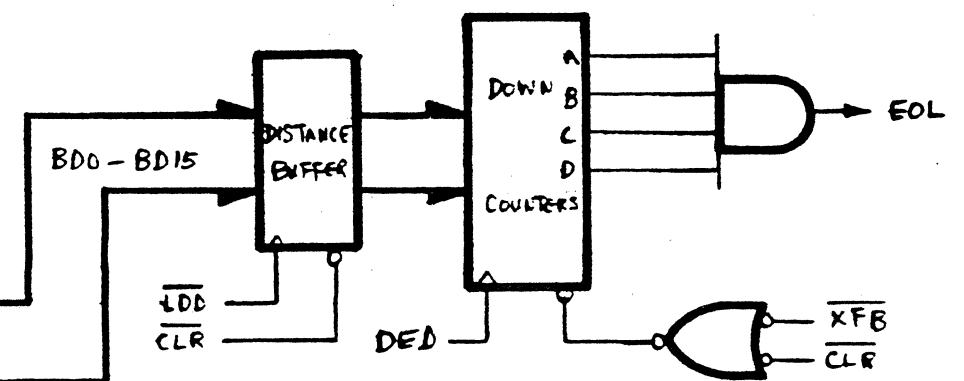
### CLOCK



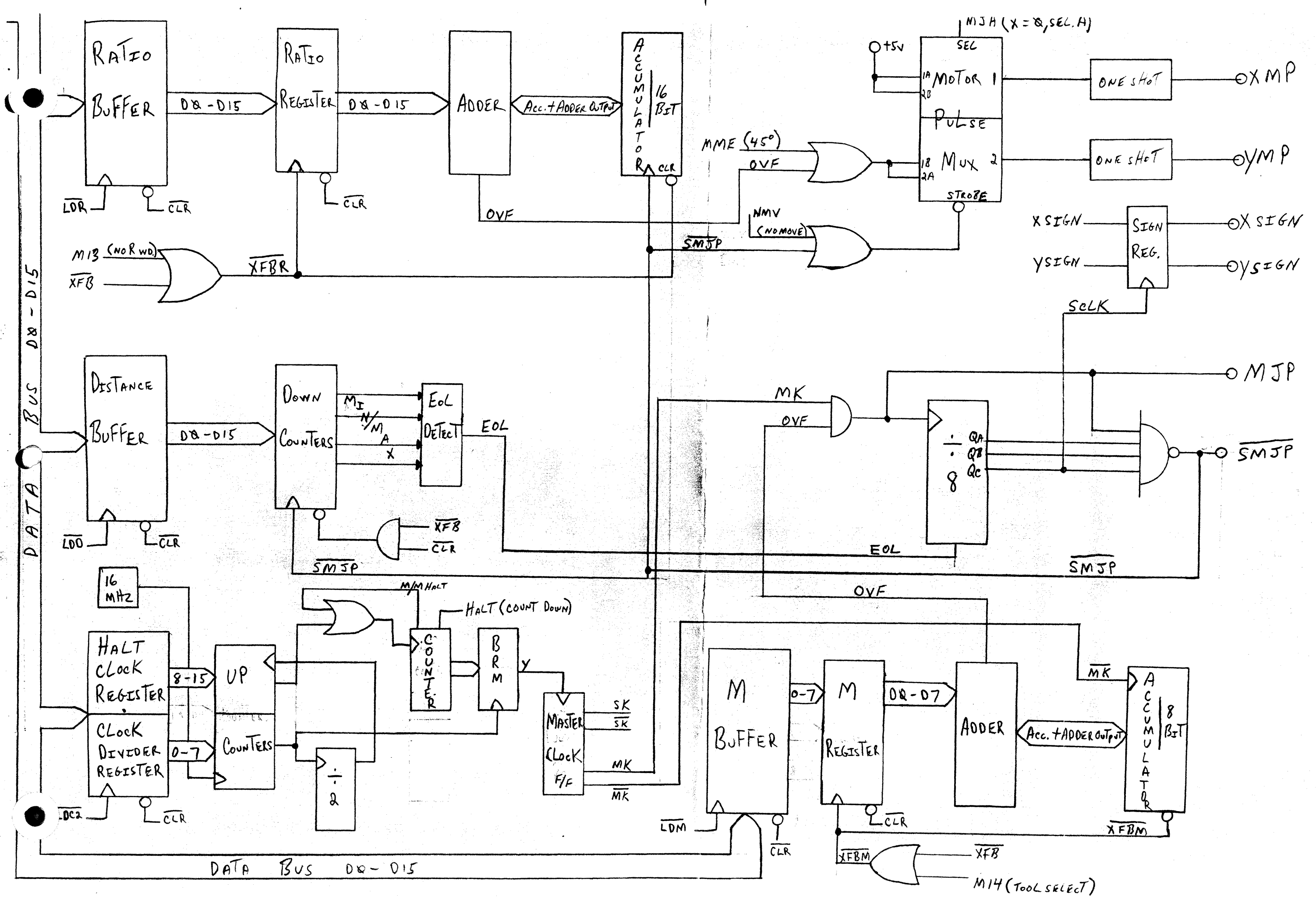
### SEQUENCER



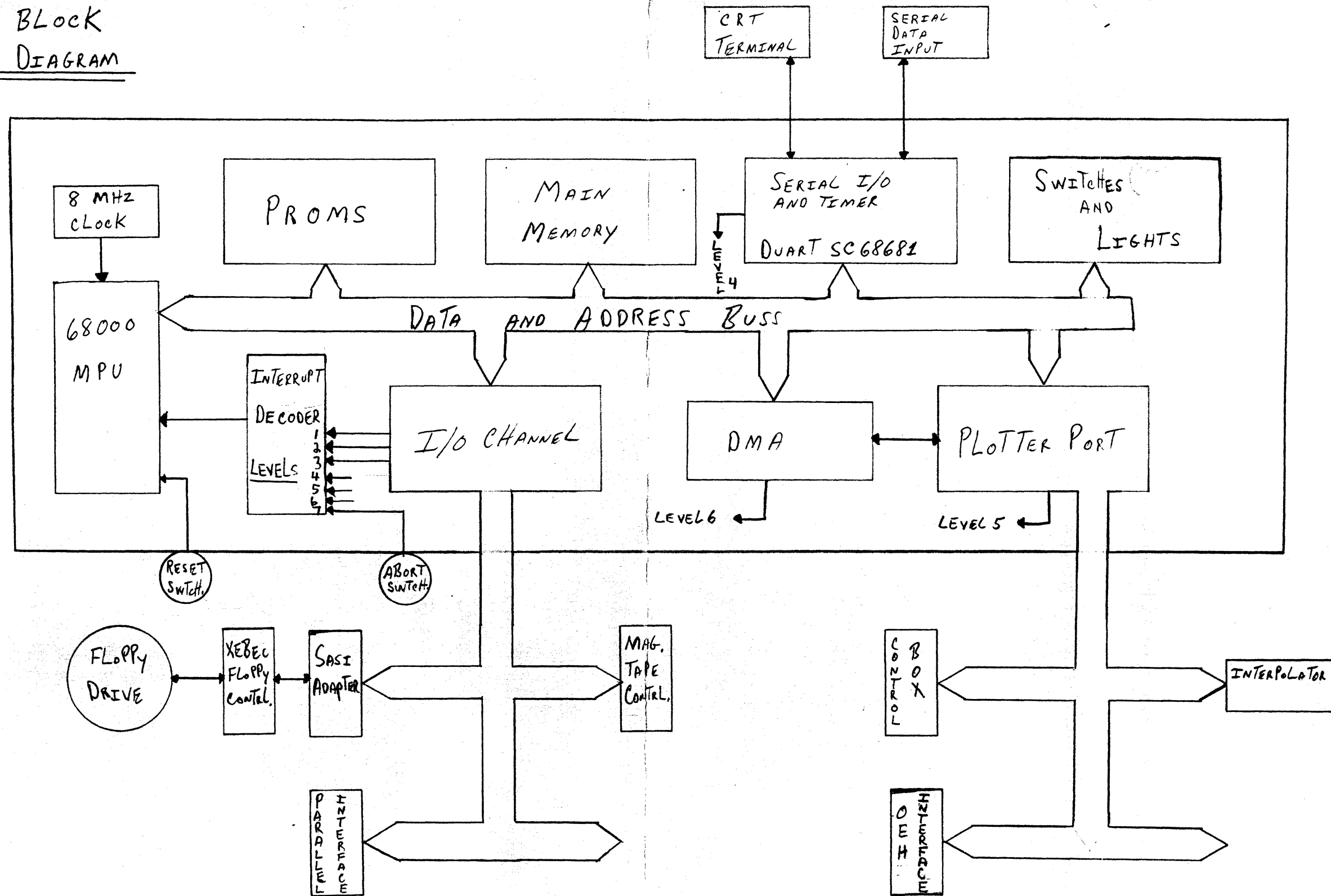
### DISTANCE (D WORD)



BDD - BD15 (BUFFERED DATA BUS)



3100  
BLOCK  
DIAGRAM





2  
3  
4  
5           00000009  
6

          x           DPT     CRE  
          x  
          x           SECTION 9  
          x  
          x           XDEF     MAGDVR,MAGINTRP  
          x           XDEF     MAGDVR1  
          x           XDEF     MAGDVR.I  
          x           XDEF     MAGDVR.T

          x  
          x           XREF     SETTMR  
          x           XREF     STARTTHR  
          x           XREF     STOPTMR  
          x

XX

          x           NAME:    MAGDVR   x  
          x           -----    x  
          x    x

          x           DATE:    05-06-83                                       x  
          x           -----    x  
          x    x

          x           AUTHOR:  M. JEAN                                        x  
          x           -----    x  
          x    x

          x           REVISION: 1.2   x  
          x           -----    x  
          x    x

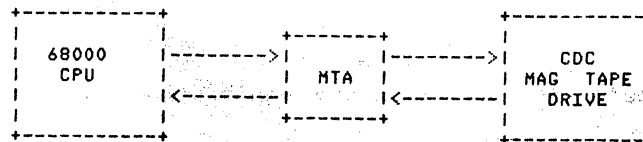
          x           MAJ 07-15-83   Added software to handle the case of file  
          x           searching over blank tape and gracefully terminating the driver.    x  
          x    x

          x           MAJ 07-25-83   Added time-out software.                    x  
          x    x

          x           MIC 08-29-83   Added software to handle invalid interrupts (MAGINTRP)   x  
          x    x

          x           MODULE DESCRIPTION:  
          x           -----  
          x

          x           This module controls all I/O functions between a Motorola 68000  
          x           I/O channel and a CDC magnetic tape drive and formatter, inter-  
          x           faced by a Motorola mag tape adapter (MTA).  
          x



          x           The MTA will use the buffered transfer method. The I/O Channel  
          x           master can buffer up to 4k of data in the on-board FIFO before  
          x

58  
59



60 x transferring the data from I/O channel master to tape (Tape Write)  
61 x or vice versa (Tape Read). This eliminates generating an inter-  
62 x rupt for every byte transferred and thus reducing overhead.  
63 x

64 x The MTA provides the following functions:

- 65 x 1. Read forward 1 record
- 66 x 2. Read reverse 1 record (not used at this time)
- 67 x 3. Write forward 1 record
- 68 x 4. Write file mark
- 69 x 5. Search file mark forward
- 70 x 6. Search file mark reverse
- 71 x 7. Space forward 1 record
- 72 x 8. Space reverse 1 record
- 73 x 9. Rewind the Magnetic tape to the BOT
- 74 x 10. Error handling

75 x  
76 x Formatter commands are divided into three categories:

- 77 x 1. Group commands
- 78 x 2. Discrete commands
- 79 x 3. Transport/density select command

80 x  
81 x The interface lines of group commands are:

Formatter commands	D7	D6	D5	D4	D3	D2	D1	D0
Read forward record				0	0	0	0	0
Read reverse record				0	0	0	0	1
Write forward record				0	0	0	1	0
Write file mark				0	0	1	1	0
Search file mark forward				1	0	1	0	0
Search file mark reverse				1	0	1	0	1
Space forward record				1	0	0	0	0
Space reverse record				1	0	0	0	1

93 x  
94 x

96 \*  
 97 \*  
 98 \*  
 99 \*  
 100 \*  
 101 \*  
 102 \*  
 103 \*  
 104 \*  
 105 \*  
 106 \*  
 107 \*  
 108 \*  
 109 \*  
 110 \*  
 111 \*  
 112 \*  
 113 \*  
 114 \*  
 115 \*  
 116 \*  
 117 \*  
 118 \*  
 119 \*  
 120 \*  
 121 \*  
 122 \*  
 123 \*  
 124 \*  
 125 \*  
 126 \*  
 127 \*  
 128 \*  
 129 \*  
 130 \*  
 131 \*  
 132 \*  
 133 \*  
 134 \*  
 135 \*  
 136 \*  
 137 \*  
 138 \*  
 139 \*  
 140 \*  
 141 \*  
 142 \*  
 143 \*  
 144 \*  
 145 \*

Formatter discrete commands are:

Command	D7	D6	D5	D4	D3	D2	D1	D0
Rewind					0	0	0	1
Off line					0	0	1	0
Formatter disable					0	1	0	0
MTA reset					1	0	0	0

The MTA status register consists of two, 8-bit registers STAT1 & STAT2:  
 (The status register is a read only register)

STAT1	STAT2
Bit 0 : READY	Bit 0 : ST1
Bit 1 : ON LINE	Bit 1 : ST2
Bit 2 : LDPNT	Bit 2 : ST3
Bit 3 : EOT	Bit 3 : ST4
Bit 4 : FNBSY	Bit 4 : FIFUL
Bit 5 : RWNDG	Bit 5 : FIHAL
Bit 6 : FLPRT	Bit 6 : FIEMP
Bit 7 : NRZI	Bit 7 : DNBSY

NOTE: ST1-ST4 cause interrupts when true. These bits  
 are reset when either STAT1 or STAT2 are read.  
 Because they are reset STAT2 should always be  
 read in first.

ST1 = LP, EOT, CCG  
 ST2 = FM, FNBSY  
 ST3 = DURING DMA 1/2 FULL READ OR WRITE  
 ST4 = TAPE WRITE ERROR OR READ AFTER WRITE

Offsets of I/O addresses as follows:

DATID	1(40000)
GRCMD	8(40000)
DSCMD	16(40000)
TRSEL	24(40000)
STAT1	33(40000)
STAT2	41(40000)
LSWRD	48(40000)
DIRSL	56(40000)



205 x -5 -> WRITE PROTECTED (NO WRITE RJNG)  
206 x -6 -> HARD ERROR  
207 x -7 -> COMMAND REJECT (BUSY)  
208 x -8 -> RECORD SIZE EXCEEDS BUFFER CAPACITY  
209 x -10 -> TIME OUT  
210 x -20 -> BLANK TAPE ERROR  
211 x  
212 x

213 x BUFFER  
214 x -----  
215 x

216 x This is a long word address of the I/O buffer to be used  
217 x for data transfers with the mag tape. The first word in the  
218 x buffer is used as a character count. When doing a READ  
219 x operation it indicates the maximum number of characters to  
220 x be placed in the buffer. When doing a WRITE operation it  
221 x indicates the actual number of characters in the buffer to  
222 x be written. The character count will be updated when the  
223 x operation being performed is complete to reflect the actual  
224 x number of characters transferred. The following is a repre-  
225 x sentation of the buffer:

226 x | word 1 | word 2 | word 3 |  
227 x -----  
228 x | buffer count | char 1 | char 2 | char 3 | char 4 | etc...  
229 x -----  
230 x

231 x  
232 x When file searching or skipping records the buffer count  
233 x is used to indicate what file to search for or how many  
234 x records to skip. When file searching the actual file number  
235 x desired is passed. Load point is defined as file number 1.  
236 x When skipping records the buffer count contains the relative  
237 x number of records to skip from the present position of the  
238 x tape. Positive numbers indicate forward record skipping and  
239 x negative numbers are used to indicate reverse record skipping.  
240 x  
241 x  
242 x

```

244
245
246
247
248
249          00000000      *
250          00000001      * INTERNAL OPERATIONS
251          00000002      *
252          00000006      * GROUP COMMANDS
253          00000014      *
254          00000015      *
255          00000010      *
256          00000011      *
257
258          *
259          * DISCRETE COMMANDS
260          00000001      *
261          00000002      *
262          00000004      *
263          00000008      *
264
265          *
266          * I/O ADDRESS OFFSETS
267          *
268          00000001      *
269          00000008      *
270          00000010      *
271          00000018      *
272          00000021      *
273          00000029      *
274          00000030      *
275          00000038      *
276
277          *
278          * REGISTER SAVE DEFINITION
279 9 00000000 00      *
280 9 00000001 00      *
281          *
282          *
283 9 00000002 0000002C      *
284          *
          MAXINVAL DC.B 0      Invalid interrupt count
          MAGINTRP DC.B 0      Valid interrupt flag
          REGLIST REG D0-D5/A0-A4      Register save list
          *
          REGSAVE DS.L 11      Register save area
          *
  
```

----- NPC 3100 MAGTAPE DRIVER -----

```

286
287
288
289
290
291
292
293
294 9      0000002E   MAGDVR   EQU      *      Driver entry point
295 9      0000002E   MAGDVR1  EQU      *      Alternate driver entry point
296
297 9 0000002E 40F9000005D8   MOVE.W   SR,PSWSAVE   Save current proc. status word
298 9 00000034 46FC2400       MOVE.W   #$2400,SR     Turn off interrupts (to level 4)
299 9 00000038 48F91F3F0000   MOVEM.L  REGLIST,REGSAVE Save registers
      0002
300
301 9 00000040 285F       MOVE.L   (SP)+,A4      Save the return address
302 9 00000042 245F       MOVE.L   (SP)+,A2      Get address of I/D buffer
303 9 00000044 225F       MOVE.L   (SP)+,A1      Get address of status parameter
304 9 00000046 201F       MOVE.L   (SP)+,D0      Get the command
305
306 9 00000048 23CA000005E0   MOVE.L   A2,BUF_ADR    Save buffer address
307 9 0000004E 23C9000005DC   MOVE.L   A1,STAT_ADR   Save status address
308 9 00000054 2079000005D4   MOVE.L   MAG_IO,A0     Get base address for MAG I/O
309
310
311
312 9 0000005A 082800010021   BTST.B   #1,STAT1(A0)  Are we on-line?
313 9 00000060 6608       BNE.S    CMND.1        Yes, OK to proceed
314 9 00000062 22BCFFFFFFF9   MOVE.L   #-4,(A1)     No, STATUS = on-line error
315 9 00000068 6048       BRA.S    RETURN.S      Return
316
317
318
319 9 0000006A 082800000021   CMND.1   BTST   #0,STAT1(A0)  Is unit ready?
320 9 00000070 6708       BEQ.S    CMND.2        No, unit is busy
321 9 00000072 082800040021   BTST   #4,STAT1(A0)  Is formatter busy?
322 9 00000078 6608       BNE.S    CMND.3        No, OK to continue
323 9 0000007A 22BCFFFFFFF9   MOVE.L   #-7,(A1)     STATUS = busy
324 9 00000080 6030       BRA.S    RETURN.S      Return
325
326
327
328
329
330
331
332
333
334 9 00000082 E500       CMND.3   ASL.B   #2,D0          Adjust for long word addressing
335 9 00000084 13C0000005DB   MOVE.B   D0,COMMAND    Save adjusted command
336 9 0000008A 47F900000096   LEA.L   CMNDS.S,A3     Get jump table address
337 9 00000090 D6C0       ADDA    D0,A3          Add index
338 9 00000092 2653       MOVE.L   (A3),A3       Get address of command instructions
339 9 00000094 4ED3       JMP.L   (A3)           Go do command
340
341
342

```

TABLE OF ENTRY POINT ADDRESSES

```
343 9 00000096 00000164 CMNDS.S DC.L INIT.S
344 9 0000009A 0000017E          DC.L READ.S
345 9 0000009E 00000262          DC.L WRITE.S
346 9 000000A2 00000300          DC.L REWIND.S
347 9 000000A6 0000034C          DC.L SEARCH.S
348 9 000000AA 0000048A          DC.L FILMRK.S
349 9 000000AE 000004E8          DC.L SKPREC.S
350                                x
351                                x      RESTORE REGISTERS & RETURN
352                                x
353 9 000000B2 2F0C          RETURN.S MOVE.L A4,-(SP)      Put return address on stack
354 9 000000B4 4CFA1F3FFF4A    MOVEM.L REGSAVE,REGLIST      Restore registers
355 9 000000B8 46F9000005D8    MOVE.W PSWSAVE,SR           Restore status word & interrupts
356 9 000000C0 4E75          RTS                          Return
357                                x
```

```

359          *
360          *
361          *
362          *           MAG TAPE DRIVER ENTRY POINT BY INTERRUPT
363          *
364          *
365          *
366 9          000000C2  MAGDVR.I EQU           x           Interrupt service routine
367          *
368 9 000000C2 48F91F3F0000          MOVEM.L  REGLIST,REGSAVE          Save registers
          0002
369 9 000000CA 2079000005D4          MOVE.L   MAG_ID,A0           Get base address of MAG I/O
370 9 000000D0 2479000005E0          MOVE.L   BUF_ADR,A2        Get buffer address
371 9 000000D6 2279000005DC          MOVE.L   STAT_ADR,A1       Get external status address
372 9 000000DC 4280          CLR.L   D0
373 9 000000DE 4281          CLR.L   D1
374 9 000000E0 10280029          MOVE.B   STAT2(A0),D0      Get status byte 2
375 9 000000E4 12280021          MOVE.B   STAT1(A0),D1      Get status byte 1
376 9 000000E8 0C3900010000          CMPI.B   #1,MAGINTRP      Valid interrupt ?
          0001
377 9 000000F0 672A          BEQ.S   CMND.4             YES, continue processing.
378 9 000000F2 08010002          BTST.L  #2,D1             Are we at load point?
379 9 000000F6 661A          BNE.S   VI
380 9 000000F8 08000003          BTST.L  #3,D0             Hard error?
381 9 000000FC 675E          BEQ.S   RETN1.I           NO, return
382 9 000000FE 22BCFFFFFFFA          MOVE.L   #-6,(A1)         YES, store error status
383 9 00000104 317C00080010          MOVE.W   #MTA_RST,DSCMD(A0) Reset MTA
384 9 0000010A 117C00040010          MOVE.B   #FMTDSBL,DSCMD(A0) Reset formatter 0
385 9 00000110 6044          BRA.S   RETURN.I         Return from interrupt
386 9 00000112 33FC00010000 VI          MOVE.W   #1,ACTFILE       YES, reset current file value.
          05E6
387 9 0000011A 603A          BRA.S   RETURN.I         Return from interrupt.
388          *
389          *           IF BUSY FLAG IS NOT SET - IGNORE INTERRUPT
390          *
391 9 0000011C 4283          CMND.4  CLR.L   D3           Busy?
392 9 0000011E 1600          MOVE.B  D0,D3
393 9 00000120 0203004B          AND.B   ##4B,D3
394 9 00000124 6736          BEQ.S   RETN1.I         No, ignore interrupt
395          *
396          *           GO BACK TO WHAT WE WERE WORKING ON
397          *
398 9 00000126 4282          CMND.5  CLR.L   D2
399 9 00000128 1439000005DB          MOVE.B  COMMAND,D2       Get the adjusted command
400 9 0000012E 47F90000013A          LEA.L   CMNDS,I,A3       Get the jump table address
401 9 00000134 D6C2          ADDA    D2,A3           Add the offset
402 9 00000136 2653          MOVE.L  (A3),A3         Get the address of command instructions
403 9 00000138 4ED3          JMP.L   (A3)           Go do it
404          *
405 9 0000013A 0000017A          CMNDS.I DC.L   INIT,I
406 9 0000013E 0000018C          DC.L   READ,I
407 9 00000142 000002CA          DC.L   WRITE,I
408 9 00000146 0000032E          DC.L   REWIND,I
409 9 0000014A 000003DA          DC.L   SEARCH,I
410 9 0000014E 000004B4          DC.L   FILMRK,I
411 9 00000152 0000054C          DC.L   SKPREC,I
412          *
413          *           RESTORE REGISTERS AND RETURN FROM EXCEPTION.

```



----- NPC 3100 MAGTAPE DRIVER -----

```
414 * RETURN POINT RETN1,I IS USED FOR COMMANDS SUCH AS
415 * FILE SEARCH THAT REQUIRE MULTIPLE INTERRUPTS. FOR
416 * THESE, THE VALID INTERRUPT FLAG WILL STAY SET UNTIL
417 * THE COMPLETION OF THE LAST COMMAND.
418 *
419 9 00000156 423900000001 RETURN.I CLR.B MAGINTRP Clear valid interrupt flag
420 9 0000015C 4CFA1F3FFEA2 RETN1,I MOVEM.L REGSAVE,REGLIST Restore the registers
421 9 00000162 4E73 RTE Return from exception
```

```
423          *
424          *
425          *      INITIALIZE MAG TAPE UNIT (SUBROUTINE)      *
426          *
427          *
428 9          00000164      INIT.S      EQU      *
429 9 00000164 317C00080010      MOVE.W      #MTA_RST,DSCMD(A0)      Reset MTA
430 9 0000016A 117C00000018      MOVE.B      #0,TRSEL(A0)      Select formatter 0
431 9 00000170 117C00040010      MOVE.B      #FMTDSBL,DSCMD(A0)      Reset formatter 0
432 9 00000176 60000188      BRA      REWIND.S      Go rewind unit
433          *
434          *
435          *      INITIALIZE MAG TAPE UNIT (INTERRUPT)      *
436          *
437          *
438 9 0000017A 600001B2      INIT.I      BRA      REWIND.I      Do rewind completion
439          *
```

```
441 *****
442 x          READ A RECORD          (SUBROUTINE)          x
443 *****
444 x
445 9          0000017E          READ.S          EQU          x
446 9 0000017E 317C00080010          MOVE.W          #MTA_RST,DSCMD(A0)          Reset FIFO to empty
447 9 00000184 13FC000A0000          MOVE.B          #10,RETRY          Initialize retry counter
      05DA
448 9 0000018C 33D2000005E4          MOVE.W          (A2),BUF_CNT          Save buffer length
449 9 00000192 082800030021          BTST.B          #3,STAT1(A0)          Is it EOT?
450 9 00000198 670A          BEQ.S          READ,S1          No, continue
451 9 0000019A 22BCFFFFFFFD          MOVE.L          #-3,(A1)          Yes, STATUS = EOT error
452 9 000001A0 6000FF10          BRA          RETURN.S          Return
453 x
454 9 000001A4 117C00000008          READ.S1          MOVE.B          #READFWD,GRCHD(A0)          Issue read record command
455 9 000001AA 22BCFFFFFFF9          MOVE.L          #-7,(A1)          STATUS = busy
456 9 000001B0 13FC00010000          MOVE.B          #1,MAGINTRP          Set valid interrupt flag
      0001
457 9 000001B8 6000FEF8          BRA          RETURN.S          Return
458 x
459 *****
460 x          READ A RECORD          (INTERRUPT)          x
461 *****
462 x
463 9          000001BC          READ.I          EQU          x
464 9 000001BC 08000000          BTST          #0,D0          Is it MR. 1600 PE interrupt?
465 9 000001C0 669A          BNE.S          RETN1.I          YES, ignore him and go home.
466 9 000001C2 08000001          BTST          #1,D0          Is it a FNBSY interrupt?
467 9 000001C6 6754          BEQ.S          READ,I3          No, must be error
468 9 000001C8 4282          CLR.L          D2          Clear byte counter
469 9 000001CA 08010004          BTST          #4,D1          EOF?
470 9 000001CE 661A          BNE.S          READ,I1          No, get data
471 9 000001D0 22BCFFFFFFF7          MOVE.L          #-1,(A1)          STATUS = EOF
472 9 000001D6 5279000005E6          ADDQ.W          #1,ACTFILE          Increment file counter
473 9 000001DC 4252          CLR.W          (A2)          Return with length = 0 bytes
474 9 000001DE 082800040021 A          BTST          #4,STAT1(A0)          Wait for FNBSY bit
475 9 000001E4 67F8          BEQ.S          A          No, try again
476 9 000001E6 6000FF6E          BRA          RETURN.I          Return
477 x
478 9 000001EA 082800060029          READ.I1          BTST          #6,STAT2(A0)          Is FIFO empty?
479 9 000001F0 6622          BNE.S          READ,I2          Yes, all done reading
480 x          MOVE.B          DATI0(A0),Z(A2,D2)          Get
      byte of data
481
482
483 9 000001F2 16280001          MOVE.B          DATI0(A0),D3          Get byte of data
484 9 000001F6 4EB90000025E          JSR          ROTCHAR          Rotate character
485 9 000001FC 15842002          MOVE.B          D4,Z(A2,D2)          Put byte in buffer
486
487
488 9 00000200 5242          ADD.W          #1,D2          Increment byte count
489 9 00000202 8479000005E4          CMP.W          BUF_CNT,D2          Reached max. record size yet?
490 9 00000208 6DE0          BLT.S          READ,I1          Get more data
491 9 0000020A 22BCFFFFFFF8          MOVE.L          #-8,(A1)          Yes, STATUS = buffer overflow error
492 9 00000210 6000FF44          BRA          RETURN.I          Return
493 x
494 9 00000214 3482          READ.I2          MOVE.W          D2,(A2)          Return # of bytes read
495 9 00000216 4291          CLR.L          (A1)          STATUS = ready
496 9 00000218 6000FF3C          BRA          RETURN.I          Return
```

```
497
498 9 0000021C 08000003      *
      READ.I3 BTST      #3,D0          Is it a hard error?
499 9 00000220 6732          BEQ.S      READ.I6          No, different error
500 9 00000222 082800040021 READ.I7 BTST      #4,STAT1(A0)      Wait for read to complete
501 9 00000228 67F8          BEQ.S      READ.I7          Not yet - try again
502 9 0000022A 117C00110008      MOVE.B     #SKPREV,GRCMD(A0)  Backspace record
503 9 00000230 5339000005DA      SUB.B      #1,RETRY          Decrement retry counter
504 9 00000236 6F12          BLE.S      READ.I5          If no more retries - hard error
505 9 00000238 082800040021 READ.I4 BTST.B    #4,STAT1(A0)      Wait for reverse record to complete
506 9 0000023E 67F8          BEQ.S      READ.I4          Not yet - try again
507 9 00000240 117C00000008      MOVE.B     #READFWD,GRCMD(A0) Issue read command again
508 9 00000246 6000FF14          BRA        RETN1.I          Return
509
510 9 0000024A 22BCFFFFFFFA READ.I5 MOVE.L     #-6,(A1)          STATUS = hard error
511 9 00000250 6000FF04          BRA        RETURN.I          Return
512
513 9 00000254 22BCFFFFFFFD READ.I6 MOVE.L     #-3,(A1)          STATUS = EDT error
514 9 0000025A 6000FEFA          BRA        RETURN.I          Return
515
516      *
517      *
518 9 0000025E 1803          ROTCHAR   MOVE.B     D3,D4
519 9 00000260 4E75          RTS
520
521      *
522      *
523      *
```

```

525 *****
526 *           WRITE A RECORD           (SUBROUTINE) *
527 *****
528 *
529 9          00000262          WRITE.S EQU          *
530 9 00000262 317C00080010      MOVE.W #MTA_RST,DSCMD(A0)  Reset FIFO to empty
531 9 00000268 33D2000005E4      MOVE.W (A2),BUF_CNT      Save buffer length
532 9 0000026E 082800060021      BTST.B #6,STAT1(A0)     Write enabled?
533 9 00000274 670A              BEQ.S WRITE.S1          Yes, continue
534 9 00000276 22BCFFFFFFFB      MOVE.L #-5,(A1)         No, STATUS = write protect error
535 9 0000027C 6000FE34          BRA RETURN.S           Return
536 *
537 9 00000280 082800030021      WRITE.S1 BTST.B #3,STAT1(A0)  Is it EOT?
538 9 00000286 670A              BEQ.S WRITE.S2          No, continue
539 9 00000288 22BCFFFFFFFD      MOVE.L #-3,(A1)         Yes, STATUS = EOT error
540 9 0000028E 6000FE22          BRA RETURN.S           Return
541 *
542 9 00000292 7401              WRITE.S2 MOVE.L #1,D2      Clear byte counter
543 9 00000294 B479000005E4      WRITE.S4 CMP.W BUF_CNT,D2    All done?
544 9 0000029A 6E10              BGT.S WRITE.S3          Yes, go issue last word command
545 *
546 *
547 *           MOVE.B 1(A2,D2),DATIO(A0) Get byte from buffer and output it
548 *
549 9 0000029C 16322001          MOVE.B 1(A2,D2),D3      Get byte from buffer
550 9 000002A0 4EBAFFBC          JSR ROTCHAR             Reverse bits
551 9 000002A4 11440001          MOVE.B D4,DATIO(A0)    Move data into FIFO
552 *
553 *
554 *
555 9 000002A8 5242              ADD.W #1,D2             Increment byte counter
556 9 000002AA 60E8              BRA WRITE.S4           Do it again
557 *
558 9 000002AC 317C00000030      WRITE.S3 MOVE.W #0,LSWRD(A0)  Send last word to MTA
559 9 000002B2 117C00020008      MOVE.B #WRTFWD,GRCMD(A0) Issue write record command
560 9 000002B8 22BCFFFFFFF9      MOVE.L #-7,(A1)        STATUS = busy
561 9 000002BE 13FC00010000      MOVE.B #1,MAGINTRP     Set valid interrupt flag
562 9 000002C6 6000FDEA          BRA RETURN.S           Return
563 *
564 *****
565 *           WRITE RECORD           (INTERRUPT) *
566 *****
567 *
568 9          000002CA          WRITE.I EQU          *
569 9 000002CA 08000001          BTST #1,D0             Is it a FNBSY interrupt?
570 9 000002CE 660A              BNE.S WRITE.I3         Yes, continue
571 9 000002D0 08000006          BTST #6,D0             No, is it a FIFO empty interrupt?
572 9 000002D4 6600FE86          BNE RETN1.I           Yes, ignore (wait for FNBSY)
573 9 000002D8 600C              BRA.S WRITE.I1         No, must be error
574 9 000002DA 34B9000005E4      WRITE.I3 MOVE.W BUF_CNT,(A2)  Yes, return # of bytes written
575 9 000002E0 4291              CLR.L (A1)             STATUS = ready
576 9 000002E2 6000FE72          BRA RETURN.I           Return
577 *
578 9 000002E6 08000003          WRITE.I1 BTST #3,D0             Is it a hard error?
579 9 000002EA 670A              BEQ.S WRITE.I2         No, different error
580 9 000002EC 22BCFFFFFFFA      MOVE.L #-6,(A1)        Yes, STATUS = hard error
581 9 000002F2 6000FE62          BRA RETURN.I           Return

```

MOTOROLA M68000 ASM VERSION 1.30SYS : 15.  
----- NPC 3100 MAGTAPE DRIVER -----

.MAGDVR .SA 03/08/84 16:38:56

PAGE 15

```
582                                     *  
583 9 000002F6 22BCFFFFFFFF WRITE.I2 MOVE.L #-3,(A1)      STATUS = EOT error  
584 9 000002FC 6000FE58                BRA      RETURN.I    Return  
585                                     *
```

```
587
588
589
590
591 9          00000300 REWIND.S EQU      *
592 9 00000300 082800020021 BTST.B   #2,STAT1(A0)   Are we at load point?
593 9 00000306 6618      BNE.S     REWND.S1      Yes, go return
594 9 00000308 317C00010010 MOVE.W   #1,DSCMD(A0)   Issue rewind command
595 9 0000030E 22BCFFFFFFF9 MOVE.L   #-7,(A1)      STATUS = busy
596 9 00000314 13FC00010000 MOVE.B   #1,MAGINTRP   Set valid interrupt flag
                    0001
597 9 0000031C 6000FD94      BRA      RETURN.S      Return
598
599 9 00000320 4291      REWND.S1 CLR.L   (A1)      STATUS = ready
600 9 00000322 33FC00010000 MOVE.W   #1,ACTFILE    Reset file counter (load point = file 1)
                    05E6
601 9 0000032A 6000FD86      BRA      RETURN.S      Return
602
603
604
605
606
607 9          0000032E REWIND.I EQU      *
608 9 0000032E 08010002      BTST   #2,D1          Check for load point
609 9 00000332 660A      BNE.S   REWND.I1     Yes, continue
610 9 00000334 22BCFFFFFFFE MOVE.L   #-2,(A1)     No, STATUS = BOT error
611 9 0000033A 6000FE1A      BRA     RETURN.I      Return
612
613 9 0000033E 4291      REWND.I1 CLR.L   (A1)     STATUS = ready
614 9 00000340 33FC00010000 MOVE.W   #1,ACTFILE    Reset file counter (load point = file 1)
                    05E6
615 9 00000348 6000FE0C      BRA     RETURN.I      Return
616
```

```
618
619
620
621
622 9 0000034C 0000034C SEARCH.S EQU x
623 9 0000034C 3212 MOVE.W (A2),D1 Get requested file #
624 9 0000034E 0C410001 CMP.W #1,D1 Is it file #1?
625 9 00000352 6E0A BGT.S SERCH.S7 No, continue
626 9 00000354 13FC000C0000 MOVE.B #12,COMMAND Yes, set adjusted command to rewind
        05DB
627 9 0000035C 60A2 BRA REWIND.S Go do rewind
628
629 9 0000035E 33C1000005E8 SERCH.S7 MOVE.W D1,REQFILE Save for later
630 9 00000364 9279000005E6 SUB.W ACTFILE,D1 Determine number of files to search and
631 9 0000036A 33C1000005EC MOVE.W D1,FILECNT direction to search
632 9 00000370 6F32 BLE.S SERCH.S2 Go search backwards
633
634
635
636 9 00000372 082800030021 BTST.B #3,STAT1(A0) Is it EOT?
637 9 00000378 670A BEQ.S SERCH.S1 No, continue
638 9 0000037A 22BCFFFFFFF9 MOVE.L #-3,(A1) Yes, STATUS = EOT error
639 9 00000380 6000FD30 BRA RETURN.S Return
640
641 9 00000384 117C00140008 SERCH.S1 MOVE.B #FILEFWD,GRCMD(A0) Issue forward file command
642 9 0000038A 13FC00010000 MOVE.B #1,DIR_FLG Set direction flag to forward
        05EA
643 9 00000392 22BCFFFFFFF9 MOVE.L #-7,(A1) STATUS = busy
644 9 00000398 13FC00010000 MOVE.B #1,MAGINTRP Set valid interrupt flag
        0001
645 9 000003A0 6000FD10 BRA RETURN.S Return
646
647
648
649 9 000003A4 082800020021 SERCH.S2 BTST.B #2,STAT1(A0) At load point?
650 9 000003AA 670A BEQ.S SERCH.S3 No, continue
651 9 000003AC 22BCFFFFFFFE MOVE.L #-2,(A1) Yes, STATUS = BOT error
652 9 000003B2 6000FCFE BRA RETURN.S
653
654 9 000003B6 117C00150008 SERCH.S3 MOVE.B #FILEREV,GRCMD(A0) Issue reverse file command
655 9 000003BC 4239000005EA CLR.B DIR_FLG Set direction flag to reverse
656 9 000003C2 22BCFFFFFFF9 MOVE.L #-7,(A1) STATUS = busy
657 9 000003C8 13FC00010000 MOVE.B #1,MAGINTRP Set valid interrupt flag
        0001
658 9 000003D0 6000FCE0 BRA RETURN.S Return
659
660
661
662
663
664 9 000003D4 000003D4 SEARCH.I EQU x
665 9 000003D4 08000000 BTST #0,D0 Is it 1600 bpi (PE)?
666 9 000003D8 6600FD82 BNE RETN1.I YES, then just ignore 1st interrupt
667 9 000003DC 08010004 BTST #4,D1 Is FNBSY set?
668 9 000003E0 66000094 BNE SRCH.I10 Check for blank tape error
669 9 000003E4 5379000005EC SUBQ.W #1,FILECNT Decrement file search
670 9 000003EA 4A39000005EA TST.B DIR_FLG Determine direction
671 9 000003F0 673C BEQ.S SERCH.I5 Search backwards
```



```
672 *
673 * SEARCH FORWARD
674 *
675 9 000003F2 08010003 SERCH.I1 BTST #3,D1 Is it EOT?
676 9 000003F6 670A BEQ.S SERCH.I2 No, continue
677 9 000003F8 22BCFFFFFFFD MOVE.L #-3,(A1) Yes, STATUS = EOT error
678 9 000003FE 6000FD56 BRA RETURN.I Return
679 *
680 9 00000402 5279000005E6 SERCH.I2 ADDQ.W #1,ACTFILE Increment file counter
681 9 00000408 3439000005E8 MOVE.W REQFILE,D2 Get requested file #
682 9 0000040E B479000005E6 CMP.W ACTFILE,D2 More to search?
683 9 00000414 6712 BEQ.S SERCH.I4 No, continue
684 9 00000416 082800070029 SERCH.I3 BTST #7,STAT2(A0) Wait for
685 9 0000041C 67F8 BEQ.S SERCH.I3 Try again
686 9 0000041E 117C00140008 MOVE.B #FILEFWD,GRCMD(A0) Yes, issue forward file command
687 9 00000424 6000FD36 BRA RETN1.I Return
688 *
689 9 00000428 4291 SERCH.I4 CLR.L (A1) STATUS = ready
690 9 0000042A 6000FD2A BRA RETURN.I Return
691 *
692 * SEARCH BACKWARDS
693 *
694 9 0000042E 08010002 SERCH.I5 BTST #2,D1 Is it BOT?
695 9 00000432 670A BEQ.S SERCH.I6 No, continue
696 9 00000434 22BCFFFFFFFE MOVE.L #-2,(A1) STATUS = BOT error
697 9 0000043A 6000FD1A BRA RETURN.I Return
698 *
699 9 0000043E 5379000005E6 SERCH.I6 SUBQ.W #1,ACTFILE Decrement file counter
700 9 00000444 3439000005E8 MOVE.W REQFILE,D2 Get requested file #
701 9 0000044A B479000005E6 CMP.W ACTFILE,D2 More to search?
702 9 00000450 6E12 BGT.S SERCH.I7 No, continue
703 9 00000452 082800070029 SERCH.I8 BTST #7,STAT2(A0) Wait for DNBSY
704 9 00000458 67F8 BEQ.S SERCH.I8
705 9 0000045A 117C00150008 MOVE.B #FILEREV,GRCMD(A0) Issue backspace file command
706 9 00000460 6000FCFA BRA RETN1.I Return
707 *
708 9 00000464 13FC00010000 SERCH.I7 MOVE.B #1,DIR_FLG Set direction flag to forward
709 9 0000046C 05EA
710 9 0000046C 082800040021 SERCH.I9 BTST #4,STAT1(A0) Wait for FNBSY (changing direction)
711 9 00000472 67F8 BEQ.S SERCH.I9 Busy - try again
712 9 00000474 60A0 BRA SERCH.I3 No, move to other side of file mark
713 *
714 * If FILECNT >= 0 then drive stopped with more files to search and
715 * is therefore a BLANK TAPE ERROR (searched past end of written tape)
716 9 00000476 4A79000005EC SRCH.I10 TST.W FILECNT Check file counter
717 9 0000047C 6F00FCDB BLE RETURN.I If count<=0 ignore interrupt
718 9 00000480 22BCFFFFFFEC MOVE.L #-20,(A1) STATUS = Blank tape error
719 9 00000486 6000FCCE BRA RETURN.I Return
720 *
```

```
722
723
724
725
726 9 0000048A FILMRK.S EQU *
727 9 0000048A 082800060021 BTST.B #6,STAT1(A0) Is there a write ring?
728 9 00000490 670A BEQ.S FILMK.S1 Yes, continue
729 9 00000492 22BCFFFFFFFB MOVE.L #-5,(A1) STATUS = file protect error
730 9 00000498 6000FC18 BRA RETURN.S Return
731
732 9 0000049C 117C00060008 FILMK.S1 MOVE.B #WRTEOF,GRCMD(A0) Issue file mark command
733 9 000004A2 22BCFFFFFFF9 MOVE.L #-7,(A1) STATUS = busy
734 9 000004A8 13FC00010000 MOVE.B #1,MAGINTRP Set valid interrupt flag
0001
735 9 000004B0 6000FC00 BRA RETURN.S Return
736
737
738
739
740
741 9 000004B4 FILMRK.I EQU *
742 9 000004B4 08000001 BTST #1,D0 Correct interrupt?
743 9 000004B8 661A BNE.S FILMK.I2 Yes, continue
744 9 000004BA 08010003 BTST #3,D1 No - error, is it EOT?
745 9 000004BE 670A BEQ.S FILMK.I1 No, must be hard error
746 9 000004C0 22BCFFFFFFFD MOVE.L #-3,(A1) Yes, STATUS = EOT error
747 9 000004C6 6000FC8E BRA RETURN.I Return
748
749 9 000004CA 22BCFFFFFFFA FILMK.I1 MOVE.L #-6,(A1) STATUS = hard error
750 9 000004D0 6000FC84 BRA RETURN.I Return
751
752 9 000004D4 08010004 FILMK.I2 BTST #4,D1 Is it FNBSY?
753 9 000004D8 6700FC82 BEQ RETN1.I No, ignore interrupt
754 9 000004DC 4291 CLR.L (A1) Yes, STATUS = ready
755 9 000004DE 5279000005E6 ADDQ.W #1,ACTFILE Increment file counter
756 9 000004E4 6000FC70 BRA RETURN.I Return
757
```

```
759
760
761
762
763 9 000004E8 000004E8 SKPREC.S EQU x
764 9 000004E8 3412 MOVE.W (A2),D2 Get repeat count
765 9 000004EA 33C2000005EC MOVE.W D2,REPEAT Save for later
766 9 000004F0 4A42 TST.W D2 Determine direction
767 9 000004F2 6700FEBE BEQ RETURN.S Repeat = 0, don't move
768 9 000004F6 6D2A BLT.S SKPRC.S2 Go skip backward
769
770
771
772 9 000004F8 082B00030021 BTST.B #3,STAT1(A0) Is it EOT?
773 9 000004FE 670A BEQ.S SKPRC.S1 No, continue
774 9 00000500 22BCFFFFFFFD MOVE.L #-3,(A1) Yes, STATUS = EOT error
775 9 00000506 6000FBAA BRA RETURN.S Return
776
777 9 0000050A 117C00100008 SKPRC.S1 MOVE.B #SKPFWD,GRCMD(A0) Issue skip record forward command
778 9 00000510 22BCFFFFFFF9 MOVE.L #-7,(A1) STATUS = busy
779 9 00000516 13FC00010000 MOVE.B #1,MAGINTRP Set valid interrupt flag
0001
780 9 0000051E 6000FB92 BRA RETURN.S Return
781
782
783
784 9 00000522 082B00020021 SKPRC.S2 BTST.B #2,STAT1(A0) Is it BOT?
785 9 00000528 670A BEQ.S SKPRC.S3 No, continue
786 9 0000052A 22BCFFFFFFFE MOVE.L #-2,(A1) Yes, STATUS = BOT error
787 9 00000530 6000FB80 BRA RETURN.S Return
788
789 9 00000534 117C00110008 SKPRC.S3 MOVE.B #SKPREV,GRCMD(A0) Issue skip record reverse command
790 9 0000053A 22BCFFFFFFF9 MOVE.L #-7,(A1) STATUS = busy
791 9 00000540 13FC00010000 MOVE.B #1,MAGINTRP Set valid interrupt flag
0001
792 9 00000548 6000FB68 BRA RETURN.S Return
793
794
795
796
797
798 9 0000054C 0000054C SKPREC.I EQU x
799 9 0000054C 3439000005EC MOVE.W REPEAT,D2 Get repeat count
800 9 00000552 4A42 TST.W D2 Determine direction
801 9 00000554 6D34 BLT.S SKPRC.I4 Skip backwards
802
803
804
805 9 00000556 08010003 BTST.B #3,D1 Is it EOT?
806 9 0000055A 670A BEQ.S SKPRC.I1 No, continue
807 9 0000055C 22BCFFFFFFFD MOVE.L #-3,(A1) Yes, STATUS = EOT error
808 9 00000562 6000FBF2 BRA RETURN.S Return
809
810 9 00000566 5342 SKPRC.I1 SUBQ.W #1,D2 Decrement repeat count
811 9 00000568 33C2000005EC MOVE.W D2,REPEAT Save new repeat count
812 9 0000056E 4A42 TST.W D2 More to skip?
813 9 00000570 6F12 BLE.S SKPRC.I3 No, continue
814 9 00000572 082B00070029 SKPRC.I2 BTST.B #7,STAT2(A0) Is DNBSY?
```

815	9	00000578	67F8		BEG.S	SKPRC.I2		No, try again
816	9	0000057A	117C00100008		MOVE.B	#SKFFWD,GRCHD(A0)		Yes, issue skip forward record command
817	9	00000580	6000FBDA		BRA	RETN1.I		Return
818				x				
819	9	00000584	4291		SKPRC.I3	CLR.L	(A1)	STATUS = ready
820	9	00000586	6000FBCE		BRA	RETURN.I		RETURN
821				x				
822				x		SKIP RECORD(S) BACKWARDS		
823				x				
824	9	0000058A	08010002		SKPRC.I4	BTST	#2,D1	Is it BOT?
825	9	0000058E	670A		BEG.S	SKPRC.I5		No, continue
826	9	00000590	22BCFFFFFFFE		MOVE.L	#-2,(A1)		Yes, STATUS = BOT error
827	9	00000596	6000FBBE		BRA	RETURN.I		Return
828				x				
829	9	0000059A	5242		SKPRC.I5	ADDQ.W	#1,D2	Increment repeat count
830	9	0000059C	33C2000005EC		MOVE.W	D2,REPEAT		Save new repeat count
831	9	000005A2	4A42		TST.W	D2		More to skip?
832	9	000005A4	6CDE		BGE.S	SKPRC.I3		No, continue
833	9	000005A6	082800070029		SKPRC.I6	BTST	#7,STAT2(A0)	Is DNBSY?
834	9	000005AC	67F8		BEG.S	SKPRC.I6		No, try again
835	9	000005AE	117C00110008		MOVE.B	#SKPREV,GRCHD(A0)		Yes, issue skip reverse command
836	9	000005B4	6000FBA6		BRA	RETN1.I		Return
837				x				

```
839
840
841
842
843 9      000005B8      MAGDVR.T EQU      *
844 9 000005B8 48F91F3F0000      MOVEM.L REGLIST,REGSAVE      Save the registers
      0002
845 9 000005C0 2279000005DC      MOVEA.L STAT_ADR,A1      Get the status address
846 9 000005C6 22BCFFFFFFF6      MOVE.L #-10,(A1)      STATUS = time out
847 9 000005CC 4CFA1F3FFA32      MOVEM.L REGSAVE,REGLIST      Restore registers
848 9 000005D2 4E73      RTE      Return
849      *
```

MOTOROLA M48000 ASM VERSION 1.30SYS : 15.  
----- NFC 3100 MAGTAPE DRIVER -----

.MAGDVR .SA 03/08/84 16:38:56

```
851 *****
852 *          CONSTANTS AND STORAGE          *
853 *****
854 *
855 9 000005D4 00040000  MAG_ID   DC.L     $40000      Base address of MAG I/O
856 9 000005D8 00000002  PSWSAVE DS.W     1          Processor status word
857 9 000005DA 00000001  RETRY   DS.B     1          Re-read counter
858 9 000005DB 00000001  COMMAND DS.B     1          Copy of present command
859 9 000005DC 00000004  STAT_ADR DS.L    1          Current status address
860 9 000005E0 00000004  BUF_ADR DS.L    1          Buffer address
861 9 000005E4 00000002  BUF_CNT DS.W     1          Byte count of buffer
862 9 000005E6 00000002  ACTFILE DS.W     1          Actual file number
863 9 000005E8 00000002  REQFILE DS.W     1          Requested file number
864 9 000005EA 00000001  DIR_FLG DS.B     1          Direction flag
865 9 000005EC 00000000  FILECNT DS.W     0          File counter for searching
866 9 000005EC 00000002  REPEAT  DS.W     1          Skip record counter
867 *
868          END
```

```
***** TOTAL ERRORS      0--
***** TOTAL WARNINGS    0--
```







MOTOROLA M68000 ASM VERSION 1.30SYS : 15.  
----- NFC 3100 MAGTAPE DRIVER -----

PAGE 26

.MAGDVR .SA 03/08/84 16:38:56

WRITE.S4	9	00000294	-543	556
WRTEOF		00000006	-252	732
WRTFND		00000002	-251	559