

.REM -

IDENTIFICATION

PRODUCT CODE: AC-E703I-MC
PRODUCT NAME: CXTMAIC TM11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITALS COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1973,1978 DIGITAL EQUIPMENT CORPORATION

MAIN DEC CHANGE NOTICE
MAY BE REQUIRED FOR
PROGRAM TO OPERATE

1. ABSTRACT

TMA IS AN IOMODX THAT EXERCISES UP TO 8 TAPE DRIVES ON AN
TM11 CONTROLLER. IT EXERCISES THE DRIVES BY DOING WRITES,
BACKSPACES, READS, AND IN-CORE COMPARISONS.
ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

HARDWARE: 1 TO 8 TAPE DRIVES WITH A TM11 CONTROLLER
STORAGE:: TMA REQUIRES:
 1. DECIMAL WORDS: 950
 2. OCTAL WORDS: 1666
 3. OCTAL BYTES: 3554

3. PASS DEFINITION

ONE PASS OF THE TMA MODULE CONSISTS OF 256 CYCLES OF THE BASIC TEST
SEQUENCE (WRITE, BACKSPACE, READ, DATA-CHECK). THE TEST SEQUENCE
WRITES 1024 WORDS, BACKSPACES SAME, READS THE FIRST 256 WORDS,
AND DATA-CHECKS SAME.

4. EXECUTION TIME

ONE PASS OF TMA RUNNING ALONE ON A PDP-11/40 TAKES APPROXIMATELY 1 MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DEVADR: 172520, VECTOR: 224, BP1: 5, DEVCNT: 1
REQUIRED PARAMETERS:
NONE

6. DEVICE/OPTION SETUP

MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY

7. MODULE OPERATION

TEST SEQUENCE:

- A. SETUP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES
- B. RESET ALL DRIVES ON-LINE AND DROP ALL THAT ARE NOT
- C. GET A FRESH BLOCK OF DATA
- D. GET A DRIVE ADDRESS
- E. DO A WRITE -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- F. DO A BACKSPACE -- IF ERRORS, REPORT
- G. DO A READ -- IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT
- H. DO A DATA-CHECK -- IF ERRORS, REPORT AND CONTINUE
- I. IF END OF PASS, REPORT AND GO TO C
- J. IF END OF DRIVES, GO TO C ELSE GO TO D
- K. IF END OF TAPE, REWIND ALL DRIVES AND GO TO B

8. OPERATION OPTIONS

SRI BIT 0 SET(1):
IF THE RETRY LIMIT IS EXCEEDED ON ANY FUNCTION, A HARD ERROR
IS ASSUMED AND THE DRIVE IS DROPPED

SRI BIT 0 CLEAR(0):
IF THE RETRY LIMIT IS EXCEEDED, THE FUNCTION IS ABORTED AND
THE TESTING CONTINUES

9. NON-STANDARD PRINTOUTS

- A. MOST PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN
THE DEC/X11 DOCUMENT
- B. ERROR MESSAGES DUMP THE CONTENTS OF THE 6 TM11 REGISTERS
AND THE CYCLE COUNT IN THE FOLLOWING ORDER:
MTS MTC MTBRC MTCMA MTD MTRD CYCLE COUNT
- C. THE CYCLE COUNT LOCATION IN THE ERROR MESSAGE CONTAINS THE
CYCLE COUNT AT THE TIME OF THE ERROR. THIS SHOULD
AID IN NOTING ANY BAD SPOTS ON A TAPE.

```

000000- IDMODX (TMAI > 172520,224,5,0,0,256,22,BUFIN,256,,1024-
000000- MODULE 150000,TMAI,172520,224,5,0,0,256,22,BUFIN,256,,1024.
; -TITLE TMAI DEC/111, SYSTEM EXERCISER MODULE
; DDXCOM VERSION 6 23-MAY-78
;*****LIST BIN*****
000000- BEGIN:
000000- MODNAM: .ASCII (TMAI / ;MODULE NAME
000005- XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000006- ADDR: 172520 ;1ST DEVICE ADDR
000010- DECTOR: 2240+0 ;1ST DEVICE DECTOR.
000012- BR1: .BYTE PRTV5+0 ;1ST BR LEVEL.
000013- BR2: .BYTE PRTV0+0 ;2ND BR LEVEL.
000014- DVIS1: 0+1 ;DEVICE INDICATOR 1.
000016- SR1: OPEN ;SWITCH REGISTER 1
000020- SR2: OPEN ;SWITCH REGISTER 2
000022- SR3: OPEN ;SWITCH REGISTER 3
000024- SR4: OPEN ;SWITCH REGISTER 4
;*****
000026- 150000 STAT: 150000 ;STATUS WORD
000030- 000022 INT1: START ;MODULE START ADDR
000032- 000022 SPOIN: MODSP ;MODULE STACK POINTER.
000034- 000000 PASCNT: 0 ;PASS COUNTER
000036- 000256 ICNT: 256 ;# OF ITERATIONS PER PASS=256
000040- 000000 ICOUN: 0 ;LOC TO COUNT ITERATIONS
000042- 000000 HRCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000044- 000000 SPPAS: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000046- 000000 HRPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050- 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052- 000000 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
000054- 000000 CONFIG: 0 ;HOLDS RANUM # WHEN RAND MACRO IS CALLED
000056- 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062- 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
000104- 000000 ACSAR: OPEN ;CONTENTS OF CSR.
000106- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
000108- 000000 ASAT: OPEN ;STATUS REG CONTENTS.
000110- 000000 ERRTYP: OPEN ;TYPE OF ERROR
000112- 000000 ASB: OPEN ;EXPECTED DATA.
000114- 000000 AWAS: OPEN ;ACTUAL DATA.
000116- 000000 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000118- 000000 WDRF: OPEN ;WORDS TO MEMORY PER ITERATION
000120- 000000 INTR: OPEN ;WORDS FROM MEMORY PER ITERATION
000122- 000022 IDNUM: 22 ;# OF INTERRUPTS PER ITERATION
;MODULE IDENTIFICATION NUMBER=22
    
```

```

000124- 002404- RBUFVA: BUFIN ;READ BUFFER VIRTUAL ADDRESS
000126- 000000 RBUFP: OPEN ;READ BUFFER PHYSICAL ADDRESS
000130- 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
000132- 000400 RBUFSZ: 256 ;SIZE OF THE READ BUFFER
000134- 000000 WBUFP: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
000136- 000000 WBUFEA: OPEN ;WRITE BUFFER EA BITS
000140- 002000 WBUFSZ: 1024 ;WRITE BUFFER SIZE REQUESTED
000142- 000000 WBUFA: OPEN ;WRITE BUFFER SIZE AVAILABLE
000144- 000000 CDRECT: OPEN ;CDATA/DATCK ERROR COUNT
000146- 000000 CDWCT: OPEN ;CDATA/DATCK WORD COUNT
000150- 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
;REPT SPSIZ ;MODULE STACK STARTS HERE.
;LIST 0
;WORD
;LIST
;ENDR
000252- MODSP:
;*****
    
```

```

216
217 000252 012767 000003 177640 START: MOV #3,INTR ;3 INTERRUPTS PER ITERATION
218 000260 012767 002000 177630 MOV #124,WDFR ;124 WORDS PER ITERATION FROM MEM
219 000266 012767 000400 177620 MOV #256,WDT0 ;256 WORDS TO MEM PER ITERATION
220 000274 105067 003250 CLR RFLAG ;CLEAR FLAGS
221 000300 016767 177510 002054 MOV DVID1,DVICE ;GET DRIVE INDICATOR
222 000306 016767 002850 002050 MOV DVICE,DRIVE ;ALSO SAVE IT IN DRIVE
223 000314 016767 000400 CLR DVICE ;ZERO UNIT NUMBER
224 000320 012767 177400 002042 MOV #400,DRVSFT ;INITIALIZE THE SHIFTED DRIVE #
225
226
227
228 000326 132737 000004 000041 ;FIND IF TM IS LOAD MEDIUM, IF SO, DROP LOAD DRIVE IF SELECTED
229 000334 001421 BITB #BIT2,@#41 ;IS TM THE LOAD MEDIUM?
230 000336 113700 BEQ LS ;NO, CONTINUE
231 000342 012701 000001 MOV #40,PO ;GET LOAD DRIVE #
232 000346 135700 TSTR #1,R1 ;SET UP DRIVE MASK
233 000350 001403 BEQ CS ;HAVE LOAD DRIVE YET?
234 000352 006301 ASL R1 ;YES, GO DROP IT
235 000354 105300 DECB R0 ;NO, SHIFT DRIVE MASK
236 000360 030167 001776 R0 ;COUNT SHIFTS, LOAD DRIVE MASK
237 000364 001405 BIT #1,DVICE ;LOAD DRIVE SELECTED?
238 000366 004767 001146 BEQ LS ;NO, CONTINUE
239 000368 004767 000000 JSR PC,DROP ;YES, GO DROP IT
240 000372 104403 MSGNS,BEGIN,DRP ;ASCII MESSAGE CALL WITH COMMON HEADER
241
242
243 000400 012767 177777 001760 1S: MOV #-1,DRVVE ;INITIALIZE DRIVE COUNTER
244 000406 004767 JSR PC,SETUP ;GENERATE REGISTER ADDRESSES
245 000412 004767 JSR PC,SETZET ;INITIALIZE TM REGS. AND ALL DRIVES
246 000416 005767 TST DVICE ;DROP THE MODULE?
247 000422 001533 BEQ YES ;YES
248 000424 004767 JSR PC,REWIND ;REWIND ALL DRIVES
249 000426 006301 CLR CYCNT ;ZERO TOTAL CYCLE COUNTER
250 000430 006301 BR COMT
251 000436 005767 177372 RESTRT: TST PASCNT ;ANY PASSES MADE YET?
252 000442 001703 BEQ ;NO BRANCH
253
254
255 000444 104415 000000 000124 CONT: GETPAS,REGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
256 000452 016767 177454 001722 MOV RBUFSZ,WCNT2 ;SAVE READ BUFFER SIZE
257 000460 005467 001712 NEG WCNT2 ;GET THE 2'S COMPLEMENT
258 000464 006367 ASL WCNT2 ;DOUBLE IT TO GET A BYTE COUNT
259
260
261 000470 004767 000000 001676 STRT: GNBUFFS, BEGIN ;GET WRITE BUFFER INFORMATION
262 000476 001367 MOV WBUFSZ,WCNT1 ;SAVE WRITE BUFFER SIZE
263 000482 005467 NEG WCNT1 ;GET THE 2'S COMPLEMENT
264 000486 006367 ASL WCNT1 ;DOUBLE IT TO GET A BYTE COUNT
265
266
267 000512 004767 001060 NEXT: JSR PC,DRVADR ;GET A DRIVE ADDRESS
268 000516 005767 TST DVICE ;ANY DRIVES LEFT?
269 000522 001473 BEQ FINI ;NO, GO DROP THE MODULE
270 000526 132767 BITB #BIT3,FLAG ;ALL DRIVES DONE?
271 000532 001367 RNE STRT ;YES, GO GET ANOTHER BLOCK
272 000534 042777 BITC #3400,@MTC ;CLEAR OUT UNIT NUMBER
273 000538 056777 DRVSFT,@MTC ;LOAD SELECTED DRIVE NUMBER
274 000542 032777 BIT #BIT2,@MTC ;WRITE PROTECTED?
275 000546 000004 BEQ ;NO, CONTINUE

```

```

272 000560 004767 000754 JSR PC,DROP ;YES, DROP THE DRIVE
273 000564 104403 MSGNS,BEGIN,DRP ;ASCII MESSAGE CALL WITH COMMON HEADER
274 000572 000737 RNE NEXT ;GO ON TO NEXT DRIVE
275 000574 032777 000001 002606 1S: BIT #BIT0,@MTC ;DRIVE READY?
276 000602 001060 BNE CS ;YES, CONTINUE
277 000604 004767 JSR PC,NOTRDY ;NO, WAIT FOR READY
278 000610 000727 RPT STRT ;TRY AGAIN
279 000612 005027 002734 2S: CLR TRV1 ;ZERO RETRY COUNTERS

```

```

280
281
282 000616- 004567 000222 RITE: JSR R5,WRITE ; WRITE SOME DATA
283 000622- 000435 BR RETRY1 ; IF ERRORS, TRY IT AGAIN
284 000622- 132767 000004 002716 BITFB #BIT2,FLAG ; DID THE TAPE REACH EOT?
285 000632- 001410 BEQ BACK ; NO, CONTINUE
286 000634- 142767 000004 002706 RITEOT: BICB #BIT2,FLAG ; YES, CLEAR THE EOT FLAG
287 000642- 004767 000404 JSR PC,REWIND ; REWIND ALL DRIVES
288 000642- 001524 CLR CNT ; START OVER AT BEGINNING OF TAPES
289 000652- 000906 BR STRT ; BACKSPACE THE DRIVE
290 000654- 004567 000216 BACK: JSR R5,BACKSP ; BACKSPACE THE DRIVE
291 000660- 000240 NOP ; ERROR RETURN
292 000662- 004567 000226 REED: JSR R5,READ ; READ THE DATA WRITTEN
293 000666- 000440 BR RETRY2 ; IF ERRORS, TRY AGAIN
294 000670- 104412 000000- 000126- CDATAS,BEGIN,RRUFPA ; REQUEST FOR MONITOR TO CHECK DATA
295 000676- 000700- +2 ; IF ERROR, CONTINUE
296
297
298 000700- 005267 001472 INC CYCKNT ; ADD 1 TO TOTAL CYCLE COUNT
299 000704- 104413 000000- ENDITS,BEGIN ; SIGNAL END OF ITERATION.
300 BR NEXT ; MONITOR SHALL TEST END OF PASS
301
302 000712- 104410 000000- FINI: ENDS,BEGIN ; DROP THE MODULE
303 ;
304
305
306
307
308
309 000716- 132767 000004 002624 RETRY1: BITB #BIT2,FLAG ; TAPE REACH EOT?
310 000724- 001343 BNE RITEOT ; YES - TO EOT ROUTINE
311 000726- 105267 002620 INCB TRV1 ; COUNT THE RETRYS
312 000740- 001404 CMPB #3,TRV1 ; LIMIT EXCEEDED?
313 000742- 004567 000130 BRG JSR R5,BACKSP ; YES, GO REWIND IT
314 000746- 000240 NOP ; NO, BACKUP TO TRY AGAIN
315 000752- 000722 BR RITE ; ERROR RETURN
316 ; GO TRY AGAIN
317
318 000752- 104403 000000- 003514- MSGNS,BEGIN,EXCED1 ; ASCII MESSAGE CALL WITH COMMON HEADER
319 000760- 004567 000162 JSR R5,WRITE ; SKIP SOME TAPE, WRITE WITH EXTENDED IRC
320 000766- 000413 BR NEXT ; ERROR RETURN
321 ; GO ON TO NEXT DRIVE
322
323 000770- 105267 002557 002551 RETRY2: INCB TRV2 ; COUNT RETRYS
324 000776- 001404 CMPB #3,TRV2 ; LIMIT EXCEEDED?
325 000780- 004567 000130 BRG JSR R5,BACK ; YES, GO REWIND IT
326 000784- 000240 BR BACK ; NO, BACKUP TO TRY AGAIN
327
328 000784- 104403 000000- 003522- 1S: MSGNS,BEGIN,EXCED2 ; ASCII MESSAGE CALL WITH COMMON HEADER
329 000790- 000240 BR NEXT ; GO ON TO NEXT DRIVE
330 ;
  
```

```

331
332
333 001016- 032767 000001 176772 NEXTA: BIT #BIT0,SRI ; DROP THE DRIVE?
334 001024- 001405 BRG JSR R5,DRP ; NO, SKIP TO NEXT DRIVE
335 001026- 004767 000506 003530- PC,DRP ; YES, DROP OFFENDING DRIVE
336 001030- 104403 000000- MSGNS,BEGIN,DRP ; ASCII MESSAGE CALL WITH COMMON HEADER
337 001040- 000167 177446 JMP NEXT ; GO ON TO NEXT DRIVE
338 ;
339
340 ----- TM11 TAPE DRIVERS -----
341
342
343 001044- 012767 060105 001304 WRITE: MOV #60105,FUNC ; LOAD WRITE FUNCTION
344 001050- 016777 060325 002334 MOV WCNTR1,&MTBRC ; LOAD BYTE COUNT
345 001056- 016777 177050 002330 MOV WRUFPA,&MTCMA ; LOAD BUFFER ADDRESS
346 001066- 016767 177044 001264 MOV WRUFPA,XMEM ; LOAD EXTENDED MEMORY BITS
347 001074- 000440 BR GOGO ; CONTINUE
348 001078- 012767 060113 001257 BACKSP: MOV #60113,FUNC ; LOAD BACKSPACE FUNCTION
349 001084- 012767 060325 002302 MOV W-1,&MTBRC ; LOAD BYTE COUNT
350 001112- 000431 BR GOGO ; CONTINUE
351 001114- 012767 060103 001234 READ: MOV #60103,FUNC ; LOAD READ FUNCTION
352 001120- 016777 060325 002302 MOV WCNTR1,&MTBRC ; LOAD BYTE COUNT
353 001130- 016777 176772 002260 MOV RRUFPA,&MTCMA ; LOAD BUFFER ADDRESS
354 001136- 016767 176766 001214 MOV RRUFPA,XMEM ; LOAD EXTENDED MEMORY BITS
355 001144- 000414 BR GOGO ; CONTINUE
356 001146- 012767 060115 001202 WRITEEX: MOV #60115,FUNC ; LOAD WRITE EXTENDED FUNCTION
357 001154- 016777 060325 002302 MOV WCNTR1,&MTBRC ; LOAD BYTE COUNT
358 001162- 016777 176746 002226 MOV WRUFPA,&MTCMA ; LOAD BUFFER ADDRESS
359 001170- 016767 176742 001162 MOV WRUFPA,XMEM ; LOAD EXTENDED MEMORY BITS
360
361
362 001176- 012777 001232- 176604 GOGO: MOV #NTRUPT,&VECTOR ; SET INTERRUPT ENTRY POINTER
363 001204- 056767 001160 001144 BIS DRYSP,FUNC ; LOAD DRIVE UNIT NUMBER
364 001210- 056767 001142 001126 BIS XMEM,FUNC ; LOAD EXTENDED MEMORY BITS
365 001226- 104400 000000- EXIT$,BEGIN ; EXECUTE THE FUNCTION
366 ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
367
368
369 001232- NTRUPT:
370 001232- 000004 000000- 001240- PRQS,BEGIN,1S ; QUEUE UP TO CONTINUE AT 1S AND RTI
371 ;
372
373 001240- 004567 060576 1S: JSR R5,ERRORS ; GO CHECK FOR ERRORS
374 001244- 000205 RTS R5 ; ERRORS DETECTED, RETURN
375 001246- 005728 TST (R5)+ ; NO ERRORS, SKIP RETRY
376 001250- 000205 RTS R5 ; RETURN OK
377 ;
  
```

```

378
380 001252 016767 001104 001104 REWIND: MOV DVICE,DRIVE ; GET ACTIVE DRIVES
381 001260 012767 000010 001104 MOV #1,DRIVE ; LOAD MAXIMUM NUMBER OF DRIVES
382 001266 016701 001100 MOV DVCNUM,R1 ; PUT IT INTO A COUNTER
383
384 001272 000241
385 001274 006267 001064 1S: CLC ; MAKE SURE C-BIT IS CLEAR
386 001300 103402 ; CHECK FOR ACTIVE DRIVE
387 001302 006361 001064 BCS 2S ; IT'S ACTIVE --- BRANCH
388 001306 006361 ; NOT ACTIVE, SUBTRACT FROM TOTAL
389 001310 003370 DEC R1 ; ALL CHECKED ?
390 ; NO, CONTINUE
391
392 001312 012767 000100 002066 MOV #100,CLK1 ; LOAD THE 2ND TIMER
393 001320 012767 177777 001040 MOV #1,DRIVE ; INITIALIZE THE DRIVE COUNTER
394 001326 005067 001042 CLR DVCNT ; CLEAR DEVICE COUNTER FOR ISR.
395 001332 016767 001024 001024 MOV DVICE,DRIVE ; RESTORE DRIVE INDICATOR
396 001340 012767 177400 001022 MOV #400,DRVSFT ; INITIALIZE SHIFTED DRIVE NUMBER
397 001346 004767 000810 002170 3S: JSR PC,ADR ; GO GET A DRIVE NUMBER
398 001352 132767 000810 002170 BITB #BITS,FLAG ; ALL DRIVES DONE ?
399 001360 001017 BNE 4S ; YES, GO WAIT FOR COMPLETION
400 001362 004767 JSR PC,WAIT ; CONTROLLER READY ?
401 001374 016767 000770 000754 MOV #340,&MTC ; YES, CLEAR OUT OLD UNIT NUMBER
402 001402 052767 000017 000746 BIS #17,&MTC ; LOAD NEW UNIT NUMBER
403 001410 000742 000742 MOV &MTC,PC ; LOAD REWIND FUNCTION
404 001416 000753 BR 3S ; EXECUTE THE REWIND
405 ; GO ON FOR THE NEXT DRIVE
406
407 001420 012767 077777 001756 4S: MOV #77777,CLK ; SET THE TIMER
408 001426 005067 000810 002110 5S: JSR PC,DRVADR ; GO GET A DRIVE NUMBER
409 001440 001036 BNE 8S ; YES, DRIVES DONE ?
410 001442 042777 003400 001742 BIC #340,&MTC ; YES, GET OUT
411 001456 056777 000714 001734 BIC #400,&MTC ; CLEAR OUT OLD UNIT NUMBER
412 ; LOAD NEW UNIT NUMBER
413
414 001456 104407 000000 000000 6S: BREAKS,BEGIN ; TEMPORARY RETURN TO MONITOR.
415 001462 104407 000000 001714 BREAKS,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
416 001474 001354 BIC #41,&MTC ; DRIVE READY AND AT BOT ?
417 001476 005367 001702 DEC CLK ; YES, GO CHECK THE NEXT DRIVE
418 001502 003365 BGT 6S ; NO, OUT OF TIME ?
419 001510 001407 BEQ 7S ; NO, WAIT SOME MORE
420 001512 012767 077777 001664 MOV #77777,CLK ; YES, CLEAR ANOTHER 40 SECONDS ?
421 001520 000756 BR 6S ; NO, TIME-OUT
422 001530 012767 000013 176356 7S: MOV #13,&ERRTP ; RESET THE TIMER
423 ; WAIT SOME MORE
424 ; REWIND ERROR
425
426 001530 104405 000000 000000 8S: *****
427 HRDERS,BEGIN,NULL ; REWIND ERROR, REWIND NOT COMPLETE
428 *****
429
430 001536 000207 RTS PC ; RETURN
431
432
433

```

```

434 001540 012701 000001 000001 DROP: MOV #1,R1 ; INITIALIZE DROP PICKER
435 001544 016700 000816 MOV DRIVE,R0 ; GET THE DRIVE NUMBER
436 001550 001403 BEQ 2S ; IF DRIVE 0 GO DROP IT
437 001554 008300 000816 1S: DEC R0 ; GET THE NEXT DRIVE
438 001556 001375 BNE 1S ; IS THIS THE ONE ?
439 001560 040167 000576 2S: RIC R,ADVICE ; NO, LOOK AGAIN
440 ; DROP THE DRIVE
441 *****
442 ; CONVERT DRIVE TO ASCII AND
443 ; STORE AT ADRI
444
445 001564 104420 000000 002366 OTOAS,BEGIN,DRIVE,ADRI
446 001572 003540 *****
447
448 001574 000207 RTS PC ; RETURN
449
450
451 001576 005267 000564 DRVADR: INC DRIVE ; COUNT A DRIVE
452 001582 004767 000400 000560 ADD #BITS,DRVSFT ; DRIVE COUNT LINED UP WITH MTC
453 001586 002767 000810 000542 BITB #BITS,FLAG ; CLEAR END OF DRIVES FLAG
454 001590 022767 000010 000542 CMP #B,DRIVE ; ALL DRIVES CHECKED ?
455 001624 001404 BEQ 1S ; YES, GO FLAG END OF DRIVES
456 001626 006267 000532 ASR DRIVE ; NO, IS NEXT DRIVE CHOSEN ?
457 001632 103361 BCC DRVADR ; NO, GO TRY ANOTHER DRIVE
458 001634 000207 RTS PC ; RETURN
459
460 001636 152767 000010 001704 1S: BISB #BITS,FLAG ; SET END OF DRIVES FLAG
461 001642 012767 177400 000514 MOV #400,DRVSFT ; RESET DRIVE COUNTER
462 001650 012767 177400 000510 MOV DVICE,DRIVE ; ZERO THE SHIFTED DRIVE #
463 001660 016767 000476 000476 MOV DVICE,DRIVE ; RESTORE CHOSEN DRIVES
464 001666 000207 RTS PC ; RETURN
465
466
467
468 001670 012767 177777 000470 NOTRDY: MOV #1,DRIVE ; START WITH FIRST DRIVE
469 001704 016767 177400 000464 MOV #400,DRVSFT ; RESET DRIVE SELECT
470 001712 004767 177400 000452 MOV DVICE,DRIVE ; GET A DRIVE ADDRESS
471 001716 000010 001624 1S: JSR PC,DRVADR ; GET A DRIVE ADDRESS
472 001724 002767 000010 001624 BITB #BITS,FLAG ; ALL DRIVES CHECKED ?
473 001726 016777 BNE 2S ; YES, RETURN
474 001734 000436 001456 MOV DRVSFT,&MTC ; NO, LOAD NEXT DRIVE ADDRESS
475 001736 032777 000001 001446 BIT #BIT0,&MTC ; IS THIS DRIVE READY ?
476 001742 001363 BNE 1S ; YES, CONTINUE
477 001744 004767 JSR PC,WAIT ; NO, WAIT FOR IT
478 001750 000760 BR 1S ; GO CHECK REST OF DRIVES
479 001752 000207 RTS PC ; RETURN
480
481
482
483
484 001754 016767 001430 176116 ERSUB1: MOV #MTC,CSRA ; LOAD ADDR. OF CURRENT CSR
485 001756 017767 001422 176116 MOV #MTC,ACSR ; LOAD CONTENTS OF CURRENT CSR
486 001770 000207 RTS PC ; RETURN
487

```

```

488
489
490
491
492 001777 012767 077777 001404 WAIT: MOV #77777,CLK ; SET THE TIMER
493 002000 ;
494 002000 104407 000000 ; BREAKS,BEGIN ; TEMPORARY RETURN TO MONITOR....
495 002004 104407 000000 ; BREAKS,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
496
497 002310 032777 000001 001372 BIT #BIT0,@MTC ; DRIVE READY ?
498 002016 001010 RNE 2S ; YES, RETURN
499 002320 005367 001360 DEC CLK ; NO, WAIT SOME MORE ?
500 002024 001365 BNE 1S ; YES, WAIT
501 002026 004767 JSR PC DROP ; TIME-OUT DROP THE DRIVE
502 002032 104403 000000 003530 MESSAGES,BEGIN,DRP ;ASCII MESSAGE CALL WITH COMMON HEADER
503 002040 000207 RTS PC ; RETURN
504
505
506
507
508
509
510 002042 004767 177706 ERRORS: JSR PC ERSUB1 ; LOAD ERROR INFORMATION
511 002046 005777 001340 TST @MTC ; ANY ERRORS ?
512 002052 000477 RPL 4S ; NO, RETURN OK
513 002053 032777 001000 001326 BIT #BIT9,@MTC ; RECORD LENGTH ERROR ?
514 002064 032777 000002 001320 RIT #BIT1,@MTC ; WAS THE COMMAND A READ ?
515 002072 001425 BEQ 2S ; NO, REPORT THE ERROR
516 002074 032777 174600 001306 BIT #174600,@MTC ; YES, RLE ERR EXPECTED,CHECK OTHER ERRORS
517 002104 042767 001000 175770 BIC #BIT9,ACSR ; RETURN OK
518 002112 000415 RR 2S ; TERMINATE RLE BIT FROM
519 002114 032777 002000 001266 1S: BIT #BIT10,@MTC ; ERROR REPORT & GO REPORT IT
520 002114 032777 BEQ 2S ; AT END OF TAPE ?
521 002124 152767 000004 001416 BLSB #BIT2,FLAG ; YES, SET EOT FLAG
522 002132 032777 174600 001250 BIT #174600,@MTC ; NO / TEST OTHER ERRORS
523 002140 001414 BEQ 3S ; NONE RETURN OK
524 002146 005067 CLR ERRTP ; UNKNOWN ERROR
525
526
527
528 002146 104405 000000 003410 2S: *****
529 HDRS,BEGIN,TABLE ;
530 *****
531 MOV #BIT12,@MTC ; ISSUE A POWER CLEAR
532 BR 5S ; RETURN TO DO A RETRY
533
534
535 002164 012777 010000 001230 3S: MOV #BIT12,@MTC ; ISSUE A POWER CLEAR
536 002172 005725 TST #R5 ; SET THE RETRY
537 002174 000205 RTS R5 ; RETURN
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584

```

```

537
538
539 002176 016700 175604 SETUP: MOV ADDR,R0 ; GET DEVICE ADDRESS
540 002182 001292 JSR R0,MTC ; GENERATE CONTROLLER REGS. ADDRESSES
541 002206 005720 TST (R0)+
542 002210 010067 MOV R0,MTC
543 002214 005720 TST (R0)+
544 002216 010067 MOV R0,MTBRC
545 002220 005720 TST (R0)+
546 002224 010067 MOV R0,MTDMA
547 002230 005720 TST (R0)+
548 002232 010067 MOV R0,MTD
549 002240 010067 MOV R0,4TRD
550
551
552 002244 016700 175540 MOV VECTOR,R0 ; GET THE VECTOR ADDRESS
553 002250 118760 175536 MOVBR BRT,2(R0) ; SET PRIORITY
554
555
556 002256 000207 2S: RTS PC ; RETURN
557
558
559
560
561
562
563 002260 012777 010000 001124 REZET: MOV #BIT12,@MTC ; EXECUTE POWER CLEAR
564 002266 004767 JSR PC,WAIT ; GO WAIT FOR CONTROLLER READY
565 002272 004767 JSR PC,NOTRDY ; MAKE SURE ALL CHOSEN DRIVES ARE READY
566 002276 000207 RTS PC ; RETURN
567
568
569
570
571
572
573 002300 012767 077777 001076 WAIT1: MOV #77777,CLK ; SET THE TIMER
574 002306 105777 001100 1S: TSTB @MTC ; CONTROLLER READY ?
575 002312 104417 BMI 2S ; YES, CONTINUE
576 002314 104407 000000 ; BREAKS,BEGIN ; TEMPORARY RETURN TO MONITOR....
577 002320 104407 000000 ; BREAKS,BEGIN ; THEN CONTINUE AT NEXT INSTRUCTION.
578 002324 005367 001054 DEC CLK ; WAIT SOME MORE ?
579 002330 001365 BNE 1S ; YES
580 002332 001365 MOV #3,ERRTP ; CONTROLLER NOT READY
581 *****
582 HDRS,BEGIN,TABLE ; CONTROLLER NOT READY
583 *****
584 JMP FINI ; GO DROP THE MODULE
585 RTS PC ; READY, RETURN
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684

```


XTMAIO.P11 12-OCT-78 12:21 CROSS REFERENCE TABLE -- USER SYMBOLS

ACSR	000102R	187#	495*																
ADDR	000005R	153#	539	518*															
ADDR22=	001000	216#																	
ADR1	003540R	494#	634#																
ASB	000106R	189#																	
ASTAT	000104R	189#																	
AWAS	000110R	192#																	
BACASP	000559R	280#	290#	326															
BEGIN	000000R	150#	239	348#															
BIF0	000001	216#	413	414	258	273	294	299	304	318	328	336	366	370					
BIF1	000002	216#	414	426	444	494	495	502	528	573	574	579							
BIF10	002000	216#	520																
BIF11	004000	216#																	
BIF12	010000	216#	530	533	561														
BIF13	020000	216#																	
BIF14	040000	216#																	
BIF15	100000	216#																	
BIF2	000004	216#	227	270	284	286	388	522											
BIF3	000016R	216#	266	397	408	453	468	472											
BIF4	000020	216#																	
BIF5	000040	216#																	
BIF6	000100	216#																	
BIF7	000200	216#																	
BIF8	000400	216#	452																
BIF9	001000	216#	512	518															
BREAKS=	104407R	216#	413	414	494	495	573	574											
BR1	000012R	216#	553																
BR2	000013R	216#																	
BTODS =	104421R	216#																	
BUFIN	002404R	216#	598#																
CDATA5=	104412R	216#	294																
CDERCT	000144R	207#																	
CDWDCT	000146R	207#																	
CLK	003404R	406#	417*																
CLK1	003404R	406#	419*	421*	492*	499*	570*	575*	599#										
CLT	002354R	586#																	
COMPIC	000056R	175#																	
COMT	000444R	748#	251#																
CSRA	000106R	189#	484*																
CVC	003424R	608#																	
CYCKNT	002376R	247#	288*	298*	595#	608													
DATECS=	104477R	216#																	
DATE4	004414	222#																	
DRIVE	002364R	322#	380*	385*	394*	456*	463*	470*	590#										
DROP	001540R	338#																	
DRP	003530R	333#																	
DRVADR	003530R	333#																	
DRVSFT	002370R	224#																	
DRVE	002366R	323#	241*																
DVCKNT	002374R	393#	594#																
DVCHUM	000000	324#																	
DVICE	002362R	324#	387*	593#															
DVIDI	000014R	157#	221	236	244	264	380	394	440*	463	470	589#							
ENDITS=	104413	216#	299																

XTMAIO.P11 12-OCT-78 12:21 CROSS REFERENCE TABLE -- USER SYMBOLS

ENDS =	104410	216#	304																
ERRVDP	000102R	187#	493#																
ERRS0B1	001754R	484#	509	525*	577*														
EXCEDA	003514R	318#	644#																
EXTRD0	104420R	216#	568#																
FINI	000712R	245#	265	303#	581*	309	397	408	453*	460*	472	522*	637#						
PLAC	000350R	220#	266	284	286*														
PRSC	000350R	220#																	
FUNC	002355R	343#	348*	351*	356*	363*	364*	365	401*	402*	403	587#							
GETPAS=	104415	216#	352																
GOGGFC=	001176R	348#	355	362#															
GWBCNT=	000044R	170#	259																
HDRSNT=	104405	216#	426	528	579														
HDRPAS	000050R	174#																	
ICONT	000136R	168#																	
ICOUNT	000136R	168#																	
IDNUM	000122R	197#																	
IMDIX =	000060	203#	259																
INIT	000130R	164#																	
INTS	000130R	164#	217*																
MAC22S=	104416	216#																	
MES2	003430R	610#	630																
MES3	003442R	614#	626																
MES4	003452R	617#	624	628															
MES5	003476R	619#	627																
MES6	003505R	619#																	
MODRPM	000000R	151#																	
MODSPR	000252R	165#	214#																
NSGNS =	104403	239#	273	318	328	336	502												
NSGNS S =	104403	239#																	
NSGNS S =	104403	239#																	
NSGRC	003414R	368#	349*	352*	357*	544*	604#												
NIC	003412R	368#	369*	368*	400*	403*	410*	411*	474*	510	514	530*	533*	542*					
NFCHA	003416R	368#	345*	345*	358*	546*	605#												
NTRD	003422R	368#	548*																
NTRD	003422R	368#	550*																
NTRD	003410R	368#	607#																
NTRD	003410R	368#	270	275	274	415	485												

PRTV0	000000	156#	216#		
PRTV1	000100	216#			
PRTV2	000200	216#			
PRTV3	000300	216#			
PRTV4	000400	216#			
PRTV5	000500	216#	216#		
PRTV6	000600	216#			
PRTV7	000700	216#			
PS	177776	216#			
PUSH	005746	216#			
PUSH2	024646	216#			
RANDS	104417	216#			
RAHNM	00064R	200#	354		
RBUPFA	000130R	199#	204	353	
RBUPFA	000126R	201#	253		
RBUPSZ	000132R	168#	252		
RBUPFA	000124R	192#	351#		
READ	001114R	292#			
REED	000662R	193#	249#		
RESRT	000336R	176#			
RES1	000020R	177#			
RES2	000020R	177#			
RETRV1	000716R	283#	309#		
RETRV2	000770R	293#	323#		
REWIND	001252R	246#	390#		
REZLN	002200R	243#	361#		
RTE	000616R	282#	316		
RTECOT	000634R	286#	310		
SBADR	000112R	193#			
SBADR	000102R	193#			
SETUP	002176R	242#	539#		
SOPCNT	000042R	169#			
SOPERS	104406	216#			
SOPPAR	000124R	168#			
SPOINT	000032R	165#			
SPSTZ	000040	1	209		
SR1	000916R	158#	333		
SR2	000920R	158#			
SR3	000022R	160#			
SR4	000024R	161#			
START	000252R	164#	217#	250	
SARR	000470R	257#	267	278	289
SVR0	000062R	178#			
SVR1	000064R	178#			
SVR2	000066R	180#			
SVR3	000070R	181#			
SVR4	000072R	182#			
SVR5	000074R	183#			
SVR6	000076R	184#			
SVSCNT	000052R	173#			
TABLE	003410R	528#	579	601#	
TRPDF	000024R	216#			
TRY1	003524R	323*	311*	312	639#
TRY2	003553R	323*	324	640#	

VECTOR	000010R	154#	362*	552			
WAIT	001772R	477#	282#				
WAIT1	002300R	399#	562	570#			
WASADR	000104R	188#					
WBUPFA	000136R	202#	346	359			
WBUPFA	000134R	202#	345	358			
WBUPRQ	000140R	204#					
WBUPSZ	000142R	205#	259				
WCNT1	002406R	253*	261*	344	357	596#	
WCNT2	002402R	253*	254*	352	597#		
WDFR	000116R	195#	218*				
WDTO	001044R	194#	219*				
WRITE	001044R	282#	343#				
WRITEX	001146R	316#	356#				
XPLAC	000005R	152#					
XMEM	02360R	246#	354*	364	588#		
.	003554R	295	598#	621#	634#	638#	

ABS. 000000 000
 003554 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0
 XTMAIO, XTMAIO/SOL/CRP:SYM=DDXCOM, XTMAIO
 RUN-TIME: 1 2 3 SECONDS
 RUN-TIME RATIO: 18/4=4.6
 CORE USED: 7K (19 PAGES)

DIAGNOSTIC ENGINEERING

23

digital

DECO DEPO SUBMISSION

FOR RELEASE ENG. USE
 NEW CHANGE DELETE

PRODUCT IDENTIFICATION															
MD	LIBRARY ZZ	PRODUCT NUMBER CXTMA	REV 1	PATCH 1	ECO TALLY DI	PRODUCT DATE DD MMM YY NOV 17 78	STATUS OBSOLETE	DISTRIBUTION X G R	1ST COPY - RIGHT YEAR 1973	LAST COPY - RIGHT YEAR 1978					
TITLE CXTMAIL TM11 MOUDLE															
AUTHOR D. BUTENHOF			MAINTAINING GROUP DEC/X11 SPT GRP			MAINTAINER D. BUTENHOF		SUBMITTING ENGINEER D. BUTENHOF							
PRODUCT COMPONENTS															
CK	DESCRIPTION	PRODUCT NO	REV	CK	DESCRIPTION	PRODUCT NO	REV								
	DOCUMENT				INDEX										
	LISTING				SOURCE MEDIA										
	OBJECT MEDIA				TEST MEDIA										
X	DEPO	AF-E7031-M1													
PRODUCTS OBSOLETE (other than previous version)															
LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV							
MD			MD			MD									
PRODUCT CHARACTERISTICS															
PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)															
03	04	05	10	20	21	34	35	40	45	50	55	60	70		
OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)															
02	03	04	06	50											
ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME									
INFORMATION FIELD		1132	Y N	Y N	SECONDS	SECONDS									
DECO/DEPO INFORMATION															
PROBLEM REPORTS CLOSED:															
DEVICE AFFECTED DEC/X11				MULTIMEDIA AFFECTED? <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO											
IT NUMBERS	ZJ129-RZ,FR	ZJ239-RB,RY	ZJ240-RB,RE	ZJ240-FR	ZJ215-RY,RZ	ZJ239-RZ,PB,FR									
	ZJ240-RZ,PB	ZJ239-FR	ZJ130-RB												
PROBLEM: DOES BIT TEST FOR LOAD MEDIA CHECK INSTEAD OF COMPARE; WILL DROP SELF IF ACTUAL LOAD MEDIA SETS IT'S DESIGNATED BIT. (FOR EXAMPLE, IF LOAD MEDIA IS RL DISK)															
UNCONDITIONAL PATCH															
SOLUTION: PATCH BIT TEST TO COMPARE BYTE															
DEPO PATCH AREA															
CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO										
326	132737	122737													
SUBMITTING ENGINEER			MANUFACTURING ENGINEER			SUPPORT ENGINEER			CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER Q98-05314						
DATE: 8-NOV-78			DATE: 29-NOV-78			DATE:									
MAINTAINER			FIELD SERVICE			WAIVERING MANAGER			COORDINATION NO. MC#2503						
DATE: 8-NOV-78			DATE:			DATE:									

DIAGNOSTIC ENGINEERING



DECO DEPO SUBMISSION

FOR RELEASE ENG. USE
 NEW CHANGE DELETE

PRODUCT IDENTIFICATION

D	LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE			STATUS	DISTRIBUTION		1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
	ZZ	CXTMA	I	2	02	DD	MMM	YY	OBSOLETE	G	R	1973	1978

TITLE: CXTMAI2 TML1 MODULE
 AUTHORD. BUTENHOF MAINTAINING GROUP: DEC/X11 SPT MAINTAINER: D. BUTENHOF SUBMITTING ENGINEER: D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO	REV	CK	DESCRIPTION	PRODUCT NO	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X		AF-E703 I-M2					

PRODUCTS OBSOLETE (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2 digit codes representing the Processor the product operates with. See separate instructions.)
 03 04 05 10 20 21 34 35 40 45 50 55 60 70

OPERATIONAL CODES (Enter all applicable 2 digit codes that describe the product. See separate instructions.)
 02 03 04 06 50

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			X Y <input type="checkbox"/> N	X Y <input type="checkbox"/> N	SECONDS	SECONDS

DECO/DEPO INFORMATION

PROBLEM REPORTS CLOSED: _____

ICE AFFECTED: DEC/X11 MULTIMEDIA AFFECTED? YES NO

KIT NUMBERS: ZJ129-RZ,FR ZJ239-RB,RY ZJ240-RB,RE ZJ240-FR ZJ215-RY,RZ ZJ239-RZ,PB,FR
 ZJ240-RZ,PB ZJ239-FR

PROBLEM: WHEN PREVIOUS PATCH CHANGED BIT TEST TO COMPARE, BRANCH CONDITIONS WERE LEFT UNCHANGED, RESULTING IN DROP UNLESS MEDIA IS LOAD MEDIA.

SOLUTION: PATCH BEQ TO BNE

DEPO PATCH AREA

FROM	TO	CHANGE TO	FROM	TO
334	1421	1021		

SUBMITTING ENGINEER DATE: <i>[Signature]</i>	MANUFACTURING ENGINEER DATE: <i>[Signature]</i>	SUPPORT ENGINEER DATE:	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER 098-05314
PROJECT MANAGER DATE:	PROJECT ENGINEER DATE:	MANAGERING MANAGER DATE:	RECORDING ROOM MC 2786