

I D E N T I F I C A T I O N  
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PRODUCT CODE: MAINDEC-11-DJFPB-R-D  
PRODUCT NAME: PDP-11/0X - FP11-E FLOATING POINT UNIT  
ADVANCED INSTRUCTION TESTS  
DATE : MAY, 1977  
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CONTENTS  
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1. ABSTRACT
2. REQUIREMENTS
  - 2.1 EQUIPMENT
  - 2.2 STORAGE
  - 2.3 PRELIMINARY PROGRAMS
3. LOADING PROCEDURE
4. STARTING PROCEDURE
  - 4.1 CONTROL SWITCH SETTINGS
  - 4.2 STARTING ADDRESS
  - 4.3 PROGRAM/OPERATOR ACTION
5. OPERATING PROCEDURE
  - 5.1 OPERATIONAL SWITCH SETTINGS
  - 5.2 PROGRAM/OPERATOR ACTION
  - 5.3 HOT (FP11-E) / WARM (PDP-11/6X) SELECTION
6. ERRORS
  - 6.1.1 ERROR MESSAGE FORMAT
  - 6.1.2 FLOATING POINT DATA FORMAT
  - 6.2 RECOVERY
  - 6.3 CAUSES
7. RESTRICTIONS
  - 7.1 STARTING
  - 7.2 OPERATIONAL
8. MISCELLANEOUS
  - 8.1 EXECUTION TIME
  - 8.2 STACK POINTER
  - 8.3 POWER FAIL
9. PROGRAM DESCRIPTION
  - 9.1 ORGANIZATION
  - 9.2 TEST DESCRIPTION
  - 9.3 SUBROUTINE ABSTRACTS
10. ACT/APT/XXDP

1. ABSTRACT

THIS PROGRAM EXTENDS THE TESTING OF INSTRUCTION FUNCTIONALITY TO THE REMAINDER OF THE PDP-11/6X FLOATING POINT INSTRUCTION SET NOT COVERED IN THE BASIC INSTRUCTION TESTS. FULL TESTING IN ALL PDP-11/6X FPU MODES OF ALL THE MULTIPLE OPERAND ARITHMETIC, COMPARISON, AND INTEGER TO FLOAT CONVERSION INSTRUCTIONS IS PERFORMED. BOTH "HOT" (FP11-E OPTION) AND "WARM" (PDP-11/6X MICROCODE) FLOATING POINT UNITS CAN BE SELECTED FOR TESTING.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-11/6X STANDARD COMPUTER WITH MINIMUM 16K OF MEMORY. OPTIONAL FP11-E FLOATING POINT UNIT, IF SELECTED.

2.2 STORAGE

THE PROGRAM USES MEMORY 0-46346(8). THE UPPER 2.0K WORDS ARE RESERVED FOR THE XXDP MONITOR, IF EMPLOYED.

2.3 PRELIMINARY PROGRAMS

THE CPU, CACHE, AND MEMORY TEST PROGRAMS MUST BE RUN FIRST TO VERIFY THE CORRECT OPERATION OF THE BASE MACHINE.

THE PDP-11/6X - FP11-E FLOATING POINT PROCESSOR INSTRUCTION SET TESTS SHOULD THEN BE RUN IN THE FOLLOWING ORDER:

- (1) DQFPA FPU BASIC INSTRUCTION TESTS
- (2) DQFPB FPU ADVANCED INSTRUCTION TESTS
- (3) DQFPC FPU INSTRUCTION EXERCISER
- (4) DQFPO FPU ADD/SUB/MUL/DIV RANDOM EXERCISER

3. LOADING PROCEDURE

USE THE STANDARD PROCEDURE FOR ABSOLUTE TAPES, OR LOAD VIA XXDP MEDIA.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE SECTION 5.1  
SWITCH REGISTER (000000) IS WORST CASE TEST.

4.2 STARTING ADDRESS

THE PROGRAM MUST ALWAYS BE STARTED AT LOCATION 200(8).

4.3 PROGRAM/OPERATOR ACTION

LOADING VIA ABSOLUTE PAPERTAPE!

- (1) LOAD PROGRAM INTO MEMORY USING ABS LOADER.
- (2) LOAD ADDRESS 200 (8).
- (3) SET SWITCHES (SEE SECTION 5.1)  
SR=(200000) IS WORST CASE TEST.
- (4) PRESS CONTROL/START TO BEGIN.
- (5) PROGRAM TYPE'S IDENTIFICATION HEADER (VERIFY THAT THE CORRECT PROGRAM HAS BEEN LOADED), AND EXECUTION BEGINS.

5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

THE DEFINITION OF THE SPECIFIC BITS IN THE SWITCH REGISTER (EITHER HARDWARE OR SOFTWARE) ARE AS FOLLOWS:

- SW15=1 100000 HALT ON ERROR
- SW14=1 040000 LOOP ON CURRENTLY EXECUTING TEST
- SW13=1 020000 INHIBIT ERROR TIMEOUTS (WHICH IS AN "ERROR MESSAGE" RESULTING FROM AN ERROR DETECTED IN THE HARDWARE)
- SW12=1 010000 INHIBIT STATUS TIMEOUTS (WHICH IS A NON-ERROR RELATED INFORMATIVE MESSAGE, SUCH AS "END PASS #XXX")
- SW11=1 004000 INHIBIT ITERATIONS PER TEST
- SW10 002000 SET=BELL ON ERROR/CLEAR=BELL ON PASS END
- SW09=1 001000 LOOP ON ERROR
- SW08=1 000400 LOOP ON TEST NUMBER IN "%LPTST" IF SET, THEN THE TEST SPECIFIED BY THE TEST NUMBER CONTAINED IN THE MEMORY WORD "%LPTST" (SEE PROGRAM LISTING) WILL SPECIFY THE DESIRED TEST ON WHICH TO LOOP.
- SW01 000002 CLEAR=TEST HOT=FP/WARM=FP ALTERNATELY EACH PASS (IE, PASS#1 HFP, PASS#1 WFP, PASS#2 HFP, PASS#2 WFP, ETC)  
SET=TEST ONLY UNIT SPECIFIED IN SW00
- SW00 000001 SET=SELECT WARM FP, IF SW01=1  
CLEAR=SELECT HOT FP, IF SW01=1

\*NOTE\* FOR SW01, SW00 - IF NO HOT FP (FP11-E) IS PRESENT, THEN WARM FP (PDP-11/6X MICROCODE) IS AUTOMATICALLY SELECTED.

## 5.2 PROGRAM/OPERATOR ACTION

ONCE EXECUTION HAS BEGUN, MINIMAL OPERATOR INTERVENTION IS REQUIRED, UNLESS THE PROGRAM DETECTS AN ERROR IN THE HARDWARE.

IF ALL IS WELL, THE PROGRAM TYPES ITS NAME UPON BEGINNING; AND AT THE START OF EACH PASS, THE CURRENT PASS NUMBER (IN OCTAL) IS ECHOED. NOTE THAT SETTING SW<12>=1 WILL INHIBIT THE TYPEOUT OF THE BEGIN AND END PASS MESSAGES.

IF SW<10>=0, THE CONSOLE BELL WILL BE RUNG AT THE END OF EACH PASS. NOTE THAT ONLY SW<10> AFFECTS THE BELL RINGING AT END OF PASS - SW<12> HAS NO EFFECT ON THIS FUNCTION.

IF AN ERROR OCCURS DURING EXECUTION, MANY VARIATIONS IN ACTION ARE POSSIBLE DEPENDING UPON THE SWITCH SETTINGS.

SW<13>=1 WILL CAUSE THE CPU TO HALT AFTER AN ERROR.

SW<13>=1 WILL ALSO INHIBIT ANY ERROR MESSAGE TYPEOUT THAT WOULD OCCUR AT THIS TIME.

SW<10>=1 WILL CAUSE THE CONSOLE BELL TO BE RUNG ONLY WHEN AN ERROR IS DETECTED (AND NOT AT THE END OF A PASS).

SW<9>=1 CAUSES THE PROGRAM TO LOOP ON THE MOST RECENT ERROR, AS LONG AS IT CONTINUES TO OCCUR.

THERE ARE ALSO SEVERAL OTHER GENERAL USE FUNCTIONS DEFINED BY THE SWITCHES:

SW<11>=1 WILL INHIBIT THE ITERATIONS (=2000(10)) PERFORMED OF EACH TEST ON PASSES 2,3,4, ... THRU THE PROGRAM.

SW<14>=1 CAUSES THE PROGRAM TO LOOP INDEFINATELY ON THE CURRENTLY EXECUTING TEST.

SW<8>=1 CAUSES THE PROGRAM TO CONTINUE EXECUTION AS NORMAL, EXCEPT WHEN THE CONTENTS OF MEMORY WORD "\$LPTST" MATCHES THE NUMBER OF THE TEST CURRENTLY EXECUTING. AT THIS POINT, THE TEST IS LOOPED ON INDEFINATELY, UNTIL EITHER SW<8>=0 OR "\$LPTST" IS CHANGED. NOTE THAT IF "\$LPTST" DOES NOT MATCH THE TEST NUMBER OF ANY TEST, THE CONTENTS OF "\$LPTST" ARE EFFECTIVELY IGNORED, AND EXECUTION PROCEEDS NORMALLY.

## 5.3 HOT (FP11-E) / WARM (PDP-11/6X) SELECTION

WHEN THE PROGRAM IS STARTED (AT 200(0)), A MESSAGE IS OPTIONALLY PRINTED INDICATING THE PRESENCE/ABSCENCE OF AN FP11-E HOT FLOATING POINT UNIT OPTION (BASED UPON WHETHER "WHAMI" BIT<04> IS 1/0 RESPECTIVELY).

IF NO FP11-E HOT FP OPTION IS PRESENT, THE MESSAGE IS TYPED, AND ANY ATTEMPTS TO SELECT IT FOR TESTING VIA SW01 AND SW00 ARE IGNORED. ONLY WARM FP (PDP-11/6X MICROCOCODE) FLOATING POINT CAN BE TESTED/SELECTED.

IF THE FP11-E IS PRESENT, TEST SELECTION IS AS FOLLOWS:

WHEN SW01=0, THE HOT AND WARM FLOATING POINT UNITS ARE TESTED

ALTERNATELY EACH PASS - IN THE ORDER (1) HOT, THEN (2) WARM.  
NOTE THAT EACH "PASS" NOW CONSISTS OF TWO SEPARATE SUB-PASSES.

WHEN SW01=1, THEN DEDICATED SELECTION OF A PARTICULAR UNIT IS  
SPECIFIED IN SW00:

SW00=0 --> TEST HFP FP11-E OPTION ONLY

SW00=1 --> TEST WFP PDP-11/6X MICROCODE ONLY

## 6. ERRORS

### 6.1 FORMAT OF MESSAGES

#### 6.1.1 ALL ERROR MESSAGES CONSIST OF THREE LINES OF DATA:

THE FIRST LINE IS A BRIEF MESSAGE WHICH EXPLAINS WHAT ERROR  
WAS DETECTED (EG, THE RESULT OF THE "ABSF" INSTRUCTION WAS  
BAD).

THE PREFIX "HOT;" OR "WARM;" IS ALSO ATTACHED TO THE MESSAGE  
TO INDICATE THE SOURCE OF THE ERROR; THE FP11-E UNIT OR THE  
PDP-11/6X RESPECTIVELY.

THE SECOND LINE CONSISTS OF DATA HEADERS TO IDENTIFY THE  
VALUES TYPED OUT ON LINE THREE; THESE HEADERS WILL EITHER BE  
OF THE FORM "EXPECTED" AND "RECEIVED" DATA, OR WILL BE A  
MNEMONIC NAME OF A WORD LOCATION IN MEMORY OR REGISTERS.

THE THIRD LINE DISPLAYS THE CONTENTS OF THE LOCATIONS  
SPECIFIED BY LINE TWO AS SIX DIGIT OCTAL NUMBERS. NOTE THAT  
ALL DATA DISPLAYED IN ANY MESSAGES ARE OCTAL NUMBERS.

AS EXPLAINED IN SECTION 5.2, SETTING SW<13>=1 WILL SUPPRESS  
THE TYPING OF THESE MESSAGES.

6.1.2 FLOATING POINT UNIT DATA FORMATS:

FLOATING POINT STATUS WORD (FPS):

BIT#	OCTAL	FUNCTION
15	100000	FER = FLOATING ERROR FLAG SET WHEN EITHER FIUV, FIU, FIV, FIC ENABLED AND APPROPRIATE EXCEPTION OCCURRED.
14	040000	FID = FLOATING DISABLE INTERRUPTS NO FP INTERRUPTS TO VECTOR 244(8) IF SET.
13, 12		NOT USED
11	004000	FIUV = FLOATING UNDEFINED VARIABLE INTERRUPT IF SET, (-0) MEMORY DATA IS ERROR
10	002000	FIU = FLOATING INTR UNDERFLOW IF SET AND UNDERFLOW, SET FER, STORE ANSWER, EXPONENT WRONG BY +400(8) IF CLEAR AND UNDERFLOW, ANSWER <= ZERO
9	001000	FIV = FLOATING OVERFLOW INTERRUPT IF SET AND OVERFLOW, SET FER, STORE ANSWER, EXPONENT WRONG BY -400(8) IF CLEAR AND OVERFLOW, ANSWER <= ZERO
8	000400	FIC = FLOATING INTEGER CONVERSION INTERRUPT IF SET AND "STCFI" ERROR, ANSWER <= ZERO, SET ERROR IF CLEAR AND "STCFI" ERROR, ANSWER <= ZERO
7	000200	FD = FLOATING MODE 1=DOUBLE, 64 BIT OPERANDS (4W) 0=SINGLE, 32 BIT OPERANDS (2W)
6	000100	FL = INTEGER MODE 1=LONG, 32 BIT INTEGERS (2W) 0=SHORT, 16 BIT INTEGERS (1W)
5	000040	FT = ROUND/TRUNCATE MODE 1=TRUNCATE RESULTS 0=ROUND RESULTS
4	000020	FMM = PUT FP11-E ONLY IN MAINTENANCE MODE
3:0	000017	FN-FZ-FV-FC = FLOATING CONDITION CODES

FLOATING EXCEPTION CODES (FEC):

OCTAL	ENABLE	
00	(NONE)	(NOT USED)
02	(NONE)	FP OPCODE ERROR
04	(NONE)	FP DIVIDE-BY-ZERO ERROR
06	W/FIC	FP INTEGER CONVERSION ERROR
10	W/FIV	FP OVERFLOW ERROR
12	W/FIU	FP UNDERFLOW ERROR
14	W/FIUV	FP UNDEFINED-VARIABLE/(-0) ERROR
16	W/FMM	FP MAINTENANCE TRAP

NOTE - IN "FEC" CODE TYPEOUTS IN ERROR MESSAGES ONLY THE LOW ORDER BYTE IS USED - IGNORE THE PROGRAM FLAG BIT IN THE UPPER BYTE.

## FLOATING POINT DATA:

IN FLOAT MODE (FD=0), IS 2-16. BIT WORDS, 32. BITS  
 IN DOUBLE MODE (FD=1), IS 4-16. BIT WORDS, 64. BITS

## FIRST WORD: (BOTH F, D MODES)

B15=SIGN OF NUMBER (1/-, 0/+)  
 B14:07=EXPONENT, 8,BITS, FROM -128,+127.  
 B06:00=FACTION, 7,BITS

## SECOND WORD: (BOTH F, D MODES)

B15:00=FACTION, 16,BITS

## THIRD, FOURTH WORDS: (ONLY D MODE)

B15:00, B15:00=FACTION, 32. BITS

IN F MODE, THE COMPOSITE 24. BIT FRACTION  
 IS FORMED BY:

.1#[WORD1-BIT<06:00>]#[WORD2-BIT<15:00>]

IN D MODE, THE COMPOSITE 56. BIT FRACTION  
 IS FORMED BY:

.1#[WORD1-BIT<06:00>]#[WORD2-BIT<15:00>]  
 #[WORD3-BIT<15:00>]#[WORD4-BIT<15:00>]

FOR A MORE DETAILED OPERATION/EXPLANATION OF FLOATING POINT  
 DATA FORMATS AND OPERATIONS, SEE THE PDP-11/6X PROCESSOR  
 HANDBOOK SECTION ON THE FLOATING POINT INSTRUCTION SET.

## 6.2 RECOVERY

RECOVERY FROM ERRORS HAS BEEN ATTEMPTED TO BE MADE AS  
 AUTOMATIC AND EFFORTLESS AS POSSIBLE. HOWEVER, IN MANY CASES,  
 DUE TO THE NATURE OF THE ERROR, THE PROGRAM MAY NOT EVEN BE  
 ABLE TO BE RUN (EG, IF THE FLOATING POINT MODULE IS IN A HUNG  
 STATE, AND CAN NEVER ENTER THE READY STATE TO ACCEPT A NEW FPP  
 INSTRUCTION). AT THIS POINT, SOLVING THE PROBLEM IS A DIRECT  
 FUNCTION OF THE OPERATORS INGENUITY. THIS TEST SERIES HAS  
 BEEN DESIGNED TO TEST THE FLOATING POINT PROCESSOR SO THAT  
 THESE TYPES OF FAILURES TO RUN WILL BE MINIMAL. THE TESTS  
 HAVE BEEN PLACED IN A SPECIFICALLY STRUCTURED SEQUENCE IN THE  
 PROGRAM TO IMPLEMENT THIS STRATEGY; TESTING THE MOST BASIC  
 ELEMENTS FIRST, PROCEEDING UPWARD IN COMPLEXITY AFTER  
 ESTABLISHING THEIR CORRECT OPERATION. THIS IS WHY IT IS  
 EXTREMELY IMPORTANT THAT THE FLOATING POINT TEST PROGRAMS BE  
 (1) RUN IN THE PRESCRIBED ORDER, AND (2) ONLY BE STARTED AT  
 THEIR BEGINNING ADDRESS (USUALLY 200(8)). THE PROGRAM WILL  
 DISPLAY, AT AN ERROR, THE MOST PERTINENT INFORMATION RELATING  
 TO THE ERROR, AND A BRIEF EXPLANATION OF THE FAILING FUNCTION.

## 6.3 CAUSES



THESE TEST PROGRAMS ARE NOT HARDWARE ORIENTED, AND AS SUCH IT IS NOT POSSIBLE TO CALL OUT PARTICULAR HARDWARE AREAS AND MODULES RELATING TO A GIVEN FUNCTIONAL FAILURE. HARDWARE DIAGNOSIS FOR A PARTICULAR MACHINE MUST BE DONE USING THE APPROPRIATE ENGINEERING ROM FLOWS AND PRINTS, ALONG WITH THE KNOWN FUNCTIONAL ERRORS (AS DETECTED BY THE PROGRAMS). THIS IS THE INTENT UNDER WHICH THESE INSTRUCTION TESTS WERE DESIGNED AND CODED.

7. RESTRICTIONS

7.1 STARTING

THE PROGRAM MUST BE STARTED AT LOCATION 200(8) ALWAYS.

7.2 OPERATIONAL

THERE ARE NO OPERATIONAL RESTRICTIONS.

8. MISCELLANEOUS

8.1 EXECUTION TIME

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.....
MODEL                AVERAGE EXECUTION TIME PER PASS
                   SHORTEST PASS                LONGEST PASS
PDP-11/6X MICROCODE          0:01                3:15
PDP-11/6X W/FP11-E          0:01                2:30
    
```

.....  
 TIMES SPECIFIED AS (MINUTES):(SECONDS)

SHORTEST PASS ::= PASS=1, NO ITERATIONS, USING:  
 SWR=(004003) FOR PDP-11/6X MICROCODE  
 SWR=(004002) FOR PDP-11/6X W/FP11-E

LONGEST PASS ::= PASS>=2, 2000. ITERATIONS/TEST, USING:  
 SWR=(000003) FOR PDP-11/6X MICROCODE  
 SWR=(000002) FOR PDP-11/6X W/FP11-E

8.2 STACK POINTER

THE STACK POINTER IS SET TO 1100(8) AT THE START OF EACH PASS. IF ALL IS OPERATING CORRECTLY, IT SHOULD ALSO BE THIS VALUE AT

THE START OF EACH TEST, AND AT THE END OF A PASS.

### 8.3 POWER FAIL

THE TESTS MAY BE POWER FAILED AT ANY TIME. SPURIOUS ERROR MESSAGES MAY OCCUR IF THE FAILURE OCCURRED WHILE THE F.P.U. WAS EXECUTING A FUNCTION, AS NONE OF ITS REGISTERS (FPS, FEC, FEA, ACCUMULATORS) ARE SAVED IN THE EVENT OF A POWER FAILURE. HOWEVER, THESE MESSAGES SHOULD ONLY OCCUR ONCE (IF AT ALL) IMMEDIATELY AFTER POWER IS RESTORED. WHEN POWER IS RESTORED, "POWER" IS TYPED ON THE CONSOLE AND EXECUTION CONTINUES WHERE IT WAS INTERRUPTED.

NOTE THAT THE "VOLATILE" SWITCH REGISTER CONTENTS ARE SAVED AND RESTORED FROM THE STACK IN A POWER FAIL SEQUENCE; THEREFORE THE SWITCH REGISTER SETTINGS SHOULD NOT BE LOST OVER A POWER FAIL.

## 9. PROGRAM DESCRIPTION

### 9.1 ORGANIZATION

THESE PROGRAMS ARE ORGANIZED AS MUCH AS POSSIBLE IN A STRAIGHTFORWARD, LINEAR MANNER. THE MAIN BODY OF CODE IS STRUCTURED AS FOLLOWS:

- (1) INITIALIZATION ROUTINE
  - SETS UP VECTORS, TYPES HEADER, ETC.
- (2) MAIN BODY OF TESTS
  - INLINE TEST CODE, INLINE TEST CALLS
- (3) END OF PASS ROUTINE
  - END OF PASS PROCESSING
- (4) TEST SUBROUTINES
  - SUBROUTINES CONTAINING COMMON TEST CODE
- (5) OVERHEAD ROUTINES
  - SERVICE SUBROUTINES (TYPEOUT, ETC.)

WHEREVER FEASIBLE, COMMON SECTIONS OF CODE FOR WIDELY USED FUNCTIONS ARE CONDENSED INTO SUBROUTINES TO CONSERVE MEMORY. THIS INCLUDES NOT ONLY STANDARD SERVICE ROUTINES (SUCH AS SCOPE, ERROR, AND ASCII TYPEOUT), BUT ALSO TESTING ROUTINES WHICH PERFORM VERY SIMILAR FUNCTIONS. THUS IN MANY CASES (THE "ADDF" INSTRUCTION TESTING, FOR EXAMPLE) A SINGLE BODY OF CODE (A SUBROUTINE) IS USED TO PERFORM ALL THE FUNCTIONAL TESTS, WITH A VARIABLE PARAMETER LIST PASSED AT EACH CALL CONTAINING THE DATA OPERANDS AND EXPECTED RESULT FOR EACH INDIVIDUAL TEST. THIS CONSTRUCTION FACILITATES THE ADDITION/DELETION OF TESTS (SHOULD THAT EVER BE NECESSARY), AND ALSO GREATLY CONSERVES MEMORY SPACE REQUIREMENTS WHEN A LARGE NUMBER OF CALLS TO A GIVEN BODY OF CODE ARE REQUIRED.

THE INDIVIDUAL TESTS WITHIN EACH PROGRAM HAVE ALSO BEEN SEQUENCED IN A PARTICULAR ORDER TO FACILITATE THE DETECTION AND RESOLUTION OF ERRORS AS QUICKLY AS POSSIBLE. EACH OF THE TESTS BEGINS AS SIMPLY AS POSSIBLE, FIRST TESTING THE MOST BASIC ELEMENTS. MORE COMPLEX ELEMENTS ARE TESTED AFTERWARDS, EMPLOYING A PHILOSOPHY THAT THE SIMPLER THE TEST, THE BETTER THE RESOLUTION. ALL FUNCTIONS ARE EVENTUALLY TESTED, BUT HOPEFULLY MOST ERRORS WILL BE CAUGHT AND CORRECTED EARLY. A MUCH MORE DETAILED ANALYSIS OF THE SEQUENCE OF TESTS PERFORMED IS PRESENTED IN SECTION 9.2.

## 9.2 TEST DESCRIPTION

THIS DIAGNOSTIC IS STRUCTURED TO SEQUENTIALLY PERFORM THE FOLLOWING SERIES OF TESTS ON THE FUNCTIONALITY OF THE FLOATING POINT DUAL OPERAND INSTRUCTIONS:

- (1) \*CMP=\* COMPARE, F/D MODES
- (2) \*ADD=\* ADD, F/D MODES
- (3) \*SUB=\* SUBTRACT, F/D MODES
- (4) \*MUL=\* MULTIPLY, F/D MODES
- (5) \*DIV=\* DIVIDE, F/D MODES
- (6) \*MOD=\* MODULO, F/D MODES, 2 ACCUMULATORS
- (7) \*MOD=\* MODULO, F/D MODES, 1 ACCUMULATOR
- (8) \*LDC=\* LOAD-CONVERT, F <-> D MODES
- (9) \*STC=\* STORE-CONVERT, F <-> D MODES
- (10) \*LDC=\* LOAD-CONVERT, I-F/I-D/L-F/L-D MODES
- (11) \*STC=\* STORE-CONVERT, F-I/D-I/F-L/D-L MODES
- (12) \*LDEXP\* LOAD EXPONENT, F/D MODES
- (13) \*STEXP\* STORE EXPONENT, F/D MODES

EACH OF THE ABOVE TESTS IS PERFORMED BY A SUBROUTINE SPECIFIC TO THE INSTRUCTION; AN ARGUMENT LIST IS PASSED CONTAINING THE INITIAL DATA, EXPECTED RESULTS/STATUS/EXCEPTIONS.

EACH OF THE ABOVE INSTRUCTIONS IS TESTED IN (WHEN APPLICABLE) THE FOLLOWING INSTANCES:

- (A) FLOATING(F)/DOUBLE(D) MODES
- (B) INTEGER(I)/LONG(L) MODES
- (C) ROUND(R)/TRUNCATE(T) MODES
- (D) EXCEPTION CONDITIONS;  
OVERFLOW, UNDERFLOW, =0, DIVIDE/0, INTEGER-CONVERT  
(ENABLED AND DISABLED MODES)

## 9.3 SUBROUTINE ABSTRACTS

### 9.3.1 TRAPCATCHER

THE TRAPCATCHER IS A SERIES OF INSTRUCTIONS OCCUPYING THE INTERRUPT VECTOR AREA OF MEMORY. IT CONSISTS OF THE SEQUENCE:

```

        .WORD    .+2    ;PC AFTER TRAP
        .WORD    0      ;PS AFTER TRAP
    
```

PLACED AT EACH VECTOR ADDRESS IN LOCATIONS 4-176(8) OF MEMORY. THE FIRST WORD OF EACH PAIR ("PC AFTER TRAP") POINTS TO THE SECOND WORD, WHICH SERVES A DUAL PURPOSE AS

(1) THE NEW LOADED PS (ALL ZEROS), AND (2) THE NEXT INSTRUCTION TO EXECUTE (0=HALT).

WHEN THE PROGRAM IS EXECUTING, ANY REQUIRED VECTORS ARE SET UP IN THE VECTOR AREA WITH APPROPRIATE VALUES; THE OTHERS BEING LEFT IN THE "TRAPCATCHER" STATE; THUS, IF AN UNEXPECTED TRAP EVER OCCURS IN THE MACHINE, IT WILL BE CAUGHT, AND THE MACHINE SUBSEQUENTLY HALTED, DISPLAYING THE VECTOR ADDRESS + PLUS FOUR \* IN THE ADDRESS LIGHTS.

9.3.2 SCOPE ROUTINE - #SCOPE

THE SCOPE ROUTINE IS ENTERED FROM THE FIRST INSTRUCTION OF EACH TEST IN THE PROGRAM. (NOTE THAT BY DEFINITION, A "TEST" WILL BE DESIGNATED AS THE SECTION OF CODE BETWEEN TWO "SCOPE" STATEMENTS.) THIS ROUTINE PROVIDES THE OVERHEAD CODE NECESSARY TO IMPLEMENT SEVERAL OF THE SWITCH REGISTER CONTROL OPTIONS. UPON ENTRANCE TO A TEST, THE SCOPE STATEMENT AT THE BEGINNING SETS UP CERTAIN LOCATIONS (SEE BELOW) TO SPECIFY THE CURRENT TEST NUMBER AND LOOPING ADDRESS (FOR ITERATIONS). CONTROL IS THEN PASSED TO THE ACTUAL TEST CODE, PERFORMING THE DESIRED TEST. UPON EXIT, THE SCOPE STATEMENT OF THE NEXT TEST IS ENTERED, WHICH DETERMINES WHETHER TO (1) LOOP BACK TO THE PREVIOUS TEST (EG, FOR ITERATIONS) OR (2) INITIALIZE FOR THE NEXT TEST (AS DESCRIBED EARLIER, ABOVE).

ENTRANCE TO THE SCOPE ROUTINE IS VIA AN "IOT" TRAP CALL THROUGH LOCATION 20(8). (FROM THE SCOPE=IOT EQUATE). DEPENDING UPON THE SWITCH SETTINGS (SEE 5.2), CODE IS PRESENT TO: LOAD THE FP11 MICRO BREAK REGISTER, LOOP ON THE CURRENTLY EXECUTING TEST, LOOP ON A SPECIFIC TEST, PERFORM ITERATIONS OF EACH TEST, AND SET UP ADDRESSES FOR POSSIBLE LOOPING ON ERRORS. IMPORTANT VALUES USED IN THIS ROUTINE ARE:

- \$MXCNT - MAXIMUM NUMBER OF ITERATIONS PER TEST (GENERALLY WILL BE 2000(10))
- \$TSTNM - A COUNTER INDICATING THE NUMBER (1-377(8)) OF THE TEST CURRENTLY BEING EXECUTED
- \$LPADR - CONTAINS THE ADDRESS TO WHICH THE SCOPE ROUTINE 10200 WILL LOOP, IF THE CURRENT TEST IS BEING LOOPEU UPON
- \$LPERR - CONTAINS THE ADDRESS TO WHICH THE ERROR ROUTINE (SEE 9.3.3) WILL LOOP, IF AN ERROR OCCURS AND THE LOOPING ON AN ERROR OPTION IS SPECIFIED IN THE SWITCHES. SET UP BY SCOPE, GENERALLY WILL BE THE SAME AS \$LPADR, ABOVE.

## 9.3.3 ERROR ROUTINE - \$ERROR

THE ERROR ROUTINE IS ENTERED WHEN THE TEST CODE HAS DETERMINED THAT AN ERROR HAS OCCURRED AS PART OF A TEST. THROUGH USE OF THIS ROUTINE, THE TEST HAS A MEANS OF SIGNALING AN ERROR TO THE 10380 OPERATOR/MONITOR, AND IMPLEMENTING THE CONTROL FUNCTIONS FOR HALTING ON ERROR, BELL ON ERROR, AND LOOPING ON ERROR. IN ADDITION, THE ERROR ROUTINE HAS THE PROVISION TO TYPE OUT ON THE OPERATOR'S CONSOLE A MESSAGE BRIEFLY EXPLAINING THE ERROR, AND SOME OF THE MOST PERTINENT DATA VALUES TO HELP DIAGNOSE THE CAUSE (SEE SECTION 6.2).

THE CALLING MECHANISM IS SIMILAR TO THAT EMPLOYED FOR THE SCOPE ROUTINE (VIA A TRAP), EXCEPT IN THIS INSTANCE, THE "EMT" INSTRUCTION IS USED, TRAPPING THROUGH LOCATION 30(H). (NOTE THE EQUATE ERROR N=EMT N). THE LOWER BYTE OF THE EMT INSTRUCTION IS CAPABLE OF TRANSMITTING A NUMBER FROM 0-377(H), WHICH WILL BE TERMED THE "ERROR ITEM NUMBER." THIS NUMBER DETERMINES WHICH ERROR MESSAGE, AND ASSOCIATED DATA VALUES WILL BE TYPED OUT WHEN A PARTICULAR ERROR IS SIGNALLED. IF THIS NUMBER IS ZERO, JUST THE PC OF THE CALLING "ERROR" INSTRUCTION WILL BE TYPED, OTHERWISE, THE NUMBER IS USED AS AN INDEX THROUGH THE ERROR TABLE (\$ERRTB) TO FIND THE APPROPRIATE VALUES TO TYPE (SEE PROGRAM LISTING FOR FURTHER DETAILS).

IMPORTANT VALUES USED IN THIS ROUTINE ARE:

EREG0 THRU EREG7 - CONTENTS OF GENERAL REGISTERS R0  
THRU R7 JUST BEFORE ERROR CALL  
\$ERRTL - CUMULATIVE NUMBER OF ERRORS ENCOUNTERED TO  
DATE  
\$ERRPC - CONTAINS THE PC OF THE "ERROR" INSTRUCTION  
JUST EXECUTED  
\$LPERR - CONTAINS THE ADDRESS WHICH WILL BE LOOPED  
UPON FOR THE ERROR LOOPING FACILITY

## 9.3.4 ERROR MESSAGE TIMEOUT ROUTINE - \$TYPEPK

THIS ROUTINE (\$TYPEPK ENTRY POINT) IS CALLED BY THE ERROR PROCESSING ROUTINE DESCRIBED IN 9.3.3 ABOVE. ITS PURPOSE IS TO IMPLEMENT THE ERROR MESSAGE/DATA VALUE ERROR TIMEOUT FACILITY. THE SUBROUTINE WILL, GIVEN THE INDEXING BYTE FROM THE ERROR CALL INSTRUCTION, PICK UP THE CORRECT ERROR MESSAGE VECTOR FROM \$ERRTB (ERROR TABLE), AND TYPE OUT THE ERROR MESSAGE, DATA HEADER, AND DATA VALUES ON THE CONSOLE.

## 9.3.5 TYPE ROUTINE - \$TYPE

THIS ROUTINE IS THE STANDARD SYSTEM TIMEOUT ROUTINE FOR ASCII SINGLE-CHARACTER-PER-BYTE STRINGS. IT IS CALLED THROUGH A TRAP INSTRUCTION WITH THE NEXT WORD CONTAINING THE ADDRESS OF THE FIRST CHARACTER IN THE STRING. TYPING TERMINATES WHEN AN ALL-ZERO BYTE IS FOUND. HORIZONTAL TAB STOPS ARE ALSO

AUTOMATICALLY PLACED.

9.3.6 OCTAL NUMBER TYPE ROUTINE = \$TYPOC

THIS ROUTINE CONVERTS THE TOP NUMBER ON THE STACK TO A 6-DIGIT OCTAL REPRESENTATION, AND TYPES IT ON THE CONSOLE USING THE TYPE ROUTINE \$TYPE. SEE LISTING FOR OPTIONS AND FURTHER DETAILS.

9.3.7 POWER UP AND DOWN ROUTINES = \$PWRUP AND \$PWRDN

THESE TWO ROUTINES ARE ENTERED FOR THE POWER UP AND DOWN CONDITIONS, RESPECTIVELY. THE POWER DOWN ROUTINE (\$PWRDN) SAVES THE GENERAL REGISTERS AND STACK POINTER. THE POWER UP ROUTINE (\$PWRUP) CORRESPONDINGLY RESTORES THE REGISTERS, STACK POINTER, AND TYPES THE MESSAGE "POWER" WHEN POWER IS RESTORED. THE VOLATILE INTERNAL SWITCH REGISTER IS ALSO SAVED/RESTORED BY THIS ROUTINE.

9.3.8 END OF PASS ROUTINE = \$EOP

THE END OF PASS ROUTINE COUNTS THE NUMBER OF PASSES PERFORMED, DINGS THE BELL/TYPES A MESSAGE (IF ENABLED), SETS/CLEARs THE T-BIT (IF ENABLED), AND ALSO INTERFACES TO THE MONITOR, IF PRESENT. IT ALSO OPTIONALLY LOOPS FOR A NUMBER OF SUBPASSES BEFORE SIGNALLING AN END OF PASS CONDITION.

10. ACT/APT/XXDP

10.1 ACT COMPATIBILITY

THIS PROGRAM WILL RUN UNDER THE ACT SYSTEM.

10.2 APT COMPATIBILITY

THIS PROGRAM WILL RUN UNDER THE APT SYSTEM MONITOR. ALL NECESSARY SOFTWARE COMMUNICATION HOOKS ARE PRESENT.

10.3 XXDP COMPATIBILITY

FOR XXDP MEDIA COMPATIBILITY, THE TOP 2K WORDS OF THE 16K WORD MINIMUM MEMORY AREA ARE NOT DISTURBED DURING EXECUTION.

14	OPERATIONAL SWITCH SETTINGS
32	BASIC DEFINITIONS
193	TRAP CATCHER
202	STARTING ADDRESS(ES)
205	ACT11 HOOKS
216	APT PARAMETER BLOCK
239	COMMON TAGS
320	APT MAILBOX=ETABLE
355	ERROR POINTER TABLE
426	PROGRAM DEFINED COMMON TAGS
467	START OF PASS ROUTINE
475	INITIALIZE THE COMMON TAGS
601	T1 TEST OF CMPF INSTR, DATA SET CMPF-1
620	T2 TEST OF CMPF INSTR, DATA SET CMPF-2
639	T3 TEST OF CMPF INSTR, DATA SET CMPF-3
658	T4 TEST OF CMPF INSTR, DATA SET CMPF-4
677	T5 TEST OF CMPF INSTR, DATA SET CMPF-5
696	T6 TEST OF CMPF INSTR, DATA SET CMPF-6
715	T7 TEST OF CMPF INSTR, DATA SET CMPF-7
734	T10 TEST OF CMPF INSTR, DATA SET CMPF-10
753	T11 TEST OF CMPF INSTR, DATA SET CMPF-11
772	T12 TEST OF CMPF INSTR, DATA SET CMPF-12
791	T13 TEST OF CMPF INSTR, DATA SET CMPF-13
810	T14 TEST OF CMPF INSTR, DATA SET CMPF-14
830	T15 TEST OF CMPD INSTR, DATA SET CMPD-1
851	T16 TEST OF CMPD INSTR, DATA SET CMPD-2
872	T17 TEST OF CMPD INSTR, DATA SET CMPD-3
893	T20 TEST OF CMPD INSTR, DATA SET CMPD-4
914	T21 TEST OF CMPD INSTR, DATA SET CMPD-5
935	T22 TEST OF CMPD INSTR, DATA SET CMPD-6
956	T23 TEST OF CMPD INSTR, DATA SET CMPD-7
977	T24 TEST OF CMPD INSTR, DATA SET CMPD-10
998	T25 TEST OF CMPD INSTR, DATA SET CMPD-11
1019	T26 TEST OF CMPD INSTR, DATA SET CMPD-12
1040	T27 TEST OF CMPD INSTR, DATA SET CMPD-13
1061	T30 TEST OF CMPD INSTR, DATA SET CMPD-14
1082	T31 TEST OF ADDF INSTR, DATA SET ADDF-1
1103	T32 TEST OF ADDF INSTR, DATA SET ADDF-2
1122	T33 TEST OF ADDF INSTR, DATA SET ADDF-3
1142	T34 TEST OF ADDF INSTR, DATA SET ADDF-4
1162	T35 TEST OF ADDF INSTR, DATA SET ADDF-5
1182	T36 TEST OF ADDF INSTR, DATA SET ADDF-6
1202	T37 TEST OF ADDF INSTR, DATA SET ADDF-7
1222	T40 TEST OF ADDF INSTR, DATA SET ADDF-10
1242	T41 TEST OF ADDF INSTR, DATA SET ADDF-11
1262	T42 TEST OF ADDF INSTR, DATA SET ADDF-12
1282	T43 TEST OF ADDF INSTR, DATA SET ADDF-13
1302	T44 TEST OF ADDF INSTR, DATA SET ADDF-14
1322	T45 TEST OF ADDF INSTR, DATA SET ADDF-15
1342	T46 TEST OF ADDF INSTR, DATA SET ADDF-16
1362	T47 TEST OF ADDF INSTR, DATA SET ADDF-17
1382	T50 TEST OF ADDF INSTR, DATA SET ADDF-20
1402	T51 TEST OF ADDF INSTR, DATA SET ADDF-21
1422	T52 TEST OF ADDF INSTR, DATA SET ADDF-22
1442	T53 TEST OF ADDF INSTR, DATA SET ADDF-23
1463	T54 TEST OF ADDD INSTR, DATA SET ADDD-1

1486	T55 TEST OF ADDD INSTR, DATA SET ADDD-2
1509	T56 TEST OF ADDD INSTR, DATA SET ADDD-3
1532	T57 TEST OF ADDD INSTR, DATA SET ADDD-4
1555	T60 TEST OF ADDD INSTR, DATA SET ADDD-5
1578	T61 TEST OF ADDD INSTR, DATA SET ADDD-6
1601	T62 TEST OF ADDD INSTR, DATA SET ADDD-7
1624	T63 TEST OF ADDD INSTR, DATA SET ADDD-10
1647	T64 TEST OF ADDD INSTR, DATA SET ADDD-11
1670	T65 TEST OF ADDD INSTR, DATA SET ADDD-12
1693	T66 TEST OF ADDD INSTR, DATA SET ADDD-13
1716	T67 TEST OF ADDD INSTR, DATA SET ADDD-14
1739	T70 TEST OF ADDD INSTR, DATA SET ADDD-15
1762	T71 TEST OF ADDD INSTR, DATA SET ADDD-16
1785	T72 TEST OF ADDD INSTR, DATA SET ADDD-17
1808	T73 TEST OF ADDD INSTR, DATA SET ADDD-20
1831	T74 TEST OF ADDD INSTR, DATA SET ADDD-21
1854	T75 TEST OF ADDD INSTR, DATA SET ADDD-22
1877	T76 TEST OF ADDD INSTR, DATA SET ADDD-23
1901	T77 TEST OF SUBF INSTR, DATA SET SUBF-1
1921	T100 TEST OF SUBF INSTR, DATA SET SUBF-2
1941	T101 TEST OF SUBF INSTR, DATA SET SUBF-3
1961	T102 TEST OF SUBF INSTR, DATA SET SUBF-4
1981	T103 TEST OF SUBF INSTR, DATA SET SUBF-5
2001	T104 TEST OF SUBF INSTR, DATA SET SUBF-6
2021	T105 TEST OF SUBF INSTR, DATA SET SUBF-7
2041	T106 TEST OF SUBF INSTR, DATA SET SUBF-10
2061	T107 TEST OF SUBF INSTR, DATA SET SUBF-11
2081	T110 TEST OF SUBF INSTR, DATA SET SUBF-12
2101	T111 TEST OF SUBF INSTR, DATA SET SUBF-13
2121	T112 TEST OF SUBF INSTR, DATA SET SUBF-14
2141	T113 TEST OF SUBF INSTR, DATA SET SUBF-15
2161	T114 TEST OF SUBF INSTR, DATA SET SUBF-16
2182	T115 TEST OF SUBD INSTR, DATA SET SUBD-1
2205	T116 TEST OF SUBD INSTR, DATA SET SUBD-2
2228	T117 TEST OF SUBD INSTR, DATA SET SUBD-3
2251	T120 TEST OF SUBD INSTR, DATA SET SUBD-4
2274	T121 TEST OF SUBD INSTR, DATA SET SUBD-5
2297	T122 TEST OF SUBD INSTR, DATA SET SUBD-6
2320	T123 TEST OF SUBD INSTR, DATA SET SUBD-7
2343	T124 TEST OF SUBD INSTR, DATA SET SUBD-10
2366	T125 TEST OF SUBD INSTR, DATA SET SUBD-11
2389	T126 TEST OF SUBD INSTR, DATA SET SUBD-12
2412	T127 TEST OF SUBD INSTR, DATA SET SUBD-13
2435	T130 TEST OF SUBD INSTR, DATA SET SUBD-14
2458	T131 TEST OF SUBD INSTR, DATA SET SUBD-15
2481	T132 TEST OF SUBD INSTR, DATA SET SUBD-16
2504	T133 TEST OF MULF INSTR, DATA SET MULF-1
2524	T134 TEST OF MULF INSTR, DATA SET MULF-2
2544	T135 TEST OF MULF INSTR, DATA SET MULF-3
2564	T136 TEST OF MULF INSTR, DATA SET MULF-4
2584	T137 TEST OF MULF INSTR, DATA SET MULF-5
2604	T140 TEST OF MULF INSTR, DATA SET MULF-6
2624	T141 TEST OF MULF INSTR, DATA SET MULF-7
2644	T142 TEST OF MULF INSTR, DATA SET MULF-10
2664	T143 TEST OF MULF INSTR, DATA SET MULF-11
2684	T144 TEST OF MULF INSTR, DATA SET MULF-12

2704	T145	TEST OF MULF INSTR, DATA SET MULF-13
2724	T146	TEST OF MULF INSTR, DATA SET MULF-14
2744	T147	TEST OF MULF INSTR, DATA SET MULF-15
2764	T154	TEST OF MULF INSTR, DATA SET MULF-16
2784	T151	TEST OF MULF INSTR, DATA SET MULF-17
2804	T152	TEST OF MULF INSTR, DATA SET MULF-20
2825	T153	TEST OF MULO INSTR, DATA SET MULO-1
2848	T154	TEST OF MULO INSTR, DATA SET MULO-2
2871	T155	TEST OF MULO INSTR, DATA SET MULO-3
2894	T156	TEST OF MULO INSTR, DATA SET MULO-4
2917	T157	TEST OF MULO INSTR, DATA SET MULO-5
2940	T160	TEST OF MULO INSTR, DATA SET MULO-6
2963	T161	TEST OF MULO INSTR, DATA SET MULO-7
2986	T162	TEST OF MULO INSTR, DATA SET MULO-10
3009	T163	TEST OF MULO INSTR, DATA SET MULO-11
3032	T164	TEST OF MULO INSTR, DATA SET MULO-12
3055	T165	TEST OF MULO INSTR, DATA SET MULO-13
3078	T166	TEST OF MULO INSTR, DATA SET MULO-14
3101	T167	TEST OF MULO INSTR, DATA SET MULO-15
3124	T170	TEST OF MULO INSTR, DATA SET MULO-16
3147	T171	TEST OF MULO INSTR, DATA SET MULO-17
3170	T172	TEST OF MULO INSTR, DATA SET MULO-20
3193	T173	TEST OF DIVF INSTR, DATA SET DIVF-1
3213	T174	TEST OF DIVF INSTR, DATA SET DIVF-2
3233	T175	TEST OF DIVF INSTR, DATA SET DIVF-3
3253	T176	TEST OF DIVF INSTR, DATA SET DIVF-4
3273	T177	TEST OF DIVF INSTR, DATA SET DIVF-5
3293	T200	TEST OF DIVF INSTR, DATA SET DIVF-6
3313	T201	TEST OF DIVF INSTR, DATA SET DIVF-7
3333	T202	TEST OF DIVF INSTR, DATA SET DIVF-10
3353	T203	TEST OF DIVF INSTR, DATA SET DIVF-11
3373	T204	TEST OF DIVF INSTR, DATA SET DIVF-12
3393	T205	TEST OF DIVF INSTR, DATA SET DIVF-13
3413	T206	TEST OF DIVF INSTR, DATA SET DIVF-14
3433	T207	TEST OF DIVF INSTR, DATA SET DIVF-15
3453	T210	TEST OF DIVF INSTR, DATA SET DIVF-16
3473	T211	TEST OF DIVF INSTR, DATA SET DIVF-17
3493	T212	TEST OF DIVF INSTR, DATA SET DIVF-20
3513	T213	TEST OF DIVF INSTR, DATA SET DIVF-21
3534	T214	TEST OF DIVD INSTR, DATA SET DIVD-1
3557	T215	TEST OF DIVD INSTR, DATA SET DIVD-2
3580	T216	TEST OF DIVD INSTR, DATA SET DIVD-3
3603	T217	TEST OF DIVD INSTR, DATA SET DIVD-4
3626	T220	TEST OF DIVD INSTR, DATA SET DIVD-5
3649	T221	TEST OF DIVD INSTR, DATA SET DIVD-6
3672	T222	TEST OF DIVD INSTR, DATA SET DIVD-7
3695	T223	TEST OF DIVD INSTR, DATA SET DIVD-10
3718	T224	TEST OF DIVD INSTR, DATA SET DIVD-11
3741	T225	TEST OF DIVD INSTR, DATA SET DIVD-12
3764	T226	TEST OF DIVD INSTR, DATA SET DIVD-13
3787	T227	TEST OF DIVD INSTR, DATA SET DIVD-14
3810	T230	TEST OF DIVD INSTR, DATA SET DIVD-15
3833	T231	TEST OF DIVD INSTR, DATA SET DIVD-16
3856	T232	TEST OF DIVD INSTR, DATA SET DIVD-17
3879	T233	TEST OF DIVD INSTR, DATA SET DIVD-20
3902	T234	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-1

3923	T235	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-2
3944	T236	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-3
3965	T237	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-4
3986	T240	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-5
4007	T241	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-6
4028	T242	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-7
4049	T243	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-10
4070	T244	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-11
4091	T245	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-12
4112	T246	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-13
4133	T247	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-14
4154	T250	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-15
4175	T251	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-16
4196	T252	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-17
4217	T253	TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-20
4239	T254	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-1
4264	T255	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-2
4289	T256	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-3
4314	T257	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-4
4339	T260	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-5
4364	T261	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-6
4389	T262	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-7
4414	T263	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-10
4439	T264	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-11
4464	T265	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-12
4489	T266	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-13
4514	T267	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-14
4539	T270	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-15
4564	T271	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-16
4589	T272	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-17
4614	T273	TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-20
4639	T274	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-1
4660	T275	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-2
4681	T276	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-3
4702	T277	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-4
4723	T300	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-5
4744	T301	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-6
4765	T302	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-7
4786	T303	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-10
4807	T304	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-11
4828	T305	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-12
4849	T306	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-13
4870	T307	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-14
4891	T310	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-15
4912	T311	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-16
4933	T312	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-17
4954	T313	TEST OF MODF(1 ACC) INSTR, DATA SET MD1F-20
4976	T314	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-1
5001	T315	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-2
5026	T316	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-3
5051	T317	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-4
5076	T320	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-5
5101	T321	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-6
5126	T322	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-7
5151	T323	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-10
5176	T324	TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-11



5201	T325	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-12
5226	T326	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-13
5251	T327	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-14
5276	T328	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-15
5301	T331	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-16
5326	T332	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-17
5351	T333	TEST OF MODD(1 ACC) INSTR, DATA SET MODD-20
5376	T334	TEST OF LDCDF INSTR, DATA SET LDCDF-1
5396	T335	TEST OF LDCDF INSTR, DATA SET LDCDF-2
5416	T336	TEST OF LDCDF INSTR, DATA SET LDCDF-3
5436	T337	TEST OF LDCDF INSTR, DATA SET LDCDF-4
5456	T340	TEST OF LDCDF INSTR, DATA SET LDCDF-5
5476	T341	TEST OF LDCDF INSTR, DATA SET LDCDF-6
5496	T342	TEST OF LDCDF INSTR, DATA SET LDCDF-7
5516	T343	TEST OF LDCDF INSTR, DATA SET LDCDF-10
5536	T344	TEST OF LDCDF INSTR, DATA SET LDCDF-11
5556	T345	TEST OF LDCDF INSTR, DATA SET LDCDF-12
5576	T346	TEST OF LDCDF INSTR, DATA SET LDCDF-13
5596	T347	TEST OF LDCDF INSTR, DATA SET LDCDF-14
5616	T350	TEST OF LDCDF INSTR, DATA SET LDCDF-15
5636	T351	TEST OF LDCDF INSTR, DATA SET LDCDF-16
5657	T352	TEST OF LDCFD INSTR, DATA SET LCFD-1
5677	T353	TEST OF LDCFD INSTR, DATA SET LCFD-2
5697	T354	TEST OF LDCFD INSTR, DATA SET LCFD-3
5717	T355	TEST OF LDCFD INSTR, DATA SET LCFD-4
5737	T356	TEST OF LDCFD INSTR, DATA SET LCFD-5
5757	T357	TEST OF LDCFD INSTR, DATA SET LCFD-6
5777	T360	TEST OF LDCFD INSTR, DATA SET LCFD-7
5797	T361	TEST OF LDCFD INSTR, DATA SET LCFD-10
5818	T362	TEST OF STCDF INSTR, DATA SET SCDF-1
5838	T363	TEST OF STCDF INSTR, DATA SET SCDF-2
5858	T364	TEST OF STCDF INSTR, DATA SET SCDF-3
5878	T365	TEST OF STCDF INSTR, DATA SET SCDF-4
5898	T366	TEST OF STCDF INSTR, DATA SET SCDF-5
5918	T367	TEST OF STCDF INSTR, DATA SET SCDF-6
5938	T370	TEST OF STCDF INSTR, DATA SET SCDF-7
5958	T371	TEST OF STCDF INSTR, DATA SET SCDF-10
5978	T372	TEST OF STCDF INSTR, DATA SET SCDF-11
5998	T373	TEST OF STCDF INSTR, DATA SET SCDF-12
6018	T374	TEST OF STCDF INSTR, DATA SET SCDF-13
6038	T375	TEST OF STCDF INSTR, DATA SET SCDF-14
6059	T376	TEST OF STCFD INSTR, DATA SET SCFD-1
6079	T377	TEST OF STCFD INSTR, DATA SET SCFD-2
6099	T400	TEST OF STCFD INSTR, DATA SET SCFD-3
6119	T401	TEST OF STCFD INSTR, DATA SET SCFD-4
6139	T402	TEST OF STCFD INSTR, DATA SET SCFD-5
6159	T403	TEST OF STCFD INSTR, DATA SET SCFD-6
6179	T404	TEST OF LDCIF INSTR, DATA SET LCIF-1
6197	T405	TEST OF LDCIF INSTR, DATA SET LCIF-2
6215	T406	TEST OF LDCIF INSTR, DATA SET LCIF-3
6233	T407	TEST OF LDCIF INSTR, DATA SET LCIF-4
6251	T410	TEST OF LDCIF INSTR, DATA SET LCIF-5
6270	T411	TEST OF LDCID INSTR, DATA SET LCID-1
6289	T412	TEST OF LDCID INSTR, DATA SET LCID-2
6308	T413	TEST OF LDCID INSTR, DATA SET LCID-3
6327	T414	TEST OF LDCID INSTR, DATA SET LCID-4

6346	T415	TEST OF LDCID INSTR, DATA SET LCID-5
6366	T416	TEST OF LDCLF INSTR, DATA SET LCLF-1
6384	T417	TEST OF LDCLF INSTR, DATA SET LCLF-2
6402	T420	TEST OF LDCLF INSTR, DATA SET LCLF-3
6420	T421	TEST OF LDCLF INSTR, DATA SET LCLF-4
6438	T422	TEST OF LDCLF INSTR, DATA SET LCLF-5
6456	T423	TEST OF LDCLF INSTR, DATA SET LCLF-6
6474	T424	TEST OF LDCLF INSTR, DATA SET LCLF-7
6493	T425	TEST OF LDCLD INSTR, DATA SET LCLD-1
6512	T426	TEST OF LDCLD INSTR, DATA SET LCLD-2
6531	T427	TEST OF LDCLD INSTR, DATA SET LCLD-3
6550	T430	TEST OF LDCLD INSTR, DATA SET LCLD-4
6569	T431	TEST OF LDCLD INSTR, DATA SET LCLD-5
6589	T432	TEST OF STCFI INSTR, DATA SET SCFI-1
6608	T433	TEST OF STCFI INSTR, DATA SET SCFI-2
6627	T434	TEST OF STCFI INSTR, DATA SET SCFI-3
6646	T435	TEST OF STCFI INSTR, DATA SET SCFI-4
6665	T436	TEST OF STCFI INSTR, DATA SET SCFI-5
6684	T437	TEST OF STCFI INSTR, DATA SET SCFI-6
6703	T440	TEST OF STCFI INSTR, DATA SET SCFI-7
6723	T441	TEST OF STCOI INSTR, DATA SET SCDI-1
6743	T442	TEST OF STCOI INSTR, DATA SET SCDI-2
6763	T443	TEST OF STCOI INSTR, DATA SET SCDI-3
6783	T444	TEST OF STCOI INSTR, DATA SET SCDI-4
6803	T445	TEST OF STCOI INSTR, DATA SET SCDI-5
6823	T446	TEST OF STCOI INSTR, DATA SET SCDI-6
6843	T447	TEST OF STCOI INSTR, DATA SET SCDI-7
6864	T450	TEST OF STCFI INSTR, DATA SET SCFI-1
6883	T451	TEST OF STCFI INSTR, DATA SET SCFI-2
6902	T452	TEST OF STCFI INSTR, DATA SET SCFI-3
6921	T453	TEST OF STCFI INSTR, DATA SET SCFI-4
6940	T454	TEST OF STCFI INSTR, DATA SET SCFI-5
6959	T455	TEST OF STCFI INSTR, DATA SET SCFI-6
6978	T456	TEST OF STCFI INSTR, DATA SET SCFI-7
6998	T457	TEST OF STCUL INSTR, DATA SET SCUL-1
7018	T460	TEST OF STCUL INSTR, DATA SET SCUL-2
7038	T461	TEST OF STCUL INSTR, DATA SET SCUL-3
7058	T462	TEST OF STCUL INSTR, DATA SET SCUL-4
7078	T463	TEST OF STCUL INSTR, DATA SET SCUL-5
7098	T464	TEST OF STCUL INSTR, DATA SET SCUL-6
7118	T465	TEST OF STCUL INSTR, DATA SET SCUL-7
7139	T466	TEST OF LDEAP/F INSTR, DATA SET LEXF-1
7159	T467	TEST OF LDEAP/F INSTR, DATA SET LEXF-2
7179	T470	TEST OF LDEAP/F INSTR, DATA SET LEXF-3
7199	T471	TEST OF LDEAP/F INSTR, DATA SET LEXF-4
7219	T472	TEST OF LDEAP/F INSTR, DATA SET LEXF-5
7239	T473	TEST OF LDEAP/F INSTR, DATA SET LEXF-6
7259	T474	TEST OF LDEAP/F INSTR, DATA SET LEXF-7
7279	T475	TEST OF LDEAP/F INSTR, DATA SET LEXF-10
7299	T476	TEST OF LDEAP/F INSTR, DATA SET LEXF-11
7319	T477	TEST OF LDEAP/F INSTR, DATA SET LEXF-12
7339	T500	TEST OF LDEAP/F INSTR, DATA SET LEXF-13
7359	T501	TEST OF LDEAP/F INSTR, DATA SET LEXF-14
7379	T502	TEST OF LDEAP/F INSTR, DATA SET LEXF-15
7399	T503	TEST OF LDEAP/F INSTR, DATA SET LEXF-16
7419	T504	TEST OF LDEAP/F INSTR, DATA SET LEXF-17

7439	T905	TEST OF LDEXP/F INSTR, DATA SET LEXF-20
7459	T906	TEST OF LDEXP/F INSTR, DATA SET LEXF-21
7479	T907	TEST OF LDEXP/F INSTR, DATA SET LEXF-22
7499	T910	TEST OF LDEXP/F INSTR, DATA SET LEXF-23
7519	T911	TEST OF LDEXP/F INSTR, DATA SET LEXF-24
7540	T912	TEST OF LDEXP/D INSTR, DATA SET LEXD-1
7562	T913	TEST OF LDEXP/D INSTR, DATA SET LEXD-2
7584	T914	TEST OF LDEXP/D INSTR, DATA SET LEXD-3
7606	T915	TEST OF LDEXP/D INSTR, DATA SET LEXD-4
7628	T916	TEST OF LDEXP/D INSTR, DATA SET LEXD-5
7650	T917	TEST OF LDEXP/D INSTR, DATA SET LEXD-6
7672	T920	TEST OF LDEXP/D INSTR, DATA SET LEXD-7
7694	T921	TEST OF LDEXP/D INSTR, DATA SET LEXD-10
7716	T922	TEST OF LDEXP/D INSTR, DATA SET LEXD-11
7738	T923	TEST OF LDEXP/D INSTR, DATA SET LEXD-12
7760	T924	TEST OF LDEXP/D INSTR, DATA SET LEXD-13
7782	T925	TEST OF LDEXP/D INSTR, DATA SET LEXD-14
7804	T926	TEST OF LDEXP/D INSTR, DATA SET LEXD-15
7826	T927	TEST OF LDEXP/D INSTR, DATA SET LEXD-16
7848	T930	TEST OF LDEXP/D INSTR, DATA SET LEXD-17
7870	T931	TEST OF LDEXP/D INSTR, DATA SET LEXD-20
7892	T932	TEST OF LDEXP/D INSTR, DATA SET LEXD-21
7914	T933	TEST OF LDEXP/D INSTR, DATA SET LEXD-22
7936	T934	TEST OF LDEXP/D INSTR, DATA SET LEXD-23
7958	T935	TEST OF LDEXP/D INSTR, DATA SET LEXD-24
7981	T936	TEST OF STEXP/F INSTR, DATA SET SEXF-1
7999	T937	TEST OF STEXP/F INSTR, DATA SET SEXF-2
8017	T940	TEST OF STEXP/F INSTR, DATA SET SEXF-3
8035	T941	TEST OF STEXP/F INSTR, DATA SET SEXF-4
8053	T942	TEST OF STEXP/F INSTR, DATA SET SEXF-5
8071	T943	TEST OF STEXP/F INSTR, DATA SET SEXF-6
8089	T944	TEST OF STEXP/F INSTR, DATA SET SEXF-7
8108	T945	TEST OF STEXP/D INSTR, DATA SET SEXD-1
8127	T946	TEST OF STEXP/D INSTR, DATA SET SEXD-2
8146	T947	TEST OF STEXP/D INSTR, DATA SET SEXD-3
8165	T950	TEST OF STEXP/D INSTR, DATA SET SEXD-4
8184	T951	TEST OF STEXP/D INSTR, DATA SET SEXD-5
8203	T952	TEST OF STEXP/D INSTR, DATA SET SEXD-6
8222	T953	TEST OF STEXP/D INSTR, DATA SET SEXD-7
8241	T9	SUB PASS END CONTROL
8284		END OF PASS ROUTINE (MODIFIED BYSMAC)
8320		SUBR TO TEST THE CNMF INSTRUCTION
8364		SUBR TO TEST THE CNPD INSTRUCTION
8410		SUBR TO TEST THE ADDF INSTRUCTION
8453		SUBR TO TEST THE ADUD INSTRUCTION
8499		SUBR TO TEST THE SUBF INSTRUCTION
8542		SUBR TO TEST THE SUBD INSTRUCTION
8588		SUBR TO TEST THE MULF INSTRUCTION
8631		SUBR TO TEST THE MULD INSTRUCTION
8677		SUBR TO TEST THE DIVF INSTRUCTION
8720		SUBR TO TEST THE DIVD INSTRUCTION
8767		SUBR TO TEST THE MOUF INSTRUCTION, USING 2 ACCUMULATORS
8823		SUBR TO TEST THE MOUD INSTRUCTION, USING 2 ACCUMULATORS
8884		SUBR TO TEST THE MOFF INSTRUCTION, USING 1 ACCUMULATOR
8940		SUBR TO TEST THE MODD INSTRUCTION, USING 1 ACCUMULATOR
9001		SUBR TO TEST THE LDCDF INSTRUCTION

9045		SUBR TO TEST THE LUCFD INSTRUCTION
9091		SUBR TO TEST THE STCDF INSTRUCTION
9134		SUBR TO TEST THE STCFD INSTRUCTION
9169		SUBR TO TEST THE LDCIF INSTRUCTION
9202		SUBR TO TEST THE LDCID INSTRUCTION
9239		SUBR TO TEST THE LDCIF INSTRUCTION
9272		SUBR TO TEST THE LDCID INSTRUCTION
9306		SUBR TO TEST THE STCFI INSTRUCTION
9356		SUBR TO TEST THE STCFI INSTRUCTION
9406		SUBR TO TEST THE STCFI INSTRUCTION
9450		SUBR TO TEST THE STCDF INSTRUCTION
9507		SUBR TO TEST THE LDEXP INSTRUCTION, F MODE
9550		SUBR TO TEST THE LDEXP INSTRUCTION, D MODE
9596		SUBR TO TEST THE STEXP INSTRUCTION, F MODE
9636		SUBR TO TEST THE STEXP INSTRUCTION, D MODE
9674		FPP UNEXPECTED TRAP CATCHER
9685		SCOPE HANDLER ROUTINE
9749		ERROR HANDLER ROUTINE
9812		ERROR MESSAGE TIMEOUT ROUTINE (MODIFIED BYSMAC)
9878		TYPE ROUTINE
9957		APT COMMUNICATIONS ROUTINE
10014		BINARY TO OCTAL (ASCII) AND TYPE
10091		TRAP DECODER
10114		TRAP TABLE
10128		POWER DOWN AND UP ROUTINES
10175		ERROR MESSAGES, DATA HEADERS, DATA VECTORS, ETC

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*TITLE FPU ADVANCED INSTR TESTS
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*MAYNARD, MASS. 01754
*
**PROGRAM BY DONALD NORTH
**
**THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
**PACKAGE (MAINDEC-11-DEGAC-C3), JAN 19, 1977.
**

.SBTTL OPERATIONAL SWITCH SETTINGS
**
** SWITCH OCTAL USE
** -----
** 15 100000 HALT ON ERROR
** 14 040000 LOOP ON CURRENTLY EXECUTING INST
** 13 020000 INHIBIT ERROR TYP0UTS
** 12 010000 INHIBIT STATUS TYP0UTS
** 11 004000 INHIBIT ITERATIONS
** 10 002000 DBELL ON PASS END
** 9 001000 DBELL ON ERROR
** 8 000400 LOOP ON ERROR
** 7 000200 LOOP ON TEST NUMBER IN "SLPTST"
** 6 000100 DBTEST HFP/WFP ALTERNATELY EACH PASS
** 5 000002 DBTEST ONLY UNIT SPECIFIED IN SWC005
** 4 000001 DBSELECT HFP, IF SWC010=1
** 3 000001 DBSELECT WFP, IF SWC010=1

.SBTTL BASIC DEFINITIONS

**INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR ;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;BASIC DEFINITION OF SCOPE CALL

**MISCELLANEOUS DEFINITIONS
MT= 11 ;CODE FOR HORIZONTAL TAB
LF= 12 ;CODE FOR LINE FEED
CR= 15 ;CODE FOR CARRIAGE RETURN
CRLF= 200 ;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774 ;STACK LIMIT REGISTER
PIRQ= 177772 ;PROGRAM INTERRUPT REQUEST REGISTER
OSWR= 177570 ;HARDWARE SWITCH REGISTER
ODISP= 177570 ;HARDWARE DISPLAY REGISTER

**GENERAL PURPOSE REGISTER DEFINITIONS
R0= 00 ;GENERAL REGISTER
R1= 01 ;GENERAL REGISTER
R2= 02 ;GENERAL REGISTER
R3= 03 ;GENERAL REGISTER
R4= 04 ;GENERAL REGISTER
R5= 05 ;GENERAL REGISTER

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R6= 06 ;GENERAL REGISTER
R7= 07 ;GENERAL REGISTER
SP= 06 ;STACK POINTER
PC= 07 ;PROGRAM COUNTER

**PRIORITY LEVEL DEFINITIONS
PR0= 0 ;PRIORITY LEVEL 0
PR1= 40 ;PRIORITY LEVEL 1
PR2= 100 ;PRIORITY LEVEL 2
PR3= 140 ;PRIORITY LEVEL 3
PR4= 200 ;PRIORITY LEVEL 4
PR5= 240 ;PRIORITY LEVEL 5
PR6= 300 ;PRIORITY LEVEL 6
PR7= 340 ;PRIORITY LEVEL 7

**"SWITCH REGISTER" SWITCH DEFINITIONS
SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

**DATA BIT DEFINITIONS (BIT00 TO BIT15)
BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20

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113      000010      BIT03= 10
114      000004      BIT02= 4
115      000002      BIT01= 2
116      000001      BIT00= 1
117      .EQUIV BIT09,BIT9
118      .EQUIV BIT08,BIT8
119      .EQUIV BIT07,BIT7
120      .EQUIV BIT06,BIT6
121      .EQUIV BIT05,BIT5
122      .EQUIV BIT04,BIT4
123      .EQUIV BIT03,BIT3
124      .EQUIV BIT02,BIT2
125      .EQUIV BIT01,BIT1
126      .EQUIV BIT00,BIT0
127
128      ;*BASIC "CPU" TRAP VECTOR ADDRESSES
129      LRRVEC= 4      ;TIME OUT AND OTHER ERRORS
130      RESVEC= 10     ;RESERVED AND ILLEGAL INSTRUCTIONS
131      TRITVEC=14     ;"I" BIT
132      TRTVEC= 14     ;TRAP TRAP
133      OPTVEC= 14     ;BREAKPOINT TRAP (OPT)
134      IOTVEC= 20     ;INPUT/OUTPUT TRAP (IOT) **SCOPE**
135      PNRVEC= 24     ;POWER FAIL
136      EMTVEC= 30     ;EMULATOR TRAP (EMT) **EMURR**
137      TRAPVEC=34     ;"TRAP" TRAP
138      TKVEC= 60      ;TTY KEYBOARD VECTOR
139      TPVEC= 64      ;TTY PRINTER VECTOR
140      PIRGVEC=240    ;PROGRAM INTERRUPT REQUEST VECTOR
141
142      ;*MED CODES
143      MED= 076000    ;OPCODE
144
145      RNHAMI= 022    ;READ WHAMI
146
147      RFLAG= 144     ;READ FLAGS
148      WFLAG= 344     ;WRITE FLAGS
149
150      ;*FLOATING POINT INTERRUPT VECTOR
151      FPPVEC= 244
152
153      ;*FLOATING POINT REGISTER DEFINITIONS
154      AC0= 00
155      AC1= 01
156      AC2= 02
157      AC3= 03
158      AC4= 04
159      AC5= 05
160
161      ;*BIT PATTERNS FOR TESTS
162      ALTP= 052525   ; 0101...01
163      AP= ALTP
164      ALTN= 125252   ; 1010...10
165      AN= ALTN
166      ALT4P= 007417 ; 0000111100001111
167      ALT4N= 170360 ; 1111000011110000
168      A2= 177776    ; 1111...10 MINUS TWO

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169      177777      M1= 177777      ; 1111...11 MINUS ONE, ALL 1'S
170      100000      M0= 100000     ; 1000...00 MINUS ZERO
171      077777      LGP= 077777     ; 0111...11 LGST + NUM (1ST WD FLT)
172      177777      LGN= 177777     ; 1111...11 LGST - NUM (1ST WD FLT)
173      000200      SMP= 000200     ; +1*2**=120, SMLT + NUM (1ST WD FLT)
174      100200      SMN= 100200     ; -1*2**=120, SMLT - NUM (1ST WD FLT)
175      000177      ZXIMP= 000177   ; ZERO EXP, ALL 1-S MANT (1ST WD FLT)
176      100177      ZXIMN= 100177   ; ZERO EXP, ALL 1-S MANT (1ST WD FLT)
177      040200      F1P= 040200     ; +1.0E+0, 1ST WD FLT
178      140200      F1N= 140200     ; -1.0E+0, 1ST WD FLT
179      104210      P13Z= 104210    ; 1000100010001000
180      000377      LB= 000377      ; 0000000011111111 LOWER BYTE
181      177400      UB= 177400     ; 1111111100000000 UPPER BYTE
182
183      ;*FPS BIT PATTERNS
184      147757      FPS1= 147757     ; ALL BITS ON (HEADABLE)
185      000000      FPS0= 000000     ; ALL BITS OFF
186      000000      NA= 000000      ; FOR FEC, WHEN NOT APPLICABLE
187
188      ;*PSW BIT PATTERNS
189      177760      CCONLY= 177760   ; FOR SIC TO GET CC BITS ONLY
190
191      ;*BTTL TRAP CATCHER
192
193      ;*BTTL TRAP CATCHER
194      .BTTL
195      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+J,MALI"
196      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
197      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
198      .BTTL
199      000174      000000      .=174      ;SOFTWARE DISPLAY REGISTER
200      000176      000000      .SHREG; .WORD 0      ;SOFTWARE SWITCH REGISTER
201      000200      000137      002400    .BTTL STARTING ADDRESS(ES)
202      .JMP .BTSTART ;JUMP TO STARTING ADDRESS OF PROGRAM
203
204      ;*BTTL ACT11 HOOKS
205
206      ;*****
207      ;HOOKS REQUIRED BY ACT11
208      .BTTL
209      000204      .BTVP; .BTVP      ;SAVE PC
210      000046      .BTVP; .BTVP      ;SAVE PC
211      000046      .BTVP; .BTVP      ;SAVE PC
212      000052      .BTVP; .BTVP      ;SAVE PC
213      000052      .BTVP; .BTVP      ;SAVE PC
214      000052      .BTVP; .BTVP      ;SAVE PC
215      000052      .BTVP; .BTVP      ;SAVE PC
216      000052      .BTVP; .BTVP      ;SAVE PC
217      000052      .BTVP; .BTVP      ;SAVE PC
218      000052      .BTVP; .BTVP      ;SAVE PC
219      000052      .BTVP; .BTVP      ;SAVE PC
220      000052      .BTVP; .BTVP      ;SAVE PC
221      000052      .BTVP; .BTVP      ;SAVE PC
222      000052      .BTVP; .BTVP      ;SAVE PC
223      000052      .BTVP; .BTVP      ;SAVE PC
224      000052      .BTVP; .BTVP      ;SAVE PC

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225          .R4X      ;;RESET LOCATION COUNTER
226          ;;*****
227          ;;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
228          ;;INTERFACE SPEC.
229
230          SAPTMDI
231          SHIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
232          SMAIL: .WORD 0      ;;ADDRESS OF APT MAILBOX (BITS 0-15)
233          STMT: .WORD 1      ;;RUN TIM OF LONGEST TEST
234          SPASTM: .WORD 1     ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
235          SUNITM: .WORD 0     ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
236          SETEND: .WORD 0     ;;LENGTH MAILBOX-ETABLE(WORDS)
237

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238          .SBTTL COMMON TAGS
239
240          ;;*****
241          ;;THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
242          ;;USED IN THE PROGRAM.
243
244          .1100
245          SCMTAG: .1100      ;;START OF COMMON TAGS
246          ;;-----START OF CLEAR COMMON TAGS-----
247          STNHN: .WORD 0
248          SERFLG: .WORD 0   ;;CONTAINS THE TEST NUMBER
249          SICNT: .WORD 0   ;;CONTAINS ERROR FLAG
250          SLPADR: .WORD 0  ;;CONTAINS SUBTEST ITERATION COUNT
251          SLPERR: .WORD 0  ;;CONTAINS SCOPE LOOP ADDRESS
252          SERTTL: .WORD 0  ;;CONTAINS SCOPE RETURN FOR ERRORS
253          SITEMB: .WORD 0  ;;CONTAINS TOTAL ERRORS DETECTED
254          SERMAX: .WORD 1  ;;CONTAINS ITEM CONTROL BYTE
255          SERRPC: .WORD 0  ;;CONTAINS MAX. ERRORS PER TEST
256          SGADR: .WORD 0  ;;CONTAINS PC OF LAST ERROR INSTRUCTION
257          SBADR: .WORD 0  ;;CONTAINS ADDRESS OF "GOOD" DATA
258          SGDDAT: .WORD 0  ;;CONTAINS ADDRESS OF "BAD" DATA
259          SBDDAT: .WORD 0  ;;CONTAINS "GOOD" DATA
260          SRTTBL: .WORD 0  ;;CONTAINS "BAD" DATA
261          SAUTOB: .WORD 0  ;;RESERVED--NOT TO BE USED
262          SINTAG: .BYTE 0  ;;AUTOMATIC MODE INDICATOR
263          SINTAG: .BYTE 0  ;;INTERRUPT MODE INDICATOR
264
265          ;;-----END OF CLEAR COMMON TAGS-----
266          SWR: .WORD D4WR   ;;ADDRESS OF SWITCH REGISTER
267          DISPLA: .WORD DDISP ;;ADDRESS OF DISPLAY REGISTER
268          SLPTST: .WORD 0   ;;CONTAINS TEST NUMBER TO LOOP UPON
269          STKS: 177560     ;;TTY KBD STATUS
270          STKB: 177562     ;;TTY KBD BUFFER
271          STPB: 177564     ;;TTY PRINTER STATUS REG. ADDRESS
272          STPB: 177566     ;;TTY PRINTER BUFFER REG. ADDRESS
273          SNULL: .BYTE 0   ;;CONTAINS NULL CHARACTER FOR FILLS
274          SFILLS: .BYTE 2  ;;CONTAINS # OF FILLER CHARACTERS REQUIRED
275          SFILLC: .BYTE 12 ;;INSERT FILL CHARS. AFTER A "LINE FEED"
276          STPFLG: .BYTE 0  ;;"TERMINAL AVAILABLE" FLAG (BIT<07>=0=1)
277          SREGAD: .WORD 0  ;;CONTAINS THE ADDRESS FROM
278          SREG0: .WORD 0   ;;WHICH ((SREG0) WAS OBTAINED
279          SREG1: .WORD 0   ;;CONTAINS ((SREG0)+0)
280          SREG2: .WORD 0   ;;CONTAINS ((SREG0)+2)
281          SREG3: .WORD 0   ;;CONTAINS ((SREG0)+4)
282          SREG4: .WORD 0   ;;CONTAINS ((SREG0)+6)
283          SREG5: .WORD 0   ;;CONTAINS ((SREG0)+8)
284          SREG6: .WORD 0   ;;CONTAINS ((SREG0)+10)
285          SREG7: .WORD 0   ;;CONTAINS ((SREG0)+12)
286          SREG8: .WORD 0   ;;CONTAINS ((SREG0)+14)
287          SREG9: .WORD 0   ;;CONTAINS ((SREG0)+16)
288          SREG10: .WORD 0  ;;CONTAINS ((SREG0)+18)
289          SREG11: .WORD 0  ;;CONTAINS ((SREG0)+20)
290          SREG12: .WORD 0  ;;CONTAINS ((SREG0)+22)
291          SREG13: .WORD 0  ;;CONTAINS ((SREG0)+24)
292          SREG14: .WORD 0  ;;CONTAINS ((SREG0)+26)
293          SREG15: .WORD 0  ;;CONTAINS ((SREG0)+28)

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294 001224 000000 0REG161 .WORD 0 ;;CONTAINS ((0REGAD)+34)
295 001226 000000 0REG171 .WORD 0 ;;CONTAINS ((0REGAD)+36)
296 001230 000000 0TMP01 .WORD 0 ;;USER DEFINED
297 001232 000000 0TMP11 .WORD 0 ;;USER DEFINED
298 001234 000000 0TMP21 .WORD 0 ;;USER DEFINED
299 001236 000000 0TMP31 .WORD 0 ;;USER DEFINED
300 001240 000000 0TMP41 .WORD 0 ;;USER DEFINED
301 001242 000000 0TMP51 .WORD 0 ;;USER DEFINED
302 001244 000000 0TMP61 .WORD 0 ;;USER DEFINED
303 001246 000000 0TMP71 .WORD 0 ;;USER DEFINED
304 001250 000000 0TMP101 .WORD 0 ;;USER DEFINED
305 001252 000000 0TMP111 .WORD 0 ;;USER DEFINED
306 001254 000000 0TMP121 .WORD 0 ;;USER DEFINED
307 001256 000000 0TMP131 .WORD 0 ;;USER DEFINED
308 001260 000000 0TMP141 .WORD 0 ;;USER DEFINED
309 001262 000000 0TMP151 .WORD 0 ;;USER DEFINED
310 001264 000000 0TMP161 .WORD 0 ;;USER DEFINED
311 001266 000000 0TMP171 .WORD 0 ;;USER DEFINED
312 001270 000000 0TMP201 .WORD 0 ;;USER DEFINED
313 001272 000000 0TMP211 .WORD 0 ;;USER DEFINED
314 001274 000000 0TMP221 .WORD 0 ;;USER DEFINED
315 001276 000000 0TMP231 .WORD 0 ;;USER DEFINED
316 001300 000000 0TMP241 .WORD 0 ;;USER DEFINED
317 001302 000000 0TMP251 .WORD 0 ;;USER DEFINED
318 001304 000000 0TMP261 .WORD 0 ;;USER DEFINED
319 001306 000000 0TMP271 .WORD 0 ;;USER DEFINED
320 001310 000000 0TIMES: 0 ;;MAX. NUMBER OF ITERATIONS
321 001312 000000 0ESCAPE: 0 ;;ESCAPE ON ERROR ADDRESS
322 001314 177007 000377 0BELL: .ASCII <207><377><377> ;;CODE FOR BELL
323 001320 077 0SQUEL: .ASCII /? ;;QUESTION MARK
324 001321 015 0CRLF: .ASCII <15> ;;CARRIAGE RETURN
325 001322 000012 0LF: .ASCII <12> ;;LINE FEED
326 ;;*****
327 .SBTTL APT MAILBOX-ETABLE
328
329 ;;*****
330 .EVEN
331 001324 000000 0MAIL: .WORD 0 ;;APT MAILBOX
332 001324 000000 0MSGTY: .WORD 0MSGTY ;;MESSAGE TYPE CODE
333 001326 000000 0FATAL: .WORD 0AFATAL ;;FATAL ERROR NUMBER
334 001330 000000 0TESTN: .WORD 0ATESTN ;;TEST NUMBER
335 001332 000000 0PASS: .WORD 0APASS ;;PASS COUNT
336 001334 000000 0DEVCT: .WORD 0ADEVCT ;;DEVICE COUNT
337 001336 000000 0UNIT: .WORD 0AUNIT ;;I/O UNIT NUMBER
338 001340 000000 0MSGAD: .WORD 0MSGAD ;;MESSAGE ADDRESS
339 001342 000000 0MSGLG: .WORD 0MSGGLG ;;MESSAGE LENGTH
340 001344 000000 0ETABLE: .WORD 0 ;;APT ENVIRONMENT TABLE
341 001344 000 0ENV: .BYTE 0AENV ;;ENVIRONMENT BYTE
342 001345 000 0ENVN: .BYTE 0AENVN ;;ENVIRONMENT MODE BITS
343 001346 000000 0SWREG: .WORD 0ASWREG ;;APT SWITCH REGISTER
344 001350 000000 0USWR: .WORD 0AUSWR ;;USER SWITCHES
345 001352 000000 0CPUOP: .WORD 0ACPUOP ;;CPU TYPE,OPTIONS
346 ;*
347 ;* BIT 15=11=CPU TYPE
348 ;* 11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
349 ;* 11/70=06,PDQ=07,Q=10
;* BIT 10=REAL TIME CLOCK

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350 ;* BIT 9=FLOATING POINT PROCESSOR
351 ;* BIT 8=MEMORY MANAGEMENT
352 001354 000000 0ETEND:
353 .MEXIT

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354 .SMTTL ERROR POINTER TABLE
355
356 @01354 $ERRTB1
357
358 ;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
359 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
360 ;*LOCATION ITEMS. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
361 ;*NOTE1: IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRFC).
362 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
363
364 ;* EM ;POINTS TO THE ERROR MESSAGE
365 ;* DH ;POINTS TO THE DATA HEADER
366 ;* DT ;POINTS TO THE DATA
367 ;* DF ;POINTS TO THE DATA FORMAT
368 ;*NOTE: ERROR VECTOR TABLE ($ERRTB) HAS BEEN MODIFIED,
369 ;* ELIMINATING UNUSED VALUE FOR DATA FORMAT POINTER.
370 ;* ERROR TYPING ROUTINE HAS ALSO BEEN MODIFIED
371 ;* ACCORDINGLY.
372 ;***** VECTORS FOR FPS ERRORS *****
373 @01354 @44147 @45366 @45642 EMV0011 .WORD EMA,DHA,DTA ; LDCIF,STCFI,STEXP/F,TRAP-TSTR
374 @01362 @44147 @45366 @45650 EMV0021 .WORD EMA,DHA,DTB ; CMPF,LDCIF,SICFL
375 @01370 @44147 @45366 @45656 EMV0031 .WORD EMA,DHA,DTC ; LDCID,STCDI,LDEXP/F,STEXP/D
376 @01376 @44147 @45366 @45664 EMV0041 .WORD EMA,DHA,DTD ; ADDF,SUBF,MULF,UIVF,LDCDF,LDCFD
377 ; STCDF,LDCLD,STCUL
378 @01404 @44147 @45366 @45672 EMV0051 .WORD EMA,DHA,DTE ; CMPD,MODF,SICFD
379 @01412 @44147 @45366 @45700 EMV0061 .WORD EMA,DHA,DTF ; LDEXP/D
380 @01420 @44147 @45366 @45706 EMV0071 .WORD EMA,DHA,DTG ; ADDD,SUBD,MULD,DIVD
381 @01426 @44147 @45366 @45714 EMV0101 .WORD EMA,DHA,UTH ; MODD
382 ;***** VECTORS FOR FEC/FEA ERRORS *****
383 @01434 @44173 @45402 @45722 EMV0111 .WORD EMB,DHB,DTI ; STCFI,TRAP-TSTR
384 @01442 @44173 @45402 @45734 EMV0121 .WORD EMB,DHB,DTJ ; CMPF,STCFL
385 @01450 @44173 @45402 @45746 EMV0131 .WORD EMB,DHB,DTK ; STCDI,LDEXP/F
386 @01456 @44173 @45402 @45760 EMV0141 .WORD EMB,DHB,DTL ; ADDF,SUBF,MULF,UIVF,LDCUF
387 ; LDCDF,STCDF,SICUL
388 @01464 @44173 @45402 @45772 EMV0151 .WORD EMB,DHB,DTM ; CMPD,MODF
389 @01472 @44173 @45402 @46004 EMV0161 .WORD EMB,DHB,DTN ; LDEXP/D
390 @01500 @44173 @45402 @46016 EMV0171 .WORD EMB,DHB,DTO ; ADDD,SUBD,MULD,DIVD
391 @01506 @44173 @45402 @46030 EMV0201 .WORD EMB,DHB,DTP ; MODD
392 ;***** VECTORS FOR RESULT ERRORS *****
393 @01514 @44357 @45442 @46056 EMV0211 .WORD EME,DHC,DTB ; CMPF
394 @01522 @44357 @45500 @46140 EMV0221 .WORD EME,DHC,DTX ; CMPD
395 @01530 @44446 @45442 @46114 EMV0231 .WORD EME,DHC,DTY ; ADDF,SUBF
396 @01536 @44446 @45500 @46250 EMV0241 .WORD EME,DHC,DTAB ; ADDD,SUBD
397 @01544 @44320 @45500 @46114 EMV0251 .WORD EME,DHC,DTV ; MULF,UIVF
398 @01552 @44320 @45500 @46250 EMV0261 .WORD EME,DHC,DTAB ; MULD,DIVD
399 @01560 @44372 @45442 @46114 EMV0271 .WORD EME,DHC,DTV ; MODF-FRAC
400 @01566 @44653 @45442 @46126 EMV0301 .WORD EMI,DHD,DTW ; MODF-INT
401 @01574 @44572 @45500 @46250 EMV0311 .WORD EMI,DHD,DTAB ; MODD-FRAC
402 @01602 @44653 @45500 @46272 EMV0321 .WORD EMI,DHD,DTAC ; MODD-INT
403 @01610 @45007 @45442 @46114 EMV0331 .WORD EMI,DHC,DTV ; LDCDF,STCDF
404 @01616 @44731 @45500 @46204 EMV0341 .WORD EMI,DHD,DTZ ; LDCFD
405 @01624 @44731 @45500 @46226 EMV0351 .WORD EMI,DHD,DTAA ; STCF
406 @01632 @45065 @45442 @46070 EMV0361 .WORD EMI,DHC,DTT ; LDCIF
407 @01640 @45065 @45500 @46162 EMV0371 .WORD EMI,DHD,DTY ; LDCID
408 @01646 @45065 @45442 @46102 EMV0401 .WORD EMI,DHC,DTU ; LDCLF
409 @01654 @45065 @45500 @46204 EMV0411 .WORD EMI,DHD,DTZ ; LDCLD

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410 @01662 @45145 @45366 @46042 EMV0421 .WORD EMI,DHA,DTG ; STCFI
411 @01670 @45145 @45366 @46050 EMV0431 .WORD EMI,DHA,DTR ; STCDI
412 @01676 @45145 @45442 @46102 EMV0441 .WORD EMI,DHC,DTU ; SICFL
413 @01704 @45145 @45442 @46114 EMV0451 .WORD EMI,DHC,DTV ; STCUL
414 @01712 @45225 @45442 @46102 EMV0461 .WORD EMI,DHC,DTU ; LDEXP/F
415 @01720 @45225 @45500 @46226 EMV0471 .WORD EMI,DHD,DTAA ; LDCDF
416 @01726 @45305 @45366 @46042 EMV0501 .WORD EMO,DHA,DTU ; STEXP/F
417 @01734 @45305 @45366 @46050 EMV0511 .WORD EMO,DHA,DTR ; STEXP/D
418 @01742 @00000 @00000 @00000 EMV0521 .WORD 0,0,0 ; (UNUSED)
419 @01750 @00000 @00000 @00000 EMV0531 .WORD 0,0,0 ; (UNUSED)
420
421 @01756 @44310 @45366 @46314 EMV0541 .WORD EMO,DHA,DTAU ; STCFI,STCDI,STEXP/F,STEXP/D
422 @01764 @44310 @45366 @46322 EMV0551 .WORD EMO,DHA,DTAL ; STCFL,STCUL
423
424 @01772 @44223 @45576 @46330 EMV0561 .WORD EMC,DHF,DTAK ; UNEXPECTED TRAF

```

```

425 .SBTTL PROGRAM DEFINED COMMON TAGS
426 ;*VARIABLES
427 FFS: .WORD 0 ; FFS STORED HERE AFTER STFFS
428 FEC: .WORD 0 ; FEC STORED HERE AFTER STBT
429 FEA: .WORD 0 ; FEA STORED HERE AFTER STBT
430 FPPOPC: .WORD 0 ; OLD PC SAVED HERE AFTER TRAP AFTER TRAP
431 FPPOPS: .WORD 0 ; OLD PS SAVED HERE
432 FPPOSP: .WORD 0 ; SP AFTER TRAP
433 EXPFEA: .WORD 0 ; EXPECTED FEA
434
435 ;*REGISTER CONTENTS, AT ERROR, FOR DISPLAY
436 LREG1: .WORD 0
437 EREG1: .WORD 0
438 EREG2: .WORD 0
439 LREG2: .WORD 0
440 EREG3: .WORD 0
441 EREG4: .WORD 0
442 EREG5: .WORD 0
443 EREG6: .WORD 0
444 EREG7: .WORD 0
445
446 ;*CONSTANTS
447 PREVAC: .WORD ALTP,M1,ALTN,0 ; PREV CONTENTS OF FLOAT AC
448
449
450 ;*MESSAGES FOR BEGIN PROGRAM/START OF PASS/ETC
451 MGNMES: .ASCII <CR><LF><LF><LF>"MD-11-DQFP88-"
452
453 .ASCII "B."
454 .ASCII "..."
455 .ASCII "FDP-11/6X F.P.U. ADVANCED INSTRUCTION TESTS"<CR><LF>
456
457
458
459
460
461
462
463
464
465

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```

466 .SBTTL START OF PASS ROUTINE
467
468
469
470 ;*****
471 .ENABL AMA ; ASSEMBLE ALL RELATIVE REFERENCES AS ABSOLUTE
472 ;*****
473
474 START:
475 .SBTTL INITIALIZE THE COMMON TAGS
476 ;CLEAR THE COMMON TAGS (SCMTAG) AREA
477 MOV #SCMTAG,R6 ;FIRST LOCATION TO BE CLEARED
478 CLR (R6)+ ;CLEAR MEMORY LOCATION
479 CMP #SWR,R6 ;DONE?
480 BNE =0 ;LOOP BACK IF NO
481 MOV #STACK,SP ;SETUP THE STACK POINTER
482
483 ;INITIALIZE A FEW VECTORS
484 MOV #SCOPE,#IOTVEC ;IOT VECTOR FOR SCOPE ROUTINE
485 MOV #340,#AOTVEC+2 ;LEVEL 7
486 MOV #ERROR,#EMTVEC ;EMT VECTOR FOR ERROR ROUTINE
487 MOV #340,#EMTVEC+2 ;LEVEL 7
488 MOV #TRAP,#TRAPVEC ;TRAP VECTOR FOR TRAP CALLS
489 MOV #340,#TRAPVEC+2 ;LEVEL 7
490 MOV #SPWRDN,#SPWRVEC ;POWER FAILURE VECTOR
491 MOV #340,#SPWRVEC+2 ;LEVEL 7
492 MOV #ENDCT,#EOPT ;SETUP END-OF-PROGRAM COUNTER
493 CLM #TIMES ;INITIALIZE NUMBER OF ITERATIONS
494 CLR #ESCAPE ;CLEAR THE ESCAPE OR ERROR ADDRESS
495 MOV #1,#ERMAX ;ALLOW ONE ERROR PER TEST
496 MOV #0,#LPADR ;INITIALIZE THE LOOP ADDRESS FOR SCOPE
497 MOV #0,#LPERR ;SETUP THE ERROR LOOP ADDRESS
498
499 ;SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
500 ;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
501 MOV #ERRVEC,=(SP) ;SAVE ERROR VECTOR
502 MOV #640,#ERRVEC ;SET UP ERROR VECTOR
503 MOV #DSWR,SWR ;SETUP FOR A HARDWARE SWICH REGISTER
504 MOV #DISP,DISPLAY ;FIND A HARDWARE DISPLAY REGISTER
505 CMP #=-1,SWR ;TRY TO REFERENCE HARDWARE SWR
506 BNE 666 ;BRANCH IF NO TIMEOUT TRAP OCCURRED
507 ;FIND THE HARDWARE SWK IS NOT = -1
508 BR 658 ;BRANCH IF NO TIMEOUT
509 MOV #658,(SP) ;SET UP FOR TRAP RETURN
510 RTI
511 MOV #SWREG,SWR ;POINT TO SOFTWARE SWR
512 MOV #DISP,DISPLAY ;DISPREG,DISPLAY
513 MOV (SP)+,#ERRVEC ;RESTORE ERROR VECTOR
514
515 CLR #PASS ;CLEAR PASS COUNT
516 BITB #APTSIZE,#ENVM ;TEST USER SIZE UNDER APT
517 BEQ 678 ;YES,USE NON-APT SWITCH
518 MOV #SSWREG,SWR ;NO,USE APT SWITCH REGISTER
519
520 ; SET UP FPP UNEXPECTED TRAP CATCHER - - - - -
521 MOV #FPLLT,#FPPVEC ; NEW PC AT FPP TRAP
522 CLR #FPPVEC+2 ; NEW PS AT FPP TRAP
523

```



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522 002662 104401 002046                TYPE      ,BGNMES          ; ID MESSAGE AT START
523
524                                     ;////////////////////////////////////
525                                     ; MESSAGE ON WHETHER OR NOT HFP UNIT IS PRESENT
526
527 002666 076600 000022                MED      ,RWHAMI          ;WHAMI INTO R0
528 002672 032700 000020                BIT      @BIT04,R0        ;IS THERE A HFP UNIT ?
529 002706 001403                        BEQ      208                ;NO, BR
530 002700 104401 002714                TYPE      ,608            ;INDICATE FP11-E PRESENT
531 002704 000453                        BR       NEWPAS            ;GO FOR SUBPASS INIT
532 002706 104401 002754                7001    TYPE      ,698            ;INDICATE NO FP11-E
533 002712 000450                        BR       NEWPAS            ;GO FOR SUBPASS INIT
534
535 002714 005015 020052 050106 6801    .ASCII <15><12>"" FP11-E HFP UNIT PRESENT ""<15><12>
536 002722 030461 042455 044040
537 002730 050106 052440 044516
538 002736 020124 051120 051505
539 002744 047105 020124 006452
540 002752 000012
541 002754 005015 020052 047516 6901    .ASCII <15><12>"" NO FP11-E HFP UNIT = ALL TESTS WFP ONLY ""<15><12>
542 002762 043040 030520 026461
543 002770 020105 043110 020120
544 002776 047125 052111 026440
545 003004 040440 046114 052040
546 003012 051505 051524 053440
547 003020 050106 047440 046116
548 003026 020131 006452 000012
549
550                                     .EVEN
551
552                                     ;////////////////////////////////////
553
554                                     ;*****
555                                     ;NEW PASS ENTERS HERE
556                                     ;*****
557
558 003034 012706 001100                NEWPAS: MOV      @STACK,SP          ;RESET STACK PTR
559
560 003040 012777 010000 176076                BIT      @BIT12,@SWR          ;INHIBIT STATUS TYPEOUTS ?
561 003046 001011                        BNE     SUBPAS              ;BR IF YES
562
563 003050 104401 002150                TYPE      ,NMPAS1          ;"PASS #"  
564 003054 013746 001332                MOV      @PASB,=(SP)        ;PASS COUNT INTO ...  
565 003060 005216                        INC     (SP)                 ; i=N RANGE  
566 003062 104403                        TYP0B          ;TYPE OCTAL  
567 003064 006 000                        .BYTE   6,0                 ; 6 DIGITS, NO LEADING ZEROS  
568 003066 104401 001321                TYPE     ,%CRLF            ;END THE LINE
569
570
571                                     ;*****
572                                     ;NEW SUBPASS ENTERS HERE
573                                     ;*****
574
575 003072 012706 001100                SUBPAS: MOV      @STACK,SP          ;RESET SP FOR INSURANCE
576
577 003076 076600 000022                MED      ,RWHAMI          ;GET WHAMI INTO R0

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578 003102 032700 000020                BIT      @BIT04,R0          ;I=HFP PRESENT, 0=NO
579 003106 001403                        BEQ     208                ;IF NO HFP, TEST WARM ONLY
580
581 003110 076600 000144                MED      ,RFLAG            ;GET FLAGS INTO R0
582
583 003114 032777 000002 176022                BIT      @SW01,@SWR        ;SW01: 1=HFP OR WFP TEST ONLY
584 003122 001413                        BEQ     18                  ; 0=ALTERNATE HFP/WFP PER PASS
585
586 003124 032777 000001 176012                BIT      @SW00,@SWR        ;SW00: 1=HFP ONLY
587 003132 001403                        BEQ     28                  ; 0=HFP ONLY
588 003134 042700 010000                BIC     @BIT12,R0          ;CLEAR HFP ENABLE FLAG<S> FOR WFP
589 003140 000402                        BR      38                  ;
590 003142 052700 010000                201    SIS     @BIT12,R0    ;SET HFP ENABLE FLAG<S> FOR HFP
591 003146 076600 000344                301    MED     ,RFLAG        ;REWRITE FLAGS
592
593 003152 032700 010000                101    BIT     @BIT12,R0    ;TEST WHO'S ENABLED! HOT, WARM
594 003156 001404                        BEQ     208                ;SET APPROPRIATE HEADERS
595
596 003160 312737 044132 042504 1901    MOV      @ASCROT,HOTWRM    ;"HOT: "  
597 003166 000403                        BR      210                ;
598 003170 012737 044140 042504 2001    MOV      @ASCWRM,HOTWRM    ;"WARM: "  
599 003176                        2101

```

```

000
001
002
003
004
005 003176 000004
006 003200 012705 003212
007 003204 004737 033330
008
009 003210 000407
010
011 003212
012 003212 000000 000000
013 003216 000000 000000
014 003222 047453 047444
015 003226 000000
016
017
018
019
020
021
022
023 003230 000004
024 003232 012705 003244
025 003236 004737 033330
026
027 003242 000407
028
029 003244
030 003244 052525 052525
031 003250 052525 052525
032 003254 047513 047504
033 003260 000000
034
035
036
037
038
039
040
041 003262 000004
042 003264 012705 003276
043 003270 004737 033330
044
045 003274 000407
046
047 003276
048 003276 077777 177777
049 003302 177777 177777
050 003306 047507 047510
051 003312 000000
052
053
054
055

```

```

);*****
;*TEST 1 TEST OF CMPF INSTR, DATA SET CMPF-1
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST1: SCOPE
MOV #CMPF1,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST2 ;;

CMPF1: ; TEST DATA SET CMPF-1
.WORD 0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 047453,047444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 2 TEST OF CMPF INSTR, DATA SET CMPF-2
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST2: SCOPE
MOV #CMPF2,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST3 ;;

CMPF2: ; TEST DATA SET CMPF-2:1
.WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD ALTP,ALTP ; INITIAL MEM FLOAT NUMBER
.WORD 047513,047504 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 3 TEST OF CMPF INSTR, DATA SET CMPF-3
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST3: SCOPE
MOV #CMPF3,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST4 ;;

CMPF3: ; TEST DATA SET CMPF-3:1
.WORD LGP,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 047507,047510 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 4 TEST OF CMPF INSTR, DATA SET CMPF-4

```

```

056
057
058
059 003314 000004
060 003316 012705 003330
061 003322 004737 033330
062
063 003326 000407
064
065 003330
066 003330 125252 125252
067 003334 125252 125252
068 003340 047453 047444
069 003344 000000
070
071
072
073
074
075
076
077 003346 000004
078 003350 012705 003362
079 003354 004737 033330
080
081 003360 000407
082
083 003362
084 003362 177777 177777
085 003366 077777 177777
086 003372 047457 047440
087 003376 000000
088
089
090
091
092
093
094
095 003400 000004
096 003402 012705 003414
097 003406 004737 033330
098
099 003412 000407
100
101 003414
102 003414 077777 177777
103 003420 040000 000000
104 003424 047517 047500
105 003430 000000
106
107
108
109
110
111

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```

;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST4: SCOPE
MOV #CMPF4,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST5 ;;

CMPF4: ; TEST DATA SET CMPF-4:1
.WORD ALTN,ALTN ; INITIAL AC FLOAT NUMBER
.WORD ALTN,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 047453,047444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 5 TEST OF CMPF INSTR, DATA SET CMPF-5
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST5: SCOPE
MOV #CMPF5,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST6 ;;

CMPF5: ; TEST DATA SET CMPF-5:1
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGP,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 047457,047440 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 6 TEST OF CMPF INSTR, DATA SET CMPF-6
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST6: SCOPE
MOV #CMPF6,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPFT ; GO TEST
BR TST7 ;;

CMPF6: ; TEST DATA SET CMPF-6:1
.WORD 037777,M1 ; INITIAL AC FLOAT NUMBER
.WORD 040000,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 047517,047500 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

);*****
;*TEST 7 TEST OF CMPF INSTR, DATA SET CMPF-7
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES

```

712  
713 003432 000004  
714 003434 012705 003446  
715 003440 004737 033330  
716  
717 003444 000407  
718  
719 003446  
720 003446 050000 000001  
721 003452 050000 000000  
722 003456 047547 047550  
723 003462 000000  
724  
725  
726  
727  
728  
729  
730  
731 003464 000004  
732 003466 012705 003500  
733 003472 004737 033330  
734  
735 003476 000407  
736  
737 003500  
738 003500 120000 000000  
739 003504 120000 000000  
740 003510 047417 047400  
741 003514 000000  
742  
743  
744  
745  
746  
747  
748  
749 003516 000004  
750 003520 012705 003532  
751 003524 004737 033330  
752  
753 003530 000407  
754  
755 003532  
756 003532 007417 007417  
757 003536 100000 000000  
758 003542 047443 147443  
759 003546 100014  
760  
761  
762  
763  
764  
765  
766  
767 003550 000004

```

*****
TST7:  SCOPE
      MOV  %CMPF7,R5      ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT     ; GO TEST
      BR   TST10        ;;

CMPF7: ; TEST DATA SET CMPF-7:
      .WORD 050000,000001 ; INITIAL AC FLOAT NUMBER
      .WORD 050000,000000 ; INITIAL MEM FLOAT NUMBER
      .WORD 047547,047550 ; FPS: BEFORE, AFTER
      .WORD NA             ; FEC AFTER ( 0 = N/A )

*****
;*TEST 10 TEST OF CMPF INSTR, DATA SET CMPF-10
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
*****
TST10: SCOPE
      MOV  %CMPF10,R5    ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT    ; GO TEST
      BR   TST11        ;;

CMPF10: ; TEST DATA SET CMPF-10:
      .WORD 120000,000000 ; INITIAL AC FLOAT NUMBER
      .WORD 120000,000000 ; INITIAL MEM FLOAT NUMBER
      .WORD 047417,047400 ; FPS: BEFORE, AFTER
      .WORD NA             ; FEC AFTER ( 0 = N/A )

*****
;*TEST 11 TEST OF CMPF INSTR, DATA SET CMPF-11
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST11: SCOPE
      MOV  %CMPF11,R5    ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT    ; GO TEST
      BR   TST12        ;;

CMPF11: ; TEST DATA SET CMPF-11:
      .WORD ALT4P,ALT4P  ; INITIAL AC FLOAT NUMBER
      .WORD M0,0         ; INITIAL MEM FLOAT NUMBER
      .WORD 047443,147443 ; FPS: BEFORE, AFTER
      .WORD 100014      ; FEC AFTER ( 0 = N/A )

*****
;*TEST 12 TEST OF CMPF INSTR, DATA SET CMPF-12
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST12: SCOPE

```

768 003552 012705 003564  
769 003556 004737 033330  
770  
771 003562 000407  
772  
773 003564  
774 003564 006177 177777  
775 003570 004177 177777  
776 003574 047507 047510  
777 003600 000000  
778  
779  
780  
781  
782  
783  
784  
785 003602 000004  
786 003604 012705 003616  
787 003610 004737 033330  
788  
789 003614 000407  
790  
791 003616  
792 003616 125252 125252  
793 003622 100177 177777  
794 003626 043557 043540  
795 003632 000000  
796  
797  
798  
799  
800  
801  
802  
803 003634 000004  
804 003636 012705 003650  
805 003642 004737 033330  
806  
807 003646 000407  
808  
809 003650  
810 003650 000377 177777  
811 003654 000377 177776  
812 003660 047407 047410  
813 003664 000000  
814  
815  
816

```

      MOV  %CMPF12,R5    ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT    ; GO TEST
      BR   TST13        ;;

CMPF12: ; TEST DATA SET CMPF-12:
      .WORD 006177,M1    ; INITIAL AC FLOAT NUMBER
      .WORD 004177,M1    ; INITIAL MEM FLOAT NUMBER
      .WORD 047507,047510 ; FPS: BEFORE, AFTER
      .WORD NA             ; FEC AFTER ( 0 = N/A )

*****
;*TEST 13 TEST OF CMPF INSTR, DATA SET CMPF-13
;*
;* -W INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
*****
TST13: SCOPE
      MOV  %CMPF13,R5    ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT    ; GO TEST
      BR   TST14        ;;

CMPF13: ; TEST DATA SET CMPF-13:
      .WORD ALTN,ALTN    ; INITIAL AC FLOAT NUMBER
      .WORD 2X1M,M1     ; INITIAL MEM FLOAT NUMBER
      .WORD 043557,043540 ; FPS: BEFORE, AFTER
      .WORD NA             ; FEC AFTER ( 0 = N/A )

*****
;*TEST 14 TEST OF CMPF INSTR, DATA SET CMPF-14
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
*****
TST14: SCOPE
      MOV  %CMPF14,R5    ; PTR TO TEST DATA SET
      JSR  PC,%CMPFT    ; GO TEST
      BR   TST15        ;;

CMPF14: ; TEST DATA SET CMPF-14:
      .WORD 000377,M1    ; INITIAL AC FLOAT NUMBER
      .WORD 000377,M2    ; INITIAL MEM FLOAT NUMBER
      .WORD 047407,047410 ; FPS: BEFORE, AFTER
      .WORD NA             ; FEC AFTER ( 0 = N/A )

```

```

017
018
019
020
021
022 003666 000004
023 003670 012705 003702
024 003674 004737 033476
025
026 003700 000413
027
028 003702
029 003702 000000 000000 000000
030 003710 000000
031 003712 000000 000000 000000
032 003720 000000
033 003722 047713 047704
034 003726 000000
035
036
037
038
039
040
041
042 003730 000004
043 003732 012705 003744
044 003736 004737 033476
045
046 003742 000413
047
048 003744
049 003744 177777 177777 177777
050 003752 177777
051 003754 177777 177777 177777
052 003762 177777
053 003764 047717 047700
054 003770 000000
055
056
057
058
059
060
061
062 003772 000004
063 003774 012705 004006
064 004000 004737 033476
065
066 004004 000413
067
068 004006
069 004006 170360 170360 170360
070 004014 170360
071 004016 170360 170360 170360
072 004024 170360

```

```

))*****
) *TEST 15 TEST OF CMPD INSTR, DATA SET CMPD-1
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, LONG INTEGER, ROUND MODES
))*****
TST15: SCOPE
MOV #CMPD1,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD1 ; GO TEST
BR TST16 ;;

CMPD1: ; TEST DATA SET CMPD-11
.WORD 0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 047713,047704 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

))*****
) *TEST 16 TEST OF CMPD INSTR, DATA SET CMPD-2
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, LONG INTEGER, ROUND MODES
))*****
TST16: SCOPE
MOV #CMPD2,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD2 ; GO TEST
BR TST17 ;;

CMPD2: ; TEST DATA SET CMPD-21
.WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 047717,047700 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

))*****
) *TEST 17 TEST OF CMPD INSTR, DATA SET CMPD-3
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, LONG INTEGER, ROUND MODES
))*****
TST17: SCOPE
MOV #CMPD3,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD3 ; GO TEST
BR TST20 ;;

CMPD3: ; TEST DATA SET CMPD-31
.WORD ALT4N,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER
.WORD ALT4N,ALT4N,ALT4N,ALT4N ; INITIAL MEM FLOAT NUMBER

```

```

073 004026 047713 047704
074 004032 000000
075
076
077
078
079
080
081
082 004034 000004
083 004036 012705 004050
084 004042 004737 033476
085
086 004046 000413
087
088 004050
089 004050 077777 177777 177777
090 004056 177777
091 004060 177777 177777 177777
092 004066 177777
093 004070 047647 047650
094 004074 000000
095
096
097
098
099
100
101
102 004076 000004
103 004100 012705 004112
104 004104 004737 033476
105
106 004110 000413
107
108 004112
109 004112 007417 007417 007417
110 004120 007417
111 004122 007417 007417 007417
112 004130 007417
113 004132 047653 047644
114 004136 000000
115
116
117
118
119
120
121
122 004140 000004
123 004142 012705 004154
124 004146 004737 033476
125
126 004152 000413
127
128 004154

```

```

))*****
) *TEST 20 TEST OF CMPD INSTR, DATA SET CMPD-4
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
))*****
TST20: SCOPE
MOV #CMPD4,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD4 ; GO TEST
BR TST21 ;;

CMPD4: ; TEST DATA SET CMPD-41
.WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 047647,047650 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

))*****
) *TEST 21 TEST OF CMPD INSTR, DATA SET CMPD-5
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
))*****
TST21: SCOPE
MOV #CMPD5,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD5 ; GO TEST
BR TST22 ;;

CMPD5: ; TEST DATA SET CMPD-51
.WORD ALT4P,ALT4P,ALT4P,ALT4P ; INITIAL AC FLOAT NUMBER
.WORD ALT4P,ALT4P,ALT4P,ALT4P ; INITIAL MEM FLOAT NUMBER
.WORD 047653,047644 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

))*****
) *TEST 22 TEST OF CMPD INSTR, DATA SET CMPD-6
) * ALL INTERRUPT ENABLES ON
) * LONG FLOAT, LONG INTEGER, ROUND MODES
))*****
TST22: SCOPE
MOV #CMPD6,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD6 ; GO TEST
BR TST23 ;;

CMPD6: ; TEST DATA SET CMPD-61

```

```
929 004154 125252 125252 125252 .WORD ALTN,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
930 004162 125252
931 004164 100177 177777 177777 .WORD EX1MN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
932 004172 177777
933 004174 047703 147703 .WORD 047703,147703 ; FPS: BEFORE, AFTER
934 004200 100014 .WORD 100014 ; FEC AFTER ( 0 = N/A )
935
936
937
938
939
940
941
942 004202 000004
943 004204 012705 004216
944 004210 004737 033476
945
946 004214 000413
947
948 004216
949 004216 002177 177777 177777 CMPD7: ; TEST DATA SET CMPD-7:
950 004224 177777 .WORD 002177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
951 004226 005177 177777 177777 .WORD 005177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
952 004234 177777
953 004236 047657 047640 .WORD 047657,047640 ; FPS: BEFORE, AFTER
954 004242 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
955
956
957
958
959
960
961
962 004244 000004
963 004246 012705 004260
964 004252 004737 033476
965
966 004256 000413
967
968 004260
969 004260 000000 000000 000000 CMPD10: ; TEST DATA SET CMPD-10:
970 004266 000000 .WORD 030000,000000,000000,000000 ; INITIAL AC FLOAT NUMBER
971 004270 027777 177777 177777 .WORD 027777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
972 004276 177777
973 004300 047647 047650 .WORD 047647,047650 ; FPS: BEFORE, AFTER
974 004304 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
975
976
977
978
979
980
981
982 004306 000004
983 004310 012705 004322
984 004314 004737 033476
;*****
;*TEST 23 TEST OF CMPD INSTR, DATA SET CMPD-7
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST23: SCOPE
MOV #CMPD7,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD7 ; GO TEST
BR TST24 ;
;*****
;*TEST 24 TEST OF CMPD INSTR, DATA SET CMPD-10
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST24: SCOPE
MOV #CMPD10,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD10 ; GO TEST
BR TST25 ;
;*****
;*TEST 25 TEST OF CMPD INSTR, DATA SET CMPD-11
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST25: SCOPE
MOV #CMPD11,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD11 ; GO TEST
```

```
985
986 004320 000413 BR TST26 ;
987
988 004322
989 004322 102000 000000 000000 CMPD11: ; TEST DATA SET CMPD-11:
990 004330 000000 .WORD 102000,000000,000000,000000 ; INITIAL AC FLOAT NUMBER
991 004332 102000 000000 000000 .WORD 102000,000000,000000,000001 ; INITIAL MEM FLOAT NUMBER
992 004340 000001
993 004342 047607 047610 .WORD 047607,047610 ; FPS: BEFORE, AFTER
994 004346 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
995
996
997
998
999
1000
1001
1002 004350 000004
1003 004352 012705 004364
1004 004356 004737 033476
1005
1006 004362 000413
1007
1008 004364
1009 004364 002000 000000 000000 CMPD12: ; TEST DATA SET CMPD-12:
1010 004372 000000 .WORD 002000,000000,000000,000000 ; INITIAL AC FLOAT NUMBER
1011 004374 014000 000000 000000 .WORD 014000,000000,000000,000000 ; INITIAL MEM FLOAT NUMBER
1012 004402 000000
1013 004404 047757 047740 .WORD 047757,047740 ; FPS: BEFORE, AFTER
1014 004410 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
1015
1016
1017
1018
1019
1020
1021
1022 004412 000004
1023 004414 012705 004426
1024 004420 004737 033476
1025
1026 004424 000413
1027
1028 004426
1029 004426 000000 000000 000000 CMPD13: ; TEST DATA SET CMPD-13:
1030 004434 000000 .WORD 000000,000000 ; INITIAL AC FLOAT NUMBER
1031 004436 100177 177777 000000 .WORD EX1MN,M1,0,M1 ; INITIAL MEM FLOAT NUMBER
1032 004444 177777
1033 004446 043713 043704 .WORD 043713,043704 ; FPS: BEFORE, AFTER
1034 004452 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
1035
1036
1037
1038
1039
1040
;*****
;*TEST 26 TEST OF CMPD INSTR, DATA SET CMPD-14
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST26: SCOPE
MOV #CMPD12,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD12 ; GO TEST
BR TST27 ;
;*****
;*TEST 27 TEST OF CMPD INSTR, DATA SET CMPD-13
;*
;* =0 INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST27: SCOPE
MOV #CMPD13,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD13 ; GO TEST
BR TST28 ;
;*****
;*TEST 28 TEST OF CMPD INSTR, DATA SET CMPD-14
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST28: SCOPE
MOV #CMPD14,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD14 ; GO TEST
BR TST29 ;
;*****
;*TEST 29 TEST OF CMPD INSTR, DATA SET CMPD-14
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST29: SCOPE
MOV #CMPD14,R5 ; PTR TO TEST DATA SET
JSR PC,#CMPD14 ; GO TEST
BR TST30 ;
```

```

1041
1042 004454 000004
1043 004456 012705 004470
1044 004462 004737 033476
1045
1046 004466 000413
1047
1048 004470
1049 004470 100777 000000 177777
1050 004476 000001
1051 004500 100777 000000 177777
1052 004506 000000
1053 004510 047657 047640
1054 004514 000000
1055
1056

```

```

;*****
TST30: SCOPE
MOV  #CMPD14,R5 ; PTR TO TEST DATA SET
JSR  PC,#CMPD1 ; GO TEST
BR  TST31 ;
;*****
CMPD14: ; TEST DATA SET CMPD-14:
.WORD 100777,000000,M1,000001 ; INITIAL AC FLOAT NUMBER
.WORD 100777,000000,M1,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 047657,047640 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****

```

```

1057
1058
1059
1060
1061
1062 004516 000001
1063 004520 012705 004532
1064 004524 004737 033664
1065
1066 004530 000411
1067
1068 004532
1069 004532 000177 177777
1070 004536 000177 177777
1071 004542 000000 000000
1072 004546 047453 047444
1073 004552 000000
1074
1075
1076
1077
1078
1079
1080
1081 004554 000004
1082 004556 012705 004570
1083 004562 004737 033664
1084
1085 004566 000411
1086
1087 004570
1088 004570 000000 000000
1089 004574 125252 125252
1090 004600 125252 125252
1091 004604 047407 047410
1092 004610 000000
1093
1094
1095
1096
1097
1098
1099
1100 004612 000004
1101 004614 012705 004626
1102 004620 004737 033664
1103
1104 004624 000411
1105
1106 004626
1107 004626 052525 052525
1108 004632 000000 000000
1109 004636 052525 052525
1110 004642 047557 047540
1111 004646 000000
1112

```

```

;*****
;*TEST 31 TEST OF ADDF INSTR, DATA SET ADDF-1
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST31: SCOPE
MOV  #ADDF1,R5 ; PTR TO TEST DATA SET
JSR  PC,#ADDF1 ; GO TEST
BR  TST32 ;
;*****
ADDF1: ; TEST DATA SET ADDF-1:
.WORD 0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047453,047444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****
;*TEST 32 TEST OF ADDF INSTR, DATA SET ADDF-2
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST32: SCOPE
MOV  #ADDF2,R5 ; PTR TO TEST DATA SET
JSR  PC,#ADDF2 ; GO TEST
BR  TST33 ;
;*****
ADDF2: ; TEST DATA SET ADDF-2:
.WORD 0,0 ; INITIAL AC FLOAT NUMBER
.WORD ALTN,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD ALTN,ALTN ; EXPECTED FLOAT RESULT
.WORD 047407,047410 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****
;*TEST 33 TEST OF ADDF INSTR, DATA SET ADDF-3
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST33: SCOPE
MOV  #ADDF3,R5 ; PTR TO TEST DATA SET
JSR  PC,#ADDF3 ; GO TEST
BR  TST34 ;
;*****
ADDF3: ; TEST DATA SET ADDF-3:
.WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER
.WORD ALTP,ALTP ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****

```

```

1113
1114
1115 ;*****
1116 ;*TEST 34 TEST OF ADDF INSTR, DATA SET ADDF-4
1117 ;* ALL INTERRUPT ENABLES ON
1118 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
1119 ;*****
1119 004650 000004
1120 004652 012705 004664
1121 004656 004737 033664
1122
1123 004662 000411
1124
1125 004664
1126 004664 077777 177777
1127 004670 177777 177777
1128 004674 000000 000000
1129 004700 047513 047504
1130 004704 000000
1131
1132
1133 ;*****
1134 ;*TEST 35 TEST OF ADDF INSTR, DATA SET ADDF-5
1135 ;* ALL INTERRUPT ENABLES ON
1136 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
1137 ;*****
1138 004706 000004
1139 004710 012705 004722
1140 004714 004737 033664
1141
1142 004720 000411
1143
1144 004722
1145 004722 042000 000000
1146 004726 050177 177777
1147 004732 050200 000000
1148 004736 047417 047400
1149 004742 000000
1150
1151
1152 ;*****
1153 ;*TEST 36 TEST OF ADDF INSTR, DATA SET ADDF-6
1154 ;* ALL INTERRUPT ENABLES ON
1155 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
1156 ;*****
1157 004744 000004
1158 004746 012705 004760
1159 004752 004737 033664
1160
1161 004756 000411
1162
1163 004760
1164 004760 042000 000000
1165 004764 050177 177777
1166 004770 050177 177777
1167 004774 047457 047440
1168 005000 000000

```

```

1169
1170
1171 ;*****
1172 ;*TEST 37 TEST OF ADDF INSTR, DATA SET ADDF-7
1173 ;* ALL INTERRUPT ENABLES ON
1174 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
1175 ;*****
1176 005002 000004
1177 005004 012705 005016
1178 005010 004737 033664
1179
1180 005014 000411
1181
1182 005016
1183 005016 141777 177777
1184 005022 150177 177777
1185 005026 150177 177777
1186 005032 047507 047510
1187 005036 000000
1188
1189
1190 ;*****
1191 ;*TEST 40 TEST OF ADDF INSTR, DATA SET ADDF-10
1192 ;* ALL INTERRUPT ENABLES ON
1193 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
1194 ;*****
1195 005040 000004
1196 005042 012705 005054
1197 005046 004737 033664
1198
1199 005052 000411
1200
1201 005054
1202 005054 141777 177777
1203 005060 150177 177777
1204 005064 150177 177777
1205 005070 047547 047550
1206 005074 000000
1207
1208
1209 ;*****
1210 ;*TEST 41 TEST OF ADDF INSTR, DATA SET ADDF-11
1211 ;* ALL INTERRUPT ENABLES ON
1212 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
1213 ;*****
1214 005076 000004
1215 005100 012705 005112
1216 005104 004737 033664
1217
1218 005110 000411
1219
1220 005112
1221 005112 040177 177777
1222 005116 032200 000000
1223 005122 040200 000000
1224 005126 047457 047440

```

```

1225 005132 000000      ,WORD NA ; FPC AFTER ( 0 = N/A )
1226
1227
1228
1229
1230
1231
1232
1233 005134 000004
1234 005136 012705 005150
1235 005142 004737 033664
1236
1237 005146 000411
1238
1239 005150
1240 005150 140252 125252
1241 005154 140052 128252
1242 005160 140377 177777
1243 005164 047407 047410
1244 005170 000000
1245
1246
1247
1248
1249
1250
1251
1252 005172 000004
1253 005174 012705 005206
1254 005200 004737 033664
1255
1256 005204 000411
1257
1258 005206
1259 005206 000010 104210
1260 005212 000010 104210
1261 005216 000210 104210
1262 005222 047557 047540
1263 005226 000000
1264
1265
1266
1267
1268
1269
1270
1271 005230 000004
1272 005232 012705 005244
1273 005236 004737 033664
1274
1275 005242 000411
1276
1277 005244
1278 005244 174177 177777
1279 005250 074177 177776
1280 005254 166200 000000

*****
;*TEST 42 TEST OF ADDF INSTR, DATA SET ADDF-14
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
*****
TST42: SCOPE
MOV #ADDF12,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF12 ; GU TEST
BR TST43 ;

ADDF12: ; TEST DATA SET ADDF-12:
,WORD 140252,125252 ; INITIAL AC FLOAT NUMBER
,WORD 140052,128252 ; INITIAL MEM FLOAT NUMBER
,WORD 140377,M1 ; EXPECTED FLOAT RESULT
,WORD 047407,047410 ; FPS: BEFORE, AFTER
,WORD NA ; FPC AFTER ( 0 = N/A )

*****
;*TEST 43 TEST OF ADDF INSTR, DATA SET ADDF-13
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
*****
TST43: SCOPE
MOV #ADDF13,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF13 ; GU TEST
BR TST44 ;

ADDF13: ; TEST DATA SET ADDF-13:
,WORD 040010,104210 ; INITIAL AC FLOAT NUMBER
,WORD 040010,104210 ; INITIAL MEM FLOAT NUMBER
,WORD 040210,104210 ; EXPECTED FLOAT RESULT
,WORD 047557,047540 ; FPS: BEFORE, AFTER
,WORD NA ; FPC AFTER ( 0 = N/A )

*****
;*TEST 44 TEST OF ADDF INSTR, DATA SET ADDF-14
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST44: SCOPE
MOV #ADDF14,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF14 ; GU TEST
BR TST45 ;

ADDF14: ; TEST DATA SET ADDF-14:
,WORD 174177,M1 ; INITIAL AC FLOAT NUMBER
,WORD 074177,M2 ; INITIAL MEM FLOAT NUMBER
,WORD 166200,000000 ; EXPECTED FLOAT RESULT

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```

1281 005260 047507 047510      ,WORD 047507,047510 ; FPS: BEFORE, AFTER
1282 005264 000000      ,WORD NA ; FPC AFTER ( 0 = N/A )
1283
1284
1285
1286
1287
1288
1289
1290 005266 000004
1291 005270 012705 005302
1292 005274 004737 033664
1293
1294 005300 000411
1295
1296 005302
1297 005302 142200 000000
1298 005306 050177 177777
1299 005312 050177 177776
1300 005316 047417 047400
1301 005322 000000
1302
1303
1304
1305
1306
1307
1308
1309 005324 000004
1310 005326 012705 005340
1311 005332 004737 033664
1312
1313 005336 000411
1314
1315 005340
1316 005340 077777 177777
1317 005344 077777 177777
1318 005350 000177 177777
1319 005354 047451 147446
1320 005360 100010
1321
1322
1323
1324
1325
1326
1327
1328 005362 000004
1329 005364 012705 005376
1330 005370 004737 033664
1331
1332 005374 000411
1333
1334 005376
1335 005376 104000 000000
1336 005402 004000 000001

*****
;*TEST 45 TEST OF ADDF INSTR, DATA SET ADDF-15
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
*****
TST45: SCOPE
MOV #ADDF15,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF15 ; GU TEST
BR TST46 ;

ADDF15: ; TEST DATA SET ADDF-15:
,WORD 142200,000000 ; INITIAL AC FLOAT NUMBER
,WORD 050177,M1 ; INITIAL MEM FLOAT NUMBER
,WORD 050177,M2 ; EXPECTED FLOAT RESULT
,WORD 047417,047400 ; FPS: BEFORE, AFTER
,WORD NA ; FPC AFTER ( 0 = N/A )

*****
;*TEST 46 TEST OF ADDF INSTR, DATA SET ADDF-16
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST46: SCOPE
MOV #ADDF16,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF16 ; GU TEST
BR TST47 ;

ADDF16: ; TEST DATA SET ADDF-16:
,WORD LQP,M1 ; INITIAL AC FLOAT NUMBER
,WORD LQP,M1 ; INITIAL MEM FLOAT NUMBER
,WORD 2X1MP,M1 ; EXPECTED FLOAT RESULT
,WORD 047451,147446 ; FPS: BEFORE, AFTER
,WORD 100010 ; FPC AFTER ( 0 = N/A )

*****
;*TEST 47 TEST OF ADDF INSTR, DATA SET ADDF-17
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST47: SCOPE
MOV #ADDF17,R5 ; PTR TO TEST DATA SET
JSR PC,#ADDF17 ; GU TEST
BR TST50 ;

ADDF17: ; TEST DATA SET ADDF-17:
,WORD 104000,0 ; INITIAL AC FLOAT NUMBER
,WORD 004000,1 ; INITIAL MEM FLOAT NUMBER

```



1337 005406 076200 000000  
1338 005412 047517 147500  
1339 005416 100012  
1340  
1341  
1342  
1343  
1344  
1345  
1346  
1347 005420 000004  
1348 005422 012705 005434  
1349 005426 004737 033664  
1350  
1351 005432 000411  
1352  
1353 005434  
1354 005434 177777 177777  
1355 005440 100000 000000  
1356 005444 177777 177777  
1357 005450 047543 147543  
1358 005454 100014  
1359  
1360  
1361

```
.WORD 076200,0 ; EXPECTED FLOAT RESULT  
.WORD 047517,147500 ; FPS: BEFORE, AFTER  
.WORD 100012 ; FEC AFTER ( 0 = N7A )  
  
*****  
; *TEST S0 TEST OF ADDF INSTR, DATA SET ADDF-20  
; * ALL INTERRUPT ENABLES ON  
; * SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
*****  
TSTS0: SCOPE  
MOV #ADDF20,R5 ; PTR TO TEST DATA SET  
JSR PC,#ADDF20 ; GO TEST  
  
BR TSTS1 ; ;  
  
ADDF20: ; TEST DATA SET ADDF-20:  
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER  
.WORD M0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD LGN,M1 ; EXPECTED FLOAT RESULT  
.WORD 047543,147543 ; FPS: BEFORE, AFTER  
.WORD 100014 ; FEC AFTER ( 0 = N7A )
```

1362  
1363  
1364  
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1366 005456 000004  
1367 005460 012705 005472  
1368 005464 004737 033664  
1369  
1370 005470 000411  
1371  
1372 005472  
1373 005472 177777 177777  
1374 005476 177777 177777  
1375 005502 000000 000000  
1376 005506 046511 046506  
1377 005512 000000  
1378  
1379  
1380  
1381  
1382  
1383  
1384  
1385 005514 000004  
1386 005516 012705 005530  
1387 005522 004737 033664  
1388  
1389 005526 000411  
1390  
1391 005530  
1392 005530 052525 052525

```
*****  
; *TEST S1 TEST OF ADDF INSTR, DATA SET ADDF-21  
; * OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
; * SHORT FLOAT, LONG INTEGER, ROUND MODES  
*****  
TSTS1: SCOPE  
MOV #ADDF21,R5 ; PTR TO TEST DATA SET  
JSR PC,#ADDF21 ; GO TEST  
  
BR TSTS2 ; ;  
  
ADDF21: ; TEST DATA SET ADDF-21:  
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER  
.WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 046511,046506 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )
```

1393  
1394  
1395  
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1397  
1398  
1399  
1400  
1401  
1402  
1403  
1404 005552 000004  
1405 005554 012705 005566  
1406 005560 004737 033664  
1407  
1408 005564 000411  
1409  
1410 005566  
1411 005566 004000 000001  
1412 005572 100000 000000  
1413 005576 000000 000000  
1414 005602 045413 045404  
1415 005606 000000  
1416  
1417  
1418

```
*****  
; *TEST S2 TEST OF ADDF INSTR, DATA SET ADDF-22  
; * 0 INTERRUPT ENABLE OFF, ALL OTHERS ON  
; * LONG FLOAT, LONG INTEGER, ROUND MODES  
*****  
TSTS2: SCOPE  
MOV #ADDF22,R5 ; PTR TO TEST DATA SET  
JSR PC,#ADDF22 ; GO TEST  
  
BR TSTS3 ; ;  
  
ADDF22: ; TEST DATA SET ADDF-22:  
.WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
```

1393 005534 100177 177777  
1394 005540 052525 052525  
1395 005544 043717 043700  
1396 005550 000000  
1397  
1398  
1399  
1400  
1401  
1402  
1403  
1404 005552 000004  
1405 005554 012705 005566  
1406 005560 004737 033664  
1407  
1408 005564 000411  
1409  
1410 005566  
1411 005566 004000 000001  
1412 005572 100000 000000  
1413 005576 000000 000000  
1414 005602 045413 045404  
1415 005606 000000  
1416  
1417  
1418

```
.WORD EX1MM,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD ALTP,ALTP ; EXPECTED FLOAT RESULT  
.WORD 043717,043700 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )  
  
*****  
; *TEST S3 TEST OF ADDF INSTR, DATA SET ADDF-23  
; * UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
; * SHORT FLOAT, SHORT INTEGER, ROUND MODES  
*****  
TSTS3: SCOPE  
MOV #ADDF23,R5 ; PTR TO TEST DATA SET  
JSR PC,#ADDF23 ; GO TEST  
  
BR TSTS4 ; ;  
  
ADDF23: ; TEST DATA SET ADDF-23:  
.WORD 004000,000001 ; INITIAL AC FLOAT NUMBER  
.WORD 100000,000000 ; INITIAL MEM FLOAT NUMBER  
.WORD 000000,000000 ; EXPECTED FLOAT RESULT  
.WORD 045413,045404 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )
```

```

1419
1420
1421
1422
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1424
1425 005610 000004
1426 005612 012705 005624
1427 005616 004737 034034
1428 005622 000417
1429
1430 005624
1431 005624 000177 177777 177777 ADDD1: ; TEST DATA SET ADDD-1:
1432 005632 177777 .WORD ZXIMP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1433 005634 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
1434 005642 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1435 005644 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1436 005652 000000 .WORD 047713,047704 ; FPS: BEFORE, AFTER
1437 005654 047713 047704 .WORD NA ; FEC AFTER ( 0 = N7A )
1438 005660 000000
1439
1440
1441
1442
1443
1444
1445
1446 005662 000004
1447 005664 012705 005676
1448 005670 004737 034034
1449
1450 005674 000417
1451
1452 005676
1453 005676 125252 125252 125252 ADDD2: ; TEST DATA SET ADDD-2:
1454 005704 125252 .WORD ALIN,ALIN,ALIN,ALIN ; INITIAL AC FLOAT NUMBER
1455 005706 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
1456 005714 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1457 005716 125252 125252 125252 .WORD ALTN,ALTN,ALTN,ALTN ; EXPECTED FLOAT RESULT
1458 005724 125252 .WORD 047747,047750 ; FPS: BEFORE, AFTER
1459 005726 047747 047750 .WORD NA ; FEC AFTER ( 0 = N7A )
1460 005732 000000
1461
1462
1463
1464
1465
1466
1467
1468 005734 000004
1469 005736 012705 005750
1470 005742 004737 034034
1471
1472 005746 000417
1473
1474 005750

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1475 005750 000177 177777 177777 .WORD ZXIMP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1476 005756 177777 .WORD ALTP,ALTP,ALTP,ALTP ; INITIAL MEM FLOAT NUMBER
1477 005760 052525 052525 052525 .WORD ALTP,ALTP,ALTP,ALTP ; EXPECTED FLOAT RESULT
1478 005766 052525 052525 052525 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1479 005770 052525 052525 052525 .WORD 047617,047600 ; FPS: BEFORE, AFTER
1480 005776 052525 .WORD NA ; FEC AFTER ( 0 = N7A )
1481 006000 047617 047600
1482 006004 000000
1483
1484
1485
1486
1487
1488
1489
1490 006006 000004
1491 006010 012705 006022
1492 006014 004737 034034
1493
1494 006020 000417
1495
1496 006022
1497 006022 177777 177777 177777 ADDD4: ; TEST DATA SET ADDD-4:
1498 006030 177777 .WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1499 006032 077777 177777 177777 .WORD LGF,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1500 006040 177777 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1501 006042 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1502 006050 000000 .WORD 047653,047644 ; FPS: BEFORE, AFTER
1503 006052 047653 047644 .WORD NA ; FEC AFTER ( 0 = N7A )
1504 006056 000000
1505
1506
1507
1508
1509
1510
1511
1512 006060 000004
1513 006062 012705 006074
1514 006066 004737 034034
1515
1516 006072 000417
1517
1518 006074
1519 006074 166177 177777 177777 ADDD5: ; TEST DATA SET ADDD-5:
1520 006102 177777 .WORD 166177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1521 006104 150000 000000 000000 .WORD 150000,0,0,0 ; INITIAL MEM FLOAT NUMBER
1522 006112 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1523 006114 166200 000000 000000 .WORD 166200,0,0,0 ; EXPECTED FLOAT RESULT
1524 006122 000000 .WORD 047607,047610 ; FPS: BEFORE, AFTER
1525 006124 047607 047610 .WORD NA ; FEC AFTER ( 0 = N7A )
1526 006130 000000
1527
1528
1529
1530

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1531 ;* ALL INTERRUPT ENABLES ON
1532 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
1533 ;*****
1534 006132 000004 TST61: SCOPE
1535 006134 012705 MOV #ADDD6,R5 ; PTR TO TEST DATA SET
1536 006140 047737 JSR PC,#ADDDT ; GO TEST
1537
1538 006144 000417 BR TST62 ;;
1539
1540 006146 ADDD6: ; TEST DATA SET ADDD-6:
1541 006154 166177 177777 177777 .WORD 166177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1542 006154 177777
1543 006156 150000 000000 000000 .WORD 150000,0,0,0 ; INITIAL MEM FLOAT NUMBER
1544 006164 000000
1545 006166 166177 177777 177777 .WORD 166177,M1,M1,M1 ; EXPECTED FLOAT RESULT
1546 006174 177777
1547 006176 047647 047650 .WORD 047647,047650 ; FPS: BEFORE, AFTER
1548 006202 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
1549
1550
1551 ;*****
1552 ;*TEST 62 TEST OF ADDD INSTR, DATA SET ADDD-7
1553 ;* ALL INTERRUPT ENABLES ON
1554 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
1555 ;*****
1556 006204 000004 TST62: SCOPE
1557 006206 012705 MOV #ADDD7,R5 ; PTR TO TEST DATA SET
1558 006212 047737 JSR PC,#ADDDT ; GO TEST
1559
1560 006216 000417 BR TST63 ;;
1561
1562 006220 ADDD7: ; TEST DATA SET ADDD-7:
1563 006220 066177 177777 177777 .WORD 066177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1564 006226 177777
1565 006230 047777 177777 177777 .WORD 047777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1566 006236 177777
1567 006240 066177 177777 177777 .WORD 066177,M1,M1,M1 ; EXPECTED FLOAT RESULT
1568 006246 177777
1569 006250 047717 047700 .WORD 047717,047700 ; FPS: BEFORE, AFTER
1570 006254 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
1571
1572
1573 ;*****
1574 ;*TEST 63 TEST OF ADDD INSTR, DATA SET ADDD-10
1575 ;* ALL INTERRUPT ENABLES ON
1576 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
1577 ;*****
1578 006256 000004 TST63: SCOPE
1579 006260 012705 MOV #ADDD10,R5 ; PTR TO TEST DATA SET
1580 006264 047737 JSR PC,#ADDDT ; GO TEST
1581
1582 006270 000417 BR TST64 ;;
1583
1584 006272 ADDD10: ; TEST DATA SET ADDD-10:
1585 006272 066177 177777 177777 .WORD 066177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1586 006300 177777

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1587 006302 047777 177777 177777 .WORD 047777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1588 006310 177777
1589 006312 066177 177777 177777 .WORD 066177,M1,M1,M1 ; EXPECTED FLOAT RESULT
1590 006320 177777
1591 006322 047757 047740 .WORD 047757,047740 ; FPS: BEFORE, AFTER
1592 006326 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
1593
1594
1595 ;*****
1596 ;*TEST 64 TEST OF ADDD INSTR, DATA SET ADDD-11
1597 ;* ALL INTERRUPT ENABLES ON
1598 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
1599 ;*****
1600 006330 000004 TST64: SCOPE
1601 006332 012705 MOV #ADDD11,R5 ; PTR TO TEST DATA SET
1602 006336 047737 JSR PC,#ADDDT ; GO TEST
1603
1604 006342 000417 BR TST65 ;;
1605
1606 006344 ADDD11: ; TEST DATA SET ADDD-11:
1607 006344 004010 104210 104210 .WORD 004010,P132,P132,P132 ; INITIAL AC FLOAT NUMBER
1608 006352 104210
1609 006354 004010 104210 104210 .WORD 004010,P132,P132,P132 ; INITIAL MEM FLOAT NUMBER
1610 006362 104210
1611 006364 004210 104210 104210 .WORD 004210,P132,P132,P132 ; EXPECTED FLOAT RESULT
1612 006372 104210
1613 006374 047617 047600 .WORD 047617,047600 ; FPS: BEFORE, AFTER
1614 006400 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
1615
1616
1617 ;*****
1618 ;*TEST 65 TEST OF ADDD INSTR, DATA SET ADDD-12
1619 ;* ALL INTERRUPT ENABLES ON
1620 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
1621 ;*****
1622 006402 000004 TST65: SCOPE
1623 006404 012705 MOV #ADDD12,R5 ; PTR TO TEST DATA SET
1624 006410 047737 JSR PC,#ADDDT ; GO TEST
1625
1626 006414 000417 BR TST66 ;;
1627
1628 006416 ADDD12: ; TEST DATA SET ADDD-12:
1629 006416 122200 000000 000000 .WORD 122200,0,0,0 ; INITIAL AC FLOAT NUMBER
1630 006424 000000
1631 006426 140177 177777 177777 .WORD 140177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1632 006434 177777
1633 006436 140200 000000 000000 .WORD 140200,0,0,0 ; EXPECTED FLOAT RESULT
1634 006444 000000
1635 006446 047747 047750 .WORD 047747,047750 ; FPS: BEFORE, AFTER
1636 006452 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
1637
1638
1639 ;*****
1640 ;*TEST 66 TEST OF ADDD INSTR, DATA SET ADDD-13
1641 ;* ALL INTERRUPT ENABLES ON
1642 ;* LONG FLOAT, LONG INTEGER, ROUND MODES

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1643
1644 006454 000004
1645 006456 012705 006470
1646 006462 004737 034034
1647
1648 006466 000417
1649
1650
1651 006470 042252 125252 125252 ADDD13: ; TEST DATA SET ADDD-13:
1652 006476 125252 ,WORD 042252,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
1653 006500 042052 125252 125252 ,WORD 042052,ALTN,ALTN,ALTN ; INITIAL MEM FLOAT NUMBER
1654 006506 125252
1655 006510 042377 177777 177777 ,WORD 042377,M1,M1,M1 ; EXPECTED FLOAT RESULT
1656 006516 177777
1657 006520 047717 047700 ,WORD 047717,047700 ; FPS: BEFORE, AFTER
1658 006524 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )
1659
1660
1661
1662
1663
1664
1665
1666 006526 000004
1667 006530 012705 006542
1668 006534 004737 034034
1669
1670 006540 000417
1671
1672
1673 006542 074177 177777 177777 ADDD14: ; TEST DATA SET ADDD-14:
1674 006550 177777 ,WORD 074177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1675 006552 174177 177777 177777 ,WORD 174177,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER
1676 006560 177776
1677 006562 056200 000000 000000 ,WORD 056200,0,0,0 ; EXPECTED FLOAT RESULT
1678 006570 000000
1679 006572 047617 047600 ,WORD 047617,047600 ; FPS: BEFORE, AFTER
1680 006576 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )
1681
1682
1683
1684
1685
1686
1687
1688 006600 000004
1689 006602 012705 006614
1690 006606 004737 034034
1691
1692 006612 000417
1693
1694
1695 006614 132200 000000 000000 ADDD15: ; TEST DATA SET ADDD-15:
1696 006622 000000 ,WORD 132200,0,0,0 ; INITIAL AC FLOAT NUMBER
1697 006624 050177 177777 177777 ,WORD 050177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1698 006632 177777

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1699 006634 050177 177777 177777 ,WORD 050177,M1,M1,M2 ; EXPECTED FLOAT RESULT
1700 006642 177776
1701 006644 047717 047700 ,WORD 047717,047700 ; FPS: BEFORE, AFTER
1702 006650 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )
1703
1704
1705
1706
1707
1708
1709
1710 006652 000004
1711 006654 012705 006666
1712 006660 004737 034034
1713
1714 006664 000417
1715
1716 006666 077777 177777 177777 ADDD16: ; TEST DATA SET ADDD-16:
1717 006674 177777 ,WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
1718 006676 100177 177777 177777 ,WORD 2X1M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
1719 006704 177777
1720 006706 077777 177777 177777 ,WORD LGP,M1,M1,M1 ; EXPECTED FLOAT RESULT
1721 006714 177777
1722 006716 047603 147603 ,WORD 047603,147603 ; FPS: BEFORE, AFTER
1723 006722 100014 ,WORD 100014 ; FEC AFTER ( 0 = N/A )
1724
1725
1726
1727
1728
1729
1730
1731
1732 006724 000004
1733 006726 012705 006740
1734 006732 004737 034034
1735
1736 006736 000417
1737
1738 006740 102000 000000 000000 ADDD17: ; TEST DATA SET ADDD-17:
1739 006742 000001 ,WORD 102000,0,0,1 ; INITIAL AC FLOAT NUMBER
1740 006746 000001
1741 006750 002000 000000 000000 ,WORD 002000,0,0,0 ; INITIAL MEM FLOAT NUMBER
1742 006756 000000
1743 006760 104200 000000 000000 ,WORD 104200,0,0,0 ; EXPECTED FLOAT RESULT
1744 006766 000000
1745 006770 047647 147650 ,WORD 047647,147650 ; FPS: BEFORE, AFTER
1746 006774 100012 ,WORD 100012 ; FEC AFTER ( 0 = N/A )
1747
1748
1749
1750
1751
1752
1753
1754 006776 000004

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```
1755 007000 012705 007012      MOV    #ADDD20,R5      ; PTR TO TEST DATA SET
1756 007004 004737 034034      JSR    PC,#ADDDT      ; GO TEST
1757
1758 007010 000417      BR     TST74          ;;
1759
1760 007012      ADDD20: ; TEST DATA SET ADDD-20:
1761 007012 177777 177777 177777      .WORD LGN,M1,M1,M1    ; INITIAL AC FLOAT NUMBER
1762 007020 177777      .WORD LGN,M1,M1,M1    ; INITIAL MEM FLOAT NUMBER
1763 007022 177777 177777 177777      .WORD EX1MN,M1,M1,M1  ; EXPECTED FLOAT RESULT
1764 007030 177777      .WORD 047701,147710  ; FPS: BEFORE, AFTER
1765 007032 100177 177777 177777      .WORD 100010          ; FEC AFTER ( 0 = N/A )
1766 007040 177777
1767 007042 047701 147716
1768 007046 100010
1769
1770
1771
1772
1773
1774
1775
1776 007050 000004      ;*****
1777 007052 012705 007064      ;*TEST 74 TEST OF ADDD INSTR, DATA SET ADDD-21
1778 007056 004737 034034      ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
1779
1780 007062 000417      ;* LONG FLOAT, LONG INTEGER, ROUND MODES
1781
1782 007064      TST74: SCOPE
1783 007064 002000 000000 000000      MOV    #ADDD21,R5      ; PTR TO TEST DATA SET
1784 007072 000000      JSR    PC,#ADDDT      ; GO TEST
1785 007074 102000 000000 000000      BR     TST75          ;;
1786 007102 000002      ADDD21: ; TEST DATA SET ADDD-21:
1787 007104 000000 000000 000000      .WORD 002000,0,0,0    ; INITIAL AC FLOAT NUMBER
1788 007112 000000 000000 000000      .WORD 102000,0,0,2    ; INITIAL MEM FLOAT NUMBER
1789 007114 045713 045704      .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1790 007120 000000      .WORD 045713,045704  ; FPS: BEFORE, AFTER
1791
1792
1793
1794
1795
1796
1797
1798 007122 000004      ;*****
1799 007124 012705 007136      ;*TEST 75 TEST OF ADDD INSTR, DATA SET ADDD-22
1800 007130 004737 034034      ;* =0 INTERRUPT ENABLE OFF, ALL OTHERS ON
1801
1802 007134 000417      ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
1803
1804 007136      TST75: SCOPE
1805 007136 077777 000000 177777      MOV    #ADDD22,R5      ; PTR TO TEST DATA SET
1806 007144 000000      JSR    PC,#ADDDT      ; GO TEST
1807 007146 100000 000000 000000      BR     TST76          ;;
1808 007154 000000      ADDD22: ; TEST DATA SET ADDD-22:
1809 007156 077777 000000 177777      .WORD LGP,0,M1,0      ; INITIAL AC FLOAT NUMBER
1810 007164 000000      .WORD M0,0,0,0        ; INITIAL MEM FLOAT NUMBER
1811
1812
1813
1814
1815
1816
1817
1818
1819
1820 007174 000004      ;*****
1821 007176 012705 007210      ;*TEST 76 TEST OF ADDD INSTR, DATA SET ADDD-23
1822 007202 004737 034034      ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
1823
1824 007206 000417      ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
1825
1826 007210      TST76: SCOPE
1827 007210 077777 177777 177777      MOV    #ADDD23,R5      ; PTR TO TEST DATA SET
1828 007216 177777      JSR    PC,#ADDDT      ; GO TEST
1829 007220 177777 177777 177777      BR     TST77          ;;
1830 007226 177777      ADDD23: ; TEST DATA SET ADDD-23:
1831 007230 000000 000000 000000      .WORD LGP,M1,M1,M1    ; INITIAL AC FLOAT NUMBER
1832 007236 000000      .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1833 007240 046011 046006      .WORD 0,0,0,0 ; FPS: BEFORE, AFTER
1834 007244 000000      .WORD NA              ; FEC AFTER ( 0 = N/A )
1835
1836
1837
```

```
1811 007166 043757 043740      .WORD 043757,043740  ; FPS: BEFORE, AFTER
1812 007172 000000      .WORD NA              ; FEC AFTER ( 0 = N/A )
1813
1814
1815
1816
1817
1818
1819
1820 007174 000004      ;*****
1821 007176 012705 007210      ;*TEST 76 TEST OF ADDD INSTR, DATA SET ADDD-23
1822 007202 004737 034034      ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
1823
1824 007206 000417      ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
1825
1826 007210      TST76: SCOPE
1827 007210 077777 177777 177777      MOV    #ADDD23,R5      ; PTR TO TEST DATA SET
1828 007216 177777      JSR    PC,#ADDDT      ; GO TEST
1829 007220 177777 177777 177777      BR     TST77          ;;
1830 007226 177777      ADDD23: ; TEST DATA SET ADDD-23:
1831 007230 000000 000000 000000      .WORD LGP,M1,M1,M1    ; INITIAL AC FLOAT NUMBER
1832 007236 000000      .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
1833 007240 046011 046006      .WORD 046611,046606  ; FPS: BEFORE, AFTER
1834 007244 000000      .WORD NA              ; FEC AFTER ( 0 = N/A )
1835
1836
1837
```

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1838  
1839  
1840  
1841  
1842  
1843 #07246 000004  
1844 #07250 012705 007262  
1845 #07254 004737 034224  
1846  
1847 #07260 000411  
1848  
1849 #07262  
1850 #07262 #00000 000000  
1851 #07266 000000 000000  
1852 #07272 000000 000000  
1853 #07276 047413 047404  
1854 #07302 000000  
1855  
1856  
1857  
1858  
1859  
1860  
1861  
1862 #07304 000004  
1863 #07306 012705 007320  
1864 #07312 004737 034224  
1865  
1866 #07316 000411  
1867  
1868 #07320  
1869 #07320 000177 177777  
1870 #07324 000177 125252  
1871 #07330 000000 000000  
1872 #07334 047453 047444  
1873 #07340 000000  
1874  
1875  
1876  
1877  
1878  
1879  
1880  
1881 #07342 000004  
1882 #07344 012705 007356  
1883 #07350 004737 034224  
1884  
1885 #07354 000411  
1886  
1887 #07356  
1888 #07356 000177 052525  
1889 #07362 100345 123456  
1890 #07366 000345 123456  
1891 #07372 047517 047500  
1892 #07376 000000  
1893  
*****  
;TEST 77 TEST OF SUBF INSTR, DATA SET SUBF-1  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
*****  
TST77: SCOPE  
MOV #SUBF1,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST100 ; ;  
  
SUBF1: ; TEST DATA SET SUBF-1:  
.WORD 0,0 ; INITIAL AC FLOAT NUMBER  
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 047413,047404 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
*****  
;TEST 100 TEST OF SUBF INSTR, DATA SET SUBF-2  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
*****  
TST100: SCOPE  
MOV #SUBF2,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST101 ; ;  
  
SUBF2: ; TEST DATA SET SUBF-2:  
.WORD 2X1M,M1 ; INITIAL AC FLOAT NUMBER  
.WORD 2X1M,ALTM ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 047453,047444 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
*****  
;TEST 101 TEST OF SUBF INSTR, DATA SET SUBF-3  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST101: SCOPE  
MOV #SUBF3,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST102 ; ;  
  
SUBF3: ; TEST DATA SET SUBF-3:  
.WORD 2X1M,ALTP ; INITIAL AC FLOAT NUMBER  
.WORD 100345,123456 ; INITIAL MEM FLOAT NUMBER  
.WORD 000345,123456 ; EXPECTED FLOAT RESULT  
.WORD 047517,047500 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

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1894  
1895  
1896  
1897  
1898  
1899  
1900 #07400 000004  
1901 #07402 012705 007414  
1902 #07406 004737 034224  
1903  
1904 #07412 000411  
1905  
1906 #07414  
1907 #07414 040200 000000  
1908 #07420 040200 000000  
1909 #07424 000000 000000  
1910 #07430 047553 047544  
1911 #07434 000000  
1912  
1913  
1914  
1915  
1916  
1917  
1918  
1919 #07436 000004  
1920 #07440 012705 007452  
1921 #07444 004737 034224  
1922  
1923 #07450 000411  
1924  
1925 #07452  
1926 #07452 140200 000000  
1927 #07456 142200 000000  
1928 #07462 000000 000000  
1929 #07466 047413 047404  
1930 #07472 000000  
1931  
1932  
1933  
1934  
1935  
1936  
1937  
1938 #07474 000004  
1939 #07476 012705 007510  
1940 #07502 004737 034224  
1941  
1942 #07506 000411  
1943  
1944 #07510  
1945 #07510 150365 052525  
1946 #07514 047252 125252  
1947 #07520 150377 177777  
1948 #07524 047447 047450  
1949 #07530 000000  
*****  
;TEST 102 TEST OF SUBF INSTR, DATA SET SUBF-4  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
*****  
TST102: SCOPE  
MOV #SUBF4,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST103 ; ;  
  
SUBF4: ; TEST DATA SET SUBF-4:  
.WORD F1P,0 ; INITIAL AC FLOAT NUMBER  
.WORD F1P,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 047553,047544 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
*****  
;TEST 103 TEST OF SUBF INSTR, DATA SET SUBF-5  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
*****  
TST103: SCOPE  
MOV #SUBF5,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST104 ; ;  
  
SUBF5: ; TEST DATA SET SUBF-5:  
.WORD F1N,0 ; INITIAL AC FLOAT NUMBER  
.WORD F1N,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 047413,047404 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
*****  
;TEST 104 TEST OF SUBF INSTR, DATA SET SUBF-6  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
*****  
TST104: SCOPE  
MOV #SUBF6,R5 ; PTR TO TEST DATA SET  
JSR PC,#SUBFT ; GO TEST  
  
BR TST105 ; ;  
  
SUBF6: ; TEST DATA SET SUBF-6:  
.WORD 150365,ALTP ; INITIAL AC FLOAT NUMBER  
.WORD 047252,ALTM ; INITIAL MEM FLOAT NUMBER  
.WORD 150377,M1 ; EXPECTED FLOAT RESULT  
.WORD 047447,047450 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

1950  
1951  
1952  
1953  
1954  
1955  
1956  
1957 007532 000004  
1958 007534 012705 007546  
1959 007540 004737 034224  
1960  
1961 007544 000411  
1962  
1963 007546  
1964 007546 050365 052525  
1965 007552 147252 125252  
1966 007556 050400 000000  
1967 007562 047517 047500  
1968 007566 000000  
1969  
1970  
1971  
1972  
1973  
1974  
1975  
1976 007570 000004  
1977 007572 012705 007604  
1978 007576 004737 034224  
1979  
1980 007602 000411  
1981  
1982 007604  
1983 007604 077777 177777  
1984 007610 100177 177777  
1985 007614 077777 177777  
1986 007620 047555 147555  
1987 007624 100014  
1988  
1989  
1990  
1991  
1992  
1993  
1994  
1995 007626 000004  
1996 007630 012705 007642  
1997 007634 004737 034224  
1998  
1999 007640 000411  
2000  
2001 007642  
2002 007642 077777 177777  
2003 007646 100177 177777  
2004 007652 077777 177777  
2005 007656 043457 043440

```

*****
;TEST 105 TEST OF SUBF INSTR, DATA SET SUBF-7
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST105: SCOPE
MOV #SUBF7,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST106 ;;

SUBF7: ; TEST DATA SET SUBF-7:
.WORD 050365,ALTP ; INITIAL AC FLOAT NUMBER
.WORD 147252,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 050400,0 ; EXPECTED FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 106 TEST OF SUBF INSTR, DATA SET SUBF-10
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
*****
TST106: SCOPE
MOV #SUBF10,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST107 ;;

SUBF10: ; TEST DATA SET SUBF-10:
.WORD LGP,M1 ; INITIAL AC FLOAT NUMBER
.WORD EX1MN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD LGP,M1 ; EXPECTED FLOAT RESULT
.WORD 047555,147555 ; FPS: BEFORE, AFTER
.WORD 100014 ; FEC AFTER ( 0 = N/A )

*****
;TEST 107 TEST OF SUBF INSTR, DATA SET SUBF-11
;* ALL INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST107: SCOPE
MOV #SUBF11,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST110 ;;

SUBF11: ; TEST DATA SET SUBF-11:
.WORD LGP,M1 ; INITIAL AC FLOAT NUMBER
.WORD EX1MN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD LGP,M1 ; EXPECTED FLOAT RESULT
.WORD 043457,043440 ; FPS: BEFORE, AFTER

```

2006 007662 000000  
2007  
2008  
2009  
2010  
2011  
2012  
2013  
2014 007664 000004  
2015 007666 012705 007700  
2016 007672 004737 034224  
2017  
2018 007676 000411  
2019  
2020 007700  
2021 007700 177777 177777  
2022 007704 071000 000000  
2023 007710 177777 177777  
2024 007714 047447 047450  
2025 007720 000000  
2026  
2027  
2028  
2029  
2030  
2031  
2032  
2033 007722 000004  
2034 007724 012705 007736  
2035 007730 004737 034224  
2036  
2037 007734 000411  
2038  
2039 007736  
2040 007736 177777 177777  
2041 007742 071000 000000  
2042 007746 100000 000000  
2043 007752 047501 147516  
2044 007756 100010  
2045  
2046  
2047  
2048  
2049  
2050  
2051  
2052 007760 000004  
2053 007762 012705 007774  
2054 007766 004737 034224  
2055  
2056 007772 000411  
2057  
2058 007774  
2059 007774 177777 177777  
2060 010000 071000 000000  
2061 010004 000000 000000

```

.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 110 TEST OF SUBF INSTR, DATA SET SUBF-12
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST110: SCOPE
MOV #SUBF12,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST111 ;;

SUBF12: ; TEST DATA SET SUBF-12:
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
.WORD 071000,0 ; INITIAL MEM FLOAT NUMBER
.WORD LGN,M1 ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 111 TEST OF SUBF INSTR, DATA SET SUBF-13
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST111: SCOPE
MOV #SUBF13,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST112 ;;

SUBF13: ; TEST DATA SET SUBF-13:
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
.WORD 071000,0 ; INITIAL MEM FLOAT NUMBER
.WORD M0,0 ; EXPECTED FLOAT RESULT
.WORD 047501,147516 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N/A )

*****
;TEST 112 TEST OF SUBF INSTR, DATA SET SUBF-14
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST112: SCOPE
MOV #SUBF14,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBFT ; GO TEST
BR TST113 ;;

SUBF14: ; TEST DATA SET SUBF-14:
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
.WORD 071000,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT

```

2062 010010 046511 046506  
 2063 010014 000000  
 2064  
 2065  
 2066  
 2067  
 2068  
 2069  
 2070  
 2071 010016 000004  
 2072 010020 012705 010032  
 2073 010024 004737 034224  
 2074  
 2075 010030 000411  
 2076  
 2077 010032  
 2078 010032 004200 000000  
 2079 010036 004200 000001  
 2080 010042 176400 000000  
 2081 010046 047447 147450  
 2082 010052 100012  
 2083  
 2084  
 2085  
 2086  
 2087  
 2088  
 2089  
 2090 010054 000004  
 2091 010056 012705 010070  
 2092 010062 004737 034224  
 2093  
 2094 010066 000411  
 2095  
 2096 010070  
 2097 010070 004200 000000  
 2098 010074 004200 000001  
 2099 010100 000000 000000  
 2100 010104 045453 045444  
 2101 010110 000000  
 2102  
 2103  
 2104

```

.WORD 046511,046506 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 113 TEST OF SUBF INSTR, DATA SET SUBF-14
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST113: SCOPE
MOV #SUBF15,R5 ; PTR TO TEST DATA SET
JBR PC,#SUBFT ; GO TEST

BR TST114 ;;

SUBF15: ; TEST DATA SET SUBF-15:
.WORD 004200,0 ; INITIAL AC FLOAT NUMBER
.WORD 004200,1 ; INITIAL MEM FLOAT NUMBER
.WORD 176400,0 ; EXPECTED FLOAT RESULT
.WORD 047447,147450 ; FPS: BEFORE, AFTER
.WORD 100012 ; FEC AFTER ( 0 = N7A )

;*****
;TEST 114 TEST OF SUBF INSTR, DATA SET SUBF-10
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST114: SCOPE
MOV #SUBF16,R5 ; PTR TO TEST DATA SET
JBR PC,#SUBFT ; GO TEST

BR TST115 ;;

SUBF16: ; TEST DATA SET SUBF-16:
.WORD 004200,0 ; INITIAL AC FLOAT NUMBER
.WORD 004200,1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 045453,045444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

```

2105  
 2106  
 2107  
 2108  
 2109  
 2110 010112 000004  
 2111 010114 012705 010126  
 2112 010120 004737 034374  
 2113  
 2114 010124 000417  
 2115  
 2116 010126  
 2117 010126 000000 000000 000000  
 2118 010134 000000  
 2119 010136 000000 000000 000000  
 2120 010144 000000  
 2121 010146 000000 000000 000000  
 2122 010154 000000  
 2123 010156 047753 047744  
 2124 010162 000000  
 2125  
 2126  
 2127  
 2128  
 2129  
 2130  
 2131  
 2132 010164 000004  
 2133 010166 012705 010200  
 2134 010172 004737 034374  
 2135  
 2136 010176 000417  
 2137  
 2138 010200  
 2139 010200 000177 052525 052525  
 2140 010206 052525  
 2141 010210 000177 177777 177777  
 2142 010216 177777  
 2143 010220 000000 000000 000000  
 2144 010226 000000  
 2145 010230 047713 047704  
 2146 010234 000000  
 2147  
 2148  
 2149  
 2150  
 2151  
 2152  
 2153  
 2154 010236 000004  
 2155 010240 012705 010252  
 2156 010244 004737 034374  
 2157  
 2158 010250 000417  
 2159  
 2160 010252

```

;*****
;TEST 115 TEST OF SUBD INSTR, DATA SET SUBD-1
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST115: SCOPE
MOV #SUBD1,R5 ; PTR TO TEST DATA SET
JBR PC,#SUBD1 ; GO TEST

BR TST116 ;;

SUBD1: ; TEST DATA SET SUBD-1:
.WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 047753,047744 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 116 TEST OF SUBD INSTR, DATA SET SUBD-2
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST116: SCOPE
MOV #SUBD2,R5 ; PTR TO TEST DATA SET
JBR PC,#SUBD2 ; GO TEST

BR TST117 ;;

SUBD2: ; TEST DATA SET SUBD-2:
.WORD EX1MP,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD EX1MP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 047713,047704 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 117 TEST OF SUBD INSTR, DATA SET SUBD-3
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST117: SCOPE
MOV #SUBD3,R5 ; PTR TO TEST DATA SET
JBR PC,#SUBD3 ; GO TEST

BR TST120 ;;

SUBD3: ; TEST DATA SET SUBD-3:

```



```

2161 010252 000000 000000 000000 ,WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
2162 010260 000000 ,WORD 012345,M1,ALTN,M0 ; INITIAL MEM FLOAT NUMBER
2163 010262 012345 177777 125252 ,WORD 012345,M1,ALTN,M0 ; INITIAL MEM FLOAT NUMBER
2164 010270 100000 ,WORD 112345,M1,ALTN,M0 ; EXPECTED FLOAT RESULT
2165 010272 112345 177777 125252 ,WORD 112345,M1,ALTN,M0 ; EXPECTED FLOAT RESULT
2166 010300 100000 ,WORD 047647,047650 ; FPS: BEFORE, AFTER
2167 010302 047647 047650 ,WORD NA ; FEC AFTER ( 0 = N7A )
2168 010306 000000
2169
2170
2171
2172 ;*****
;*TEST 120 TEST OF SUBD INSTR, DATA SET SUBD=4
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST120: SCOPE
MOV #SUBD4,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBDT ; GO TEST
BR TST121 ;;
SUBD4: ; TEST DATA SET SUBD=4
,WORD FIN,0,0,0 ; INITIAL AC FLOAT NUMBER
,WORD FIN,0,0,0 ; INITIAL MEM FLOAT NUMBER
,WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
,WORD 047753,047744 ; FPS: BEFORE, AFTER
,WORD NA ; FEC AFTER ( 0 = N7A )
2176 010310 000004
2177 010312 012705 010324
2178 010316 004737 034374
2179
2180 010322 000417
2181
2182 010324
2183 010324 140200 000000 000000
2184 010332 000000
2185 010334 140200 000000 000000
2186 010342 000000
2187 010344 000000 000000 000000
2188 010352 000000
2189 010354 047753 047744
2190 010360 000000
2191
2192
2193 ;*****
;*TEST 121 TEST OF SUBD INSTR, DATA SET SUBD=5
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST121: SCOPE
MOV #SUBD5,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBDT ; GO TEST
BR TST122 ;;
SUBD5: ; TEST DATA SET SUBD=5
,WORD FIP,0,0,0 ; INITIAL AC FLOAT NUMBER
,WORD FIP,0,0,0 ; INITIAL MEM FLOAT NUMBER
,WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
,WORD 047613,047604 ; FPS: BEFORE, AFTER
,WORD NA ; FEC AFTER ( 0 = N7A )
2198 010362 000004
2199 010364 012705 010376
2200 010370 004737 034374
2201
2202 010374 000417
2203
2204 010376
2205 010376 040200 000000 000000
2206 010404 000000
2207 010406 040200 000000 000000
2208 010414 000000
2209 010416 000000 000000 000000
2210 010421 000000
2211 010426 047613 047604
2212 010432 000000
2213
2214
2215 ;*****
;*TEST 122 TEST OF SUBD INSTR, DATA SET SUBD=6
;*****

```

```

2217 ;*****
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST122: SCOPE
MOV #SUBD6,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBDT ; GO TEST
BR TST123 ;;
SUBD6: ; TEST DATA SET SUBD=6
,WORD 037252,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
,WORD 140365,ALTP,ALTP,ALTP ; INITIAL MEM FLOAT NUMBER
,WORD 040377,M1,M1,M1 ; EXPECTED FLOAT RESULT
,WORD 047757,047740 ; FPS: BEFORE, AFTER
,WORD NA ; FEC AFTER ( 0 = N7A )
2224 010434 000004
2225 010436 012705 010450
2226 010442 004737 034374
2227
2228 010446 000417
2229
2230 010450
2231 010450 037252 125252 125252
2232 010454 125252
2233 010460 140365 052525 052525
2234 010466 052525
2235 010470 040377 177777 177777
2236 010476 040377
2237 010500 047757 047740
2238 010504 000000
2239
2240
2241 ;*****
;*TEST 123 TEST OF SUBD INSTR, DATA SET SUBD=7
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST123: SCOPE
MOV #SUBD7,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBDT ; GO TEST
BR TST124 ;;
SUBD7: ; TEST DATA SET SUBD=7
,WORD 137252,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
,WORD 040365,ALTP,ALTP,ALTP ; INITIAL MEM FLOAT NUMBER
,WORD 140400,0,0,0 ; EXPECTED FLOAT RESULT
,WORD 047607,047610 ; FPS: BEFORE, AFTER
,WORD NA ; FEC AFTER ( 0 = N7A )
2244 010506 000004
2245 010510 012705 010522
2246 010514 004737 034374
2247
2248 010520 000417
2249
2250 010522
2251 010522 137252 125252 125252
2252 010530 125252
2253 010532 040365 052525 052525
2254 010540 052525
2255 010542 140400 000000 000000
2256 010550 000000
2257 010552 047607 047610
2258 010556 000000
2259
2260 ;*****
;*TEST 124 TEST OF SUBD INSTR, DATA SET SUBD=10
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST124: SCOPE
MOV #SUBD10,R5 ; PTR TO TEST DATA SET
JBR PC,##SUBDT ; GO TEST
BR TST125 ;;
SUBD10: ; TEST DATA SET SUBD=10
,WORD LGN,M1,0,M1 ; INITIAL AC FLOAT NUMBER

```

```

2273 010604 100000 000000 177777          .WORD  M0,0,M1,0          ; INITIAL MEM FLOAT NUMBER
2274 010612 000000 000000 177777          .WORD  LGN,M1,0,M1        ; EXPECTED FLOAT RESULT
2275 010614 177777 177777 000000          .WORD  047603,147603     ; FPS: BEFORE, AFTER
2276 010622 177777 177777 000000          .WORD  100014           ; FEC AFTER ( 0 = N/A )
2277 010624 047603 147603
2278 010630 100014
2279
2280
2281
2282
2283 ;*TEST 125 TEST OF SUBD INSTR, DATA SET SUBD-11
2284 ;*      =0 INTERRUPT ENABLE OFF, ALL OTHERS ON
2285 ;*      LONG FLOAT, LONG INTEGER, ROUND MODES
2286 ;*
2287 ;*-----
2288 TST125: SCOPE
2289      MOV      #SUBD11,R5      ; PTR TO TEST DATA SET
2290      JSR      PC,#SUBDT      ; GO TEST
2291
2292      BR      TST126          ;
2293
2294 SUBD11: ; TEST DATA SET SUBD-11:
2295      .WORD  LGN,M1,0,M1        ; INITIAL AC FLOAT NUMBER
2296      .WORD  M0,0,M1,0          ; INITIAL MEM FLOAT NUMBER
2297      .WORD  LGN,M1,0,M1        ; EXPECTED FLOAT RESULT
2298      .WORD  043707,043710     ; FPS: BEFORE, AFTER
2299      .WORD  NA                 ; FEC AFTER ( 0 = N/A )
2300
2301
2302
2303 ;*-----
2304 ;*TEST 126 TEST OF SUBD INSTR, DATA SET SUBD-12
2305 ;*      ALL INTERRUPT ENABLES ON
2306 ;*      LONG FLOAT, LONG INTEGER, ROUND MODES
2307 ;*
2308 ;*-----
2309 TST126: SCOPE
2310      MOV      #SUBD12,R5      ; PTR TO TEST DATA SET
2311      JSR      PC,#SUBDT      ; GO TEST
2312
2313      BR      TST127          ;
2314
2315 SUBD12: ; TEST DATA SET SUBD-12:
2316      .WORD  104200,0,0,0     ; INITIAL AC FLOAT NUMBER
2317      .WORD  104200,0,0,1     ; INITIAL MEM FLOAT NUMBER
2318      .WORD  066400,0,0,0     ; EXPECTED FLOAT RESULT
2319      .WORD  047717,147700     ; FPS: BEFORE, AFTER
2320      .WORD  100012           ; FEC AFTER ( 0 = N/A )
2321
2322
2323 ;*-----
2324 ;*TEST 127 TEST OF SUBD INSTR, DATA SET SUBD-13
2325 ;*      UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
2326 ;*      LONG FLOAT, LONG INTEGER, ROUND MODES
2327 ;*
2328 ;*-----

```

```

2329
2330 010756 000004
2331 010760 012705 010772
2332 010764 004737 034374
2333
2334 010770 000417
2335
2336 010772
2337 010772 104200 000000 000000 SUBD13: ; TEST DATA SET SUBD-13:
2338 011000 000001          .WORD  104200,0,0,1     ; INITIAL AC FLOAT NUMBER
2339 011002 104200 000000 000000          .WORD  104200,0,0,0     ; INITIAL MEM FLOAT NUMBER
2340 011010 000000          .WORD  0,0,0,0          ; EXPECTED FLOAT RESULT
2341 011012 000000 000000 000000          .WORD  048713,048704     ; FPS: BEFORE, AFTER
2342 011020 000000          .WORD  NA                 ; FEC AFTER ( 0 = N/A )
2343 011022 045713 045704
2344 011026 000000
2345
2346
2347 ;*-----
2348 ;*TEST 120 TEST OF SUBD INSTR, DATA SET SUBD-14
2349 ;*      ALL INTERRUPT ENABLES ON
2350 ;*      LONG FLOAT, LONG INTEGER, TRUNCATE MODES
2351 ;*
2352 ;*-----
2353 TST130: SCOPE
2354      MOV      #SUBD14,R5      ; PTR TO TEST DATA SET
2355      JSR      PC,#SUBDT      ; GO TEST
2356
2357      BR      TST131          ;
2358
2359 SUBD14: ; TEST DATA SET SUBD-14:
2360      .WORD  LGP,M1,M1,M1      ; INITIAL AC FLOAT NUMBER
2361      .WORD  161600,0,0,0     ; INITIAL MEM FLOAT NUMBER
2362      .WORD  LGP,M1,M1,M1      ; EXPECTED FLOAT RESULT
2363      .WORD  047757,047740     ; FPS: BEFORE, AFTER
2364      .WORD  NA                 ; FEC AFTER ( 0 = N/A )
2365
2366
2367
2368 ;*-----
2369 ;*TEST 131 TEST OF SUBD INSTR, DATA SET SUBD-15
2370 ;*      ALL INTERRUPT ENABLES ON
2371 ;*      LONG FLOAT, SHORT INTEGER, ROUND MODES
2372 ;*
2373 ;*-----
2374 TST131: SCOPE
2375      MOV      #SUBD15,R5      ; PTR TO TEST DATA SET
2376      JSR      PC,#SUBDT      ; GO TEST
2377
2378      BR      TST132          ;
2379
2380 SUBD15: ; TEST DATA SET SUBD-15:
2381      .WORD  LGP,M1,M1,M1      ; INITIAL AC FLOAT NUMBER
2382      .WORD  161600,0,0,0     ; INITIAL MEM FLOAT NUMBER
2383
2384

```

```

2385 011136 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
2386 011144 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
2387 011146 047611 147606 .WORD 047611,147606 ; FPS: BEFORE, AFTER
2388 011152 100010 .WORD 100010 ; FEC AFTER ( 0 = N7A )
2389
2390
2391
2392 ;*****
2393 ;*TEST 132 TEST OF SUBD INSTR, DATA SET SUBD-10
2394 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
2395 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
2396 ;*****
2396 011154 000004 TST132: SCOPE
2397 011156 012705 011170 MOV #SUBD10,R5 ; PTR TO TEST DATA SET
2398 011162 004737 034374 JSR PC,#SUBDT ; GO TEST
2399
2400 011166 000417 BR TST133 ;;
2401
2402 011170 SUBD10: ; TEST DATA SET SUBD-10:
2403 011170 077777 177777 177777 .WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
2404 011176 177777 .WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER
2405 011200 161600 000000 000000 .WORD 161600,0,0,0 ; INITIAL MEM FLOAT NUMBER
2406 011206 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
2407 011210 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
2408 011216 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
2409 011220 046611 046606 .WORD 046611,046606 ; FPS: BEFORE, AFTER
2410 011224 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2411
2412

```

```

2413 ;*****
2414 ;*TEST 133 TEST OF MULF INSTR, DATA SET MULF-1
2415 ;* ALL INTERRUPT ENABLES ON
2416 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
2417 ;*****
2418 011226 000004 TST133: SCOPE
2419 011230 012705 011242 MOV #MULF1,R5 ; PTR TO TEST DATA SET
2420 011234 004737 034564 JSR PC,#MULFT ; GO TEST
2421
2422 011240 000411 BR TST134 ;;
2423
2424 011242 MULF1: ; TEST DATA SET MULF-1:
2425 011242 000000 000000 .WORD 0,0 ; INITIAL AC FLOAT NUMBER
2426 011246 177777 177777 .WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER
2427 011252 000000 000000 .WORD 0,0 ; EXPECTED FLOAT RESULT
2428 011256 047413 047404 .WORD 047413,047404 ; FPS: BEFORE, AFTER
2429 011262 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2430
2431
2432 ;*****
2433 ;*TEST 134 TEST OF MULF INSTR, DATA SET MULF-2
2434 ;* ALL INTERRUPT ENABLES ON
2435 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
2436 ;*****
2437 011264 000004 TST134: SCOPE
2438 011266 012705 011300 MOV #MULF2,R5 ; PTR TO TEST DATA SET
2439 011272 004737 034564 JSR PC,#MULFT ; GO TEST
2440
2441 011276 000411 BR TST135 ;;
2442
2443 011300 MULF2: ; TEST DATA SET MULF-2:
2444 011300 077777 177777 .WORD LGP,M1 ; INITIAL AC FLOAT NUMBER
2445 011304 000177 177777 .WORD ZX,MP,M1 ; INITIAL MEM FLOAT NUMBER
2446 011310 000000 000000 .WORD 0,0 ; EXPECTED FLOAT RESULT
2447 011314 047503 047504 .WORD 047503,047504 ; FPS: BEFORE, AFTER
2448 011320 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2449
2450 ;*****
2451 ;*TEST 135 TEST OF MULF INSTR, DATA SET MULF-3
2452 ;* ALL INTERRUPT ENABLES ON
2453 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
2454 ;*****
2455 011322 000004 TST135: SCOPE
2456 011324 012705 011336 MOV #MULF3,R5 ; PTR TO TEST DATA SET
2457 011330 004737 034564 JSR PC,#MULFT ; GO TEST
2458
2459 011334 000411 BR TST136 ;;
2460
2461 011336 MULF3: ; TEST DATA SET MULF-3:
2462 011336 123652 125252 .WORD 123652,ALTM ; INITIAL AC FLOAT NUMBER
2463 011342 017500 000000 .WORD 017500,000000 ; INITIAL MEM FLOAT NUMBER
2464 011346 103177 177777 .WORD 103177,M1 ; EXPECTED FLOAT RESULT
2465 011352 047447 047450 .WORD 047447,047450 ; FPS: BEFORE, AFTER
2466 011356 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2467
2468

```

```
2469
2470
2471
2472
2473
2474
2475 011360 000004
2476 011362 012705 011374
2477 011366 004737 034564
2478
2479 011372 000411
2480
2481 011374
MULF4: ; TEST DATA SET MULF-4:
2482 011374 217500 000000 ; WORD 017500,000000 ; INITIAL AC FLOAT NUMBER
2483 011400 023652 125252 ; WORD 023652,ALTN ; INITIAL MEM FLOAT NUMBER
2484 011404 003177 177777 ; WORD 003177,M1 ; EXPECTED FLOAT RESULT
2485 011410 047417 047400 ; WORD 047417,047400 ; FPS: BEFORE, AFTER
2486 011414 000000 ; WORD NA ; FEC AFTER ( 0 = N7A )
2487
2488
2489
2490
2491
2492
2493
2494 011416 000004
2495 011420 012705 011432
2496 011424 004737 034564
2497
2498 011430 000411
2499
2500 011432
MULF5: ; TEST DATA SET MULF-5:
2501 011432 036400 000001 ; WORD 036400,000001 ; INITIAL AC FLOAT NUMBER
2502 011436 106777 177776 ; WORD 106777,M2 ; INITIAL MEM FLOAT NUMBER
2503 011442 105177 177777 ; WORD 105177,M1 ; EXPECTED FLOAT RESULT
2504 011446 047547 047550 ; WORD 047547,047550 ; FPS: BEFORE, AFTER
2505 011452 000000 ; WORD NA ; FEC AFTER ( 0 = N7A )
2506
2507
2508
2509
2510
2511
2512
2513 011454 000004
2514 011456 012705 011470
2515 011462 004737 034564
2516
2517 011466 000411
2518
2519 011470
MULF6: ; TEST DATA SET MULF-6:
2520 011470 036400 000001 ; WORD 036400,000001 ; INITIAL AC FLOAT NUMBER
2521 011474 106777 177776 ; WORD 106777,M2 ; INITIAL MEM FLOAT NUMBER
2522 011500 105200 000000 ; WORD 105200,000000 ; EXPECTED FLOAT RESULT
2523 011504 047407 047410 ; WORD 047407,047410 ; FPS: BEFORE, AFTER
2524 011510 000000 ; WORD NA ; FEC AFTER ( 0 = N7A )
```

```
2525
2526
2527
2528
2529
2530
2531
2532 011512 000004
2533 011514 012705 011526
2534 011520 004737 034564
2535
2536 011524 000411
2537
2538 011526
MULF7: ; TEST DATA SET MULF-7:
2539 011526 140277 000000 ; WORD 140277,000000 ; INITIAL AC FLOAT NUMBER
2540 011532 000000 000001 ; WORD 000000,000001 ; INITIAL MEM FLOAT NUMBER
2541 011536 160077 000001 ; WORD 160077,000001 ; EXPECTED FLOAT RESULT
2542 011542 047407 047410 ; WORD 047407,047410 ; FPS: BEFORE, AFTER
2543 011546 000000 ; WORD NA ; FEC AFTER ( 0 = N7A )
2544
2545
2546
2547
2548
2549
2550
2551 011550 000004
2552 011552 012705 011564
2553 011556 004737 034564
2554
2555 011562 000411
2556
2557 011564
MULF10: ; TEST DATA SET MULF-10:
2558 011564 000000 000001 ; WORD 000000,000001 ; INITIAL AC FLOAT NUMBER
2559 011570 042277 000000 ; WORD 042277,000000 ; INITIAL MEM FLOAT NUMBER
2560 011574 000077 000001 ; WORD 000077,000001 ; EXPECTED FLOAT RESULT
2561 011600 047457 047440 ; WORD 047457,047440 ; FPS: BEFORE, AFTER
2562 011604 000000 ; WORD NA ; FEC AFTER ( 0 = N7A )
2563
2564
2565
2566
2567
2568
2569
2570 011606 000004
2571 011610 012705 011622
2572 011614 004737 034564
2573
2574 011620 000411
2575
2576 011622
MULF11: ; TEST DATA SET MULF-11:
2577 011622 140300 000000 ; WORD 140300,000000 ; INITIAL AC FLOAT NUMBER
2578 011626 100000 000001 ; WORD 100000,000001 ; INITIAL MEM FLOAT NUMBER
2579 011632 000100 000002 ; WORD 000100,000002 ; EXPECTED FLOAT RESULT
2580 011636 047517 047500 ; WORD 047517,047500 ; FPS: BEFORE, AFTER
```

```

2581 @11642 @00000          ,WORD NA          ; FEC AFTER ( 0 = N/A )
2582
2583
2584
2585
2586
2587
2588
2589 @11644 @00004
2590 @11646 @12705 @11660
2591 @11652 @04737 @34564
2592
2593 @11656 @00011
2594
2595 @11660
2596 @11660 @00000 @00001
2597 @11664 140300 @00000
2598 @11670 100100 @00001
2599 @11674 @47547 @47550
2600 @11700 @00000
2601
2602
2603
2604
2605
2606
2607
2608 @11702 @00004
2609 @11704 @12705 @11716
2610 @11710 @04737 @34564
2611
2612 @11714 @00011
2613
2614 @11716
2615 @11716 @02177 17777
2616 @11722 @02177 17777
2617 @11726 @44177 17776
2618 @11732 @47513 147500
2619 @11736 100012
2620
2621
2622
2623
2624
2625
2626
2627 @11740 @00004
2628 @11742 @12705 @11754
2629 @11746 @04737 @34564
2630
2631 @11752 @00011
2632
2633 @11754
2634 @11754 170000 @00000
2635 @11760 @50200 @00000
2636 @11764 100000 @00000

```

```

;*****
;TEST 144 TEST OF MULF INSTR, DATA SET MULF-11
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST144: SCOPE
MOV #MULF12,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST145 ;;

MULF12: ; TEST DATA SET MULF-12:
;WORD @00000,@00001 ; INITIAL AC FLOAT NUMBER
;WORD 140300,@00000 ; INITIAL MEM FLOAT NUMBER
;WORD 100100,@00001 ; EXPECTED FLOAT RESULT
;WORD @47547,@47550 ; FPS: BEFORE, AFTER
;WORD NA ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 145 TEST OF MULF INSTR, DATA SET MULF-11
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST145: SCOPE
MOV #MULF13,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST146 ;;

MULF13: ; TEST DATA SET MULF-13:
;WORD @02177,M1 ; INITIAL AC FLOAT NUMBER
;WORD @02177,M1 ; INITIAL MEM FLOAT NUMBER
;WORD @44177,M2 ; EXPECTED FLOAT RESULT
;WORD @47513,@47500 ; FPS: BEFORE, AFTER
;WORD 100012 ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 146 TEST OF MULF INSTR, DATA SET MULF-14
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST146: SCOPE
MOV #MULF14,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST147 ;;

MULF14: ; TEST DATA SET MULF-14:
;WORD 170000,@00000 ; INITIAL AC FLOAT NUMBER
;WORD @50200,@00000 ; INITIAL MEM FLOAT NUMBER
;WORD M0,0 ; EXPECTED FLOAT RESULT

```

```

2637 @11770 @47441 147456
2638 @11774 100010
2639
2640
2641
2642
2643
2644
2645
2646 @11776 @00004
2647 @12000 @12705 @12012
2648 @12004 @04737 @34564
2649
2650 @12010 @00011
2651
2652 @12012
2653 @12012 17777 17777
2654 @12016 100177 17777
2655 @12022 17777 17777
2656 @12026 @47447 147447
2657 @12032 100014
2658
2659
2660
2661
2662
2663
2664
2665 @12034 @00004
2666 @12036 @12705 @12050
2667 @12042 @04737 @34564
2668
2669 @12046 @00011
2670
2671 @12050
2672 @12050 @00377 @00000
2673 @12054 @00000 17777
2674 @12060 @00000 @00000
2675 @12064 @46411 @46406
2676 @12070 @00000
2677
2678
2679
2680
2681
2682
2683
2684 @12072 @00004
2685 @12074 @12705 @12106
2686 @12100 @04737 @34564
2687
2688 @12104 @00011
2689
2690 @12106
2691 @12106 @02177 17777
2692 @12112 @02177 17777

```

```

;*****
;TEST 147 TEST OF MULF INSTR, DATA SET MULF-15
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST147: SCOPE
MOV #MULF15,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST150 ;;

MULF15: ; TEST DATA SET MULF-15:
;WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
;WORD Z1,MN,M1 ; INITIAL MEM FLOAT NUMBER
;WORD LGN,M1 ; EXPECTED FLOAT RESULT
;WORD @47447,@47447 ; FPS: BEFORE, AFTER
;WORD 100014 ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 150 TEST OF MULF INSTR, DATA SET MULF-16
;*
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST150: SCOPE
MOV #MULF16,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST151 ;;

MULF16: ; TEST DATA SET MULF-16:
;WORD @50377,@00000 ; INITIAL AC FLOAT NUMBER
;WORD @70000,M1 ; INITIAL MEM FLOAT NUMBER
;WORD 0,0 ; EXPECTED FLOAT RESULT
;WORD @46411,@46406 ; FPS: BEFORE, AFTER
;WORD NA ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 151 TEST OF MULF INSTR, DATA SET MULF-17
;*
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST151: SCOPE
MOV #MULF17,R5 ; PTR TO TEST DATA SET
JSR PC,@MULFT ; GO TEST
BR TST152 ;;

MULF17: ; TEST DATA SET MULF-17:
;WORD @02177,M1 ; INITIAL AC FLOAT NUMBER
;WORD @02177,M1 ; INITIAL MEM FLOAT NUMBER

```

2693 012116 000000 000000  
 2694 012122 045553 045544  
 2695 012126 000000  
 2696  
 2697  
 2698  
 2699  
 2700  
 2701  
 2702  
 2703 012130 000004  
 2704 012132 012705 012144  
 2705 012136 004737 034564  
 2706  
 2707 012142 000411  
 2708  
 2709 012144  
 2710 012144 052525 052525  
 2711 012150 100000 177777  
 2712 012154 000000 000000  
 2713 012160 043513 043504  
 2714 012164 000000  
 2715  
 2716  
 2717

```

;*****
;TEST 152 TEST OF MULF INSTR, DATA SET MULF-20
;*
;* -0 INTERRUPT ENABLE OFF; ALL OTHERS ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST152: SCOPE
MOV #MULF20,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULFT ; GO TEST
;*****
BR TST153 ;

MULF20: ; TEST DATA SET MULF-20:
;WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
;WORD M0,M1 ; INITIAL MEM FLOAT NUMBER
;WORD 0,0 ; EXPECTED FLOAT RESULT
;WORD 043513,043504 ; FPS: BEFORE, AFTER
;WORD NA ; FEC AFTER ( 0 = N7A )

```

2718  
 2719  
 2720  
 2721  
 2722  
 2723 012166 000004  
 2724 012174 012705 012202  
 2725 012174 004737 034734  
 2726  
 2727 012200 000417  
 2728  
 2729 012202  
 2730 012202 077777 177777 177777  
 2731 012210 177777  
 2732 012212 000000 000000 000000  
 2733 012220 000000  
 2734 012222 000000 000000  
 2735 012224 000000  
 2736 012232 047713 047704  
 2737 012230 000000  
 2738  
 2739  
 2740  
 2741  
 2742  
 2743  
 2744  
 2745 012240 000004  
 2746 012242 012705 012254  
 2747 012246 004737 034734  
 2748  
 2749 012252 000417  
 2750  
 2751 012254  
 2752 012254 000177 177777 177777  
 2753 012262 177777  
 2754 012264 177777 177777 177777  
 2755 012272 177777  
 2756 012274 000000 000000 000000  
 2757 012302 000000  
 2758 012304 047603 047604  
 2759 012310 000000  
 2760  
 2761  
 2762  
 2763  
 2764  
 2765  
 2766  
 2767 012312 000004  
 2768 012314 012705 012326  
 2769 012320 004737 034734  
 2770  
 2771 012324 000417  
 2772  
 2773 012326

```

;*****
;TEST 153 TEST OF MULD INSTR, DATA SET MULD-1
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST153: SCOPE
MOV #MULD1,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULD1 ; GO TEST
;*****
BR TST154 ;

MULD1: ; TEST DATA SET MULD-1:
;WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
;WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
;WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
;WORD 047713,047704 ; FPS: BEFORE, AFTER
;WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 154 TEST OF MULD INSTR, DATA SET MULD-2
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST154: SCOPE
MOV #MULD2,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULD2 ; GO TEST
;*****
BR TST155 ;

MULD2: ; TEST DATA SET MULD-2:
;WORD EX1MP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
;WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
;WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
;WORD 047603,047604 ; FPS: BEFORE, AFTER
;WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 155 TEST OF MULD INSTR, DATA SET MULD-3
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST155: SCOPE
MOV #MULD3,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULD3 ; GO TEST
;*****
BR TST156 ;

MULD3: ; TEST DATA SET MULD-3:

```

```

2774 012326 023652 125252 125252 .WORD 023652,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
2775 012334 125252 .WORD 017500,0,0,0 ; INITIAL MEM FLOAT NUMBER
2776 012336 017500 000000 000000 .WORD 003177,M1,M1,M1 ; EXPECTED FLOAT RESULT
2777 012344 000000 .WORD 047757,047740 ; FPS: BEFORE, AFTER
2778 012346 003177 177777 177777 .WORD NA ; FEC AFTER ( 0 = N7A )
2779 012354 177777
2780 012356 047757 047740
2781 012362 000000
2782
2783
2784
2785
2786
2787
2788
2789 012364 000004
2790 012366 012705 012400
2791 012372 004737 034734
2792
2793 012376 000417
2794
2795 012400
2796 012400 117500 000000 000000 MULU4: ; TEST DATA SET MULU-4:
2797 012406 000000 .WORD 117500,0,0,0 ; INITIAL AC FLOAT NUMBER
2798 012410 123652 125252 125252 .WORD 123652,ALTN,ALTN,ALTN ; INITIAL MEM FLOAT NUMBER
2799 012416 125252 .WORD 003177,M1,M1,M1 ; EXPECTED FLOAT RESULT
2800 012420 003177 177777 177777 .WORD 047617,047600 ; FPS: BEFORE, AFTER
2801 012426 177777 .WORD NA ; FEC AFTER ( 0 = N7A )
2802 012430 047617 047600
2803 012434 000000
2804
2805
2806
2807
2808
2809
2810
2811 012436 000004
2812 012440 012705 012452
2813 012444 004737 034734
2814
2815 012450 000417
2816
2817 012452
2818 012452 165400 000000 000000 MULU5: ; TEST DATA SET MULU-5:
2819 012460 000001 .WORD 165400,0,0,1 ; INITIAL AC FLOAT NUMBER
2820 012462 037577 177777 177777 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER
2821 012470 177776 .WORD 164777,M1,M1,M1 ; EXPECTED FLOAT RESULT
2822 012472 164777 177777 177777 .WORD 047747,047750 ; FPS: BEFORE, AFTER
2823 012500 177777 .WORD NA ; FEC AFTER ( 0 = N7A )
2824 012502 047747 047750
2825 012506 000000
2826
2827
2828
2829

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```

2830
2831
2832
2833 012510 000004
2834 012512 012705 012524
2835 012516 004737 034734
2836
2837 012522 000417
2838
2839 012524
2840 012524 165400 000000 000000 MULU6: ; TEST DATA SET MULU-6:
2841 012532 000001 .WORD 165400,0,0,1 ; INITIAL AC FLOAT NUMBER
2842 012534 037577 177777 177777 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER
2843 012542 177776 .WORD 165000,0,0,0 ; EXPECTED FLOAT RESULT
2844 012544 165000 000000 000000 .WORD 047707,047710 ; FPS: BEFORE, AFTER
2845 012552 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2846 012554 047707 047710
2847 012560 000000
2848
2849
2850
2851
2852
2853
2854
2855 012562 000004
2856 012564 012705 012576
2857 012570 004737 034734
2858
2859 012574 000417
2860
2861 012576
2862 012576 040277 000000 000000 MULU7: ; TEST DATA SET MULU-7:
2863 012604 000000 .WORD 040277,0,0,0 ; INITIAL AC FLOAT NUMBER
2864 012606 034200 000000 000000 .WORD 034200,0,0,1 ; INITIAL MEM FLOAT NUMBER
2865 012614 000001 .WORD 034277,0,0,1 ; EXPECTED FLOAT RESULT
2866 012616 034277 000000 000000 .WORD 047657,047640 ; FPS: BEFORE, AFTER
2867 012624 000001 .WORD NA ; FEC AFTER ( 0 = N7A )
2868 012626 047657 047640
2869 012632 000000
2870
2871
2872
2873
2874
2875
2876
2877 012634 000004
2878 012636 012705 012650
2879 012642 004737 034734
2880
2881 012646 000417
2882
2883 012650
2884 012650 140277 000000 000000 MULU8: ; TEST DATA SET MULU-8:
2885 012656 000001 .WORD 140277,0,0,0 ; INITIAL AC FLOAT NUMBER

```

```
2886 012660 034200 000000 000000 .WORD 034200,0,0,1 ; INITIAL MEM FLOAT NUMBER
2887 012666 000001 000000 .WORD 134277,0,0,1 ; EXPECTED FLOAT RESULT
2888 012670 134277 000000 000000 .WORD 047607,047610 ; FPS: BEFORE, AFTER
2889 012676 000001 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
2890 012700 047607 047610
2891 012704 000000
2892
2893
2894
2895 ;*****
;*TEST 103 TEST OF MULD INSTR, DATA SET MULD-11
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST103: SCOPE
MOV #MULD11,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST164 ;;
MULD111 ; TEST DATA SET MULD-11:
.WORD 040300,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 134200,0,0,1 ; INITIAL MEM FLOAT NUMBER
.WORD 134300,0,0,1 ; EXPECTED FLOAT RESULT
.WORD 047747,047750 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
2905 012722 000000 000000
2906 012722 040300 000000 000000
2907 012730 000000 000000
2908 012732 134200 000000 000000
2909 012740 000001 000000
2910 012742 134300 000000 000000
2911 012750 000001 000000
2912 012752 047747 047750
2913 012756 000000
2914
2915
2916 ;*****
;*TEST 104 TEST OF MULD INSTR, DATA SET MULD-12
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST104: SCOPE
MOV #MULD12,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST165 ;;
MULD121 ; TEST DATA SET MULD-12:
.WORD 140300,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 134200,0,0,1 ; INITIAL MEM FLOAT NUMBER
.WORD 034300,0,0,2 ; EXPECTED FLOAT RESULT
.WORD 047717,047700 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
2927 012774 140300 000000 000000
2928 013002 000000 000000
2929 013004 134200 000000 000000
2930 013012 000001 000000
2931 013014 034300 000000 000000
2932 013022 000002 000000
2933 013024 047717 047700
2934 013030 000000
2935
2936
2937
2938
2939 ;*****
;*TEST 105 TEST OF MULD INSTR, DATA SET MULD-13
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST105: SCOPE
MOV #MULD13,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST166 ;;
MULD131 ; TEST DATA SET MULD-13:
.WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 037577,M1,M1,M2 ; EXPECTED FLOAT RESULT
.WORD 047655,147642 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N7A )
2942 013044 000001 000000
2943 013044 000001 000000
2944 013044 000001 000000
2945 013044 004737 034734
2946
2947 013044 000417
2948
2949 013046
2950 013046 177777 177777 177777
2951 013054 177777 177777
2952 013056 177777 177777 177777
2953 013064 177777 177777
2954 013066 037577 177777 177777
2955 013074 177776 147642
2956 013076 047655 147642
2957 013102 100010
2958
2959
2960 ;*****
;*TEST 106 TEST OF MULD INSTR, DATA SET MULD-14
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST106: SCOPE
MOV #MULD14,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST167 ;;
MULD141 ; TEST DATA SET MULD-14:
.WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 046751,046746 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
2961 013104 000004 000000
2962 013106 012705 013120
2963 013112 004737 034734
2964
2965 013116 000417
2966
2967 013116 000417
2968
2969 013116 000417
2970
2971 013120
2972 013120 077777 177777 177777
2973 013126 177777 177777
2974 013130 077777 177777 177777
2975 013136 177777 177777
2976 013140 000000 000000 000000
2977 013146 000000 000000
2978 013150 046751 046746
2979 013154 000000
2980
2981
2982 ;*****
;*TEST 107 TEST OF MULD INSTR, DATA SET MULD-15
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST107: SCOPE
MOV #MULD15,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST170 ;;
MULD151 ; TEST DATA SET MULD-15:
.WORD 003177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
2983 013156 000004 000000
2984 013160 012705 013172
2985 013164 004737 034734
2986
2987 013170 000417
2988
2989 013170 000417
2990
2991 013170 000417
2992
2993 013172
2994 013172 003177 177777 177777
2995 013200 177777 177777
2996 013202 101177 177777 177777
2997 013210 177777 177777
```

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2942
2943 013032 000000 000000
2944 013034 012705 013046
2945 013040 004737 034734
2946
2947 013044 000417
2948
2949 013046
2950 013046 177777 177777 177777
2951 013054 177777 177777
2952 013056 177777 177777 177777
2953 013064 177777 177777
2954 013066 037577 177777 177777
2955 013074 177776 147642
2956 013076 047655 147642
2957 013102 100010
2958
2959
2960 ;*****
;*TEST 106 TEST OF MULD INSTR, DATA SET MULD-14
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST106: SCOPE
MOV #MULD14,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST167 ;;
MULD141 ; TEST DATA SET MULD-14:
.WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 046751,046746 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
2961 013104 000004 000000
2962 013106 012705 013120
2963 013112 004737 034734
2964
2965 013116 000417
2966
2967 013116 000417
2968
2969 013116 000417
2970
2971 013120
2972 013120 077777 177777 177777
2973 013126 177777 177777
2974 013130 077777 177777 177777
2975 013136 177777 177777
2976 013140 000000 000000 000000
2977 013146 000000 000000
2978 013150 046751 046746
2979 013154 000000
2980
2981
2982 ;*****
;*TEST 107 TEST OF MULD INSTR, DATA SET MULD-15
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST107: SCOPE
MOV #MULD15,R5 ; PTR TO TEST DATA SET
JSR PC,0#MULDT ; GO TEST
BR TST170 ;;
MULD151 ; TEST DATA SET MULD-15:
.WORD 003177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
2983 013156 000004 000000
2984 013160 012705 013172
2985 013164 004737 034734
2986
2987 013170 000417
2988
2989 013170 000417
2990
2991 013170 000417
2992
2993 013172
2994 013172 003177 177777 177777
2995 013200 177777 177777
2996 013202 101177 177777 177777
2997 013210 177777 177777
```



```
2998 013212 144177 177777 177777 .WORD 144177,M1,M1,M2 ; EXPECTED FLOAT RESULT
2999 013220 177776 177776 .WORD 177776,M1,M1,M2 ; EXPECTED FLOAT RESULT
3000 013222 047647 147650 .WORD 047647,147650 ; FPS: BEFORE, AFTER
3001 013226 100012 100012 .WORD 100012 ; FEC AFTER ( 0 = N7A )
3002
3003
3004
3005 ;*****
3006 ;*TEST 170 TEST OF MULD INSTR, DATA SET MULD-10
3007 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
3008 ;* LONG FLOAT, LONG INTEGEM, ROUND MODES
3009 ;*****
3010 TST170: SCOPE
3011 MOV #MULD16,R5 ; PTR TO TEST DATA SET
3012 JSR PC,#MULD1 ; GO TEST
3013 BR TST171 ;;
3014
3015 MULD16: ; TEST DATA SET MULD=101
3016 .WORD 103177,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
3017 .WORD 177777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3018 .WORD 001177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3019 .WORD 177777,M1,M1,M1 ; EXPECTED FLOAT RESULT
3020 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
3021 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
3022 .WORD 045713,045704 ; FPS: BEFORE, AFTER
3023 .WORD NA ; FEC AFTER ( 0 = N7A )
3024
3025
3026 ;*****
3027 ;*TEST 171 TEST OF MULD INSTR, DATA SET MULD-11
3028 ;* ALL INTERRUPT ENABLES ON
3029 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
3030 ;*****
3031 TST171: SCOPE
3032 MOV #MULD17,R5 ; PTR TO TEST DATA SET
3033 JSR PC,#MULD1 ; GO TEST
3034 BR TST172 ;;
3035
3036 MULD17: ; TEST DATA SET MULD=171
3037 .WORD ALTP,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
3038 .WORD 052525,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3039 .WORD 052525,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3040 .WORD 177777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3041 .WORD 177777,M1,M1,M1 ; EXPECTED FLOAT RESULT
3042 .WORD 052525,M1,M1,M1 ; EXPECTED FLOAT RESULT
3043 .WORD 052525,M1,M1,M1 ; EXPECTED FLOAT RESULT
3044 .WORD 047657,147657 ; FPS: BEFORE, AFTER
3045 .WORD 100014 ; FEC AFTER ( 0 = N7A )
3046
3047
3048 ;*****
3049 ;*TEST 172 TEST OF MULD INSTR, DATA SET MULD-20
3050 ;* =0 INTERRUPT ENABLE OFF, ALL DIMENS ON
3051 ;* LONG FLOAT, LONG INTEGEM, TRUNCATE MODES
3052 ;*****
3053 TST172: SCOPE
```

```
3054 013356 012705 013376 .WORD 012705,M1,M1,M1 ; PTR TO TEST DATA SET
3055 013362 004737 034734 .WORD 004737,M1,M1,M1 ; GO TEST
3056
3057 013366 000417 000417 BR TST173 ;;
3058
3059 MULD20: ; TEST DATA SET MULD=201
3060 .WORD ALTN,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
3061 .WORD 125252,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3062 .WORD 177777,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3063 .WORD 125252,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
3064 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
3065 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
3066 .WORD 043753,043744 ; FPS: BEFORE, AFTER
3067 .WORD NA ; FEC AFTER ( 0 = N7A )
3068
3069
```

```

3070
3071
3072
3073
3074
3075 013426 000004
3076 013430 012705 013442
3077 013434 004737 035124
3078
3079 013440 000411
3080
3081 013442
3082 013442 103177 177777
3083 013446 023052 125252
3084 013452 117500 000000
3085 013456 047447 047450
3086 013462 000000
3087
3088
3089
3090
3091
3092
3093
3094 013464 000004
3095 013466 012705 013500
3096 013472 004737 035124
3097
3098 013476 000411
3099
3100 013500
3101 013500 052525 052525
3102 013504 000000 000000
3103 013510 052525 052525
3104 013514 047517 147517
3105 013520 100004
3106
3107
3108
3109
3110
3111
3112
3113 013522 000004
3114 013524 012705 013536
3115 013530 004737 035124
3116
3117 013534 000411
3118
3119 013536
3120 013536 140400 000000
3121 013542 040500 000000
3122 013546 140052 125252
3123 013552 047447 047450
3124 013556 000000
3125

```

```

*****
;TEST 173 TEST OF DIVF INSTR, DATA SET DIVF-1
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST173: SCOPE
MOV #DIVF1,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST174 ;;

DIVF1: ; TEST DATA SET DIVF-1
.WORD 103177,M1 ; INITIAL AC FLOAT NUMBER
.WORD 023052,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 117500,0 ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 174 TEST OF DIVF INSTR, DATA SET DIVF-2
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST174: SCOPE
MOV #DIVF2,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST175 ;;

DIVF2: ; TEST DATA SET DIVF-2
.WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER
.WORD ALTP,ALTP ; EXPECTED FLOAT RESULT
.WORD 047517,147517 ; FPS: BEFORE, AFTER
.WORD 100004 ; FEC AFTER ( 0 = N/A )

*****
;TEST 175 TEST OF DIVF INSTR, DATA SET DIVF-3
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST175: SCOPE
MOV #DIVF3,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST176 ;;

DIVF3: ; TEST DATA SET DIVF-3
.WORD 140400,000000 ; INITIAL AC FLOAT NUMBER
.WORD 040500,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 140052,ALTN ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

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3126
3127
3128
3129
3130
3131
3132 013560 000004
3133 013562 012705 013574
3134 013566 004737 035124
3135
3136 013572 000411
3137
3138 013574
3139 013574 040400 000000
3140 013600 140500 000000
3141 013604 140052 125253
3142 013610 047507 047510
3143 013614 000000
3144
3145
3146
3147
3148
3149
3150
3151 013616 000004
3152 013620 012705 013632
3153 013624 004737 035124
3154
3155 013630 000411
3156
3157 013632
3158 013632 007417 007417
3159 013636 007417 007417
3160 013642 040200 000000
3161 013646 047417 047400
3162 013652 000000
3163
3164
3165
3166
3167
3168
3169
3170 013654 000004
3171 013656 012705 013670
3172 013662 004737 035124
3173
3174 013666 000411
3175
3176 013670
3177 013670 100400 000000
3178 013674 134000 000000
3179 013700 140000 000000
3180 013704 047557 047540
3181 013710 000000

```

```

*****
;TEST 176 TEST OF DIVF INSTR, DATA SET DIVF-4
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST176: SCOPE
MOV #DIVF4,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST177 ;;

DIVF4: ; TEST DATA SET DIVF-4
.WORD 040400,000000 ; INITIAL AC FLOAT NUMBER
.WORD 140500,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 140052,ALTN+1 ; EXPECTED FLOAT RESULT
.WORD 047507,047510 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 177 TEST OF DIVF INSTR, DATA SET DIVF-5
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
*****
TST177: SCOPE
MOV #DIVF5,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST200 ;;

DIVF5: ; TEST DATA SET DIVF-5
.WORD ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD ALTP,ALTP ; INITIAL MEM FLOAT NUMBER
.WORD FIP,0 ; EXPECTED FLOAT RESULT
.WORD 047417,047400 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 200 TEST OF DIVF INSTR, DATA SET DIVF-6
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
*****
TST200: SCOPE
MOV #DIVF6,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST201 ;;

DIVF6: ; TEST DATA SET DIVF-6
.WORD 100400,000000 ; INITIAL AC FLOAT NUMBER
.WORD 134000,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 040000,000000 ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

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3182
3183
3184
3185
3186
3187
3188
3189 013712 000004
3190 013714 012705 013726
3191 013720 004737 035124
3192
3193 013724 000411
3194
3195 013726
3196 013726 000177 177777
3197 013732 177777 177777
3198 013736 000000 000000
3199 013742 047453 047444
3200 013746 000000
3201
3202
3203
3204
3205
3206
3207
3208 013750 000004
3209 013752 012705 013764
3210 013756 004737 035124
3211
3212 013762 000411
3213
3214 013764
3215 013764 100077 000000
3216 013770 100277 000000
3217 013774 000000 000000
3218 014000 047517 047500
3219 014004 000000
3220
3221
3222
3223
3224
3225
3226
3227 014006 000004
3228 014010 012705 014022
3229 014014 004737 035124
3230
3231 014020 000411
3232
3233 014022
3234 014022 100077 000000
3235 014026 000277 000000
3236 014032 100000 000000
3237 014036 047447 047450

;*****
;TEST 201 TEST OF DIVF INSTR, DATA SET DIVF=7
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST201: SCOPE
MOV #DIVF7,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST202 ;;

DIVF7: ; TEST DATA SET DIVF=7:
.WORD 2X1MP,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047453,047444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 202 TEST OF DIVF INSTR, DATA SET DIVF=10
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, ROUND MOVES
;*****
TST202: SCOPE
MOV #DIVF10,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST203 ;;

DIVF10: ; TEST DATA SET DIVF=10:
.WORD 160077,000000 ; INITIAL AC FLOAT NUMBER
.WORD 140277,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 000000,000000 ; EXPECTED FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 203 TEST OF DIVF INSTR, DATA SET DIVF=11
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST203: SCOPE
MOV #DIVF11,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST204 ;;

DIVF11: ; TEST DATA SET DIVF=11:
.WORD 160077,000000 ; INITIAL AC FLOAT NUMBER
.WORD 040277,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 100000,000000 ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER

```

```

3238 014042 000000
3239
3240
3241
3242
3243
3244
3245
3246 014044 000004
3247 014046 012705 014060
3248 014052 004737 035124
3249
3250 014056 000411
3251
3252 014060
3253 014060 000100 000001
3254 014064 040300 000000
3255 014070 000000 000001
3256 014074 047517 047500
3257 014100 000000
3258
3259
3260
3261
3262
3263
3264
3265 014102 000004
3266 014104 012705 014116
3267 014110 004737 035124
3268
3269 014114 000411
3270
3271 014116
3272 014116 000100 000001
3273 014122 140300 000000
3274 014126 100000 000000
3275 014132 047447 047450
3276 014136 000000
3277
3278
3279
3280
3281
3282
3283
3284 014140 000004
3285 014142 012705 014154
3286 014146 004737 035124
3287
3288 014152 000411
3289
3290 014154
3291 014154 000177 177777
3292 014160 100177 177777
3293 014164 000177 177777

.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 204 TEST OF DIVF INSTR, DATA SET DIVF=12
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, ROUND MOVES
;*****
TST204: SCOPE
MOV #DIVF12,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST205 ;;

DIVF12: ; TEST DATA SET DIVF=12:
.WORD 000100,000001 ; INITIAL AC FLOAT NUMBER
.WORD 040300,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 000000,000001 ; EXPECTED FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 205 TEST OF DIVF INSTR, DATA SET DIVF=13
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST205: SCOPE
MOV #DIVF13,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST206 ;;

DIVF13: ; TEST DATA SET DIVF=13:
.WORD 000100,000001 ; INITIAL AC FLOAT NUMBER
.WORD 140300,000000 ; INITIAL MEM FLOAT NUMBER
.WORD 100000,000000 ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 206 TEST OF DIVF INSTR, DATA SET DIVF=14
;
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST206: SCOPE
MOV #DIVF14,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVFT ; GO TEST
BR TST207 ;;

DIVF14: ; TEST DATA SET DIVF=14:
.WORD 2X1MP,M1 ; INITIAL AC FLOAT NUMBER
.WORD 2X1MN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 2X1MP,M1 ; EXPECTED FLOAT RESULT

```

3294 014170 047543 147543  
3295 014174 100014  
3296  
3297  
3298  
3299  
3300  
3301  
3302  
3303 014176 000004  
3304 014200 012705 014212  
3305 014204 004737 035124  
3306  
3307 014210 000411  
3308  
3309 014212  
3310 014212 000177 177777  
3311 014210 100177 177777  
3312 014222 000177 177777  
3313 014226 043413 143413  
3314 014232 100004  
3315  
3316  
3317  
3318  
3319  
3320  
3321  
3322 014234 000004  
3323 014236 012705 014250  
3324 014242 004737 035124  
3325  
3326 014246 000411  
3327  
3328 014250  
3329 014250 077777 052525  
3330 014254 003777 170360  
3331 014260 034177 062134  
3332 014264 047515 147502  
3333 014270 100010  
3334  
3335  
3336  
3337  
3338  
3339  
3340  
3341 014272 000004  
3342 014274 012705 014306  
3343 014300 004737 035124  
3344  
3345 014304 000411  
3346  
3347 014306  
3348 014306 177777 052525  
3349 014312 103777 170360

```
.WORD 047543,147543 ; FPS: BEFORE, AFTER  
.WORD 100014 ; FEC AFTER ( 0 = N/A )  
  
;*****  
;*TEST 207 TEST OF DIVF INSTR, DATA SET DIVF-14  
;*  
;* =0 INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
;*****  
TST207: SCOPE  
MOV #DIVF14,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVFT ; GO TEST  
  
BR TST210 ;;  
  
DIVF14: ; TEST DATA SET DIVF-14:  
.WORD 2X10P,M1 ; INITIAL AC FLOAT NUMBER  
.WORD 2X10M,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 2X10P,M1 ; EXPECTED FLOAT RESULT  
.WORD 043413,143413 ; FPS: BEFORE, AFTER  
.WORD 100004 ; FEC AFTER ( 0 = N/A )
```

```
;*****  
;*TEST 210 TEST OF DIVF INSTR, DATA SET DIVF-15  
;*  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
;*****  
TST210: SCOPE  
MOV #DIVF15,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVFT ; GO TEST  
  
BR TST211 ;;  
  
DIVF15: ; TEST DATA SET DIVF-15:  
.WORD LGP,ALTP ; INITIAL AC FLOAT NUMBER  
.WORD 003777,ALT4N ; INITIAL MEM FLOAT NUMBER  
.WORD 034177,062134 ; EXPECTED FLOAT RESULT  
.WORD 047515,147502 ; FPS: BEFORE, AFTER  
.WORD 100010 ; FEC AFTER ( 0 = N/A )
```

```
;*****  
;*TEST 211 TEST OF DIVF INSTR, DATA SET DIVF-16  
;*  
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
;*****  
TST211: SCOPE  
MOV #DIVF16,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVFT ; GO TEST  
  
BR TST212 ;;  
  
DIVF16: ; TEST DATA SET DIVF-16:  
.WORD LGN,ALTP ; INITIAL AC FLOAT NUMBER  
.WORD 103777,ALT4N ; INITIAL MEM FLOAT NUMBER
```

3350 014316 000000 000000  
3351 014322 046411 046406  
3352 014326 000000  
3353  
3354  
3355  
3356  
3357  
3358  
3359  
3360 014330 000004  
3361 014332 012705 014344  
3362 014336 004737 035124  
3363  
3364 014342 000411  
3365  
3366 014344  
3367 014344 100200 000000  
3368 014350 077777 177777  
3369 014354 100400 000000  
3370 014360 047547 147550  
3371 014364 100012  
3372  
3373  
3374  
3375  
3376  
3377  
3378  
3379 014366 000004  
3380 014370 012705 014402  
3381 014374 004737 035124  
3382  
3383 014400 000411  
3384  
3385 014402  
3386 014402 000200 000000  
3387 014406 177777 177777  
3388 014412 000000 000000  
3389 014416 045453 045444  
3390 014422 000000  
3391  
3392  
3393

```
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 046411,046406 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
;*****  
;*TEST 212 TEST OF DIVF INSTR, DATA SET DIVF-20  
;*  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
;*****  
TST212: SCOPE  
MOV #DIVF20,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVFT ; GO TEST  
  
BR TST213 ;;  
  
DIVF20: ; TEST DATA SET DIVF-20:  
.WORD 8M0,0 ; INITIAL AC FLOAT NUMBER  
.WORD LGP,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 140400,000000 ; EXPECTED FLOAT RESULT  
.WORD 047547,147550 ; FPS: BEFORE, AFTER  
.WORD 100012 ; FEC AFTER ( 0 = N/A )
```

```
;*****  
;*TEST 213 TEST OF DIVF INSTR, DATA SET DIVF-21  
;*  
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
;*****  
TST213: SCOPE  
MOV #DIVF21,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVFT ; GO TEST  
  
BR TST214 ;;  
  
DIVF21: ; TEST DATA SET DIVF-21:  
.WORD 8M0,0 ; INITIAL AC FLOAT NUMBER  
.WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 045453,045444 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

```

3394
3395
3396
3397
3398
3399 014424 000004
3400 014426 012705 014440
3401 014432 004737 035274
3402
3403 014436 000417
3404
3405 014440
3406 014440 000177 177777 177777
3407 014446 177777
3408 014450 077777 177777 177777
3409 014456 177777
3410 014460 000000 000000 000000
3411 014466 000000
3412 014470 047613 047604
3413 014474 000000
3414
3415
3416
3417
3418
3419
3420
3421 014476 000004
3422 014500 012705 014512
3423 014504 004737 035274
3424
3425 014510 000417
3426
3427 014512
3428 014512 034277 000000 000000
3429 014520 000000
3430 014522 040277 000000 000000
3431 014530 000000
3432 014532 034200 000000 000000
3433 014540 000000
3434 014542 047700
3435 014546 000000
3436
3437
3438
3439
3440
3441
3442
3443 014550 000004
3444 014552 012705 014564
3445 014556 004737 035274
3446
3447 014562 000417
3448
3449 014564

```

;\*\*\*\*\*  
;TEST 214 TEST OF DIVD INSTR, DATA SET DIVD-1  
; ALL INTERRUPT ENABLES ON  
; LONG FLOAT, SHORT INTEGER, ROUND MODES  
;\*\*\*\*\*  
TST214: SCOPE  
MOV #DIVD1,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST215 ;  
DIVD1: ; TEST DATA SET DIVD-1  
.WORD ZXIMP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER  
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT  
.WORD 047613,047604 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

;\*\*\*\*\*  
;TEST 215 TEST OF DIVD INSTR, DATA SET DIVD-2  
; ALL INTERRUPT ENABLES ON  
; LONG FLOAT, LONG INTEGER, ROUND MODES  
;\*\*\*\*\*  
TST215: SCOPE  
MOV #DIVD2,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST216 ;  
DIVD2: ; TEST DATA SET DIVD-2  
.WORD 034277,0,0,0 ; INITIAL AC FLOAT NUMBER  
.WORD 040277,0,0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 034200,0,0,0 ; EXPECTED FLOAT RESULT  
.WORD 047717,047700 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

;\*\*\*\*\*  
;TEST 216 TEST OF DIVD INSTR, DATA SET DIVD-3  
; ALL INTERRUPT ENABLES ON  
; LONG FLOAT, SHORT INTEGER, TRUNCATE MODES  
;\*\*\*\*\*  
TST216: SCOPE  
MOV #DIVD3,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST217 ;  
DIVD3: ; TEST DATA SET DIVD-3

```

3450 014564 134277 000000 000000
3451 014572 000000
3452 014574 040277 000000 000000
3453 014600 000000
3454 014604 134200 000000 000000
3455 014612 000000
3456 014614 047647 047650
3457 014620 000000
3458
3459
3460
3461
3462
3463
3464
3465 014622 000004
3466 014624 012705 014636
3467 014630 004737 035274
3468
3469 014634 000417
3470
3471 014636
3472 014636 134300 000000 000000
3473 014644 000001
3474 014646 140300 000000 000000
3475 014654 000000
3476 014656 034200 000000 000000
3477 014664 000000
3478 014666 047757 047740
3479 014672 000000
3480
3481
3482
3483
3484
3485
3486
3487 014674 000004
3488 014676 012705 014710
3489 014702 004737 035274
3490
3491 014706 000417
3492
3493 014710
3494 014710 034300 000000 000000
3495 014716 000001
3496 014720 140300 000000 000000
3497 014726 000000
3498 014730 134200 000000 000000
3499 014736 000001
3500 014740 047607 047610
3501 014744 000000
3502
3503
3504
3505

```

;\*\*\*\*\*  
;TEST 217 TEST OF DIVD INSTR, DATA SET DIVD-4  
; ALL INTERRUPT ENABLES ON  
; LONG FLOAT, LONG INTEGER, TRUNCATE MODES  
;\*\*\*\*\*  
TST217: SCOPE  
MOV #DIVD4,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST220 ;  
DIVD4: ; TEST DATA SET DIVD-4  
.WORD 134300,0,0,1 ; INITIAL AC FLOAT NUMBER  
.WORD 140300,0,0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 034200,0,0,0 ; EXPECTED FLOAT RESULT  
.WORD 047757,047740 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

;\*\*\*\*\*  
;TEST 220 TEST OF DIVD INSTR, DATA SET DIVD-5  
; ALL INTERRUPT ENABLES ON  
; LONG FLOAT, SHORT INTEGER, ROUND MODES  
;\*\*\*\*\*  
TST220: SCOPE  
MOV #DIVD5,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST221 ;  
DIVD5: ; TEST DATA SET DIVD-5  
.WORD 034300,0,0,1 ; INITIAL AC FLOAT NUMBER  
.WORD 140300,0,0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 134200,0,0,1 ; EXPECTED FLOAT RESULT  
.WORD 047607,047610 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

;\*\*\*\*\*  
;TEST 221 TEST OF DIVD INSTR, DATA SET DIVD-6

3506  
3507  
3508  
3509 014746 000004  
3510 014750 012705 014762  
3511 014754 004737 035274  
3512  
3513 014760 000417  
3514  
3515 014762  
3516 014762 100400 000000 000000  
3517 014770 000000  
3518 014772 000500 000000 000000  
3519 015000 000000  
3520 015002 100052 125252 125252  
3521 015010 125252  
3522 015012 047647 047650  
3523 015016 000000  
3524  
3525  
3526  
3527  
3528  
3529  
3530  
3531 015020 000004  
3532 015022 012705 015034  
3533 015026 004737 035274  
3534  
3535 015032 000417  
3536  
3537 015034  
3538 015034 100400 000000 000000  
3539 015042 000000  
3540 015044 000500 000000 000000  
3541 015052 000000  
3542 015054 100052 125252 125252  
3543 015062 125252  
3544 015064 047607 047610  
3545 015070 000000  
3546  
3547  
3548  
3549  
3550  
3551  
3552  
3553 015072 000004  
3554 015074 012705 015106  
3555 015100 004737 035274  
3556  
3557 015104 000417  
3558  
3559 015106  
3560 015106 170360 170360 170360  
3561 015114 170360

```

;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST221: SCOPE
MOV 0DIVD6,R5 ; PTR TO TEST DATA SET
JSR PC,0DIVDT ; GO TEST
;*****
BR TST222 ;
;
DIVD6: ; TEST DATA SET DIVD=6:
.WORD 100400,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 000500,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 100052,ALTN,ALTN,ALTN ; EXPECTED FLOAT RESULT
.WORD 047647,047650 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )
;*****
;*****
;TEST 222 TEST OF DIVD INSTR, DATA SET DIVD=7
;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST222: SCOPE
MOV 0DIVD7,R5 ; PTR TO TEST DATA SET
JSR PC,0DIVDT ; GO TEST
;*****
BR TST223 ;
;
DIVD7: ; TEST DATA SET DIVD=7:
.WORD 100400,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 000500,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 100052,ALTN,ALTN,ALTN+1 ; EXPECTED FLOAT RESULT
.WORD 047607,047610 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )
;*****
;*****
;TEST 223 TEST OF DIVD INSTR, DATA SET DIVD=10
;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST223: SCOPE
MOV 0DIVD10,R5 ; PTR TO TEST DATA SET
JSR PC,0DIVDT ; GO TEST
;*****
BR TST224 ;
;
DIVD10: ; TEST DATA SET DIVD=10:
.WORD ALT4N,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER

```

3562 015116 170360 170360 170360  
3563 015124 170360  
3564 015126 040200 000000 000000  
3565 015134 000000  
3566 015136 047717 047700  
3567 015142 000000  
3568  
3569  
3570  
3571  
3572  
3573  
3574  
3575 015144 000004  
3576 015146 012705 015160  
3577 015152 004737 035274  
3578  
3579 015156 000417  
3580  
3581 015160  
3582 015160 070200 000000 000000  
3583 015166 000000  
3584 015170 000400 000000 000000  
3585 015176 000000  
3586 015200 000000 000000 000000  
3587 015206 000000  
3588 015210 047657 047640  
3589 015214 000000  
3590  
3591  
3592  
3593  
3594  
3595  
3596  
3597 015216 000004  
3598 015220 012705 015232  
3599 015224 004737 035274  
3600  
3601 015230 000417  
3602  
3603 015232  
3604 015232 125252 125252 125252  
3605 015240 125252  
3606 015242 000000 000000 000000  
3607 015250 000000  
3608 015252 125252 125252 125252  
3609 015260 125252  
3610 015262 047707 147707  
3611 015266 100004  
3612  
3613  
3614  
3615  
3616  
3617

```

.WORD ALT4N,ALT4N,ALT4N,ALT4N ; INITIAL MEM FLOAT NUMBER
.WORD F1P,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 047717,047700 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )
;*****
;*****
;TEST 224 TEST OF DIVD INSTR, DATA SET DIVD=11
;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST224: SCOPE
MOV 0DIVD11,R5 ; PTR TO TEST DATA SET
JSR PC,0DIVDT ; GO TEST
;*****
BR TST225 ;
;
DIVD11: ; TEST DATA SET DIVD=11:
.WORD 070200,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 050400,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 060000,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 047657,047640 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )
;*****
;*****
;TEST 225 TEST OF DIVD INSTR, DATA SET DIVD=14
;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST225: SCOPE
MOV 0DIVD12,R5 ; PTR TO TEST DATA SET
JSR PC,0DIVDT ; GO TEST
;*****
BR TST226 ;
;
DIVD12: ; TEST DATA SET DIVD=12:
.WORD ALTN,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
.WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD ALTN,ALTN,ALTN,ALTN ; EXPECTED FLOAT RESULT
.WORD 047707,147707 ; FPS: BEFORE, AFTER
.WORD 100004 ; FLC AFTER ( 0 = N/A )
;*****
;*****
;TEST 226 TEST OF DIVD INSTR, DATA SET DIVD=13
;*
;* ALL INTERRUPT ENABLES ON
LONG FLOAT, SHORT INTEGER, TRUNCATE MODES

```

```

3618
3619 015270 000004
3620 015272 012705 015304
3621 015276 004737 035274
3622
3623 015302 000417
3624
3625 015304
3626 015304 000177 177777 177777
3627 015312 177777
3628 015314 100177 177777 177777
3629 015322 177777
3630 015324 000177 177777 177777
3631 015332 177777
3632 015334 047643 147643
3633 015340 100014
3634
3635
3636
3637
3638
3639
3640
3641 015342 000004
3642 015344 012705 015356
3643 015350 004737 035274
3644
3645 015354 000417
3646
3647 015356
3648 015356 000177 177777 177777
3649 015364 177777
3650 015366 100177 177777 177777
3651 015374 177777
3652 015376 000177 177777 177777
3653 015404 177777
3654 015406 043643 143643
3655 015412 100004
3656
3657
3658
3659
3660
3661
3662
3663 015414 000004
3664 015416 012705 015430
3665 015422 004737 035274
3666
3667 015426 000417
3668
3669 015430
3670 015430 052525 052525 052525
3671 015436 052525
3672 015440 000200 000000 000000
3673 015446 000000

```

```

;*****
TST226: SCOPE
MOV #DIVD13,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVDT ; GO TEST
BR TST227 ;;

DIVD13: ; TEST DATA SET DIVD-13:
;WORD ZX1MP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
;WORD ZX1MN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
;WORD ZX1MP,M1,M1,M1 ; EXPECTED FLOAT RESULT
;WORD 047643,147643 ; FPS: BEFORE, AFTER
;WORD 100014 ; FLC AFTER ( 0 = N7A )
;*****

;*TEST 227 TEST OF DIVD INSTR, DATA SET DIVD-14
;*
;* -M INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST227: SCOPE
MOV #DIVD14,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVDT ; GO TEST
BR TST230 ;;

DIVD14: ; TEST DATA SET DIVD-14:
;WORD ZX1MP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
;WORD ZX1MN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
;WORD ZX1MP,M1,M1,M1 ; EXPECTED FLOAT RESULT
;WORD 043643,143643 ; FPS: BEFORE, AFTER
;WORD 100004 ; FLC AFTER ( 0 = N7A )
;*****

;*TEST 230 TEST OF DIVD INSTR, DATA SET DIVD-15
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST230: SCOPE
MOV #DIVD15,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVDT ; GO TEST
BR TST231 ;;

DIVD15: ; TEST DATA SET DIVD-15:
;WORD ALTP,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
;WORD 0MP,0,0,0 ; INITIAL MEM FLOAT NUMBER
;*****

```

```

3674 015450 012525 052525 052525
3675 015456 052525
3676 015460 047615 147602
3677 015464 100010
3678
3679
3680
3681
3682
3683
3684
3685 015466 000004
3686 015470 012705 015502
3687 015474 004737 035274
3688
3689 015500 000417
3690
3691 015502
3692 015502 052525 052525 052525
3693 015510 052525
3694 015512 100200 000000 000000
3695 015520 000000
3696 015522 000000 000000 000000
3697 015530 000000
3698 015532 046611 046606
3699 015536 000000
3700
3701
3702
3703
3704
3705
3706
3707 015540 000004
3708 015542 012705 015554
3709 015546 004737 035274
3710
3711 015552 000417
3712
3713 015554
3714 015554 100200 177777 125252
3715 015562 000000
3716 015564 077777 177777 177777
3717 015572 177777
3718 015574 140400 177777 125252
3719 015602 000001
3720 015604 047707 147710
3721 015610 100012
3722
3723
3724
3725
3726
3727
3728
3729 015612 000004

```

```

;*****
;*TEST 231 TEST OF DIVD INSTR, DATA SET DIVD-16
;*
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST231: SCOPE
MOV #DIVD16,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVDT ; GO TEST
BR TST232 ;;

DIVD16: ; TEST DATA SET DIVD-16:
;WORD ALTP,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
;WORD 0MN,0,0,0 ; INITIAL MEM FLOAT NUMBER
;WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
;WORD 046611,046606 ; FPS: BEFORE, AFTER
;WORD NA ; FLC AFTER ( 0 = N7A )
;*****

;*TEST 232 TEST OF DIVD INSTR, DATA SET DIVD-17
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST232: SCOPE
MOV #DIVD17,R5 ; PTR TO TEST DATA SET
JSR PC,#DIVDT ; GO TEST
BR TST233 ;;

DIVD17: ; TEST DATA SET DIVD-17:
;WORD 0MN,M1,ALTN,0 ; INITIAL AC FLOAT NUMBER
;WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
;WORD 140400,M1,ALTN,1 ; EXPECTED FLOAT RESULT
;WORD 047707,147710 ; FPS: BEFORE, AFTER
;WORD 100012 ; FLC AFTER ( 0 = N7A )
;*****

;*TEST 233 TEST OF DIVD INSTR, DATA SET DIVD-20
;*
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST233: SCOPE

```

3730 015614 012705 015626  
3731 015620 004737 035274  
3732  
3733 015624 000417  
3734  
3735 015626  
3736 015626 100200 177777 125252  
3737 015634 000000  
3738 015636 177777 177777 177777  
3739 015644 177777  
3740 015646 000000 000000 000000  
3741 015654 000000  
3742 015656 045713 045704  
3743 015662 000000  
3744  
3745

MOV #DIVD20,R5 ; PTR TO TEST DATA SET  
JSR PC,#DIVDT ; GO TEST  
BR TST234 ;  
DIVD20: ; TEST DATA SET DIVD-20!  
.WORD 54N,M1,ALTN,0 ; INITIAL AC FLOAT NUMBER  
.WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT  
.WORD 045713,045704 ; FPS: BEFORE, AFTER  
.WORD NA ; FLC AFTER ( 0 = N7A )

3746  
3747  
3748  
3749  
3750  
3751 015664 000004  
3752 015666 012705 015700  
3753 015672 004737 035464  
3754  
3755 015676 000413  
3756  
3757 015700  
3758 015700 000000 000000  
3759 015704 000000 000000  
3760 015710 000000 000000  
3761 015714 000000 000000  
3762 015720 047513 047504  
3763 015721 000000  
3764  
3765  
3766  
3767  
3768  
3769  
3770  
3771 015726 000004  
3772 015730 012705 015742  
3773 015734 004737 035464  
3774  
3775 015740 000413  
3776  
3777 015742  
3778 015742 000177 177777  
3779 015746 077777 177777  
3780 015752 000000 000000  
3781 015756 000000 000000  
3782 015762 047553 047544  
3783 015766 000000  
3784  
3785  
3786  
3787  
3788  
3789  
3790  
3791 015770 000004  
3792 015772 012705 016004  
3793 015776 004737 035464  
3794  
3795 016002 000413  
3796  
3797 016004  
3798 016004 177777 177777  
3799 016010 100177 177777  
3800 016014 000000 000000  
3801 016020 000000 000000

\*\*\*\*\*  
;TEST 234 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-1  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, ROUND MODES  
\*\*\*\*\*  
TST234: SCOPE  
MOV #MD2F1,R5 ; PTR TO TEST DATA SET  
JSR PC,#MD2FT ; GO TEST  
BR TST235 ;  
MD2F1: ; TEST DATA SET MD2F-1!  
.WORD 0,0 ; INITIAL AC FLOAT NUMBER  
.WORD 0,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047513,047504 ; FPS: BEFORE, AFTER  
.WORD NA ; FLC AFTER ( 0 = N7A )  
\*\*\*\*\*  
;TEST 235 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-2  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
\*\*\*\*\*  
TST235: SCOPE  
MOV #MD2F2,R5 ; PTR TO TEST DATA SET  
JSR PC,#MD2FT ; GO TEST  
BR TST236 ;  
MD2F2: ; TEST DATA SET MD2F-2!  
.WORD 2X1MP,M1 ; INITIAL AC FLOAT NUMBER  
.WORD LGP,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047553,047544 ; FPS: BEFORE, AFTER  
.WORD NA ; FLC AFTER ( 0 = N7A )  
\*\*\*\*\*  
;TEST 236 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-3  
;\* ALL INTERRUPT ENABLE OFF, ALL OTHERS ON  
;\* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
\*\*\*\*\*  
TST236: SCOPE  
MOV #MD2F3,R5 ; PTR TO TEST DATA SET  
JSR PC,#MD2FT ; GO TEST  
BR TST237 ;  
MD2F3: ; TEST DATA SET MD2F-3!  
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER  
.WORD 2X1MN,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT



3002 010024 043413 043404  
 3003 010030 000000  
 3004  
 3005  
 3006  
 3007  
 3008  
 3009  
 3010  
 3011 010032 000004  
 3012 010034 012705 016046  
 3013 010040 004737 035464  
 3014  
 3015 010044 000413  
 3016  
 3017 010046  
 3018 010046 177777 177777  
 3019 010052 100177 177777  
 3020 010056 177777 177777  
 3021 010062 052525 177777  
 3022 010066 047447 147447  
 3023 010072 100014  
 3024  
 3025  
 3026  
 3027  
 3028  
 3029  
 3030  
 3031 010074 000004  
 3032 010076 012705 016110  
 3033 010102 004737 035464  
 3034  
 3035 010106 000413  
 3036  
 3037 010110  
 3038 010110 042177 000000  
 3039 010114 100200 000000  
 3040 010120 000000 000000  
 3041 010124 102177 000000  
 3042 010130 047553 047544  
 3043 010134 000000  
 3044  
 3045  
 3046  
 3047  
 3048  
 3049  
 3050  
 3051 010136 000004  
 3052 010140 012705 016152  
 3053 010144 004737 035464  
 3054  
 3055 010150 000413  
 3056  
 3057 010152

```

;*****
;TEST 237 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-4
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST237: SCOPE
MOV #MD2F4,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST240 ;;

MD2F4: ; TEST DATA SET MD2F-41
.WORD LGN,M1 ; INITIAL AC FLOAT NUMBER
.WORD 2X,MN,M1 ; INITIAL MEM FLOAT NUMBER
.WORD LGN,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD ALTP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047447,147447 ; FPS: BEFORE, AFTER
.WORD 100014 ; FEC AFTER ( 0 = N7A )

;*****
;TEST 240 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-5
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST240: SCOPE
MOV #MD2F5,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST241 ;;

MD2F5: ; TEST DATA SET MD2F-51
.WORD 042177,0 ; INITIAL AC FLOAT NUMBER
.WORD F1N,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 142177,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047553,047544 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 241 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-6
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST241: SCOPE
MOV #MD2F6,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST242 ;;

MD2F6: ; TEST DATA SET MD2F-61

```

3058 010152 040230 000000  
 3059 010156 100177 177777  
 3060 010162 100177 177777  
 3061 010166 000000 000000  
 3062 010172 047507 047510  
 3063 010176 000000  
 3064  
 3065  
 3066  
 3067  
 3068  
 3069  
 3070  
 3071 010200 000004  
 3072 010202 012705 016214  
 3073 010206 004737 035464  
 3074  
 3075 010212 000413  
 3076  
 3077 010214  
 3078 010214 102176 077600  
 3079 010220 100200 000000  
 3080 010224 037777 000000  
 3081 010230 042176 000000  
 3082 010234 047457 047440  
 3083 010240 000000  
 3084  
 3085  
 3086  
 3087  
 3088  
 3089  
 3090  
 3091 010242 000004  
 3092 010244 012705 016256  
 3093 010250 004737 035464  
 3094  
 3095 010254 000413  
 3096  
 3097 010256  
 3098 010256 042177 100000  
 3099 010262 042200 000000  
 3000 010266 000000 000000  
 3001 010272 042177 000000  
 3002 010276 047417 047400  
 3003 010302 000000  
 3004  
 3005  
 3006  
 3007  
 3008  
 3009  
 3010  
 3011 010304 000004  
 3012 010306 012705 016320  
 3013 010312 004737 035464

```

;*****
;TEST 242 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-7
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST242: SCOPE
MOV #MD2F7,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST243 ;;

MD2F7: ; TEST DATA SET MD2F-71
.WORD 142176,077600 ; INITIAL AC FLOAT NUMBER
.WORD F1N,0 ; INITIAL MEM FLOAT NUMBER
.WORD 037777,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 042176,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047457,047440 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 243 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-10
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST243: SCOPE
MOV #MD2F10,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST244 ;;

MD2F10: ; TEST DATA SET MD2F-101
.WORD 042177,0 ; INITIAL AC FLOAT NUMBER
.WORD F1P,0 ; INITIAL MEM FLOAT NUMBER
.WORD 040000,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 042177,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047417,047400 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;TEST 244 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-11
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST244: SCOPE
MOV #MD2F11,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST

```

```

3914
3915 016316 000413
3916
3917 016320
3918 016320 140200 000000
3919 016324 040377 177777
3920 016330 140177 177776
3921 016336 140200 000000
3922 016340 047547 047550
3923 016344 000000
3924
3925
3926
3927
3928
3929
3930
3931 016346 000004
3932 016350 012705 016362
3933 016354 004737 035464
3934
3935 016360 000413
3936
3937 016362
3938 016362 000452 125252
3939 016366 021700 000000
3940 016372 040177 177400
3941 016376 042177 000000
3942 016402 047517 047500
3943 016406 000000
3944
3945
3946
3947
3948
3949
3950
3951 016410 000004
3952 016412 012705 016424
3953 016416 004737 035464
3954
3955 016422 000413
3956
3957 016424
3958 016424 041400 000001
3959 016432 141377 177776
3960 016434 140177 177777
3961 016440 142177 000000
3962 016444 047547 047550
3963 016450 000000
3964
3965
3966
3967
3968
3969

```

```

BR TST245 ;;
MD2F11: ; TEST DATA SET MD2F-11:
.WORD 016316,0 ; INITIAL AC FLOAT NUMBER
.WORD 040377,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 140177,M2 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 016340,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047547,047550 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;*TEST 245 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-12
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST245: SCOPE
MOV #MD2F12,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST246 ;;

MD2F12: ; TEST DATA SET MD2F-12:
.WORD 060452,ALTN ; INITIAL AC FLOAT NUMBER
.WORD 021700,0 ; INITIAL MEM FLOAT NUMBER
.WORD 040177,UB ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 042177,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;*TEST 246 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-13
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST246: SCOPE
MOV #MD2F13,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST247 ;;

MD2F13: ; TEST DATA SET MD2F-13:
.WORD 041000,000001 ; INITIAL AC FLOAT NUMBER
.WORD 141377,M2 ; INITIAL MEM FLOAT NUMBER
.WORD 140177,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 142177,000000 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047547,047550 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;*TEST 247 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-14
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****

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3970
3971 016452 000004
3972 016454 012705 016466
3973 016460 004737 035464
3974
3975 016464 000413
3976
3977 016466
3978 016466 041000 000001
3979 016472 141377 177776
3980 016476 140200 000000
3981 016502 142177 000000
3982 016526 047507 047510
3983 016512 000000
3984
3985
3986
3987
3988
3989
3990
3991 016514 000004
3992 016516 012705 016530
3993 016522 004737 035464
3994
3995 016526 000413
3996
3997 016530
3998 016530 077600 000000
3999 016534 044452 125252
4000 016540 000000 000000
4001 016544 040452 125252
4002 016550 047411 147406
4003 016554 100010
4004
4005
4006
4007
4008
4009
4010
4011 016556 000004
4012 016560 012705 016572
4013 016564 004737 035464
4014
4015 016570 000413
4016
4017 016572
4018 016572 077600 000000
4019 016576 044452 125252
4020 016602 000000 000000
4021 016606 000000 000000
4022 016612 046411 046406
4023 016616 000000
4024
4025

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;*****
;*TEST 247 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-14
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST247: SCOPE
MOV #MD2F14,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST250 ;;

MD2F14: ; TEST DATA SET MD2F-14:
.WORD 041000,000001 ; INITIAL AC FLOAT NUMBER
.WORD 141377,M2 ; INITIAL MEM FLOAT NUMBER
.WORD 140200,000000 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 142177,000000 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047507,047510 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;*TEST 250 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-15
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST250: SCOPE
MOV #MD2F15,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST251 ;;

MD2F15: ; TEST DATA SET MD2F-15:
.WORD 077600,0 ; INITIAL AC FLOAT NUMBER
.WORD 044452,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 040452,ALTN ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047411,147406 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N/A )

;*****
;*TEST 251 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-16
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST251: SCOPE
MOV #MD2F16,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST252 ;;

MD2F16: ; TEST DATA SET MD2F-16:
.WORD 077600,0 ; INITIAL AC FLOAT NUMBER
.WORD 044452,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 046411,046406 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

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4026
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4031 016620 000004
4032 016622 012705 016634
4033 016626 004737 035464
4034
4035 016632 000413
4036
4037 016634
4038 016634 001577 177777
4039 016640 101000 000000
4040 016644 142377 177777
4041 016650 000000 000000
4042 016654 047547 147550
4043 016660 100012
4044
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4050
4051 016662 000004
4052 016664 012705 016676
4053 016670 004737 035464
4054
4055 016674 000413
4056
4057 016676
4058 016676 001577 177777
4059 016702 101000 000000
4060 016706 000000 000000
4061 016712 000000 000000
4062 016716 045553 045544
4063 016722 000000
4064
4065
4066

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```

))*****
)*TEST 252 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-17
)*
)* ALL INTERRUPT ENABLES ON
)* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
))*****
TST252: SCOPE
MOV #MD2F17,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST253 ;;
MD2F17: ; TEST DATA SET MD2F-17:
.WORD 001577,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101000,0 ; INITIAL MEM FLOAT NUMBER
.WORD 142377,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047547,147550 ; FPS: BEFORE, AFTER
.WORD 100012 ; FEC AFTER ( 0 = N7A )
))*****
))*****
)*TEST 253 TEST OF MODF(2 ACC) INSTR, DATA SET MD2F-20
)*
)* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
)* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
))*****
TST253: SCOPE
MOV #MD2F20,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2FT ; GO TEST
BR TST254 ;;
MD2F20: ; TEST DATA SET MD2F-20:
.WORD 001577,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101000,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 045553,045544 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
))*****
))*****

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4067
4068
4069
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4071
4072 016724 000004
4073 016726 012705 016740
4074 016732 004737 035666
4075
4076 016736 000423
4077
4078 016740
4079 016740 000000 000000 000000
4080 016746 000000 000000 000000
4081 016750 000000 000000 000000
4082 016756 000000 000000 000000
4083 016760 000000 000000 000000
4084 016766 000000 000000 000000
4085 016770 000000 000000 000000
4086 016776 000000 000000 000000
4087 017000 047653 047644
4088 017004 000000
4089
4090
4091
4092
4093
4094
4095
4096 017006 000004
4097 017010 012705 017022
4098 017014 004737 035666
4099
4100 017020 000423
4101
4102 017022
4103 017022 000177 177777 177777
4104 017030 177777 177777 177777
4105 017032 177777 177777 177777
4106 017040 177777 177777 177777
4107 017042 000000 000000 000000
4108 017050 000000 000000 000000
4109 017052 000000 000000 000000
4110 017060 000000 000000 000000
4111 017062 047713 047704
4112 017066 000000
4113
4114
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4116
4117
4118
4119
4120 017070 000004
4121 017072 012705 017104
4122 017076 004737 035666

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))*****
)*TEST 254 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-1
)*
)* ALL INTERRUPT ENABLES ON
)* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
))*****
TST254: SCOPE
MOV #MD2D1,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2DT ; GO TEST
BR TST255 ;;
MD2D1: ; TEST DATA SET MD2D-1:
.WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047653,047644 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
))*****
))*****
)*TEST 255 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-2
)*
)* ALL INTERRUPT ENABLES ON
)* LONG FLOAT, LONG INTEGER, ROUND MODES
))*****
TST255: SCOPE
MOV #MD2D2,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2DT ; GO TEST
BR TST256 ;;
MD2D2: ; TEST DATA SET MD2D-2:
.WORD 2X1MP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047713,047704 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )
))*****
))*****
)*TEST 256 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-3
)*
)* INTERRUPT ENABLE OFF, ALL OTHERS ON
)* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
))*****
TST256: SCOPE
MOV #MD2D3,R5 ; PTR TO TEST DATA SET
JSR PC,#MD2DT ; GO TEST

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4123
4124 017102 000423 BR TST257 ;
4125
4126 017104 MD2D3: ; TEST DATA SET MD2D-3:
4127 017104 077777 177777 177777 .WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4128 017112 177777 .WORD 2X1M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4129 017114 100177 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4130 017122 177777 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4131 017124 000000 000000 000000 .WORD 043653,043644 ; FPS: BEFORE, AFTER
4132 017132 000000 000000 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
4133 017134 000000 000000 000000
4134 017142 000000
4135 017144 043653 043644
4136 017150 000000
4137
4138
4139
4140
4141
4142
4143
4144 017152 000004
4145 017154 012705 017106
4146 017160 004737 035666
4147
4148 017164 000423 BR TST260 ;
4149
4150 017166 MD2D4: ; TEST DATA SET MD2D-4:
4151 017166 E77777 177777 177777 .WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4152 017174 177777 .WORD 2X1M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4153 017176 100177 177777 177777 .WORD LGP,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
4154 017204 177777 .WORD ALTP,M1,ALTN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4155 017206 077777 177777 177777 .WORD 047713,147713 ; FPS: BEFORE, AFTER
4156 017214 177777 .WORD 100014 ; FEC AFTER ( 0 = N/A )
4157 017215 052525 177777 125252
4158 017224 000000
4159 017226 047713 147713
4160 017232 100014
4161
4162
4163
4164
4165
4166
4167
4168 017234 000004
4169 017236 012705 017250
4170 017242 004737 035666
4171
4172 017246 000423 BR TST261 ;
4173
4174 017250 MD2D5: ; TEST DATA SET MD2D-5:
4175 017254 042177 000000 000000 .WORD 042177,0,0,0 ; INITIAL AC FLOAT NUMBER
4176 017256 000000 .WORD FIP,0,0,0 ; INITIAL MEM FLOAT NUMBER
4177 017260 000200 000000 000000
4178 017266 000000

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4179 017270 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4180 017276 000000 .WORD 042177,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4181 017300 042177 000000 000000 .WORD 047613,047604 ; FPS: BEFORE, AFTER
4182 017306 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
4183 017310 047613 047604
4184 017314 000000
4185
4186
4187
4188
4189
4190
4191
4192 017316 000004
4193 017320 012705 017332
4194 017324 004737 035666
4195
4196 017330 000423 BR TST262 ;
4197
4198 017332 MD2D6: ; TEST DATA SET MD2D-6:
4199 017332 100200 000000 000000 .WORD FIN,0,0,0 ; INITIAL AC FLOAT NUMBER
4200 017340 000000 .WORD 040177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4201 017342 040177 177777 177777 .WORD 140177,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
4202 017350 177777 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4203 017352 040177 177777 177777 .WORD 047747,047750 ; FPS: BEFORE, AFTER
4204 017360 177777 .WORD NA ; FEC AFTER ( 0 = N/A )
4205 017362 000000 000000
4206 017370 000000
4207 017372 047747 047750
4208 017376 000000
4209
4210
4211
4212
4213
4214
4215
4216 017400 000004
4217 017402 012705 017414
4218 017406 004737 035666
4219
4220 017412 000423 BR TST263 ;
4221
4222 017414 MD2D7: ; TEST DATA SET MD2D-7:
4223 017414 042176 077600 000000 .WORD 042176,077600,0,0 ; INITIAL AC FLOAT NUMBER
4224 017422 000000 .WORD FIN,0,0,0 ; INITIAL MEM FLOAT NUMBER
4225 017424 100200 000000 000000 .WORD 137777,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4226 017432 000000 .WORD 142176,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4227 017434 137777 000000 000000 .WORD 047607,047610 ; FPS: BEFORE, AFTER
4228 017442 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
4229 017444 142176 000000 000000
4230 017452 000000
4231 017454 047607 047610
4232 017460 000000
4233
4234

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4235 ;*****  
4236 ;*TEST 263 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-10  
4237 ;* ALL INTERRUPT ENABLES ON  
4238 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES  
4239 ;*****  
4240 TST263: SCOPE  
4241 MOV #MD2D10,R5 ; PTR TO TEST DATA SET  
4242 J&K PC,#MD2DT ; GO TEST  
4243  
4244 BR TST264 ;  
4245  
4246 MD2D10: ; TEST DATA SET MD2D-10:  
4247 .WORD 142177,M0,0,0 ; INITIAL AC FLOAT NUMBER  
4248 .WORD 142177,M0,0,0 ; INITIAL MEM FLOAT NUMBER  
4249 .WORD F1P,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
4250 .WORD 140000,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
4251 .WORD 142177,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
4252 .WORD 142177,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
4253 .WORD 047747,047750 ; FPS: BEFORE, AFTER  
4254 .WORD NA ; FEC AFTER ( 0 = N7A )  
4255  
4256  
4257  
4258  
4259 ;*****  
4260 ;*TEST 264 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-11  
4261 ;* ALL INTERRUPT ENABLES ON  
4262 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES  
4263 ;*****  
4264 TST264: SCOPE  
4265 MOV #MD2D11,R5 ; PTR TO TEST DATA SET  
4266 J&K PC,#MD2DT ; GO TEST  
4267  
4268 BR TST265 ;  
4269  
4270 MD2D11: ; TEST DATA SET MD2D-11:  
4271 .WORD F1N,0,0,0 ; INITIAL AC FLOAT NUMBER  
4272 .WORD 140377,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER  
4273 .WORD 040177,M1,M1,M2 ; EXPECTED FRACTION-PART FLOAT RESULT  
4274 .WORD F1P,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
4275 .WORD 047617,047600 ; FPS: BEFORE, AFTER  
4276 .WORD NA ; FEC AFTER ( 0 = N7A )  
4277  
4278  
4279  
4280  
4281  
4282  
4283 ;*****  
4284 ;*TEST 265 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-12  
4285 ;* ALL INTERRUPT ENABLES ON  
4286 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES  
4287 ;*****  
4288 TST265: SCOPE  
4289 MOV #MD2D12,R5 ; PTR TO TEST DATA SET  
4290 J&K PC,#MD2DT ; GO TEST  
4291  
4292 BR TST266 ;  
4293  
4294 MD2D12: ; TEST DATA SET MD2D-12:  
4295 .WORD 167452,AN,AN,AN ; INITIAL AC FLOAT NUMBER  
4296 .WORD 112700,0,0,0 ; INITIAL MEM FLOAT NUMBER  
4297 .WORD 040177,M1,M1,UB ; EXPECTED FRACTION-PART FLOAT RESULT  
4298 .WORD 042177,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
4299 .WORD 047757,047740 ; FPS: BEFORE, AFTER  
4300 .WORD NA ; FEC AFTER ( 0 = N7A )  
4301  
4302  
4303  
4304  
4305  
4306  
4307 ;*****  
4308 ;*TEST 266 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-13  
4309 ;* ALL INTERRUPT ENABLES ON  
4310 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES  
4311 ;*****  
4312 TST266: SCOPE  
4313 MOV #MD2D13,R5 ; PTR TO TEST DATA SET  
4314 J&K PC,#MD2DT ; GO TEST  
4315  
4316 BR TST267 ;  
4317  
4318 MD2D13: ; TEST DATA SET MD2D-13:  
4319 .WORD 041000,0,0,1 ; INITIAL AC FLOAT NUMBER  
4320 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER  
4321 .WORD 040177,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT  
4322 .WORD 040200,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT  
4323 .WORD 047657,047600 ; FPS: BEFORE, AFTER  
4324 .WORD NA ; FEC AFTER ( 0 = N7A )  
4325  
4326  
4327  
4328  
4329  
4330  
4331 ;*****  
4332 ;*TEST 267 TEST OF MODD(2 ACC) INSTR, DATA SET MD2D-14  
4333 ;* ALL INTERRUPT ENABLES ON  
4334 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES  
4335 ;*****  
4336 TST267: SCOPE  
4337 MOV #MD2D14,R5 ; PTR TO TEST DATA SET  
4338 J&K PC,#MD2DT ; GO TEST  
4339  
4340 BR TST270 ;  
4341  
4342 MD2D14: ; TEST DATA SET MD2D-14:  
4343 .WORD 041000,0,0,1 ; INITIAL AC FLOAT NUMBER  
4344 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER  
4345  
4346
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4347 020026 000000 000000 000000
4348 020034 000000
4349 020036 000000 000000
4350 020044 000000
4351 020046 047617 047600
4352 020052 000000
4353
4354
4355
4356
4357
4358
4359
4360 020054 000004
4361 020056 012705 020070
4362 020062 004737 035666
4363
4364 020066 000423
4365
4366 020070
4367 020070 140452 125252 125252 MD2D15: ; TEST DATA SET MD2D-15:
4368 020076 125252 .WORD 140452,AN,AN,AN ; INITIAL AC FLOAT NUMBER
4369 020100 077600 000000 000000 .WORD 077600,0,0,0 ; INITIAL MEM FLOAT NUMBER
4370 020106 000000
4371 020110 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4372 020116 000000
4373 020120 100052 125252 125252 .WORD 100052,AN,AN,AN ; EXPECTED INTEGER-PART FLOAT RESULT
4374 020126 125252
4375 020130 047651 147646 .WORD 047651,147646 ; FPS: BEFORE, AFTER
4376 020134 100010 .WORD 100010 ; FEC AFTER ( 0 = N7A )
4377
4378
4379
4380
4381
4382
4383
4384 020136 000004
4385 020140 012705 020152
4386 020144 004737 035666
4387
4388 020150 000423
4389
4390 020152
4391 020152 140452 125252 125252 MD2D16: ; TEST DATA SET MD2D-16:
4392 020160 125252 .WORD 140452,AN,AN,AN ; INITIAL AC FLOAT NUMBER
4393 020162 077600 000000 000000 .WORD 077600,0,0,0 ; INITIAL MEM FLOAT NUMBER
4394 020170 000000
4395 020172 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4396 020200 000000
4397 020202 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4398 020210 000000
4399 020212 046751 046746 .WORD 046751,046746 ; FPS: BEFORE, AFTER
4400 020216 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4401
4402

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4403
4404
4405
4406
4407
4408 020220 000004
4409 020222 012705 020234
4410 020226 004737 035666
4411
4412 020232 000423
4413
4414 020234
4415 020236 101577 177777 177777 MD2D17: ; TEST DATA SET MD2D-17:
4416 020242 177777 .WORD 101577,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4417 020244 101000 000000 000000 .WORD 101000,0,0,0 ; INITIAL MEM FLOAT NUMBER
4418 020252 000000
4419 020254 042377 177777 177777 .WORD 042377,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
4420 020262 177777
4421 020264 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4422 020272 000000
4423 020274 047617 147600 .WORD 047617,147600 ; FPS: BEFORE, AFTER
4424 020300 100012 .WORD 100012 ; FEC AFTER ( 0 = N7A )
4425
4426
4427
4428
4429
4430
4431
4432 020302 000004
4433 020304 012705 020316
4434 020310 004737 035666
4435
4436 020314 000423
4437
4438 020316
4439 020316 101577 177777 177777 MD2D20: ; TEST DATA SET MD2D-20:
4440 020324 177777 .WORD 101577,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4441 020326 101000 000000 000000 .WORD 101000,0,0,0 ; INITIAL MEM FLOAT NUMBER
4442 020334 000000
4443 020336 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4444 020344 000000
4445 020346 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4446 020354 000000
4447 020356 045713 045704 .WORD 045713,045704 ; FPS: BEFORE, AFTER
4448 020362 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4449
4450

```

```
4451 ;*****  
4452 ;*TEST 274 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-1  
4453 ;* ALL INTERRUPT ENABLES ON  
4454 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
4455 ;*****  
4456 020364 000004  
4457 020366 012705 020400  
4458 020372 004737 036130  
4459  
4460 020376 000413  
4461  
4462 020400  
4463 020400 000000 000000  
4464 020404 000000 000000  
4465 020410 000000 000000  
4466 020414 052525 177777  
4467 020420 047513 047504  
4468 020424 000000  
4469  
4470  
4471  
4472 ;*****  
4473 ;*TEST 275 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-2  
4474 ;* ALL INTERRUPT ENABLES ON  
4475 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
4476 ;*****  
4476 020426 000004  
4477 020430 012705 020442  
4478 020434 004737 036130  
4479  
4480 020440 000413  
4481  
4482 020442  
4483 020442 000177 177777  
4484 020446 077777 177777  
4485 020452 000000 000000  
4486 020456 052525 177777  
4487 020462 047553 047544  
4488 020466 000000  
4489  
4490  
4491 ;*****  
4492 ;*TEST 276 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-3  
4493 ;* ALL INTERRUPT ENABLES OFF, ALL OTHERS ON  
4494 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
4495 ;*****  
4496 020470 000004  
4497 020472 012705 020504  
4498 020476 004737 036130  
4499  
4500 020502 000413  
4501  
4502 020504  
4503 020504 177777 177777  
4504 020510 100177 177777  
4505 020514 000000 000000  
4506 020520 052525 177777
```

```
4507 020524 043413 043404  
4508 020530 000000  
4509  
4510  
4511 ;*****  
4512 ;*TEST 277 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-4  
4513 ;* ALL INTERRUPT ENABLES ON  
4514 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
4515 ;*****  
4516 020532 000004  
4517 020534 012705 020546  
4518 020540 004737 036130  
4519  
4520 020544 000413  
4521  
4522 020546  
4523 020546 177777 177777  
4524 020552 100177 177777  
4525 020556 177777 177777  
4526 020562 052525 177777  
4527 020566 047447 147447  
4528 020572 100014  
4529  
4530  
4531 ;*****  
4532 ;*TEST 300 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-5  
4533 ;* ALL INTERRUPT ENABLES ON  
4534 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
4535 ;*****  
4536 020574 000004  
4537 020576 012705 020610  
4538 020602 004737 036130  
4539  
4540 020606 000413  
4541  
4542 020610  
4543 020610 042177 000000  
4544 020614 100200 000000  
4545 020620 000000 000000  
4546 020624 052525 177777  
4547 020630 047553 047544  
4548 020634 000000  
4549  
4550  
4551 ;*****  
4552 ;*TEST 301 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-6  
4553 ;* ALL INTERRUPT ENABLES ON  
4554 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
4555 ;*****  
4556 020636 000004  
4557 020640 012705 020652  
4558 020644 004737 036130  
4559  
4560 020650 000413  
4561  
4562 020652
```

4563 020652 040200 000000  
4564 020656 140177 177777  
4565 020662 140177 177777  
4566 020666 052525 177777  
4567 020672 047507 047510  
4568 020676 000000  
4569  
4570  
4571

.WORD F1P,0 ; INITIAL AC FLOAT NUMBER  
.WORD 140177,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 140177,M1 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047507,047510 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 302 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=7  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
\*\*\*\*\*

4576 020700 000004  
4577 020702 012705 020714  
4578 020706 004737 036130  
4579  
4580 020712 000413  
4581

TST302: SCOPE  
MOV #MDIF7,R5 ; PTR TO TEST DATA SET  
JSR PC,#MDIFT ; GO TEST  
  
BR TST303 ;

4582 020714  
4583 020714 142176 077600  
4584 020720 140200 000000  
4585 020724 037777 000000  
4586 020730 052525 177777  
4587 020734 047457 047440  
4588 020740 000000  
4589  
4590

MDIF7: ; TEST DATA SET MDIF=7  
.WORD 142176,077600 ; INITIAL AC FLOAT NUMBER  
.WORD F1P,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 037777,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047457,047440 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 303 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=10  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
\*\*\*\*\*

4596 020742 040004  
4597 020744 012705 020756  
4598 020750 004737 036130  
4599  
4600 020754 000413  
4601

TST303: SCOPE  
MOV #MDIF10,R5 ; PTR TO TEST DATA SET  
JSR PC,#MDIFT ; GO TEST  
  
BR TST304 ;

4602 020756  
4603 020756 042177 100000  
4604 020762 040200 000000  
4605 020766 040000 000000  
4606 020772 052525 177777  
4607 020776 047417 047400  
4608 021002 000000  
4609  
4610  
4611

MDIF10: ; TEST DATA SET MDIF=10  
.WORD 042177,00 ; INITIAL AC FLOAT NUMBER  
.WORD F1P,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 040000,0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047417,047400 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 304 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=11  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
\*\*\*\*\*

4616 021004 040004  
4617 021006 012705 021020  
4618 021012 004737 036130

TST304: SCOPE  
MOV #MDIF11,R5 ; PTR TO TEST DATA SET  
JSR PC,#MDIFT ; GO TEST

4619  
4620 021016 000413  
4621  
4622 021020  
4623 021020 140200 000000  
4624 021024 004737 177777  
4625 021030 140177 177776  
4626 021034 052525 177777  
4627 021040 047547 047550  
4628 021044 000000  
4629  
4630  
4631  
4632  
4633  
4634  
4635

BR TST305 ;  
  
MDIF11: ; TEST DATA SET MDIF=11  
.WORD F1P,0 ; INITIAL AC FLOAT NUMBER  
.WORD 004737,M1 ; INITIAL MEM FLOAT NUMBER  
.WORD 140177,M2 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047547,047550 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 305 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=12  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, ROUND MODES  
\*\*\*\*\*

4636 021046 000004  
4637 021050 012705 021062  
4638 021054 004737 036130  
4639  
4640 021060 000413  
4641

TST305: SCOPE  
MOV #MDIF12,R5 ; PTR TO TEST DATA SET  
JSR PC,#MDIFT ; GO TEST  
  
BR TST306 ;

4642 021062  
4643 021062 060452 125252  
4644 021066 021700 000000  
4645 021072 040177 177400  
4646 021076 052525 177777  
4647 021102 047517 047500  
4648 021106 000000  
4649  
4650

MDIF12: ; TEST DATA SET MDIF=12  
.WORD 060452,ALTN ; INITIAL AC FLOAT NUMBER  
.WORD 021700,0 ; INITIAL MEM FLOAT NUMBER  
.WORD 040177,U0 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047517,047500 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 306 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=13  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
\*\*\*\*\*

4656 021110 000004  
4657 021112 012705 021124  
4658 021116 004737 036130  
4659  
4660 021122 000413  
4661

TST306: SCOPE  
MOV #MDIF13,R5 ; PTR TO TEST DATA SET  
JSR PC,#MDIFT ; GO TEST  
  
BR TST307 ;

4662 021124  
4663 021124 041000 000001  
4664 021130 141377 177776  
4665 021134 140177 177777  
4666 021140 052525 177777  
4667 021144 047547 047550  
4668 021150 000000  
4669  
4670  
4671

MDIF13: ; TEST DATA SET MDIF=13  
.WORD 041000,000001 ; INITIAL AC FLOAT NUMBER  
.WORD 141377,M2 ; INITIAL MEM FLOAT NUMBER  
.WORD 140177,M1 ; EXPECTED FRACTION-PART FLOAT RESULT  
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT  
.WORD 047547,047550 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N7A )

\*\*\*\*\*  
;\*TEST 307 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF=14  
;\* ALL INTERRUPT ENABLES ON  
;\* SHORT FLOAT, LONG INTEGER, ROUND MODES  
\*\*\*\*\*

4672  
4673  
4674



4675  
4676 021152 000004  
4677 021154 012705 021166  
4678 021160 004737 036130  
4679  
4680 021164 000413  
4681  
4682 021166  
4683 021166 041000 000001  
4684 021172 141377 177776  
4685 021176 140200 000000  
4686 021202 052525 177777  
4687 021206 047507 047510  
4688 021212 000000  
4689  
4690  
4691  
4692  
4693  
4694  
4695  
4696 021214 000004  
4697 021216 012705 021230  
4698 021222 004737 036130  
4699  
4700 021226 000413  
4701  
4702 021230  
4703 021230 077600 000000  
4704 021234 040452 125252  
4705 021240 000000 000000  
4706 021244 052525 177777  
4707 021250 047611 147406  
4708 021254 100010  
4709  
4710  
4711  
4712  
4713  
4714  
4715  
4716 021256 000004  
4717 021260 012705 021272  
4718 021264 004737 036130  
4719  
4720 021270 000413  
4721  
4722 021272  
4723 021272 077600 000000  
4724 021276 040452 125252  
4725 021302 000000 000000  
4726 021306 052525 177777  
4727 021312 046411 046406  
4728 021316 000000  
4729  
4730

```

;*****
TST307: SCOPE
MOV  #MDIF14,R5 ; PTR TO TEST DATA SET
JSR  PC,#MDIFT ; GO TEST
BR   TST310 ;

MDIF14: ; TEST DATA SET MDIF-14:
.WORD 041000,000001 ; INITIAL AC FLOAT NUMBER
.WORD 141377,M2 ; INITIAL MEM FLOAT NUMBER
.WORD 140200,000000 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047507,047510 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 310 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-15
;#
;# ALL INTERRUPT ENABLES ON
;# SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST310: SCOPE
MOV  #MDIF15,R5 ; PTR TO TEST DATA SET
JSR  PC,#MDIFT ; GO TEST
BR   TST311 ;

MDIF15: ; TEST DATA SET MDIF-15:
.WORD 077600,0 ; INITIAL AC FLOAT NUMBER
.WORD 040452,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047411,147406 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 311 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-16
;#
;# OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;# SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST311: SCOPE
MOV  #MDIF16,R5 ; PTR TO TEST DATA SET
JSR  PC,#MDIFT ; GO TEST
BR   TST312 ;

MDIF16: ; TEST DATA SET MDIF-16:
.WORD 077600,0 ; INITIAL AC FLOAT NUMBER
.WORD 040452,ALTN ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 046411,046406 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

4731  
4732  
4733  
4734  
4735  
4736 021320 000004  
4737 021322 012705 021334  
4738 021326 004737 036130  
4739  
4740 021332 000413  
4741  
4742 021334  
4743 021334 001577 177777  
4744 021340 101000 000000  
4745 021344 142377 177777  
4746 021350 052525 177777  
4747 021354 047547 147550  
4748 021360 100012  
4749  
4750  
4751  
4752  
4753  
4754  
4755  
4756 021362 000004  
4757 021364 012705 021376  
4758 021370 004737 036130  
4759  
4760 021374 000413  
4761  
4762 021376  
4763 021376 001577 177777  
4764 021402 101000 000000  
4765 021406 000000 000000  
4766 021412 052525 177777  
4767 021416 045553 045544  
4768 021422 000000  
4769  
4770  
4771

```

;*****
;TEST 312 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-17
;#
;# ALL INTERRUPT ENABLES ON
;# SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST312: SCOPE
MOV  #MDIF17,R5 ; PTR TO TEST DATA SET
JSR  PC,#MDIFT ; GO TEST
BR   TST313 ;

MDIF17: ; TEST DATA SET MDIF-17:
.WORD 001577,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101000,0 ; INITIAL MEM FLOAT NUMBER
.WORD 142377,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 047547,147550 ; FPS: BEFORE, AFTER
.WORD 100012 ; FEC AFTER ( 0 = N/A )

```

```

;*****
;TEST 313 TEST OF MODF(1 ACC) INSTR, DATA SET MDIF-20
;#
;# UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;# SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST313: SCOPE
MOV  #MDIF20,R5 ; PTR TO TEST DATA SET
JSR  PC,#MDIFT ; GO TEST
BR   TST314 ;

MDIF20: ; TEST DATA SET MDIF-20:
.WORD 001577,M1 ; INITIAL AC FLOAT NUMBER
.WORD 101000,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
.WORD AP,M1 ; EXPECTED INTEGER-PART FLOAT RESULT
.WORD 045553,045544 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

```

4772 ;*****
4773 ;*TEST 314 TEST OF MODD(1 ACC) INSTR, DATA SET MD10-1
4774 ;* ALL INTERRUPT ENABLES ON
4775 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
4776 ;*****
4777 021424 000004 TST314: SCOPE
4778 021426 012705 021440 MOV #MD10,R5 ; PTR TO TEST DATA SET
4779 021432 004737 036332 JSR PC,#MD10T ; GO TEST
4780
4781 021436 000423 BR TST315 ;;
4782
4783 021440 MD10: ; TEST DATA SET MD10-1
4784 021440 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
4785 021446 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
4786 021450 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4787 021456 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4788 021460 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4789 021470 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4790 021476 000000 000000 000000 .WORD 047653,047644 ; FPS: BEFORE, AFTER
4791 021480 000000 000000 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4792
4793 021500 000000
4794
4795
4796 ;*****
4797 ;*TEST 315 TEST OF MODD(1 ACC) INSTR, DATA SET MD10-2
4798 ;* ALL INTERRUPT ENABLES ON
4799 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
4800 ;*****
4801 021506 000004 TST315: SCOPE
4802 021510 012705 021522 MOV #MD10,R5 ; PTR TO TEST DATA SET
4803 021514 004737 036332 JSR PC,#MD10T ; GO TEST
4804
4805 021520 000423 BR TST316 ;;
4806
4807 021522 MD10: ; TEST DATA SET MD10-2
4808 021530 000177 177777 177777 .WORD ZX1M,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4809 021532 177777 177777 177777 .WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4810 021540 177777 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4811 021542 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4812 021550 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4813 021552 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4814 021560 000000 000000 000000 .WORD 047713,047704 ; FPS: BEFORE, AFTER
4815 021562 047713 047704 .WORD NA ; FEC AFTER ( 0 = N7A )
4816 021566 000000
4817
4818
4819
4820 ;*****
4821 ;*TEST 316 TEST OF MODD(1 ACC) INSTR, DATA SET MD10-3
4822 ;* =0 INTERRUPT ENABLE OFF, ALL OTHERS ON
4823 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
4824 ;*****
4825 021570 000004 TST316: SCOPE
4826 021572 012705 021604 MOV #MD10,R5 ; PTR TO TEST DATA SET
4827 021576 004737 036332 JSR PC,#MD10T ; GO TEST

```

```

4828
4829 021602 000423 BR TST317 ;;
4830
4831 021604 MD10: ; TEST DATA SET MD10-3
4832 021604 077777 177777 177777 .WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4833 021612 177777 177777 177777 .WORD ZX1M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4834 021614 100177 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4835 021622 177777 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4836 021624 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4837 021632 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4838 021634 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4839 021642 000000 000000 000000 .WORD 043653,043644 ; FPS: BEFORE, AFTER
4840 021644 043653 043644 .WORD NA ; FEC AFTER ( 0 = N7A )
4841 021650 000000
4842
4843
4844 ;*****
4845 ;*TEST 317 TEST OF MODD(1 ACC) INSTR, DATA SET MD10-4
4846 ;* ALL INTERRUPT ENABLES ON
4847 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
4848 ;*****
4849 021652 000004 TST317: SCOPE
4850 021654 012705 021666 MOV #MD10,R5 ; PTR TO TEST DATA SET
4851 021660 004737 036332 JSR PC,#MD10T ; GO TEST
4852
4853 021664 000423 BR TST320 ;;
4854
4855 021666 MD10: ; TEST DATA SET MD10-4
4856 021666 077777 177777 177777 .WORD LGP,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
4857 021674 177777 177777 177777 .WORD LGN,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4858 021676 100177 177777 177777 .WORD ZX1M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4859 021704 177777 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4860 021706 077777 177777 177777 .WORD LGP,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
4861 021714 177777 177777 177777 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
4862 021716 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
4863 021724 000000 000000 000000 .WORD 047713,147713 ; FPS: BEFORE, AFTER
4864 021726 047713 147713 .WORD 100014 ; FEC AFTER ( 0 = N7A )
4865 021732 100014
4866
4867
4868 ;*****
4869 ;*TEST 320 TEST OF MODD(1 ACC) INSTR, DATA SET MD10-5
4870 ;* ALL INTERRUPT ENABLES ON
4871 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
4872 ;*****
4873 021734 000004 TST320: SCOPE
4874 021736 012705 021750 MOV #MD10,R5 ; PTR TO TEST DATA SET
4875 021742 004737 036332 JSR PC,#MD10T ; GO TEST
4876
4877 021746 000423 BR TST321 ;;
4878
4879 021750 MD10: ; TEST DATA SET MD10-5
4880 021750 042177 000000 000000 .WORD 042177,0,0,0 ; INITIAL AC FLOAT NUMBER
4881 021756 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
4882 021762 000000 000000 000000 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
4883 021766 000000

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4884 021770 000000 000000 .WORD 0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
4885 021776 000000 .WORD AP,M1,AN,0 ; EXPECTED INTEGEN=PART FLOAT RESULT
4886 022000 052525 177777 125252 .WORD 047613,047604 ; FPS: BEFORE, AFTER
4887 022006 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4888 022010 047613 047604
4889 022014 000000
4890
4891
4892
4893
4894
4895
4896
4897 022016 000004 TST321: SCOPE
4898 022020 012705 022032 MOV #MD1D6,R5 ; PTR TO TEST DATA SET
4899 022024 004737 036332 JSR PC,#MD1DT ; GO TEST
4900
4901 022030 000423 BR TST322 ;;
4902
4903 022032 MD1D6: ; TEST DATA SET MD1D=6:
4904 022032 140200 000000 000000 .WORD F1N,0,0,0 ; INITIAL AC FLOAT NUMBER
4905 022040 000000 .WORD 040177,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4906 022042 040177 177777 177777 .WORD 140177,M1,M1,M1 ; EXPECTED FRACTION=PART FLOAT RESULT
4907 022050 177777 .WORD AP,M1,AN,0 ; EXPECTED INTEGEN=PART FLOAT RESULT
4908 022052 140177 177777 177777 .WORD 047747,047750 ; FPS: BEFORE, AFTER
4909 022060 177777 .WORD NA ; FEC AFTER ( 0 = N7A )
4910 022062 052525 177777 125252
4911 022070 000000
4912 022072 047747 047750
4913 022076 000000
4914
4915
4916
4917
4918
4919
4920
4921 022100 000004 TST322: SCOPE
4922 022102 012705 022114 MOV #MD1D7,R5 ; PTR TO TEST DATA SET
4923 022106 004737 036332 JSR PC,#MD1DT ; GO TEST
4924
4925 022112 000423 BR TST323 ;;
4926
4927 022114 MD1D7: ; TEST DATA SET MD1D=7:
4928 022114 042176 077600 000000 .WORD 042176,077600,0,0 ; INITIAL AC FLOAT NUMBER
4929 022122 000000 .WORD F1N,0,0,0 ; INITIAL MEM FLOAT NUMBER
4930 022124 140200 000000 000000 .WORD 137777,0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
4931 022132 000000 .WORD AP,M1,AN,0 ; EXPECTED INTEGEN=PART FLOAT RESULT
4932 022134 137777 000000 000000 .WORD 047607,047610 ; FPS: BEFORE, AFTER
4933 022142 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4934 022144 052525 177777 125252
4935 022152 000000
4936 022154 047607 047610
4937 022160 000000
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4940
4941
4942
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4944
4945 022162 000004 TST323: SCOPE
4946 022164 012705 022176 MOV #MD1D10,R5 ; PTR TO TEST DATA SET
4947 022170 004737 036332 JSR PC,#MD1DT ; GO TEST
4948
4949 022174 000423 BR TST324 ;;
4950
4951 022176 MD1D10: ; TEST DATA SET MD1D=10:
4952 022176 142177 100000 000000 .WORD 142177,M0,0,0 ; INITIAL AC FLOAT NUMBER
4953 022204 000000 .WORD F1P,0,0,0 ; INITIAL MEM FLOAT NUMBER
4954 022206 040200 000000 000000 .WORD 140000,0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
4955 022214 000000 .WORD AP,M1,AN,0 ; EXPECTED INTEGEN=PART FLOAT RESULT
4956 022216 140000 000000 000000 .WORD 047747,047750 ; FPS: BEFORE, AFTER
4957 022224 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
4958 022226 052525 177777 125252
4959 022234 000000
4960 022236 047747 047750
4961 022242 000000
4962
4963
4964
4965
4966
4967
4968
4969
4970 022244 000004 TST324: SCOPE
4971 022246 012705 022260 MOV #MD1D11,R5 ; PTR TO TEST DATA SET
4972 022252 004737 036332 JSR PC,#MD1DT ; GO TEST
4973
4974 022256 000423 BR TST325 ;;
4975
4976 022260 MD1D11: ; TEST DATA SET MD1D=11:
4977 022260 140200 000000 000000 .WORD F1N,0,0,0 ; INITIAL AC FLOAT NUMBER
4978 022270 140377 177777 177777 .WORD 140377,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
4979 022276 177777 .WORD 040177,M1,M1,M2 ; EXPECTED FRACTION=PART FLOAT RESULT
4980 022300 040177 177777 177777 .WORD AP,M1,AN,0 ; EXPECTED INTEGEN=PART FLOAT RESULT
4981 022306 177776 .WORD 047617,047600 ; FPS: BEFORE, AFTER
4982 022310 052525 177777 125252 .WORD NA ; FEC AFTER ( 0 = N7A )
4983 022316 000000
4984 022320 047617 047600
4985 022324 000000
4986
4987
4988
4989
4990
4991
4992
4993 022326 000004 TST325: SCOPE
4994 022330 012705 022342 MOV #MD1D12,R5 ; PTR TO TEST DATA SET
4995 022334 004737 036332 JSR PC,#MD1DT ; GO TEST

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4996
4997 022340 000423
4998
4999 022342
5000 022342 167452 125252 125252 MD1D12 ; TEST DATA SET MD1D-12;
5001 022350 125252 .WORD 167452,AN,AN,AN ; INITIAL AC FLOAT NUMBER
5002 022352 112700 000000 000000 .WORD 112700,0,0,0 ; INITIAL MEM FLOAT NUMBER
5003 022360 000000 .WORD 040177,M1,M1,UB ; EXPECTED FRACTION=PART FLOAT RESULT
5004 022362 040177 177777 177777 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5005 022370 177400
5006 022372 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5007 022400 000000
5008 022402 047757 047740 .WORD 047757,047740 ; FPS: BEFORE, AFTER
5009 022406 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5010
5011
5012
5013
5014
5015
5016
5017 022410 000004
5018 022412 012705 022424
5019 022416 004737 036332
5020
5021 022422 000423
5022
5023 022424
5024 022424 041000 000000 000000 MD1D13 ; TEST DATA SET MD1D-13;
5025 022432 000001 .WORD 041000,0,0,1 ; INITIAL AC FLOAT NUMBER
5026 022434 037577 177777 177777 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER
5027 022442 177776 .WORD 040177,M1,M1,M1 ; EXPECTED FRACTION=PART FLOAT RESULT
5028 022444 040177 177777 177777 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5029 022452 177777
5030 022454 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5031 022462 000000
5032 022464 047657 047640 .WORD 047657,047640 ; FPS: BEFORE, AFTER
5033 022470 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5034
5035
5036
5037
5038
5039
5040
5041 022472 000004
5042 022474 012705 022506
5043 022500 004737 036332
5044
5045 022504 000423
5046
5047 022506
5048 022506 041000 000000 000000 MD1D14 ; TEST DATA SET MD1D-14;
5049 022514 000001 .WORD 041000,0,0,1 ; INITIAL AC FLOAT NUMBER
5050 022516 037577 177777 177777 .WORD 037577,M1,M1,M2 ; INITIAL MEM FLOAT NUMBER
5051 022524 177776

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5052 022526 040200 000000 000000 .WORD 040200,0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
5053 022534 000000
5054 022536 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5055 022544 000000
5056 022546 047617 047600 .WORD 047617,047600 ; FPS: BEFORE, AFTER
5057 022552 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5058
5059
5060
5061
5062
5063
5064
5065 022554 000004
5066 022556 012705 022570
5067 022562 004737 036332
5068
5069 022566 000423
5070
5071 022570
5072 022570 142452 125252 125252 MD1D15 ; TEST DATA SET MD1D-15;
5073 022576 125252 .WORD 142452,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
5074 022600 077600 000000 000000 .WORD 077600,0,0,0 ; INITIAL MEM FLOAT NUMBER
5075 022606 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
5076 022610 000000 000000 000000 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5077 022616 000000
5078 022620 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5079 022626 000000
5080 022630 047651 147646 .WORD 047651,147646 ; FPS: BEFORE, AFTER
5081 022634 100010 .WORD 100010 ; FEC AFTER ( 0 = N/A )
5082
5083
5084
5085
5086
5087
5088
5089 022636 000004
5090 022640 012705 022652
5091 022644 004737 036332
5092
5093 022650 000423
5094
5095 022652
5096 022652 142452 125252 125252 MD1D16 ; TEST DATA SET MD1D-16;
5097 022660 125252 .WORD 142452,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
5098 022662 077600 000000 000000 .WORD 077600,0,0,0 ; INITIAL MEM FLOAT NUMBER
5099 022670 000000 .WORD 0,0,0,0 ; EXPECTED FRACTION=PART FLOAT RESULT
5100 022672 000000 000000 000000 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5101 022676 000000
5102 022702 052525 177777 125252 .WORD AP,M1,AN,0 ; EXPECTED INTEGER=PART FLOAT RESULT
5103 022710 000000
5104 022712 047651 047646 .WORD 047651,047646 ; FPS: BEFORE, AFTER
5105 022716 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5106
5107

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5100 ;*****
5109 ;*TEST 332 TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-17
5110 ;* ALL INTERRUPT ENABLES ON
5111 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
5112 ;*****
5113 TST332: SCOPE
5114 MOV #MD1D17,R5 ; PTR TO TEST DATA SET
5115 JBR PC,#MD1DT ; GO TEST
5116
5117 BR TST333 ;;
5118
5119 MD1D17: ; TEST DATA SET MD1D-17:
5120 .WORD 101577,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
5121 .WORD 101000,0,0,0 ; INITIAL MEM FLOAT NUMBER
5122 .WORD 042377,M1,M1,M1 ; EXPECTED FRACTION-PART FLOAT RESULT
5123 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
5124 .WORD 047617,147600 ; FPS: BEFORE, AFTER
5125 .WORD 100012 ; FEC AFTER ( 0 = N/A )
5126
5127
5128 ;*****
5129 ;*TEST 333 TEST OF MODD(1 ACC) INSTR, DATA SET MD1D-20
5130 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
5131 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
5132 ;*****
5133 TST333: SCOPE
5134 MOV #MD1D20,R5 ; PTR TO TEST DATA SET
5135 JBR PC,#MD1DT ; GO TEST
5136
5137 BR TST334 ;;
5138
5139 MD1D20: ; TEST DATA SET MD1D-20:
5140 .WORD 101577,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
5141 .WORD 101000,0,0,0 ; INITIAL MEM FLOAT NUMBER
5142 .WORD 0,0,0,0 ; EXPECTED FRACTION-PART FLOAT RESULT
5143 .WORD AP,M1,AN,0 ; EXPECTED INTEGER-PART FLOAT RESULT
5144 .WORD 045713,045704 ; FPS: BEFORE, AFTER
5145 .WORD NA ; FEC AFTER ( 0 = N/A )
5146
5147
5148 ;*****
5149 ;*TEST 334 TEST OF LDCDF INSTR, DATA SET LCDF-1
5150 ;* ALL INTERRUPT ENABLES ON
5151 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
5152 ;*****
5153 TST334: SCOPE
5154 MOV #LCDF1,R5 ; PTR TO TEST DATA SET
5155 JBR PC,#LCDF1 ; GO TEST
5156
5157 BR TST335 ;;
5158
5159 LCDF1: ; TEST DATA SET LCDF-1:
5160 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
5161 .WORD 0,0,0 ; EXPECTED FLOAT RESULT
5162 .WORD 047413,047404 ; FPS: BEFORE, AFTER
5163 .WORD NA ; FEC AFTER ( 0 = N/A )
5164
5165
5166 ;*****
5167 ;*TEST 335 TEST OF LDCDF INSTR, DATA SET LCDF-4
5168 ;* ALL INTERRUPT ENABLES ON
5169 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5170 ;*****
5171 TST335: SCOPE
5172 MOV #LCDF2,R5 ; PTR TO TEST DATA SET
5173 JBR PC,#LCDF2 ; GO TEST
5174
5175 BR TST336 ;;
5176
5177 LCDF2: ; TEST DATA SET LCDF-2:
5178 .WORD Z14M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
5179 .WORD ALTP,M1 ; EXPECTED FLOAT RESULT
5180 .WORD 047503,147514 ; FPS: BEFORE, AFTER
5181 .WORD 100014 ; FEC AFTER ( 0 = N/A )
5182
5183
5184 ;*****
5185 ;*TEST 336 TEST OF LDCDF INSTR, DATA SET LCDF-3
5186 ;* ALL INTERRUPT ENABLES ON
5187 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
5188 ;*****
5189 TST336: SCOPE
5190 MOV #LCDF3,R5 ; PTR TO TEST DATA SET
5191 JBR PC,#LCDF3 ; GO TEST
5192
5193 BR TST337 ;;
5194
5195 LCDF3: ; TEST DATA SET LCDF-3:
5196 .WORD Z14M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
5197 .WORD 0,0 ; EXPECTED FLOAT RESULT
5198 .WORD 047453,047444 ; FPS: BEFORE, AFTER
5199 .WORD NA ; FEC AFTER ( 0 = N/A )
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5156 ;*****
5157 ;*TEST 334 TEST OF LDCDF INSTR, DATA SET LCDF-1
5158 ;* ALL INTERRUPT ENABLES ON
5159 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
5160 ;*****
5161 TST334: SCOPE
5162 MOV #LCDF1,R5 ; PTR TO TEST DATA SET
5163 JBR PC,#LCDF1 ; GO TEST
5164
5165 BR TST335 ;;
5166
5167 LCDF1: ; TEST DATA SET LCDF-1:
5168 .WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
5169 .WORD 0,0,0 ; EXPECTED FLOAT RESULT
5170 .WORD 047413,047404 ; FPS: BEFORE, AFTER
5171 .WORD NA ; FEC AFTER ( 0 = N/A )
5172
5173
5174 ;*****
5175 ;*TEST 335 TEST OF LDCDF INSTR, DATA SET LCDF-4
5176 ;* ALL INTERRUPT ENABLES ON
5177 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5178 ;*****
5179 TST335: SCOPE
5180 MOV #LCDF2,R5 ; PTR TO TEST DATA SET
5181 JBR PC,#LCDF2 ; GO TEST
5182
5183 BR TST336 ;;
5184
5185 LCDF2: ; TEST DATA SET LCDF-2:
5186 .WORD Z14M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
5187 .WORD ALTP,M1 ; EXPECTED FLOAT RESULT
5188 .WORD 047503,147514 ; FPS: BEFORE, AFTER
5189 .WORD 100014 ; FEC AFTER ( 0 = N/A )
5190
5191
5192 ;*****
5193 ;*TEST 336 TEST OF LDCDF INSTR, DATA SET LCDF-3
5194 ;* ALL INTERRUPT ENABLES ON
5195 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
5196 ;*****
5197 TST336: SCOPE
5198 MOV #LCDF3,R5 ; PTR TO TEST DATA SET
5199 JBR PC,#LCDF3 ; GO TEST
5200
5201 BR TST337 ;;
5202
5203 LCDF3: ; TEST DATA SET LCDF-3:
5204 .WORD Z14M,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
5205 .WORD 0,0 ; EXPECTED FLOAT RESULT
5206 .WORD 047453,047444 ; FPS: BEFORE, AFTER
5207 .WORD NA ; FEC AFTER ( 0 = N/A )
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5218 023216 000004
5219 023220 012705 023232
5220 023224 004737 036574
5221
5222 023230 000411
5223
5224 023232
5225 023232 040200 000000 000000
5226 023240 000000
5227 023242 040200 000000
5228 023246 047557 047540
5229 023252 000000
5230
5231
5232
5233
5234
5235
5236
5237 023254 000004
5238 023256 012705 023270
5239 023262 004737 036574
5240
5241 023266 000411
5242
5243 023270
5244 023274 140200 000000 100000
5245 023276 000000
5246 023300 140200 000001
5247 023304 047407 047410
5248 023310 000000
5249
5250
5251
5252
5253
5254
5255
5256 023312 000004
5257 023314 012705 023326
5258 023320 004737 036574
5259
5260 023324 000411
5261
5262 023326
5263 023326 140200 000000 100000
5264 023334 000000
5265 023336 140200 000000
5266 023342 047447 047450
5267 023346 000000

;*****
;TEST 337 TEST OF LCDF INSTR, DATA SET LCDF-3
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST337: SCOPE
MOV #LCDF4,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF4 ; GO TEST
BR TST340 ;;

LCDF4: ; TEST DATA SET LCDF-4:
.WORD F1P,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD F1P,0 ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )

;*****
;TEST 340 TEST OF LCDF INSTR, DATA SET LCDF-5
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST340: SCOPE
MOV #LCDF5,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF5 ; GO TEST
BR TST341 ;;

LCDF5: ; TEST DATA SET LCDF-5:
.WORD F1N,0,M0,0 ; INITIAL MEM FLOAT NUMBER
.WORD F1N,1 ; EXPECTED FLOAT RESULT
.WORD 047407,047410 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )

;*****
;TEST 341 TEST OF LCDF INSTR, DATA SET LCDF-6
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST341: SCOPE
MOV #LCDF6,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF6 ; GO TEST
BR TST342 ;;

LCDF6: ; TEST DATA SET LCDF-6:
.WORD F1N,0,M0,0 ; INITIAL MEM FLOAT NUMBER
.WORD F1N,0 ; EXPECTED FLOAT RESULT
.WORD 047447,047450 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )

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5268
5269
5270
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5272
5273
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5275 023350 000004
5276 023352 012705 023364
5277 023356 004737 036574
5278
5279 023362 000411
5280
5281 023364
5282 023364 077777 177777 177777
5283 023372 177777
5284 023374 000000 000000
5285 023400 047511 147506
5286 023404 100010
5287
5288
5289
5290
5291
5292
5293
5294 023406 000004
5295 023410 012705 023422
5296 023414 004737 036574
5297
5298 023420 000411
5299
5300 023422
5301 023422 077777 177777 177777
5302 023430 177777
5303 023432 077777 177777
5304 023436 047557 047540
5305 023442 000000
5306
5307
5308
5309
5310
5311
5312
5313 023444 000004
5314 023446 012705 023460
5315 023452 004737 036574
5316
5317 023456 000411
5318
5319 023460
5320 023460 121177 177777 100000
5321 023466 000000
5322 023470 121200 000000
5323 023474 047407 047410

;*****
;TEST 342 TEST OF LCDF INSTR, DATA SET LCDF-7
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST342: SCOPE
MOV #LCDF7,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF7 ; GO TEST
BR TST343 ;;

LCDF7: ; TEST DATA SET LCDF-7:
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047511,147506 ; FPS: BEFORE, AFTER
.WORD 100010 ; FLC AFTER ( 0 = N/A )

;*****
;TEST 343 TEST OF LCDF INSTR, DATA SET LCDF-10
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST343: SCOPE
MOV #LCDF10,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF10 ; GO TEST
BR TST344 ;;

LCDF10: ; TEST DATA SET LCDF-10:
.WORD LGP,M1,M1,M1 ; INITIAL MEM FLOAT NUMBER
.WORD LGP,M1 ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER
.WORD NA ; FLC AFTER ( 0 = N/A )

;*****
;TEST 344 TEST OF LCDF INSTR, DATA SET LCDF-11
; ALL INTERRUPT ENABLES ON
; SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST344: SCOPE
MOV #LCDF11,R5 ; PTR TO TEST DATA SET
JSR PC,#LCDF11 ; GO TEST
BR TST345 ;;

LCDF11: ; TEST DATA SET LCDF-11:
.WORD 121177,M1,M0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 121200,0 ; EXPECTED FLOAT RESULT
.WORD 047407,047410 ; FPS: BEFORE, AFTER

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5324 023500 000000          ,WORD NA          ; FEC AFTER ( 0 = N/A )
5325
5326
5327
5328 ;*****
5329 ;*TEST 345 TEST OF LDCDF INSTR, DATA SET LDCDF-12
5330 ;* ALL INTERRUPT ENABLES ON
5331 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
5332 ;*****
5332 023502 000004          TST345: SCOPE
5333 023504 012705 023516      MOV          #LDCDF12,R5      ; PTR TO TEST DATA SET
5334 023510 004737 036574      JSR          PC,#LDCDF1      ; GO TEST
5335
5336 023514 000411          BR          TST346          ;;
5337
5338 023516          LDCDF12: ; TEST DATA SET LDCDF-12:
5339 023516 121177 177777 100000 ,WORD      121177,M1,M0,0      ; INITIAL MEM FLOAT NUMBER
5340 023524 000000          ,WORD      0,0              ; EXPECTED FLOAT RESULT
5341 023526 121177 177777      ,WORD      047447,047450      ; FPS: BEFORE, AFTER
5342 023532 047447 047450      ,WORD      NA                ; FEC AFTER ( 0 = N/A )
5343 023536 000000
5344
5345
5346 ;*****
5347 ;*TEST 346 TEST OF LDCDF INSTR, DATA SET LDCDF-13
5348 ;* ALL INTERRUPT ENABLES ON
5349 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5350 ;*****
5351 023540 000004          TST346: SCOPE
5352 023542 012705 023554      MOV          #LDCDF13,R5      ; PTR TO TEST DATA SET
5353 023546 004737 036574      JSR          PC,#LDCDF1      ; GO TEST
5354
5355 023552 000411          BR          TST347          ;;
5356
5357 023554          LDCDF13: ; TEST DATA SET LDCDF-13:
5358 023554 040200 000000 077777 ,WORD      FIP,0,LGP,M1      ; INITIAL MEM FLOAT NUMBER
5359 023562 177777          ,WORD      0,0              ; EXPECTED FLOAT RESULT
5360 023564 040200 000000      ,WORD      047517,047500      ; FPS: BEFORE, AFTER
5361 023570 047517 047500      ,WORD      NA                ; FEC AFTER ( 0 = N/A )
5362 023574 000000
5363
5364
5365 ;*****
5366 ;*TEST 347 TEST OF LDCDF INSTR, DATA SET LDCDF-14
5367 ;* ALL INTERRUPT ENABLES ON
5368 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
5369 ;*****
5370 023576 000004          TST347: SCOPE
5371 023600 012705 023612      MOV          #LDCDF14,R5      ; PTR TO TEST DATA SET
5372 023604 004737 036574      JSR          PC,#LDCDF1      ; GO TEST
5373
5374 023610 000411          BR          TST350          ;;
5375
5376 023612          LDCDF14: ; TEST DATA SET LDCDF-14:
5377 023612 040200 000000 077777 ,WORD      FIP,0,LGP,M1      ; INITIAL MEM FLOAT NUMBER
5378 023620 177777          ,WORD      0,0              ; EXPECTED FLOAT RESULT
5379 023622 040200 000000

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5380 023626 047557 047540      ,WORD      047557,047540      ; FPS: BEFORE, AFTER
5381 023632 000000          ,WORD      NA                ; FEC AFTER ( 0 = N/A )
5382
5383
5384 ;*****
5385 ;*TEST 350 TEST OF LDCDF INSTR, DATA SET LDCDF-15
5386 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
5387 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5388 ;*****
5389 023634 000004          TST350: SCOPE
5390 023636 012705 023650      MOV          #LDCDF15,R5      ; PTR TO TEST DATA SET
5391 023642 004737 036574      JSR          PC,#LDCDF1      ; GO TEST
5392
5393 023646 000411          BR          TST351          ;;
5394
5395 023650          LDCDF15: ; TEST DATA SET LDCDF-15:
5396 023650 177777 177777 100000 ,WORD      LGN,M1,M0,0      ; INITIAL MEM FLOAT NUMBER
5397 023656 000000          ,WORD      0,0              ; EXPECTED FLOAT RESULT
5398 023660 000000 000000      ,WORD      046511,046506      ; FPS: BEFORE, AFTER
5399 023664 046511 046506      ,WORD      NA                ; FEC AFTER ( 0 = N/A )
5400 023670 000000
5401
5402
5403 ;*****
5404 ;*TEST 351 TEST OF LDCDF INSTR, DATA SET LDCDF-16
5405 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
5406 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
5407 ;*****
5408 023672 000004          TST351: SCOPE
5409 023674 012705 023706      MOV          #LDCDF16,R5      ; PTR TO TEST DATA SET
5410 023700 004737 036574      JSR          PC,#LDCDF1      ; GO TEST
5411
5412 023704 000411          BR          TST352          ;;
5413
5414 023706          LDCDF16: ; TEST DATA SET LDCDF-16:
5415 023706 100000 177777 177777 ,WORD      M0,M1,M1,0      ; INITIAL MEM FLOAT NUMBER
5416 023714 000000          ,WORD      0,0              ; EXPECTED FLOAT RESULT
5417 023716 000000 000000      ,WORD      043453,043444      ; FPS: BEFORE, AFTER
5418 023722 043453 043444      ,WORD      NA                ; FEC AFTER ( 0 = N/A )
5419 023726 000000
5420
5421
5422

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5423 ;*****
5424 ;*TEST 352 TEST OF LDCFD INSTR, DATA SET LCFD-1
5425 ;* ALL INTERRUPT ENABLES ON
5426 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
5427 ;*****
5428 TST352: SCOPE
5429 MOV #LCFD1,R5 ; PTR TO TEST DATA SET
5430 JSR PC,#LCFDT ; GU TEST
5431 BR TST353 ;
5432
5433 LCFD1: ; TEST DATA SET LCFD-1:
5434 .WORD M0,0 ; INITIAL MEM FLOAT NUMBER
5435 .WORD ALTP,M1,ALTN,0 ; EXPECTED FLOAT RESULT
5436
5437 .WORD 047643,047654 ; FPS: BEFORE, AFTER
5438 .WORD 100014 ; FEC AFTER ( 0 = N7A )
5439
5440 ;*****
5441 ;*TEST 353 TEST OF LDCFD INSTR, DATA SET LCFD-2
5442 ;* ALL INTERRUPT ENABLES ON
5443 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
5444 ;*****
5445 TST353: SCOPE
5446 MOV #LCFD2,R5 ; PTR TO TEST DATA SET
5447 JSR PC,#LCFDT ; GU TEST
5448 BR TST354 ;
5449
5450 LCFD2: ; TEST DATA SET LCFD-2:
5451 .WORD ALIN,ALTN ; INITIAL MEM FLOAT NUMBER
5452 .WORD ALTN,ALTN,0,0 ; EXPECTED FLOAT RESULT
5453
5454 .WORD 047607,047610 ; FPS: BEFORE, AFTER
5455 .WORD NA ; FEC AFTER ( 0 = N7A )
5456
5457 ;*****
5458 ;*TEST 354 TEST OF LDCFD INSTR, DATA SET LCFD-3
5459 ;* ALL INTERRUPT ENABLES ON
5460 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
5461 ;*****
5462 TST354: SCOPE
5463 MOV #LCFD3,R5 ; PTR TO TEST DATA SET
5464 JSR PC,#LCFDT ; GU TEST
5465 BR TST355 ;
5466
5467 LCFD3: ; TEST DATA SET LCFD-3:
5468 .WORD 0,0 ; INITIAL MEM FLOAT NUMBER
5469 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
5470
5471 .WORD 047753,047744 ; FPS: BEFORE, AFTER
5472 .WORD NA ; FEC AFTER ( 0 = N7A )
5473
5474
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5479 ;*****
5480 ;*TEST 355 TEST OF LDCFD INSTR, DATA SET LCFD-4
5481 ;* ALL INTERRUPT ENABLES ON
5482 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
5483 ;*****
5484 TST355: SCOPE
5485 MOV #LCFD4,R5 ; PTR TO TEST DATA SET
5486 JSR PC,#LCFDT ; GU TEST
5487 BR TST356 ;
5488
5489 LCFD4: ; TEST DATA SET LCFD-4:
5490 .WORD LGP,M1 ; INITIAL MEM FLOAT NUMBER
5491 .WORD LGP,M1,0,0 ; EXPECTED FLOAT RESULT
5492
5493 .WORD 047717,047700 ; FPS: BEFORE, AFTER
5494 .WORD NA ; FEC AFTER ( 0 = N7A )
5495
5496 ;*****
5497 ;*TEST 356 TEST OF LDCFD INSTR, DATA SET LCFD-5
5498 ;* ALL INTERRUPT ENABLES ON
5499 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
5500 ;*****
5501 TST356: SCOPE
5502 MOV #LCFD5,R5 ; PTR TO TEST DATA SET
5503 JSR PC,#LCFDT ; GU TEST
5504 BR TST357 ;
5505
5506 LCFD5: ; TEST DATA SET LCFD-5:
5507 .WORD ZXIMP,M1 ; INITIAL MEM FLOAT NUMBER
5508 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
5509
5510 .WORD 047653,047644 ; FPS: BEFORE, AFTER
5511 .WORD NA ; FEC AFTER ( 0 = N7A )
5512
5513 ;*****
5514 ;*TEST 357 TEST OF LDCFD INSTR, DATA SET LCFD-6
5515 ;* ALL INTERRUPT ENABLES ON
5516 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
5517 ;*****
5518 TST357: SCOPE
5519 MOV #LCFD6,R5 ; PTR TO TEST DATA SET
5520 JSR PC,#LCFDT ; GU TEST
5521 BR TST358 ;
5522
5523 LCFD6: ; TEST DATA SET LCFD-6:
5524 .WORD LGN,M1 ; INITIAL MEM FLOAT NUMBER
5525 .WORD LGN,M1,0,0 ; EXPECTED FLOAT RESULT
5526
5527 .WORD 047607,047610 ; FPS: BEFORE, AFTER
5528 .WORD NA ; FEC AFTER ( 0 = N7A )
5529
5530
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5533
5534

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5535
5536
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5540
5541
5542 024214 000004
5543 024216 012705 024230
5544 024222 004737 036744
5545
5546 024226 000411
5547
5548 024230
5549 024230 100177 177777
5550 024234 000000 000000 000000
5551 024242 000000
5552 024244 043753 043744
5553 024250 000000
5554
5555
5556
5557
5558
5559
5560
5561 024252 000004
5562 024254 012705 024266
5563 024260 004737 036744
5564
5565 024264 000411
5566
5567 024266
5568 024266 007417 007417
5569 024272 007417 007417 000000
5570 024300 000000
5571 024302 047717 047700
5572 024306 000000
5573
5574
5575

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```

*****
; *TEST 300 TEST OF LDCFD INSTR, DATA SET LCFD-0
; *
; * ALL INTERRUPT ENABLES OFF, ALL OTHERS ON
; * LONG FLOAT, LONG INTEGER, TRUNCATE MODES
; *
; *
TST300! SCOPE
MOV 0LCFD7,R5 ; PTR TO TEST DATA SET
JSR PC,0LCFD7 ; GO TEST
BR TST301 ;;

LCFD7: ; TEST DATA SET LCFD-7
.WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 043753,043744 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
*****

; *TEST 361 TEST OF LDCFD INSTR, DATA SET LCFD-10
; *
; * ALL INTERRUPT ENABLES ON
; * LONG FLOAT, LONG INTEGER, ROUND MODES
; *
; *
TST361! SCOPE
MOV 0LCFD10,R5 ; PTR TO TEST DATA SET
JSR PC,0LCFD10 ; GO TEST
BR TST362 ;;

LCFD10: ; TEST DATA SET LCFD-10
.WORD 0,0,0,0 ; INITIAL MEM FLOAT NUMBER
.WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
.WORD 047717,047700 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

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5576
5577
5578
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5580
5581 024310 000004
5582 024312 012705 024324
5583 024316 004737 037134
5584
5585 024322 000411
5586
5587 024324
5588 024324 000000 000000 000000
5589 024332 000000
5590 024334 000000 000000
5591 024340 047753 047744
5592 024344 000000
5593
5594
5595
5596
5597
5598
5599
5600 024346 000004
5601 024350 012705 024362
5602 024354 004737 037134
5603
5604 024360 000411
5605
5606 024362
5607 024362 140200 000000 100000
5608 024370 000000
5609 024372 140200 000001
5610 024376 047707 047710
5611 024402 000000
5612
5613
5614
5615
5616
5617
5618
5619 024404 000004
5620 024406 012705 024420
5621 024412 004737 037134
5622
5623 024416 000411
5624
5625 024420
5626 024420 040200 000000 100000
5627 024426 000000
5628 024430 040200 000000
5629 024434 047657 047640
5630 024440 000000
5631

```

```

*****
; *TEST 362 TEST OF SCDF INSTR, DATA SET SCDF-1
; *
; * ALL INTERRUPT ENABLES ON
; * LONG FLOAT, LONG INTEGER, TRUNCATE MODES
; *
; *
TST362! SCOPE
MOV 0SCDF1,R5 ; PTR TO TEST DATA SET
JSR PC,0SCDF1 ; GO TEST
BR TST363 ;;

SCDF1: ; TEST DATA SET SCDF-1
.WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047753,047744 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
*****

; *TEST 363 TEST OF SCDF INSTR, DATA SET SCDF-2
; *
; * ALL INTERRUPT ENABLES ON
; * LONG FLOAT, LONG INTEGER, ROUND MODES
; *
; *
TST363! SCOPE
MOV 0SCDF2,R5 ; PTR TO TEST DATA SET
JSR PC,0SCDF2 ; GO TEST
BR TST364 ;;

SCDF2: ; TEST DATA SET SCDF-2
.WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047707,047710 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
*****

; *TEST 364 TEST OF SCDF INSTR, DATA SET SCDF-3
; *
; * ALL INTERRUPT ENABLES ON
; * LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
; *
; *
TST364! SCOPE
MOV 0SCDF3,R5 ; PTR TO TEST DATA SET
JSR PC,0SCDF3 ; GO TEST
BR TST365 ;;

SCDF3: ; TEST DATA SET SCDF-3
.WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047657,047640 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

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5632
5633
5634
5635
5636
5637
5638 024442 000004
5639 024444 012705 024456
5640 024450 004737 037134
5641
5642 024454 000411
5643
5644
5645 024456 000177 177777 177777
5646 024464 177777
5647 024466 000000 000000
5648 024472 047013 047604
5649 024476 000000
5650
5651
5652
5653
5654
5655
5656
5657 024500 000004
5658 024502 012705 024514
5659 024506 004737 037134
5660
5661 024512 000411
5662
5663 024514
5664 024514 040200 000000 100000
5665 024522 000000
5666 024524 040200 000001
5667 024530 047717 047700
5668 024534 000000
5669
5670
5671
5672
5673
5674
5675
5676 024536 000004
5677 024540 012705 024552
5678 024544 004737 037134
5679
5680 024550 000411
5681
5682 024552
5683 024552 177777 177777 177777
5684 024560 177777
5685 024562 177777 177777
5686 024566 047747 047750
5687 024572 000000

;*****
;*TEST 365 TEST OF STCDF INSTR, DATA SET SCDF-3
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST365: SCOPE
MOV #SCDF4,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST366 ;;

SCDF4: ; TEST DATA SET SCDF-4:
.WORD 2X1M0,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD 047613,047604 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 366 TEST OF STCDF INSTR, DATA SET SCDF-3
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST366: SCOPE
MOV #SCDF5,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST367 ;;

SCDF5: ; TEST DATA SET SCDF-5:
.WORD F1P,0,M0,0 ; INITIAL AC FLOAT NUMBER
.WORD F1P,1 ; EXPECTED FLOAT RESULT
.WORD 047717,047700 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 367 TEST OF STCDF INSTR, DATA SET SCDF-0
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST367: SCOPE
MOV #SCDF6,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST370 ;;

SCDF6: ; TEST DATA SET SCDF-6:
.WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD LGN,M1 ; EXPECTED FLOAT RESULT
.WORD 047747,047750 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

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5688
5689
5690
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5692
5693
5694
5695 024574 000004
5696 024576 012705 024610
5697 024602 004737 037134
5698
5699 024606 000411
5700
5701 024610
5702 024613 040200 000000 077777
5703 024616 177777
5704 024620 040200 000000
5705 024623 047017 047600
5706 024630 000000
5707
5708
5709
5710
5711
5712
5713
5714 024632 000004
5715 024634 012705 024646
5716 024640 004737 037134
5717
5718 024644 000411
5719
5720 024646
5721 024646 177777 177777 177777
5722 024654 177777
5723 024656 100000 000000
5724 024662 047001 147616
5725 024666 100010
5726
5727
5728
5729
5730
5731
5732
5733 024670 000004
5734 024672 012705 024704
5735 024676 004737 037134
5736
5737 024702 000411
5738
5739 024704
5740 024704 040200 000000 077777
5741 024712 177777
5742 024714 040200 000000
5743 024720 047757 047740

;*****
;*TEST 370 TEST OF STCDF INSTR, DATA SET SCDF-7
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST370: SCOPE
MOV #SCDF7,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST371 ;;

SCDF7: ; TEST DATA SET SCDF-7:
.WORD F1P,0,LGP,M1 ; INITIAL AC FLOAT NUMBER
.WORD F1P,0 ; EXPECTED FLOAT RESULT
.WORD 047617,047600 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 371 TEST OF STCDF INSTR, DATA SET SCDF-10
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST371: SCOPE
MOV #SCDF10,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST372 ;;

SCDF10: ; TEST DATA SET SCDF-10:
.WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD M0,0 ; EXPECTED FLOAT RESULT
.WORD 047601,147616 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 372 TEST OF STCDF INSTR, DATA SET SCDF-11
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST372: SCOPE
MOV #SCDF11,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDF1 ; GO TEST
BR TST373 ;;

SCDF11: ; TEST DATA SET SCDF-11:
.WORD F1P,0,LGP,M1 ; INITIAL AC FLOAT NUMBER
.WORD F1P,1 ; EXPECTED FLOAT RESULT
.WORD 047757,047740 ; FPS: BEFORE, AFTER

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```
5744 #24724 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5745
5746
5747
5748 ;*****
5749 ;*TEST 373 TEST OF STCDF INSTR, DATA SET SCDF-12
5750 ;* ALL INTERRUPT ENABLES ON
5751 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
5752 ;*****
5752 #24726 000004 TST373: SCOPE
5753 #24730 012705 024742 MOV #SCDF12,R5 ; PTR TO TEST DATA SET
5754 #24734 004737 037134 JSH PC,#SCDF1 ; GO TEST
5755
5756 #24740 000411 BR TST374 ;
5757
5758 #24742 SCDF12: ; TEST DATA SET SCDF-12:
5759 #24742 101777 177777 100000 .WORD 101777,M1,M0,0 ; INITIAL AC FLOAT NUMBER
5760 #24750 000000
5761 #24752 102000 000000 .WORD 102000,0 ; EXPECTED FLOAT RESULT
5762 #24756 047707 047710 .WORD 047707,047710 ; FPS: BEFORE, AFTER
5763 #24762 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5764
5765
5766 ;*****
5767 ;*TEST 374 TEST OF STCDF INSTR, DATA SET SCDF-13
5768 ;* ALL INTERRUPT ENABLES ON
5769 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
5770 ;*****
5771 #24764 000004 TST374: SCOPE
5772 #24766 012705 025000 MOV #SCDF13,R5 ; PTR TO TEST DATA SET
5773 #24772 004737 037134 JSH PC,#SCDF1 ; GO TEST
5774
5775 #24776 000411 BR TST375 ;
5776
5777 #25000 SCDF13: ; TEST DATA SET SCDF-13:
5778 #25000 101777 177777 100000 .WORD 101777,M1,M0,0 ; INITIAL AC FLOAT NUMBER
5779 #25006 000000
5780 #25010 101777 177777 .WORD 101777,M1 ; EXPECTED FLOAT RESULT
5781 #25014 047647 047650 .WORD 047647,047650 ; FPS: BEFORE, AFTER
5782 #25020 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5783
5784
5785 ;*****
5786 ;*TEST 375 TEST OF STCDF INSTR, DATA SET SCDF-14
5787 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
5788 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
5789 ;*****
5790 #25022 000004 TST375: SCOPE
5791 #25024 012705 025036 MOV #SCDF14,R5 ; PTR TO TEST DATA SET
5792 #25030 004737 037134 JSH PC,#SCDF1 ; GO TEST
5793
5794 #25034 000411 BR TST376 ;
5795
5796 #25036 SCDF14: ; TEST DATA SET SCDF-14:
5797 #25036 077777 177777 100000 .WORD LDP,M1,M0,0 ; INITIAL AC FLOAT NUMBER
5798 #25044 000000
5799 #25046 000000 000000 .WORD 0,0 ; EXPECTED FLOAT RESULT
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5800 #25052 046011 046606 .WORD 046611,046606 ; FPS: BEFORE, AFTER
5801 #25056 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
5802
5803
5804
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5805 ;*****
5806 ;*TEST 376 TEST OF STCFD INSTR, DATA SET SCFD-1
5807 ;* ALL INTERRUPT ENABLES ON
5808 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
5809 ;*****
5810 TST376: SCOPE
5811 MOV #SCFD1,R5 ; PTR TO TEST DATA SET
5812 JSR PC,#SCFDT ; GO TEST
5813
5814 BR TST377 ;;
5815
5816 SCFD1: ; TEST DATA SET SCFD-1
5817 .WORD ALTP,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
5818 .WORD ALTP,ALTP,0,0 ; EXPECTED FLOAT RESULT
5819 .WORD 047417,047400 ; FPS: BEFORE, AFTER
5820
5821
5822
5823
5824 ;*****
5825 ;*TEST 377 TEST OF STCFD INSTR, DATA SET SCFD-2
5826 ;* ALL INTERRUPT ENABLES ON
5827 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5828 ;*****
5829 TST377: SCOPE
5830 MOV #SCFD2,R5 ; PTR TO TEST DATA SET
5831 JSR PC,#SCFDT ; GO TEST
5832
5833 BR TST400 ;;
5834
5835 SCFD2: ; TEST DATA SET SCFD-2
5836 .WORD EX1M1,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
5837 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
5838 .WORD 047513,047504 ; FPS: BEFORE, AFTER
5839
5840
5841
5842
5843 ;*****
5844 ;*TEST 400 TEST OF STCFD INSTR, DATA SET SCFD-3
5845 ;* ALL INTERRUPT ENABLES ON
5846 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
5847 ;*****
5848 TST400: SCOPE
5849 MOV #SCFD3,R5 ; PTR TO TEST DATA SET
5850 JSR PC,#SCFDT ; GO TEST
5851
5852 BR TST401 ;;
5853
5854 SCFD3: ; TEST DATA SET SCFD-3
5855 .WORD LGN,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
5856 .WORD LGN,M1,0,0 ; EXPECTED FLOAT RESULT
5857 .WORD 047407,047410 ; FPS: BEFORE, AFTER
5858
5859
5860

```

```

5861 ;*****
5862 ;*TEST 401 TEST OF STCFD INSTR, DATA SET SCFD-4
5863 ;* ALL INTERRUPT ENABLES ON
5864 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
5865 ;*****
5866 TST401: SCOPE
5867 MOV #SCFD4,R5 ; PTR TO TEST DATA SET
5868 JSR PC,#SCFDT ; GO TEST
5869
5870 BR TST402 ;;
5871
5872 SCFD4: ; TEST DATA SET SCFD-4
5873 .WORD ALT4N,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER
5874 .WORD ALT4N,ALT4N,0,0 ; EXPECTED FLOAT RESULT
5875 .WORD 047547,047550 ; FPS: BEFORE, AFTER
5876
5877
5878
5879
5880 ;*****
5881 ;*TEST 402 TEST OF STCFD INSTR, DATA SET SCFD-5
5882 ;* ALL INTERRUPT ENABLES ON
5883 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
5884 ;*****
5885 TST402: SCOPE
5886 MOV #SCFD5,R5 ; PTR TO TEST DATA SET
5887 JSR PC,#SCFDT ; GO TEST
5888
5889 BR TST403 ;;
5890
5891 SCFD5: ; TEST DATA SET SCFD-5
5892 .WORD 0,0,0,0 ; INITIAL AC FLOAT NUMBER
5893 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
5894 .WORD 047453,047444 ; FPS: BEFORE, AFTER
5895
5896
5897
5898 ;*****
5899 ;*TEST 403 TEST OF STCFD INSTR, DATA SET SCFD-6
5900 ;* ALL INTERRUPT ENABLES ON
5901 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
5902 ;*****
5903 TST403: SCOPE
5904 MOV #SCFD6,R5 ; PTR TO TEST DATA SET
5905 JSR PC,#SCFDT ; GO TEST
5906
5907 BR TST404 ;;
5908
5909 SCFD6: ; TEST DATA SET SCFD-6
5910 .WORD LGP,0,M1,M1 ; INITIAL AC FLOAT NUMBER
5911 .WORD LGP,0,0,0 ; EXPECTED FLOAT RESULT
5912 .WORD 047517,047500 ; FPS: BEFORE, AFTER
5913
5914
5915

```

5917  
5918

5919  
5920  
5921  
5922  
5923  
5924 025360 000004  
5925 025362 012705 025374  
5926 025366 004737 037422  
5927  
5928 025372 000405  
5929  
5930 025374  
5931 025374 100000  
5932 025376 144000 000000  
5933 025402 047407 047410  
5934  
5935  
5936  
5937  
5938  
5939  
5940  
5941 025406 000004  
5942 025410 012705 025422  
5943 025414 004737 037422  
5944  
5945 025420 000405  
5946  
5947 025422  
5948 025422 007417  
5949 025424 043100 170000  
5950 025430 047457 047440  
5951  
5952  
5953  
5954  
5955  
5956  
5957  
5958 025434 000004  
5959 025436 012705 025450  
5960 025442 004737 037422  
5961  
5962 025446 000405  
5963  
5964 025450  
5965 025450 000000  
5966 025452 000000 000000  
5967 025456 047413 047404  
5968  
5969  
5970  
5971  
5972  
5973  
5974

```
*****  
;TEST 404 TEST OF LDCIF INSTR, DATA SET LCIF-1  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
*****  
TST404: SCOPE  
MOV RLCIF1,R5 ; PTR TO TEST DATA SET  
JSR PC,0RLCIFT ; GO TEST  
BR TST405 ;;  
  
LCIF1: ; TEST DATA SET LCIF-11  
.WORD 100000 ; INITIAL INTEGER VALUE  
.WORD 144000,000000 ; EXPECTED FLOAT RESULT  
.WORD 047407,047410 ; FPS: BEFORE, AFTER  
  
*****  
;TEST 405 TEST OF LDCIF INSTR, DATA SET LCIF-2  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
*****  
TST405: SCOPE  
MOV RLCIF2,R5 ; PTR TO TEST DATA SET  
JSR PC,0RLCIFT ; GO TEST  
BR TST406 ;;  
  
LCIF2: ; TEST DATA SET LCIF-21  
.WORD 007417 ; INITIAL INTEGER VALUE  
.WORD 043100,170000 ; EXPECTED FLOAT RESULT  
.WORD 047457,047440 ; FPS: BEFORE, AFTER  
  
*****  
;TEST 406 TEST OF LDCIF INSTR, DATA SET LCIF-3  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
*****  
TST406: SCOPE  
MOV RLCIF3,R5 ; PTR TO TEST DATA SET  
JSR PC,0RLCIFT ; GO TEST  
BR TST407 ;;  
  
LCIF3: ; TEST DATA SET LCIF-31  
.WORD 000000 ; INITIAL INTEGER VALUE  
.WORD 000000,000000 ; EXPECTED FLOAT RESULT  
.WORD 047413,047404 ; FPS: BEFORE, AFTER  
  
*****  
;TEST 407 TEST OF LDCIF INSTR, DATA SET LCIF-4  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
*****
```

5975 025462 000004  
 5976 025461 012705 025476  
 5977 025470 004737 037422  
 5978  
 5979 025474 000405  
 5980  
 5981 025476  
 5982 025476 170360  
 5983 025500 143161 000000  
 5984 025504 047447 047450  
 5985  
 5986  
 5987  
 5988  
 5989  
 5990  
 5991  
 5992 025510 000004  
 5993 025512 012705 025524  
 5994 025516 004737 037422  
 5995  
 5996 025522 000405  
 5997  
 5998 025524  
 5999 025524 077777  
 0000 025526 043777 177000  
 0001 025532 047417 047400  
 0002  
 0003  
 0004

TST407: SCOPE  
 MOV 0LCIF4,R5 ; PTR TO TEST DATA SET  
 JSR PC,00LCIFT ; GO TEST  
 BR TST410 ;  
 LCIF4: ; TEST DATA SET LCIF-4:  
 .WORD 170360 ; INITIAL INTEGER VALUE  
 .WORD 143161,000000 ; EXPECTED FLOAT RESULT  
 .WORD 047447,047450 ; FPS: BEFORE, AFTER  
 ;\*\*\*\*\*  
 ;\*TEST 410 TEST OF LDCIF INSTR, DATA SET LCIF-5  
 ;\* ALL INTERRUPT ENABLES ON  
 ;\* SHORT FLOAT, SHORT INTEGER, ROUND MODES  
 ;\*\*\*\*\*  
 TST410: SCOPE  
 MOV 0LCIF5,R5 ; PTR TO TEST DATA SET  
 JSR PC,00LCIFT ; GO TEST  
 BR TST411 ;  
 LCIF5: ; TEST DATA SET LCIF-5:  
 .WORD 077777 ; INITIAL INTEGER VALUE  
 .WORD 043777,177000 ; EXPECTED FLOAT RESULT  
 .WORD 047417,047400 ; FPS: BEFORE, AFTER

0005  
 0006  
 0007  
 0008  
 0009  
 0010 025536 000004  
 0011 025540 012705 025552  
 0012 025544 004737 037522  
 0013  
 0014 025550 000407  
 0015  
 0016 025552  
 0017 025552 107070  
 0018 025554 143743 110000 000000  
 0019 025562 000000  
 0020 025564 047600 047610  
 0021  
 0022  
 0023  
 0024  
 0025  
 0026  
 0027  
 0028 025570 000004  
 0029 025572 012705 025604  
 0030 025576 004737 037522  
 0031  
 0032 025602 000407  
 0033  
 0034 025604  
 0035 025604 000000  
 0036 025606 000000 000000 000000  
 0037 025614 000000  
 0038 025616 047653 047644  
 0039  
 0040  
 0041  
 0042  
 0043  
 0044  
 0045  
 0046 025622 000004  
 0047 025624 012705 025636  
 0048 025630 004737 037522  
 0049  
 0050 025634 000407  
 0051  
 0052 025636 077777  
 0053 025640 043777 177000 000000  
 0054 025646 000000  
 0055 025650 047657 047640  
 0056  
 0057  
 0058  
 0059  
 0060

LCID1: ; TEST DATA SET LCID-1:  
 .WORD 107070 ; INITIAL INTEGER VALUE  
 .WORD 143743,110000,0,0 ; EXPECTED FLOAT RESULT  
 .WORD 047600,047610 ; FPS: BEFORE, AFTER  
 ;\*\*\*\*\*  
 ;\*TEST 412 TEST OF LDCID INSTR, DATA SET LCID-2  
 ;\* ALL INTERRUPT ENABLES ON  
 ;\* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES  
 ;\*\*\*\*\*  
 TST412: SCOPE  
 MOV 0LCID2,R5 ; PTR TO TEST DATA SET  
 JSR PC,00LCIDT ; GO TEST  
 BR TST413 ;  
 LCID2: ; TEST DATA SET LCID-2:  
 .WORD 000000 ; INITIAL INTEGER VALUE  
 .WORD 000000,000000,0,0 ; EXPECTED FLOAT RESULT  
 .WORD 047653,047644 ; FPS: BEFORE, AFTER  
 ;\*\*\*\*\*  
 ;\*TEST 413 TEST OF LDCID INSTR, DATA SET LCID-3  
 ;\* ALL INTERRUPT ENABLES ON  
 ;\* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES  
 ;\*\*\*\*\*  
 TST413: SCOPE  
 MOV 0LCID3,R5 ; PTR TO TEST DATA SET  
 JSR PC,00LCIDT ; GO TEST  
 BR TST414 ;  
 LCID3: ; TEST DATA SET LCID-3:  
 .WORD 077777 ; INITIAL INTEGER VALUE  
 .WORD 043777,177000,0,0 ; EXPECTED FLOAT RESULT  
 .WORD 047657,047640 ; FPS: BEFORE, AFTER  
 ;\*\*\*\*\*  
 ;\*TEST 414 TEST OF LDCID INSTR, DATA SET LCID-4

0061  
0062  
0063  
0064 025654 000004  
0065 025656 012705 025670  
0066 025662 004737 037522  
0067  
0068 025666 000407  
0069  
0070 025670  
0071 025670 070707  
0072 025672 043743 107000 000000  
0073 025700 000000  
0074 025702 047617 047600  
0075  
0076  
0077  
0078  
0079  
0080  
0081  
0082 025706 000004  
0083 025710 012705 025722  
0084 025714 004737 037522  
0085  
0086 025720 000407  
0087  
0088 025722  
0089 025722 100000  
0090 025724 140000 000000 000000  
0091 025732 000000  
0092 025734 047647 047650  
0093  
0094  
0095

```

;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST414: SCOPE
MOV #LDCID4,R5 ; PTR TO TEST DATA SET
JSR PC,#LDCIDT ; GO TEST
BR TST415 ;;

LDCID4: ; TEST DATA SET LDCID=4
.WORD 070707 ; INITIAL INTEGER VALUE
.WORD 043743,107000,0,0 ; EXPECTED FLOAT RESULT
.WORD 047617,047600 ; FPS: BEFORE, AFTER

;*****
;*TEST 415 TEST OF LDCID INSTR, DATA SET LDCID=3
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST415: SCOPE
MOV #LDCID5,R5 ; PTR TO TEST DATA SET
JSR PC,#LDCIDT ; GO TEST
BR TST416 ;;

LDCID5: ; TEST DATA SET LDCID=5
.WORD 100000 ; INITIAL INTEGER VALUE
.WORD 140000,000000,0,0 ; EXPECTED FLOAT RESULT
.WORD 047647,047650 ; FPS: BEFORE, AFTER

```

0096  
0097  
0098  
0099  
0100  
0101 025740 000004  
0102 025742 012705 025754  
0103 025746 004737 037642  
0104  
0105 025752 000400  
0106  
0107 025754  
0108 025754 077777 177777  
0109 025760 000000 000000  
0110 025764 047517 047500  
0111  
0112  
0113  
0114  
0115  
0116  
0117  
0118 025770 000004  
0119 025772 012705 026004  
0120 025776 004737 037642  
0121  
0122 026002 000400  
0123  
0124 026004  
0125 026004 077777 177777  
0126 026010 047777 177777  
0127 026014 047557 047540  
0128  
0129  
0130  
0131  
0132  
0133  
0134  
0135 026020 000004  
0136 026022 012705 026034  
0137 026026 004737 037642  
0138  
0139 026032 000400  
0140  
0141 026034  
0142 026034 170360 170360  
0143 026040 147160 170361  
0144 026044 047507 047510  
0145  
0146  
0147  
0148  
0149  
0150  
0151

```

;*****
;*TEST 416 TEST OF LDCLF INSTR, DATA SET LCLF-1
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST416: SCOPE
MOV #LCLF1,R5 ; PTR TO TEST DATA SET
JSR PC,#LCLFT ; GO TEST
BR TST417 ;;

LCLF1: ; TEST DATA SET LCLF=1
.WORD 077777,M1 ; INITIAL INTEGER VALUE
.WORD 050000,000000 ; EXPECTED FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER

;*****
;*TEST 417 TEST OF LDCLF INSTR, DATA SET LCLF=2
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST417: SCOPE
MOV #LCLF2,R5 ; PTR TO TEST DATA SET
JSR PC,#LCLFT ; GO TEST
BR TST420 ;;

LCLF2: ; TEST DATA SET LCLF=2
.WORD 077777,M1 ; INITIAL INTEGER VALUE
.WORD 047777,M1 ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER

;*****
;*TEST 420 TEST OF LDCLF INSTR, DATA SET LCLF=3
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST420: SCOPE
MOV #LCLF3,R5 ; PTR TO TEST DATA SET
JSR PC,#LCLFT ; GO TEST
BR TST421 ;;

LCLF3: ; TEST DATA SET LCLF=3
.WORD 170360,170360 ; INITIAL INTEGER VALUE
.WORD 147160,170361 ; EXPECTED FLOAT RESULT
.WORD 047507,047510 ; FPS: BEFORE, AFTER

;*****
;*TEST 421 TEST OF LDCLF INSTR, DATA SET LCLF=4
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****

```

0152 026050 000004  
0153 026052 012705 026064  
0154 026056 004737 037642  
0155  
0156 026062 000406  
0157  
0158 026064  
0159 026064 000000 000000  
0160 026070 000000 000000  
0161 026074 047513 047504  
0162  
0163  
0164  
0165  
0166  
0167  
0168  
0169 026100 000004  
0170 026102 012705 026114  
0171 026106 004737 037642  
0172  
0173 026112 000406  
0174  
0175 026114  
0176 026114 077777 177677  
0177 026120 047777 177777  
0178 026124 047517 047500  
0179  
0180  
0181  
0182  
0183  
0184  
0185  
0186 026130 000004  
0187 026132 012705 026144  
0188 026136 004737 037642  
0189  
0190 026142 000406  
0191  
0192 026144  
0193 026144 100000 000000  
0194 026150 150000 000000  
0195 026154 047547 047550  
0196  
0197  
0198  
0199  
0200  
0201  
0202  
0203 026160 000004  
0204 026162 012705 026174  
0205 026166 004737 037642  
0206  
0207 026172 000406

```
TST421: SCOPE
MOV  #LCLF4,R5 ; PTR TO TEST DATA SET
JBR  PC,#LCLFT ; GO TEST
BR   TST422 ;

LCLF4: ; TEST DATA SET LCLF=41
.WORD 000000,000000 ; INITIAL INTEGER VALUE
.WORD 000000,000000 ; EXPECTED FLOAT RESULT
.WORD 047513,047504 ; FPS: BEFORE, AFTER

;*****
;TEST 422 TEST OF LDCLF INSTR, DATA SET LCLF-4
;# ALL INTERRUPT ENABLES ON
;# SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST422: SCOPE
MOV  #LCLF5,R5 ; PTR TO TEST DATA SET
JBR  PC,#LCLFT ; GO TEST
BR   TST423 ;

LCLF5: ; TEST DATA SET LCLF=51
.WORD 077777,177677 ; INITIAL INTEGER VALUE
.WORD 047777,M1 ; EXPECTED FLOAT RESULT
.WORD 047517,047500 ; FPS: BEFORE, AFTER

;*****
;TEST 423 TEST OF LDCLF INSTR, DATA SET LCLF-5
;# ALL INTERRUPT ENABLES ON
;# SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST423: SCOPE
MOV  #LCLF6,R5 ; PTR TO TEST DATA SET
JBR  PC,#LCLFT ; GO TEST
BR   TST424 ;

LCLF6: ; TEST DATA SET LCLF=61
.WORD 100000,000000 ; INITIAL INTEGER VALUE
.WORD 150000,000000 ; EXPECTED FLOAT RESULT
.WORD 047547,047550 ; FPS: BEFORE, AFTER

;*****
;TEST 424 TEST OF LDCLF INSTR, DATA SET LCLF=7
;# ALL INTERRUPT ENABLES ON
;# SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST424: SCOPE
MOV  #LCLF7,R5 ; PTR TO TEST DATA SET
JBR  PC,#LCLFT ; GO TEST
BR   TST425 ;
```

0208  
0209 026174  
0210 026174 043434 070707  
0211 026200 047616 034343  
0212 026204 047557 047540  
0213  
0214  
0215

```
LCLF7: ; TEST DATA SET LCLF=71
.WORD 043434,070707 ; INITIAL INTEGER VALUE
.WORD 047616,034343 ; EXPECTED FLOAT RESULT
.WORD 047557,047540 ; FPS: BEFORE, AFTER
```



```

0210 ;*****
0211 ;*TEST 425 TEST OF LDCLD INSTR, DATA SET LCLD-1
0212 ;*
0213 ;* ALL INTERRUPT ENABLES ON
0214 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
0215 ;*****
0216 TST425: SCOPE
0217 MOV $LCLD1,R5 ; PTR TO TEST DATA SET
0218 JSR PC,$LCLD1 ; GO TEST
0219 BR TST426 ;;
0220
0221 LCLD1: ; TEST DATA SET LCLD=1
0222 .WORD 007417,007417 ; INITIAL INTEGER VALUE
0223 .WORD 047160,170360,170000,0 ; EXPECTED FLOAT RESULT
0224 .WORD 047717,047700 ; FPS: BEFORE, AFTER
0225
0226 ;*****
0227 ;*TEST 426 TEST OF LDCLD INSTR, DATA SET LCLD-2
0228 ;*
0229 ;* ALL INTERRUPT ENABLES ON
0230 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
0231 ;*****
0232 TST426: SCOPE
0233 MOV $LCLD2,R5 ; PTR TO TEST DATA SET
0234 JSR PC,$LCLD2 ; GO TEST
0235 BR TST427 ;;
0236
0237 LCLD2: ; TEST DATA SET LCLD=2
0238 .WORD 100000,000000 ; INITIAL INTEGER VALUE
0239 .WORD 150000,000000,000000,0 ; EXPECTED FLOAT RESULT
0240 .WORD 047747,047750 ; FPS: BEFORE, AFTER
0241
0242 ;*****
0243 ;*TEST 427 TEST OF LDCLD INSTR, DATA SET LCLD-3
0244 ;*
0245 ;* ALL INTERRUPT ENABLES ON
0246 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
0247 ;*****
0248 TST427: SCOPE
0249 MOV $LCLD3,R5 ; PTR TO TEST DATA SET
0250 JSR PC,$LCLD3 ; GO TEST
0251 BR TST428 ;;
0252
0253 LCLD3: ; TEST DATA SET LCLD=3
0254 .WORD 077777,M1 ; INITIAL INTEGER VALUE
0255 .WORD 047777,M1,177000,0 ; EXPECTED FLOAT RESULT
0256 .WORD 047757,047740 ; FPS: BEFORE, AFTER
0257
0258 ;*****
0259 ;*TEST 430 TEST OF LDCLD INSTR, DATA SET LCLD-4
0260 ;*
0261 ;* ALL INTERRUPT ENABLES ON
0262 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
0263 ;*****
0264 TST430: SCOPE
0265 MOV $LCLD4,R5 ; PTR TO TEST DATA SET
0266 JSR PC,$LCLD4 ; GO TEST
0267 BR TST431 ;;
0268
0269 LCLD4: ; TEST DATA SET LCLD=4
0270 .WORD 107070,161616 ; INITIAL INTEGER VALUE
0271 .WORD 147743,107070,102000,0 ; EXPECTED FLOAT RESULT
0272 .WORD 047700,047710 ; FPS: BEFORE, AFTER
0273
0274 ;*****
0275 ;*TEST 431 TEST OF LDCLD INSTR, DATA SET LCLD-5
0276 ;*
0277 ;* ALL INTERRUPT ENABLES ON
0278 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
0279 ;*****
0280 TST431: SCOPE
0281 MOV $LCLD5,R5 ; PTR TO TEST DATA SET
0282 JSR PC,$LCLD5 ; GO TEST
0283 BR TST432 ;;
0284
0285 LCLD5: ; TEST DATA SET LCLD=5
0286 .WORD 000000,000000 ; INITIAL INTEGER VALUE
0287 .WORD 000000,000000,000000,0 ; EXPECTED FLOAT RESULT
0288 .WORD 047753,047744 ; FPS: BEFORE, AFTER
0289
0290
0291
0292
0293
0294
0295
0296

```

```

0272 ;*
0273 ;* ALL INTERRUPT ENABLES ON
0274 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
0275 ;*****
0276 TST430: SCOPE
0277 MOV $LCLD4,R5 ; PTR TO TEST DATA SET
0278 JSR PC,$LCLD4 ; GO TEST
0279 BR TST431 ;;
0280
0281 LCLD4: ; TEST DATA SET LCLD=4
0282 .WORD 107070,161616 ; INITIAL INTEGER VALUE
0283 .WORD 147743,107070,102000,0 ; EXPECTED FLOAT RESULT
0284 .WORD 047700,047710 ; FPS: BEFORE, AFTER
0285
0286 ;*****
0287 ;*TEST 431 TEST OF LDCLD INSTR, DATA SET LCLD-5
0288 ;*
0289 ;* ALL INTERRUPT ENABLES ON
0290 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
0291 ;*****
0292 TST431: SCOPE
0293 MOV $LCLD5,R5 ; PTR TO TEST DATA SET
0294 JSR PC,$LCLD5 ; GO TEST
0295 BR TST432 ;;
0296
0297 LCLD5: ; TEST DATA SET LCLD=5
0298 .WORD 000000,000000 ; INITIAL INTEGER VALUE
0299 .WORD 000000,000000,000000,0 ; EXPECTED FLOAT RESULT
0300 .WORD 047753,047744 ; FPS: BEFORE, AFTER
0301
0302
0303
0304
0305
0306

```

```

0307
0308
0309
0310
0311
0312 026424 000004
0313 026426 012705 026440
0314 026432 004737 040002
0315
0316 026436 000406
0317
0318 026440
0319 026444 000000 000000
0320 026444 000000
0321 026446 047453 047444
0322 026452 000000
0323
0324
0325
0326
0327
0328
0329
0330 026454 000004
0331 026456 012705 026470
0332 026462 004737 040002
0333
0334 026466 000406
0335
0336 026470
0337 026470 041532 000000
0338 026474 000006
0339 026476 047457 047440
0340 026502 000000
0341
0342
0343
0344
0345
0346
0347
0348 026504 000004
0349 026506 012705 026520
0350 026512 004737 040002
0351
0352 026516 000406
0353
0354 026520
0355 026520 052525 052525
0356 026524 000000
0357 026526 047457 147445
0358 026532 100006
0359
0360
0361
0362

```

```

*****
; *TEST 432 TEST OF STCFI INSTR, DATA SET SCFI-1
; *
; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
; *
TST432: SCOPE
MOV #SCFI1,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST433 ;;

SCFI1: ; TEST DATA SET SCFI=11
.WORD 000000,000000 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047453,047444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
; *TEST 433 TEST OF STCFI INSTR, DATA SET SCFI-2
; *
; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
; *
TST433: SCOPE
MOV #SCFI2,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST434 ;;

SCFI2: ; TEST DATA SET SCFI=21
.WORD 041532,000000 ; INITIAL FLOAT VALUE
.WORD 000006 ; EXPECTED INTEGER RESULT
.WORD 047457,047440 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
; *TEST 434 TEST OF STCFI INSTR, DATA SET SCFI-3
; *
; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
; *
TST434: SCOPE
MOV #SCFI3,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST435 ;;

SCFI3: ; TEST DATA SET SCFI=31
.WORD 052524,052525 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047452,147445 ; FPS: BEFORE, AFTER
.WORD 100006 ; FEC AFTER ( 0 = N/A )

*****
; *TEST 435 TEST OF STCFI INSTR, DATA SET SCFI-4

```

```

0363
0364
0365
0366 026534 000004
0367 026536 012705 026550
0368 026542 004737 040002
0369
0370 026546 000406
0371
0372 026550
0373 026550 141531 177777
0374 026554 177712
0375 026556 047407 047410
0376 026562 000000
0377
0378
0379
0380
0381
0382
0383
0384 026564 000004
0385 026566 012705 026600
0386 026572 004737 040002
0387
0388 026576 000406
0389
0390 026600
0391 026600 041532 000000
0392 026604 000006
0393 026606 047417 047400
0394 026612 000000
0395
0396
0397
0398
0399
0400
0401
0402 026614 000004
0403 026616 012705 026630
0404 026622 004737 040002
0405
0406 026626 000406
0407
0408 026630
0409 026630 172011 123456
0410 026634 000000
0411 026636 047052 047045
0412 026642 000000
0413
0414
0415
0416
0417
0418

```

```

; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, ROUND MODES
; *
TST435: SCOPE
MOV #SCFI4,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST436 ;;

SCFI4: ; TEST DATA SET SCFI=41
.WORD 141531,01 ; INITIAL FLOAT VALUE
.WORD 177712 ; EXPECTED INTEGER RESULT
.WORD 047407,047410 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
; *TEST 436 TEST OF STCFI INSTR, DATA SET SCFI-5
; *
; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, ROUND MODES
; *
TST436: SCOPE
MOV #SCFI5,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST437 ;;

SCFI5: ; TEST DATA SET SCFI=51
.WORD 041532,000000 ; INITIAL FLOAT VALUE
.WORD 000006 ; EXPECTED INTEGER RESULT
.WORD 047417,047400 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
; *TEST 437 TEST OF STCFI INSTR, DATA SET SCFI-6
; *
; * INTEGER CONVERSION INTERRUPT ENABLE OFF, ALL OTHERS ON
; * SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
; *
TST437: SCOPE
MOV #SCFI6,R5 ; PTR TO TEST DATA SET
JSR PC,#SCFIT ; GO TEST

BR TST440 ;;

SCFI6: ; TEST DATA SET SCFI=61
.WORD 172011,123456 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047052,047045 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
; *TEST 440 TEST OF STCFI INSTR, DATA SET SCFI-7
; *
; * ALL INTERRUPT ENABLES ON
; * SHORT FLOAT, SHORT INTEGER, ROUND MODES

```

0419  
0420 026644 000004  
0421 026646 012705 026660  
0422 026652 004737 040062  
0423  
0424 026656 000400  
0425  
0426 026660  
0427 026660 000000 177777  
0428 026664 000000  
0429 026666 047413 047404  
0430 026672 000000  
0431  
0432  
0433

```

;*****
;TEST 440: SCOPE
MOV 0SCFI7,R5 ; PTR TO TEST DATA SET
JSR PC,0:SCFI7 ; GO TEST
BR TST441 ;;

SCFI7: ; TEST DATA SET SCFI=7
.WORD 000000,M1 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047413,047404 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****
```

0434  
0435  
0436  
0437  
0438  
0439 026674 000004  
0440 026676 012705 026710  
0441 026702 004737 040260  
0442  
0443 026706 000410  
0444  
0445 026710  
0446 026710 044000 000000 000000  
0447 026716 000000  
0448 026720 000000  
0449 026722 047652 147645  
0450 026726 100006  
0451  
0452  
0453  
0454  
0455  
0456  
0457  
0458  
0459 026730 000004  
0460 026732 012705 026744  
0461 026736 004737 040260  
0462  
0463 026742 000410  
0464  
0465 026744 043777 177377 177777  
0466 026752 177777  
0467 026754 077777  
0468 026756 047617 047600  
0469 026762 000000  
0470  
0471  
0472  
0473  
0474  
0475  
0476  
0477 026764 000004  
0478 026766 012705 027000  
0479 026772 004737 040260  
0480  
0481 026776 000410  
0482  
0483 027000  
0484 027000 000000 000000 000000  
0485 027006 000000  
0486 027010 000000  
0487 027012 047613 047604  
0488 027016 000000  
0489

```

;*****
;TEST 441 TEST OF STCDI INSTR, DATA SET SCDI-1
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST441: SCOPE
MOV 0SCDI1,R5 ; PTR TO TEST DATA SET
JSR PC,0:SCDI1 ; GO TEST
BR TST442 ;;

SCDI1: ; TEST DATA SET SCDI=1
.WORD 044000,000000,000000,000000 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047652,147645 ; FPS: BEFORE, AFTER
.WORD 100006 ; FEC AFTER ( 0 = N/A )
;*****

;*****
;TEST 442 TEST OF STCDI INSTR, DATA SET SCDI-2
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST442: SCOPE
MOV 0SCDI2,R5 ; PTR TO TEST DATA SET
JSR PC,0:SCDI2 ; GO TEST
BR TST443 ;;

SCDI2: ; TEST DATA SET SCDI=2
.WORD 043777,177377,M1,M1 ; INITIAL FLOAT VALUE
.WORD 077777 ; EXPECTED INTEGER RESULT
.WORD 047617,047600 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****

;*****
;TEST 443 TEST OF STCDI INSTR, DATA SET SCDI-3
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST443: SCOPE
MOV 0SCDI3,R5 ; PTR TO TEST DATA SET
JSR PC,0:SCDI3 ; GO TEST
BR TST444 ;;

SCDI3: ; TEST DATA SET SCDI=3
.WORD 000000,000000,000000,000000 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047613,047604 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )
;*****
```

```

0490
0491
0492
0493
0494
0495
0496 #27020 000004
0497 #27022 012705 #27034
0498 #27026 004737 040260
0499
0500 #27032 000410
0501
0502 #27034
0503 #27034 143161 007777 177777
0504 #27042 177777
0505 #27044 170360
0506 #27046 047607 047610
0507 #27052 000000
0508
0509
0510
0511
0512
0513
0514
0515 #27054 000004
0516 #27056 012705 #27070
0517 #27062 004737 040260
0518
0519 #27060 000410
0520
0521 #27070
0522 #27070 143777 177777 177777
0523 #27076 177777
0524 #27100 100001
0525 #27102 047647 047650
0526 #27106 000000
0527
0528
0529
0530
0531
0532
0533
0534 #27110 000004
0535 #27112 012705 #27124
0536 #27116 004737 040260
0537
0538 #27122 000410
0539
0540 #27124
0541 #27124 152525 052525 177777
0542 #27132 000000
0543 #27134 000000
0544 #27136 047212 047205
0545 #27142 000000

;*****
;*TEST 444 TEST OF STCDI INSTR, DATA SET SCD1-4
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST444: SCOPE
MOV #SCD14,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDIT ; GO TEST
BR TST445 ;;

SCD14: ; TEST DATA SET SCD1-4
.WORD 143161,007777,M1,M1 ; INITIAL FLOAT VALUE
.WORD 170360 ; EXPECTED INTEGER RESULT
.WORD 047607,047610 ; FFS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 445 TEST OF STCDI INSTR, DATA SET SCD1-5
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST445: SCOPE
MOV #SCD15,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDIT ; GO TEST
BR TST446 ;;

SCD15: ; TEST DATA SET SCD1-5
.WORD 143777,M1,M1,M1 ; INITIAL FLOAT VALUE
.WORD 100001 ; EXPECTED INTEGER RESULT
.WORD 047647,047650 ; FFS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

;*****
;*TEST 446 TEST OF STCDI INSTR, DATA SET SCD1-6
;* INTEGER CONVERSION INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST446: SCOPE
MOV #SCD16,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDIT ; GO TEST
BR TST447 ;;

SCD16: ; TEST DATA SET SCD1-6
.WORD 152525,052525,M1,000000 ; INITIAL FLOAT VALUE
.WORD 000000 ; EXPECTED INTEGER RESULT
.WORD 047212,047205 ; FFS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

```

```

0546
0547
0548
0549
0550
0551
0552
0553 #27144 000004
0554 #27146 012705 #27160
0555 #27152 004737 040260
0556
0557 #27156 000410
0558
0559 #27160
0560 #27160 140377 177777 177777
0561 #27166 052525
0562 #27170 177777
0563 #27172 047647 047650
0564 #27176 000000
0565
0566
0567

;*****
;*TEST 447 TEST OF STCDI INSTR, DATA SET SCD1-7
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST447: SCOPE
MOV #SCD17,R5 ; PTR TO TEST DATA SET
JSR PC,#SCDIT ; GO TEST
BR TST450 ;;

SCD17: ; TEST DATA SET SCD1-7
.WORD 140377,M1,M1,052525 ; INITIAL FLOAT VALUE
.WORD M1 ; EXPECTED INTEGER RESULT
.WORD 047647,047650 ; FFS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N7A )

```



0680  
0681 027434 000004  
0682 027436 012705 027450  
0683 027442 004737 040456  
0684  
0685 027446 000407  
0686  
0687 027450  
0688 027450 000000 123456  
0689 027454 000000 000000  
0690 027460 047152 047145  
0691 027464 000000  
0692  
0693  
0694

```
*****  
TST456: SCOPE  
MOV 0SCFL7,R5 ; PTR TO TEST DATA SET  
JSR PC,00SCFLT ; GO TEST  
  
BR TST457 ;  
  
SCFL7: ; TEST DATA SET SCFL=7:  
.WORD 006666,123456 ; INITIAL FLOAT VALUE  
.WORD 000000,000000 ; EXPECTED INTEGER RESULT  
.WORD 047152,047145 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

0695  
0696  
0697  
0698  
0699  
0700 027466 000004  
0701 027470 012705 027502  
0702 027474 004737 040664  
0703  
0704 027500 000411  
0705  
0706 027502  
0707 027502 000000 177000 177000  
0708 027510 177000  
0709 027512 000000 000000  
0710 027516 047712 147705  
0711 027522 100006  
0712  
0713  
0714  
0715  
0716  
0717  
0718  
0719 027524 000004  
0720 027526 012705 027540  
0721 027532 004737 040664  
0722  
0723 027536 000411  
0724  
0725 027540  
0726 027540 047777 177777 177377  
0727 027546 177777  
0728 027550 077777 177777  
0729 027554 047717 047700  
0730 027560 000000  
0731  
0732  
0733  
0734  
0735  
0736  
0737  
0738 027562 000004  
0739 027564 012705 027576  
0740 027570 004737 040664  
0741  
0742 027574 000411  
0743  
0744 027576  
0745 027576 137777 125252 177777  
0746 027604 177777  
0747 027606 000000 000000  
0748 027612 047713 047704  
0749 027616 000000  
0750

```
*****  
;*TEST 457 TEST OF STCDL INSTR, DATA SET SCDL=1  
;* ALL INTERRUPT ENABLES ON  
;* LONG FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST457: SCOPE  
MOV 0SCDL1,R5 ; PTR TO TEST DATA SET  
JSR PC,00SCDLT ; GO TEST  
  
BR TST460 ;  
  
SCDL1: ; TEST DATA SET SCDL=1:  
.WORD 000000,177000,177000 ; INITIAL FLOAT VALUE  
.WORD 000000,000000 ; EXPECTED INTEGER RESULT  
.WORD 047712,147705 ; FPS: BEFORE, AFTER  
.WORD 100006 ; FEC AFTER ( 0 = N/A )  
  
*****  
;*TEST 460 TEST OF STCDL INSTR, DATA SET SCDL=1  
;* ALL INTERRUPT ENABLES ON  
;* LONG FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST460: SCOPE  
MOV 0SCDL2,R5 ; PTR TO TEST DATA SET  
JSR PC,00SCDLT ; GO TEST  
  
BR TST461 ;  
  
SCDL2: ; TEST DATA SET SCDL=2:  
.WORD 047777,M1,177377,M1 ; INITIAL FLOAT VALUE  
.WORD 077777,M1 ; EXPECTED INTEGER RESULT  
.WORD 047717,047700 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )  
  
*****  
;*TEST 461 TEST OF STCDL INSTR, DATA SET SCDL=3  
;* ALL INTERRUPT ENABLES ON  
;* LONG FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST461: SCOPE  
MOV 0SCDL3,R5 ; PTR TO TEST DATA SET  
JSR PC,00SCDLT ; GO TEST  
  
BR TST462 ;  
  
SCDL3: ; TEST DATA SET SCDL=3:  
.WORD 137777,125252,M1,M1 ; INITIAL FLOAT VALUE  
.WORD 000000,000000 ; EXPECTED INTEGER RESULT  
.WORD 047713,047704 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

```

0751
0752
0753
0754
0755
0756
0757 027620 000004
0758 027622 012705 027634
0759 027626 004737 040664
0760
0761 027632 000411
0762
0763 027634
0764 027634 147777 177777 177777
0765 027642 177777
0766 027644 100000 000001
0767 027650 047707 047710
0768 027654 000000
0769
0770
0771
0772
0773
0774
0775
0776 027656 000004
0777 027660 012705 027672
0778 027664 004737 040664
0779
0780 027670 000411
0781
0782 027672
0783 027672 047160 170360 177777
0784 027700 177777
0785 027702 007417 007417
0786 027706 047757 047740
0787 027712 000000
0788
0789
0790
0791
0792
0793
0794
0795 027714 000004
0796 027716 012705 027730
0797 027722 004737 040664
0798
0799 027726 000411
0800
0801 027730
0802 027730 000177 177777 125252
0803 027736 101010
0804 027740 000000 000000
0805 027744 047713 047704
0806 027750 000000

;*****
;TEST 462 TEST OF STCDL INSTR, DATA SET SCDL=3
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST462: SCOPE
MOV #SCDL4,R5 ; PTR TO TEST DATA SET
JSR PC,0#SCDLT ; GO TEST
BR TST463 ;;

SCDL4: ; TEST DATA SET SCDL=4;
.WORD 147777,M1,M1,M1 ; INITIAL FLOAT VALUE
.WORD 100000,000001 ; EXPECTED INTEGER RESULT
.WORD 047707,047710 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 463 TEST OF STCDL INSTR, DATA SET SCDL=5
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST463: SCOPE
MOV #SCDL5,R5 ; PTR TO TEST DATA SET
JSR PC,0#SCDLT ; GO TEST
BR TST464 ;;

SCDL5: ; TEST DATA SET SCDL=5;
.WORD 047160,170360,M1,M1 ; INITIAL FLOAT VALUE
.WORD 007417,007417 ; EXPECTED INTEGER RESULT
.WORD 047757,047740 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

;*****
;TEST 464 TEST OF STCDL INSTR, DATA SET SCDL=6
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST464: SCOPE
MOV #SCDL6,R5 ; PTR TO TEST DATA SET
JSR PC,0#SCDLT ; GO TEST
BR TST465 ;;

SCDL6: ; TEST DATA SET SCDL=6;
.WORD 000177,M1,125252,101010 ; INITIAL FLOAT VALUE
.WORD 000000,000000 ; EXPECTED INTEGER RESULT
.WORD 047713,047704 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

```

0807
0808
0809
0810
0811
0812
0813
0814 027752 000004
0815 027754 012705 027766
0816 027760 004737 040664
0817
0818 027764 000411
0819
0820 027766
0821 027766 062141 125252 052525
0822 027774 125252
0823 027776 000000 000000
0824 030002 047312 047305
0825 030006 000000
0826
0827
0828

;*****
;TEST 465 TEST OF STCDL INSTR, DATA SET SCDL=7
;*
;* INTEGER CONVERSION INTERRUPT ENABLE OFF, ALL OTHERS ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
;*****
TST465: SCOPE
MOV #SCDL7,R5 ; PTR TO TEST DATA SET
JSR PC,0#SCDLT ; GO TEST
BR TST466 ;;

SCDL7: ; TEST DATA SET SCDL=7;
.WORD 062141,125252,052525,125252 ; INITIAL FLOAT VALUE
.WORD 000000,000000 ; EXPECTED INTEGER RESULT
.WORD 047312,047305 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```





0941  
0942  
0943  
0944  
0945  
0946  
0947  
0948 030260 000004  
0949 030262 012705 030274  
0950 030266 004737 041072  
0951  
0952 030272 000410  
0953  
0954 030274  
0955 030274 020177 177777  
0956 030300 037777 177777  
0957 030304 177777  
0958 030306 047457 047440  
0959 030312 000000  
0960  
0961  
0962

```

;*****
;TEST 474 TEST OF LDEXP/F INSTR, DATA SET LEXF=7
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST474: SCOPE
      MOV     $LEXF7,R5      ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT    ; GO TEST
      BR      TST475        ;;

LEXF7: ; TEST DATA SET LEXF=7:
      .WORD  020177,M1      ; INITIAL AC FLOAT NUMBER
      .WORD  037777,M1      ; EXPECTED FLOAT RESULT
      .WORD  -1             ; EXPONENT TO BE LOADED
      .WORD  047457,047440 ; FPS: BEFORE, AFTER
      .WORD  NA             ; FEC AFTER ( 0 = N/A )

```

0963  
0964  
0965  
0966  
0967 030314 000004  
0968 030316 012705 030330  
0969 030322 004737 041072  
0970  
0971 030326 000410  
0972  
0973 030330  
0974 030330 120000 000000  
0975 030334 120000 000000  
0976 030340 177700  
0977 030342 047407 047410  
0978 030346 000000  
0979  
0980  
0981  
0982

```

;*****
;TEST 475 TEST OF LDEXP/F INSTR, DATA SET LEXF=10
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
;*****
TST475: SCOPE
      MOV     $LEXF10,R5   ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT  ; GO TEST
      BR      TST476      ;;

LEXF10: ; TEST DATA SET LEXF=10:
      .WORD  120000,0      ; INITIAL AC FLOAT NUMBER
      .WORD  120000,0      ; EXPECTED FLOAT RESULT
      .WORD  -100         ; EXPONENT TO BE LOADED
      .WORD  047407,047410 ; FPS: BEFORE, AFTER
      .WORD  NA           ; FEC AFTER ( 0 = N/A )

```

0983  
0984  
0985  
0986 030350 000004  
0987 030352 012705 030364  
0988 030356 004737 041072  
0989  
0990 030362 000410  
0991  
0992 030364  
0993 030364 020125 052525  
0994 030370 000325 052525  
0995 030374 177001  
0996 030376 047557 047540

```

;*****
;TEST 476 TEST OF LDEXP/F INSTR, DATA SET LEXF=11
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
;*****
TST476: SCOPE
      MOV     $LEXF11,R5   ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT  ; GO TEST
      BR      TST477      ;;

LEXF11: ; TEST DATA SET LEXF=11:
      .WORD  020125,ALTP   ; INITIAL AC FLOAT NUMBER
      .WORD  000325,ALTP   ; EXPECTED FLOAT RESULT
      .WORD  -177         ; EXPONENT TO BE LOADED
      .WORD  047557,047540 ; FPS: BEFORE, AFTER

```

0997 030402 000000  
0998  
0999  
7000  
7001  
7002  
7003  
7004  
7005 030404 000004  
7006 030406 012705 030420  
7007 030412 004737 041072  
7008  
7009 030416 000410  
7010  
7011 030420  
7012 030420 120052 125252  
7013 030421 100052 125252  
7014 030430 177000  
7015 030432 047503 147514  
7016 030436 100012  
7017  
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7021  
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7024 030440 000004  
7025 030442 012705 030454  
7026 030446 004737 041072  
7027  
7028 030452 000410  
7029  
7030 030454  
7031 030454 020017 007417  
7032 030460 077017 007417  
7033 030464 177577  
7034 030466 047457 147440  
7035 030472 100012  
7036  
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7042  
7043 030474 000004  
7044 030476 012705 030510  
7045 030502 004737 041072  
7046  
7047 030506 000410  
7048  
7049 030510  
7050 030510 120160 170360  
7051 030514 177560 170360  
7052 030520 177570

```

      .WORD  NA           ; FEC AFTER ( 0 = N/A )

;*****
;TEST 477 TEST OF LDEXP/F INSTR, DATA SET LEXF=12
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST477: SCOPE
      MOV     $LEXF12,R5   ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT  ; GO TEST
      BR      TST500      ;;

LEXF12: ; TEST DATA SET LEXF=12:
      .WORD  120052,ALIN   ; INITIAL AC FLOAT NUMBER
      .WORD  100052,ALIN   ; EXPECTED FLOAT RESULT
      .WORD  -200         ; EXPONENT TO BE LOADED
      .WORD  047503,147514 ; FPS: BEFORE, AFTER
      .WORD  100012       ; FEC AFTER ( 0 = N/A )

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7200

```

;*****
;TEST 500 TEST OF LDEXP/F INSTR, DATA SET LEXF=13
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
;*****
TST500: SCOPE
      MOV     $LEXF13,R5   ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT  ; GO TEST
      BR      TST501      ;;

LEXF13: ; TEST DATA SET LEXF=13:
      .WORD  020017,ALT4P  ; INITIAL AC FLOAT NUMBER
      .WORD  077017,ALT4P  ; EXPECTED FLOAT RESULT
      .WORD  -201         ; EXPONENT TO BE LOADED
      .WORD  047457,147440 ; FPS: BEFORE, AFTER
      .WORD  100012       ; FEC AFTER ( 0 = N/A )

```

7201  
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7300

```

;*****
;TEST 501 TEST OF LDEXP/F INSTR, DATA SET LEXF=14
;*
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TST501: SCOPE
      MOV     $LEXF14,R5   ; PTR TO TEST DATA SET
      JSR     PC,$$LEXFT  ; GO TEST
      BR      TST502      ;;

LEXF14: ; TEST DATA SET LEXF=14:
      .WORD  120160,ALT4N  ; INITIAL AC FLOAT NUMBER
      .WORD  177560,ALT4N  ; EXPECTED FLOAT RESULT
      .WORD  -202         ; EXPONENT TO BE LOADED

```

7053 030522 047507 147510  
7054 030526 100012  
7055  
7056  
7057  
7058  
7059  
7060  
7061  
7062 030530 000004  
7063 030532 012705 030544  
7064 030536 004737 041072  
7065  
7066 030542 000410  
7067  
7068 030544  
7069 030544 020177 177777  
7070 030550 077377 177777  
7071 030554 177575  
7072 030556 047457 147440  
7073 030562 100012  
7074  
7075  
7076  
7077  
7078  
7079  
7080

```
*****  
;TEST 502 TEST OF LDEXP/F INSTR, DATA SET LEXF-15  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES  
*****  
TST502: SCOPE  
MOV $LEXF15,R5 ; PTR TO TEST DATA SET  
JSR PC,$LEXF15 ; GO TEST  
  
BR TST503 ;  
  
LEXF15: ; TEST DATA SET LEXF-15:  
.WORD 020177,M1 ; INITIAL AC FLOAT NUMBER  
.WORD 077377,M1 ; EXPECTED FLOAT RESULT  
.WORD -203 ; EXPONENT TO BE LOADED  
.WORD 047457,147440 ; FPS: BEFORE, AFTER  
.WORD 100012 ; FEC AFTER ( 0 = N/A )
```

7081 030564 000004  
7082 030566 012705 030600  
7083 030572 004737 041072  
7084  
7085 030576 000410  
7086  
7087 030600  
7088 030604 142000 000000  
7089 030604 140000 000000  
7090 030610 000000  
7091 030612 047547 047550  
7092 030616 000000  
7093  
7094  
7095  
7096  
7097  
7098  
7099  
7100

```
*****  
;TEST 503 TEST OF LDEXP/F INSTR, DATA SET LEXF-16  
;* ALL INTERRUPT ENABLES ON  
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
*****  
TST503: SCOPE  
MOV $LEXF16,R5 ; PTR TO TEST DATA SET  
JSR PC,$LEXF16 ; GO TEST  
  
BR TST504 ;  
  
LEXF16: ; TEST DATA SET LEXF-16:  
.WORD 142000,0 ; INITIAL AC FLOAT NUMBER  
.WORD 140000,0 ; EXPECTED FLOAT RESULT  
.WORD 0 ; EXPONENT TO BE LOADED  
.WORD 047547,047550 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

7101 030620 000004  
7102 030622 012705 030634  
7103 030626 004737 041072  
7104  
7105 030632 000410  
7106  
7107 030634  
7108 030634 020177 177777  
7109 030640 000000 000000  
7110  
7111  
7112  
7113  
7114  
7115  
7116  
7117  
7118  
7119 030654 000004  
7120 030656 012705 030670  
7121 030662 004737 041072  
7122  
7123 030666 000410  
7124  
7125 030670  
7126 030670 120000 000000  
7127 030674 000000 000000  
7128 030700 000200  
7129 030702 046511 046506  
7130 030700 000000  
7131  
7132  
7133  
7134  
7135  
7136  
7137  
7138 030710 000004  
7139 030712 012705 030724  
7140 030716 004737 041072  
7141  
7142 030722 000410  
7143  
7144 030724  
7145 030724 120052 125252  
7146 030730 000000 000000  
7147 030734 177600  
7148 030736 045513 045504  
7149 030742 000000  
7150  
7151  
7152  
7153  
7154  
7155  
7156  
7157 030744 000004  
7158 030746 012705 030760  
7159 030752 004737 041072  
7160  
7161 030756 000410  
7162  
7163 030760  
7164 030760 020017 007417

```
*****  
;TEST 504 TEST OF LDEXP/F INSTR, DATA SET LEXF-17  
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES  
*****  
TST504: SCOPE  
MOV $LEXF17,R5 ; PTR TO TEST DATA SET  
JSR PC,$LEXF17 ; GO TEST  
  
BR TST505 ;  
  
LEXF17: ; TEST DATA SET LEXF-17:  
.WORD 020177,M1 ; INITIAL AC FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT
```

7109 030644 000201  
7110 030646 046551 046546  
7111 030652 000000  
7112  
7113  
7114  
7115  
7116  
7117  
7118  
7119 030654 000004  
7120 030656 012705 030670  
7121 030662 004737 041072  
7122  
7123 030666 000410  
7124  
7125 030670  
7126 030670 120000 000000  
7127 030674 000000 000000  
7128 030700 000200  
7129 030702 046511 046506  
7130 030700 000000  
7131  
7132  
7133  
7134  
7135  
7136  
7137  
7138 030710 000004  
7139 030712 012705 030724  
7140 030716 004737 041072  
7141  
7142 030722 000410  
7143  
7144 030724  
7145 030724 120052 125252  
7146 030730 000000 000000  
7147 030734 177600  
7148 030736 045513 045504  
7149 030742 000000  
7150  
7151  
7152  
7153  
7154  
7155  
7156  
7157 030744 000004  
7158 030746 012705 030760  
7159 030752 004737 041072  
7160  
7161 030756 000410  
7162  
7163 030760  
7164 030760 020017 007417

```
*****  
;TEST 505 TEST OF LDEXP/F INSTR, DATA SET LEXF-20  
;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST505: SCOPE  
MOV $LEXF20,R5 ; PTR TO TEST DATA SET  
JSR PC,$LEXF20 ; GO TEST  
  
BR TST506 ;  
  
LEXF20: ; TEST DATA SET LEXF-20:  
.WORD 120000,0 ; INITIAL AC FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD 200 ; EXPONENT TO BE LOADED  
.WORD 046511,046506 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

7139 030712 012705 030724  
7140 030716 004737 041072  
7141  
7142 030722 000410  
7143  
7144 030724  
7145 030724 120052 125252  
7146 030730 000000 000000  
7147 030734 177600  
7148 030736 045513 045504  
7149 030742 000000  
7150  
7151  
7152  
7153  
7154  
7155  
7156  
7157 030744 000004  
7158 030746 012705 030760  
7159 030752 004737 041072  
7160  
7161 030756 000410  
7162  
7163 030760  
7164 030760 020017 007417

```
*****  
;TEST 506 TEST OF LDEXP/F INSTR, DATA SET LEXF-21  
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON  
;* SHORT FLOAT, LONG INTEGER, ROUND MODES  
*****  
TST506: SCOPE  
MOV $LEXF21,R5 ; PTR TO TEST DATA SET  
JSR PC,$LEXF21 ; GO TEST  
  
BR TST507 ;  
  
LEXF21: ; TEST DATA SET LEXF-21:  
.WORD 120052,ALTN ; INITIAL AC FLOAT NUMBER  
.WORD 0,0 ; EXPECTED FLOAT RESULT  
.WORD -200 ; EXPONENT TO BE LOADED  
.WORD 045513,045504 ; FPS: BEFORE, AFTER  
.WORD NA ; FEC AFTER ( 0 = N/A )
```

7165 030764 000000 000000  
 7166 030770 177577  
 7167 030772 045453 045444  
 7168 030776 000000  
 7169  
 7170  
 7171  
 7172  
 7173  
 7174  
 7175  
 7176 031000 000004  
 7177 031002 012705 031014  
 7178 031006 004737 041072  
 7179  
 7180 031012 000410  
 7181  
 7182 031014  
 7183 031014 120160 170360  
 7184 031020 000000 000000  
 7185 031024 177576  
 7186 031026 045513 045504  
 7187 031032 000000  
 7188  
 7189  
 7190  
 7191  
 7192  
 7193  
 7194  
 7195 031034 000004  
 7196 031036 012705 031050  
 7197 031042 004737 041072  
 7198  
 7199 031046 000410  
 7200  
 7201 031050  
 7202 031050 020177 177777  
 7203 031054 000000 000000  
 7204 031060 177575  
 7205 031062 045453 045444  
 7206 031060 000000  
 7207  
 7208  
 7209

```

*****
;TEST 510 TEST OF LDEXP/F INSTR, DATA SET LEXF-22
;*
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
*****
TST510: SCOPE
MOV #LEXF23,R5 ; PTR TO TEST DATA SET
JSR PC,00LEXF ; GO TEST
BR TST511 ;;

LEXF23: ; TEST DATA SET LEXF-23:
.WORD 120160,ALT4N ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD -201 ; EXPONENT TO BE LOADED
.WORD 045513,045504 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

*****
;TEST 511 TEST OF LDEXP/F INSTR, DATA SET LEXF-24
;*
;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST511: SCOPE
MOV #LEXF24,R5 ; PTR TO TEST DATA SET
JSR PC,00LEXF ; GO TEST
BR TST512 ;;

LEXF24: ; TEST DATA SET LEXF-24:
.WORD 020177,M1 ; INITIAL AC FLOAT NUMBER
.WORD 0,0 ; EXPECTED FLOAT RESULT
.WORD -203 ; EXPONENT TO BE LOADED
.WORD 045453,045444 ; FPS: BEFORE, AFTER
.WORD NA ; FEC AFTER ( 0 = N/A )

```

7210  
 7211  
 7212  
 7213  
 7214  
 7215 031070 000004  
 7216 031072 012705 031104  
 7217 031076 004737 041242  
 7218  
 7219 031102 000414  
 7220  
 7221 031104  
 7222 031104 152325 052525 052525  
 7223 031112 052525  
 7224 031114 100325 052525 052525  
 7225 031122 052525  
 7226 031124 000401  
 7227 031126 047645 147652  
 7228 031132 100010  
 7229  
 7230  
 7231  
 7232  
 7233  
 7234  
 7235  
 7236 031134 000004  
 7237 031136 012705 031150  
 7238 031142 004737 041242  
 7239  
 7240 031146 000414  
 7241  
 7242 031150  
 7243 031150 052377 177777 177777  
 7244 031156 177777  
 7245 031160 000177 177777 177777  
 7246 031166 177777  
 7247 031170 000200  
 7248 031172 047711 147706  
 7249 031176 100010  
 7250  
 7251  
 7252  
 7253  
 7254  
 7255  
 7256  
 7257 031200 000004  
 7258 031202 012705 031214  
 7259 031206 004737 041242  
 7260  
 7261 031212 000414  
 7262  
 7263 031214  
 7264 031214 152360 170360 170360  
 7265 031222 170360

```

*****
;TEST 512 TEST OF LDEXP/D INSTR, DATA SET LEXD-1
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
*****
TST512: SCOPE
MOV #LEXD1,R5 ; PTR TO TEST DATA SET
JSR PC,00LEXD1 ; GO TEST
BR TST513 ;;

LEXD1: ; TEST DATA SET LEXD-1:
.WORD 152325,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
.WORD 100325,ALTP,ALTP,ALTP ; EXPECTED FLOAT RESULT
.WORD 201 ; EXPONENT TO BE LOADED
.WORD 047645,147652 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N/A )

*****
;TEST 513 TEST OF LDEXP/D INSTR, DATA SET LEXD-2
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, LONG INTEGER, ROUND MODES
*****
TST513: SCOPE
MOV #LEXD2,R5 ; PTR TO TEST DATA SET
JSR PC,00LEXD2 ; GO TEST
BR TST514 ;;

LEXD2: ; TEST DATA SET LEXD-2:
.WORD 052377,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
.WORD 000177,M1,M1,M1 ; EXPECTED FLOAT RESULT
.WORD 200 ; EXPONENT TO BE LOADED
.WORD 047711,147706 ; FPS: BEFORE, AFTER
.WORD 100010 ; FEC AFTER ( 0 = N/A )

*****
;TEST 514 TEST OF LDEXP/D INSTR, DATA SET LEXD-3
;*
;* ALL INTERRUPT ENABLES ON
;* LONG FLOAT, SHORT INTEGER, ROUND MODES
*****
TST514: SCOPE
MOV #LEXD3,R5 ; PTR TO TEST DATA SET
JSR PC,00LEXD3 ; GO TEST
BR TST515 ;;

LEXD3: ; TEST DATA SET LEXD-3:
.WORD 152360,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER

```

7266 031224 177760 170360 170360 ,WORD 177760,ALT4N,ALT4N,ALT4N ; EXPECTED FLOAT RESULT  
7267 031232 170360  
7268 031234 000177 ,WORD 177 ; EXPONENT TO BE LOADED  
7269 031236 047607 047610 ,WORD 047607,047610 ; FPS: BEFORE, AFTER  
7270 031242 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7271  
7272  
7273  
7274 ;\*\*\*\*\*  
;TEST 515 TEST OF LDEXP/D INSTR, DATA SET LEAD-4  
;\* ALL INTERRUPT ENABLES ON  
;\* LONG FLOAT, LONG INTEGER, TRUNCATE MODES  
;\*\*\*\*\*  
TST515: SCOPE  
MOV #LEXD4,R5 ; PTR TO TEST DATA SET  
JSR PC,#LEXD4 ; GO TEST  
7281  
7282 031256 000414 BR TST516 ; ;  
7283  
7284 031260  
LEXD4: ; TEST DATA SET LEXU-4 ;  
7285 031260 052200 000000 000000 ,WORD 052200,0,0,0 ; INITIAL AC FLOAT NUMBER  
7286 031266 000000  
7287 031270 000000 000000 000000 ,WORD 000000,0,0,0 ; EXPECTED FLOAT RESULT  
7288 031276 000000  
7289 031300 000100 ,WORD 100 ; EXPONENT TO BE LOADED  
7290 031302 047757 047740 ,WORD 047757,047740 ; FPS: BEFORE, AFTER  
7291 031306 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7292  
7293  
7294 ;\*\*\*\*\*  
;TEST 516 TEST OF LDEXP/D INSTR, DATA SET LEXU-5  
;\* ALL INTERRUPT ENABLES ON  
;\* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES  
;\*\*\*\*\*  
TST516: SCOPE  
MOV #LEXD5,R5 ; PTR TO TEST DATA SET  
JSR PC,#LEXD5 ; GO TEST  
7301 031316 004737 041242  
7302  
7303 031322 000414 BR TST517 ; ;  
7304  
7305 031324  
LEXD5: ; TEST DATA SET LEXU-5 ;  
7306 031324 152252 125252 125252 ,WORD 152252,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER  
7307 031332 125252  
7308 031334 140252 125252 125252 ,WORD 140252,ALTN,ALTN,ALTN ; EXPECTED FLOAT RESULT  
7309 031342 125252  
7310 031344 000001 ,WORD 1 ; EXPONENT TO BE LOADED  
7311 031346 047647 047650 ,WORD 047647,047650 ; FPS: BEFORE, AFTER  
7312 031352 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7313  
7314 ;\*\*\*\*\*  
;TEST 517 TEST OF LDEXP/D INSTR, DATA SET LEXU-6  
;\* ALL INTERRUPT ENABLES ON  
;\* LONG FLOAT, LONG INTEGER, ROUND MODES  
;\*\*\*\*\*  
TST517: SCOPE  
MOV #LEXD6,R5 ; PTR TO TEST DATA SET  
7321 031356 012705 031370

7322 031362 004737 041242 JSR PC,#LEXD7 ; GO TEST  
7323  
7324 031366 000414 BR TST520 ; ;  
7325  
7326 031370  
LEXD6: ; TEST DATA SET LEXU-6 ;  
7327 031370 052217 007417 007417 ,WORD 052217,ALT4P,ALT4P,ALT4P ; INITIAL AC FLOAT NUMBER  
7328 031376 007417  
7329 031400 000017 007417 007417 ,WORD 040017,ALT4P,ALT4P,ALT4P ; EXPECTED FLOAT RESULT  
7330 031406 007417  
7331 031410 000000 ,WORD 0 ; EXPONENT TO BE LOADED  
7332 031412 047717 047700 ,WORD 047717,047700 ; FPS: BEFORE, AFTER  
7333 031416 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7334  
7335 ;\*\*\*\*\*  
;TEST 520 TEST OF LDEXP/D INSTR, DATA SET LEAD-7  
;\* ALL INTERRUPT ENABLES ON  
;\* LONG FLOAT, SHORT INTEGER, ROUND MODES  
;\*\*\*\*\*  
TST520: SCOPE  
MOV #LEXD7,R5 ; PTR TO TEST DATA SET  
JSR PC,#LEXD7 ; GO TEST  
7341 031420 000004  
7342 031422 012705 031434  
7343 031426 004737 041242  
7344  
7345 031432 000414 BR TST521 ; ;  
7346  
7347 031434  
LEXD7: ; TEST DATA SET LEXU-7 ;  
7348 031434 152325 052525 052525 ,WORD 152325,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER  
7349 031442 052525  
7350 031444 137725 052525 052525 ,WORD 137725,ALTP,ALTP,ALTP ; EXPECTED FLOAT RESULT  
7351 031452 052525  
7352 031454 177777 ,WORD -1 ; EXPONENT TO BE LOADED  
7353 031456 047607 047610 ,WORD 047607,047610 ; FPS: BEFORE, AFTER  
7354 031462 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7355  
7356 ;\*\*\*\*\*  
;TEST 521 TEST OF LDEXP/D INSTR, DATA SET LEAD-10  
;\* ALL INTERRUPT ENABLES ON  
;\* LONG FLOAT, LONG INTEGER, TRUNCATE MODES  
;\*\*\*\*\*  
TST521: SCOPE  
MOV #LEXD10,R5 ; PTR TO TEST DATA SET  
JSR PC,#LEXD10 ; GO TEST  
7361  
7362 031464 000004  
7363 031466 012705 031500  
7364 031472 004737 041242  
7365  
7366 031476 000414 BR TST522 ; ;  
7367  
7368 031500  
LEXD10: ; TEST DATA SET LEXU-10 ;  
7369 031500 052377 177777 177777 ,WORD 052377,M1,M1,M1 ; INITIAL AC FLOAT NUMBER  
7370 031506 177777  
7371 031510 020177 177777 177777 ,WORD 020177,M1,M1,M1 ; EXPECTED FLOAT RESULT  
7372 031516 177777  
7373 031520 177700 ,WORD -100 ; EXPONENT TO BE LOADED  
7374 031522 047757 047740 ,WORD 047757,047740 ; FPS: BEFORE, AFTER  
7375 031526 000000 ,WORD NA ; FEC AFTER ( 0 = N/A )  
7376  
7377

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7380
7381
7382
7383 031530 000004
7384 031532 012705 031544
7385 031536 004737 041242
7386
7387 031542 000414
7388
7389 031544
7390 031544 152360 170360 170360 LEXD111 ; TEST DATA SET LEXD-111
7391 031552 170360 .WORD 152360,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER
7392 031554 100360 170360 170360 .WORD 100360,ALT4N,ALT4N,ALT4N ; EXPECTED FLOAT RESULT
7393 031562 170360
7394 031564 177601 .WORD -177 ; EXPONENT TO BE LOADED
7395 031566 047647 047650 .WORD 047647,047650 ; FPS: BEFORE, AFTER
7396 031572 000000 .WORD NA ; FEC AFTER ( 0 = N/A )
7397
7398
7399
7400
7401
7402
7403
7404 031574 000004
7405 031576 012705 031610
7406 031602 004737 041242
7407
7408 031606 000414
7409
7410 031610
7411 031610 052200 000000 000000 LEXD121 ; TEST DATA SET LEXD-121
7412 031616 000000 .WORD 052200,0,0,0 ; INITIAL AC FLOAT NUMBER
7413 031620 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7414 031626 000000
7415 031630 177601 .WORD -200 ; EXPONENT TO BE LOADED
7416 031632 047713 147704 .WORD 047713,147704 ; FPS: BEFORE, AFTER
7417 031636 100012 .WORD 100012 ; FEC AFTER ( 0 = N/A )
7418
7419
7420
7421
7422
7423
7424
7425 031640 000004
7426 031642 012705 031654
7427 031646 004737 041242
7428
7429 031652 000414
7430
7431 031654
7432 031654 152252 125252 125252 LEXD131 ; TEST DATA SET LEXD-131
7433 031662 125252 .WORD 152252,ALT4N,ALT4N,ALT4N ; INITIAL AC FLOAT NUMBER

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7434 031664 177652 125252 125252 .WORD 177652,ALT4N,ALT4N,ALT4N ; EXPECTED FLOAT RESULT
7435 031672 125252
7436 031674 177577 .WORD -201 ; EXPONENT TO BE LOADED
7437 031676 047607 147610 .WORD 047607,147610 ; FPS: BEFORE, AFTER
7438 031702 100012 .WORD 100012 ; FEC AFTER ( 0 = N/A )
7439
7440
7441
7442
7443
7444
7445
7446 031704 000004
7447 031706 012705 031720
7448 031712 004737 041242
7449
7450 031716 000414
7451
7452 031720
7453 031720 052217 007417 007417 LEXD141 ; TEST DATA SET LEXD-141
7454 031726 007417 .WORD 052217,ALT4P,ALT4P,ALT4P ; INITIAL AC FLOAT NUMBER
7455 031730 077417 007417 007417 .WORD 077417,ALT4P,ALT4P,ALT4P ; EXPECTED FLOAT RESULT
7456 031736 007417
7457 031740 177576 .WORD -202 ; EXPONENT TO BE LOADED
7458 031742 047757 147740 .WORD 047757,147740 ; FPS: BEFORE, AFTER
7459 031746 100012 .WORD 100012 ; FEC AFTER ( 0 = N/A )
7460
7461
7462
7463
7464
7465
7466
7467 031750 000004
7468 031752 012705 031764
7469 031756 004737 041242
7470
7471 031762 000414
7472
7473 031764
7474 031764 152325 052525 052525 LEXD151 ; TEST DATA SET LEXD-151
7475 031772 052525 .WORD 152325,ALT4P,ALT4P,ALT4P ; INITIAL AC FLOAT NUMBER
7476 031774 177325 052525 052525 .WORD 177325,ALT4P,ALT4P,ALT4P ; EXPECTED FLOAT RESULT
7477 032002 052525
7478 032004 177575 .WORD -203 ; EXPONENT TO BE LOADED
7479 032006 047647 147650 .WORD 047647,147650 ; FPS: BEFORE, AFTER
7480 032012 100012 .WORD 100012 ; FEC AFTER ( 0 = N/A )
7481
7482
7483
7484
7485
7486
7487
7488 032014 000004
7489 032016 012705 032030

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7490 032022 004737 041242      JSR    PC,00LEXDT      ; GO TEST
7491
7492 032026 000414              BR     TST530          ;
7493
7494 032030      000000      LEXD16 ; TEST DATA SET LEXU-16:
7495 032030 177600 000000 000000 .WORD 177600,0,0,0 ; INITIAL AC FLOAT NUMBER
7496 032036 000000
7497 032040 100000 000000 000000 .WORD 140000,0,0,0 ; EXPECTED FLOAT RESULT
7498 032046 000000
7499 032050 000000 .WORD 0 ; EXPONENT TO BE LOADED
7500 032052 047707 047710 .WORD 047707,047710 ; FPS: BEFORE, AFTER
7501 032056 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
7502
7503
7504
7505 ;*****
7506 ;*TEST 530 TEST OF LDEXP/D INSTR, DATA SET LEXU-17
7507 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7508 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7509 ;*****
7509 032060 000004      TST530: SCOPE
7510 032062 012705 032074      MOV    0LEXD17,R5 ; PTR TO TEST DATA SET
7511 032066 004737 041242      JSR    PC,00LEXDT ; GO TEST
7512
7513
7514 032072 000414              BR     TST531          ;
7515
7516 032074      052525 052525 052525 LEXD17 ; TEST DATA SET LEXU-17:
7517 032102 052525 .WORD 152325,ALTP,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
7518 032104 000000
7519 032112 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7520 032114 000201
7521 032116 046651 046646 .WORD 201 ; EXPONENT TO BE LOADED
7522 032122 000000 .WORD 046651,046646 ; FPS: BEFORE, AFTER
7523 .WORD NA ; FEC AFTER ( 0 = N7A )
7524
7525 ;*****
7526 ;*TEST 531 TEST OF LDEXP/D INSTR, DATA SET LEXU-20
7527 ;* OVERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7528 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7529 ;*****
7530 032124 000004      TST531: SCOPE
7531 032126 012705 032140      MOV    0LEXD20,R5 ; PTR TO TEST DATA SET
7532 032132 004737 041242      JSR    PC,00LEXDT ; GO TEST
7533
7534 032136 000414              BR     TST532          ;
7535
7536 032140      177777 177777 177777 LEXD20 ; TEST DATA SET LEXU-20:
7537 032140 052377 .WORD 052377,M1,M1,M1 ; INITIAL AC FLOAT NUMBER
7538 032146 177777
7539 032150 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7540 032156 000000
7541 032160 000200 .WORD 200 ; EXPONENT TO BE LOADED
7542 032162 046711 046706 .WORD 046711,046706 ; FPS: BEFORE, AFTER
7543 032166 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
7544
7545

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7546 ;*****
7547 ;*TEST 532 TEST OF LDEXP/D INSTR, DATA SET LEXU-21
7548 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7549 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7550 ;*****
7551 032170 000004      TST532: SCOPE
7552 032172 012705 032204      MOV    0LEXD21,R5 ; PTR TO TEST DATA SET
7553 032176 004737 041242      JSR    PC,00LEXDT ; GO TEST
7554
7555 032202 000414              BR     TST533          ;
7556
7557 032204      000000 000000 000000 LEXD21 ; TEST DATA SET LEXU-21:
7558 032204 052201 .WORD 052201,2,3,0 ; INITIAL AC FLOAT NUMBER
7559 032212 000000
7560 032214 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7561 032222 000000
7562 032224 170000 .WORD -200 ; EXPONENT TO BE LOADED
7563 032226 045713 045704 .WORD 045713,045704 ; FPS: BEFORE, AFTER
7564 032232 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
7565
7566 ;*****
7567 ;*TEST 533 TEST OF LDEXP/D INSTR, DATA SET LEXU-22
7568 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7569 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
7570 ;*****
7571 032234 000004      TST533: SCOPE
7572 032236 012705 032250      MOV    0LEXD22,R5 ; PTR TO TEST DATA SET
7573 032242 004737 041242      JSR    PC,00LEXDT ; GO TEST
7574
7575 032246 000414              BR     TST534          ;
7576
7577 032250      125252 125252 125252 LEXD22 ; TEST DATA SET LEXU-22:
7578 032250 152252 .WORD 152252,ALTN,ALTN,ALTN ; INITIAL AC FLOAT NUMBER
7579 032256 125252
7580 032260 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7581 032266 000000
7582 032270 175777 .WORD -201 ; EXPONENT TO BE LOADED
7583 032272 045613 045604 .WORD 045613,045604 ; FPS: BEFORE, AFTER
7584 032276 000000 .WORD NA ; FEC AFTER ( 0 = N7A )
7585
7586 ;*****
7587 ;*TEST 534 TEST OF LDEXP/D INSTR, DATA SET LEXU-23
7588 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7589 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
7590 ;*****
7591 032300 000004      TST534: SCOPE
7592 032302 012705 032314      MOV    0LEXD23,R5 ; PTR TO TEST DATA SET
7593 032306 004737 041242      JSR    PC,00LEXDT ; GO TEST
7594
7595 032312 000414              BR     TST535          ;
7596
7597 032314      007417 007417 007417 LEXD23 ; TEST DATA SET LEXU-23:
7598 032314 052217 .WORD 052217,ALT4P,ALT4P,ALT4P ; INITIAL AC FLOAT NUMBER
7599 032322 007417

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7602 032324 000000 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7603 032332 000000
7604 032334 177576
7605 032336 045753 045744 .WORD -202 ; EXPONENT TO BE LOADED
7606 032342 000000 .WORD 045753,045744 ; FPS: BEFORE, AFTER
7607 .WORD NA ; FEC AFTER ( 0 = N7A )
7608
7609
7610 ;*****
7611 ;*TEST 535 TEST OF LDEXP/D INSTR, DATA SET LEXU-24
7612 ;* UNDERFLOW INTERRUPT ENABLE OFF, ALL OTHERS ON
7613 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7614 ;*****
7615 032344 000004 T535: SCOPE
7616 032346 012705 032360 MOV #LEXD24,R5 ; PTR TO TEST DATA SET
7617 032352 004737 041242 JBR PC,#0LEXDT ; GO TEST
7618
7619 BR T5356 ;;
7620
7621 LEXD24: ; TEST DATA SET LEXU-24:
7622 032360 152325 052525 052525 .WORD 152325,ALTP,ALTP ; INITIAL AC FLOAT NUMBER
7623 032366 052525
7624 032370 000000 000000 .WORD 0,0,0,0 ; EXPECTED FLOAT RESULT
7625 032400 177575
7626 032402 045653 045644 .WORD -203 ; EXPONENT TO BE LOADED
7627 032406 000000 .WORD 045653,045644 ; FPS: BEFORE, AFTER
7628 .WORD NA ; FEC AFTER ( 0 = N7A )
7629
7630

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7631 ;*****
7632 ;*TEST 536 TEST OF STEXP/F INSTR, DATA SET SEXF-1
7633 ;* ALL INTERRUPT ENABLES ON
7634 ;* SHORT FLOAT, SHORT INTEGER, TRUNCATE MODES
7635 ;*****
7636 032410 000004 T536: SCOPE
7637 032412 012705 032424 MOV #SEXF1,R5 ; PTR TO TEST DATA SET
7638 032416 004737 041432 JBR PC,#SEXF1 ; GO TEST
7639
7640 BR T537 ;;
7641
7642 SEXF1: ; TEST DATA SET SEXF-1:
7643 032424 .WORD M1,M1 ; INITIAL AC FLOAT NUMBER
7644 032430 000177 .WORD 177 ; EXPONENT EXPECTED TO BE STORED
7645 032432 047457 047440 .WORD 047457,047440 ; FPS: BEFORE, AFTER
7646
7647 ;*****
7648 ;*TEST 537 TEST OF STEXP/F INSTR, DATA SET SEXF-2
7649 ;* ALL INTERRUPT ENABLES ON
7650 ;* SHORT FLOAT, LONG INTEGER, ROUND MODES
7651 ;*****
7652 T537: SCOPE
7653 032436 000004 MOV #SEXF2,R5 ; PTR TO TEST DATA SET
7654 032440 012705 032452 JBR PC,#SEXF2 ; GO TEST
7655 032444 004737 041432
7656 BR T538 ;;
7657
7658 SEXF2: ; TEST DATA SET SEXF-2:
7659 032452 .WORD 000052,ALTN ; INITIAL AC FLOAT NUMBER
7660 032456 000100 .WORD 100 ; EXPONENT EXPECTED TO BE STORED
7661 032460 047517 047500 .WORD 047517,047500 ; FPS: BEFORE, AFTER
7662
7663 ;*****
7664 ;*TEST 540 TEST OF STEXP/F INSTR, DATA SET SEXF-3
7665 ;* ALL INTERRUPT ENABLES ON
7666 ;* SHORT FLOAT, LONG INTEGER, TRUNCATE MODES
7667 ;*****
7668 T538: SCOPE
7669 032464 000004 MOV #SEXF3,R5 ; PTR TO TEST DATA SET
7670 032466 012705 032500 JBR PC,#SEXF3 ; GO TEST
7671 032472 004737 041432
7672 BR T539 ;;
7673
7674 SEXF3: ; TEST DATA SET SEXF-3:
7675 032500 .WORD 140270,107070 ; INITIAL AC FLOAT NUMBER
7676 032504 000001 .WORD 1 ; EXPONENT EXPECTED TO BE STORED
7677 032506 047557 047540 .WORD 047557,047540 ; FPS: BEFORE, AFTER
7678
7679 ;*****
7680 ;*TEST 541 TEST OF STEXP/F INSTR, DATA SET SEXF-4
7681 ;* ALL INTERRUPT ENABLES ON
7682 ;* SHORT FLOAT, SHORT INTEGER, ROUND MODES
7683 ;*****
7684
7685
7686

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7607 032512 000004  
 7608 032514 012705 032526  
 7609 032520 004737 041432  
 7690  
 7691 032524 000405  
 7692  
 7693 032526  
 7694 032526 040125 007417  
 7695 032532 000000  
 7696 032534 047413 047404  
 7697  
 7698  
 7699  
 7700  
 7701  
 7702  
 7703  
 7704 032540 000004  
 7705 032542 012705 032554  
 7706 032546 004737 041432  
 7707  
 7708 032552 000405  
 7709  
 7710 032554  
 7711 032554 137760 170360  
 7712 032560 177777  
 7713 032562 047407 047410  
 7714  
 7715  
 7716  
 7717  
 7718  
 7719  
 7720  
 7721 032566 000004  
 7722 032570 012705 032602  
 7723 032574 004737 041432  
 7724  
 7725 032600 000405  
 7726  
 7727 032602  
 7728 032602 100307 070707  
 7729 032606 177091  
 7730 032610 047507 047510  
 7731  
 7732  
 7733  
 7734  
 7735  
 7736  
 7737  
 7738 032614 000004  
 7739 032616 012705 032630  
 7740 032622 004737 041432  
 7741  
 7742 032626 000405

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TSTS41: SCOPE
MOV    #SEXF4,R5      ; PTR TO TEST DATA SET
JBR    PC,#SEXFT      ; GO TEST
BR     TSTS42         ;;

SEXF4: ; TEST DATA SET SEXF-4:
.WORD  040125,ALT4P    ; INITIAL AC FLOAT NUMBER
.WORD  0          ; EXPONENT EXPECTED TO BE STORED
.WORD  047413,047404  ; FPS: BEFORE, AFTER

;*****
;TEST 542 TEST OF STEXP/F INSTR, DATA SET SEXF-5
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHURT INTEGEN, ROUND MODES
;*****
TSTS42: SCOPE
MOV    #SEXF5,R5      ; PTR TO TEST DATA SET
JBR    PC,#SEXFT      ; GO TEST
BR     TSTS43         ;;

SEXF5: ; TEST DATA SET SEXF-5:
.WORD  137760,ALT4N    ; INITIAL AC FLOAT NUMBER
.WORD  -1          ; EXPONENT EXPECTED TO BE STORED
.WORD  047407,047410  ; FPS: BEFORE, AFTER

;*****
;TEST 543 TEST OF STEXP/F INSTR, DATA SET SEXF-6
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, LONG INTEGER, ROUND MODES
;*****
TSTS43: SCOPE
MOV    #SEXF6,R5      ; PTR TO TEST DATA SET
JBR    PC,#SEXFT      ; GO TEST
BR     TSTS44         ;;

SEXF6: ; TEST DATA SET SEXF-6:
.WORD  100307,070707  ; INITIAL AC FLOAT NUMBER
.WORD  -177          ; EXPONENT EXPECTED TO BE STORED
.WORD  047507,047510  ; FPS: BEFORE, AFTER

;*****
;TEST 544 TEST OF STEXP/F INSTR, DATA SET SEXF-7
;* ALL INTERRUPT ENABLES ON
;* SHORT FLOAT, SHORT INTEGEN, TRUNCATE MODES
;*****
TSTS44: SCOPE
MOV    #SEXF7,R5      ; PTR TO TEST DATA SET
JBR    PC,#SEXFT      ; GO TEST
BR     TSTS45         ;;

```

7743  
 7744 032630  
 7745 032630 000000 000000  
 7746 032634 177000  
 7747 032636 047447 047450  
 7748  
 7749  
 7750

```

SEXF7: ; TEST DATA SET SEXF-7:
.WORD  0,0          ; INITIAL AC FLOAT NUMBER
.WORD  -200         ; EXPONENT EXPECTED TO BE STORED
.WORD  047447,047450 ; FPS: BEFORE, AFTER

```



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7751 ;*****
7752 ;*TEST 545 TEST OF STEAP/D INSTR, DATA SET SEXD-1
7753 ;* ALL INTERRUPT ENABLES ON
7754 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7755 ;*****
7756 T545: SCOPE
7757 MOV #SEXD1,R5 ; PTR TO TEST DATA SET
7758 JSR PC,#SEADT ; GO TEST
7759
7760 BR T545 ;
7761
7762 ;*****
7763 ;*TEST 546 TEST OF STEAP/D INSTR, DATA SET SEXD-2
7764 ;* ALL INTERRUPT ENABLES ON
7765 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7766 ;*****
7767 T546: SCOPE
7768 MOV #SEXD2,R5 ; PTR TO TEST DATA SET
7769 JSR PC,#SEADT ; GO TEST
7770
7771 BR T546 ;
7772
7773 ;*****
7774 ;*TEST 547 TEST OF STEAP/D INSTR, DATA SET SEXD-3
7775 ;* ALL INTERRUPT ENABLES ON
7776 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
7777 ;*****
7778 T547: SCOPE
7779 MOV #SEXD3,R5 ; PTR TO TEST DATA SET
7780 JSR PC,#SEADT ; GO TEST
7781
7782 BR T547 ;
7783
7784 ;*****
7785 ;*TEST 548 TEST OF STEAP/D INSTR, DATA SET SEXD-4
7786 ;* ALL INTERRUPT ENABLES ON
7787 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7788 ;*****
7789 T548: SCOPE
7790 MOV #SEXD4,R5 ; PTR TO TEST DATA SET
7791 JSR PC,#SEADT ; GO TEST
7792
7793 BR T548 ;
7794
7795 ;*****
7796 ;*TEST 549 TEST OF STEAP/D INSTR, DATA SET SEXD-5
7797 ;* ALL INTERRUPT ENABLES ON
7798 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
7799 ;*****
7800 T549: SCOPE
7801 MOV #SEXD5,R5 ; PTR TO TEST DATA SET
7802 JSR PC,#SEADT ; GO TEST
7803
7804 BR T549 ;
7805
7806 ;*****
7807 ;*TEST 550 TEST OF STEAP/D INSTR, DATA SET SEXD-6
7808 ;* ALL INTERRUPT ENABLES ON
7809 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7810 ;*****
7811 T550: SCOPE
7812 MOV #SEXD6,R5 ; PTR TO TEST DATA SET
7813 JSR PC,#SEADT ; GO TEST
7814
7815 BR T550 ;
7816
7817 ;*****
7818 ;*TEST 551 TEST OF STEAP/D INSTR, DATA SET SEXD-7
7819 ;* ALL INTERRUPT ENABLES ON
7820 ;* LONG FLOAT, SHORT INTEGER, ROUND MODES
7821 ;*****
7822 T551: SCOPE
7823 MOV #SEXD7,R5 ; PTR TO TEST DATA SET
7824 JSR PC,#SEADT ; GO TEST
7825
7826 BR T551 ;
7827
7828 ;*****
7829 ;*TEST 552 TEST OF STEAP/D INSTR, DATA SET SEXD-8
7830 ;* ALL INTERRUPT ENABLES ON
7831 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7832 ;*****
7833 T552: SCOPE
7834 MOV #SEXD8,R5 ; PTR TO TEST DATA SET
7835 JSR PC,#SEADT ; GO TEST
7836
7837 BR T552 ;
7838
7839 ;*****
7840 ;*TEST 553 TEST OF STEAP/D INSTR, DATA SET SEXD-9
7841 ;* ALL INTERRUPT ENABLES ON
7842 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7843 ;*****
7844 T553: SCOPE
7845 MOV #SEXD9,R5 ; PTR TO TEST DATA SET
7846 JSR PC,#SEADT ; GO TEST
7847
7848 BR T553 ;
7849
7850 ;*****
7851 ;*TEST 554 TEST OF STEAP/D INSTR, DATA SET SEXD-10
7852 ;* ALL INTERRUPT ENABLES ON
7853 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7854 ;*****
7855 T554: SCOPE
7856 MOV #SEXD10,R5 ; PTR TO TEST DATA SET
7857 JSR PC,#SEADT ; GO TEST
7858
7859 BR T554 ;
7860
7861 ;*****
7862 ;*TEST 555 TEST OF STEAP/D INSTR, DATA SET SEXD-11
7863 ;* ALL INTERRUPT ENABLES ON
7864 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7865 ;*****
7866 T555: SCOPE
7867 MOV #SEXD11,R5 ; PTR TO TEST DATA SET
7868 JSR PC,#SEADT ; GO TEST
7869
7870 BR T555 ;

```

```

7807 ;* ALL INTERRUPT ENABLES ON
7808 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7809 ;*****
7810 T550: SCOPE
7811 MOV #SEXD4,R5 ; PTR TO TEST DATA SET
7812 JSR PC,#SEADT ; GO TEST
7813
7814 BR T550 ;
7815
7816 ;*****
7817 ;*TEST 551 TEST OF STEAP/D INSTR, DATA SET SEXD-5
7818 ;* ALL INTERRUPT ENABLES ON
7819 ;* LONG FLOAT, LONG INTEGER, TRUNCATE MODES
7820 ;*****
7821 T551: SCOPE
7822 MOV #SEXD5,R5 ; PTR TO TEST DATA SET
7823 JSR PC,#SEADT ; GO TEST
7824
7825 BR T551 ;
7826
7827 ;*****
7828 ;*TEST 552 TEST OF STEAP/D INSTR, DATA SET SEXD-6
7829 ;* ALL INTERRUPT ENABLES ON
7830 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7831 ;*****
7832 T552: SCOPE
7833 MOV #SEXD6,R5 ; PTR TO TEST DATA SET
7834 JSR PC,#SEADT ; GO TEST
7835
7836 BR T552 ;
7837
7838 ;*****
7839 ;*TEST 553 TEST OF STEAP/D INSTR, DATA SET SEXD-7
7840 ;* ALL INTERRUPT ENABLES ON
7841 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7842 ;*****
7843 T553: SCOPE
7844 MOV #SEXD7,R5 ; PTR TO TEST DATA SET
7845 JSR PC,#SEADT ; GO TEST
7846
7847 BR T553 ;
7848
7849 ;*****
7850 ;*TEST 554 TEST OF STEAP/D INSTR, DATA SET SEXD-8
7851 ;* ALL INTERRUPT ENABLES ON
7852 ;* LONG FLOAT, SHORT INTEGER, TRUNCATE MODES
7853 ;*****
7854 T554: SCOPE
7855 MOV #SEXD8,R5 ; PTR TO TEST DATA SET
7856 JSR PC,#SEADT ; GO TEST
7857
7858 BR T554 ;
7859
7860 ;*****
7861 ;*TEST 555 TEST OF STEAP/D INSTR, DATA SET SEXD-9
7862 ;* ALL INTERRUPT ENABLES ON
7863 ;* LONG FLOAT, LONG INTEGER, ROUND MODES
7864 ;*****
7865 T555: SCOPE
7866 MOV #SEXD9,R5 ; PTR TO TEST DATA SET
7867 JSR PC,#SEADT ; GO TEST
7868
7869 BR T555 ;

```

```

7063
7064 033076 000004
7065 033100 012705 033112
7066 033104 004737 041506
7067
7068 033110 000407
7069
7070 033112
7071 033112 000177 177777 177777
7072 033120 177777
7073 033122 177600
7074 033124 047607 047610
7075
7076
7077

```

```

T553: SCOPE
MOV 05EXD7,R5 ; PTR TO TEST DATA SET
JSR PC,018EADT ; GO TEST
BR T5534 ;;

SEXD7: ; TEST DATA SET SEXD=7:
.WORD 000177,R1,R1,R1 ; INITIAL AC FLOAT NUMBER
.WORD -200 ; EXPONENT EXPECTED TO BE STORED
.WORD 047607,047610 ; FPS: BEFORE, AFTER

```

```

7078
7079
7080
7081 033130
7082 033130 000004
7083 033132 005037 001310
7084 033136 005037 001104
7085 033142 005037 001102
7086
7087
7088
7089
7090
7091
7092
7093
7094
7095
7096
7097 033146 076000 000022
7098 033152 032700 000020
7099 033156 001423
7100
7101 033160 032777 000002 145756
7102 033166 001017
7103
7104 033170 012701 010000
7105 033174 076000 000144
7106 033200 000100
7107 033202 001402
7108 033204 000100
7109 033206 000401
7110 033210 000100
7111 033212 076000 000344
7112
7113 033216 000100
7114 033220 001002
7115 033222 000137 003072
7116
7117
7118
7119
7120
7121
7122
7123
7124
7125
7126
7127
7128 033226
7129 033226 005037 001104
7130 033232 005037 001102
7131 033236 005037 001310
7132 033242 005237 001332
7133 033246 042737 100000 001332

```

```

T554: ;FORCE LAST TEST NUMBER
SCOPE ;CHECK FOR TEST ITERATIONS HERE
CLR STIMES ;DONT ITERATE THIS "TEST"
CLR SERFLG ;NO ERRORS HERE
CLR STSTNN ;ZAP TEST # WHEN DONE WITH A PASS

;IF TEST ONLY EITHER HFP OR WFP. ENTER "EUP" ROUTINE DIRECTLY

; IF IN ALTERNATE HFP/WFP MODE,
; COMPLEMENT FLAG<3>, HFP ENABLE BIT,
; ENTER EOP ROUTINE ONLY IF ABOUT TO TEST HFP NEXT,
; TESTING SEQUENCE IS: PASS#1 HFP SUB-PASS
; PASS#1 WFP SUB-PASS
; PASS#2 HFP SUB-PASS
; ...

MED ,RWHANI ;GET WHANI INTO R0
BIT #BIT04,R0 ;HFP PRESENT, 0=NONE
BEO #EOP ;EXIT IF NONE

BIT #SW01,0SWR ;HFP OR WFP TEST ONLY
BNE #EOP ;=ALTERNATE HFP AND WFP TESTS

MOV #BIT12,R1 ;HFP PRESENT, AND IN ALTERNATE MODE
MED ,RFLAG ;SO READ FLAGS
BIT R1,R0 ;COMPLEMENT FLAG<3>=BIT12=HFP ENABLE FLAG
BEO 10 ;
BIC R1,R0 ;CLEAR BIT 12
BR 20 ;
BIS R1,R0 ;SET BIT 12
MED ,RFLAG ;REWRITE FLAGS

BIT R1,R0 ;HFP OR WFP NEXT ?
BNE #EOP ;IF HFP AGAIN, START NEW PASS
JMP #SUBPAS ;IF WFP, NEXT SUBPAS

;*****
.SBTTL END OF PASS ROUTINE (MODIFIED SISMAC)

;INCREMENT THE PASS NUMBER (SPASS)
;IF SW<10>=00, USING BELL ON PASS END
;IF THERE'S A MONITOR, GO TO IT
; ELSE JUMP TO NEWPAS

SEOP: ;
CLR SERFLG ;ZERO ERROR COUNT
CLR STSTNN ;ZERO TEST NUMBER
CLR STIMES ;ZERO NUMBER OF ITERATIONS
INC SPASS ;INCREMENT PASS COUNT,
BIC #100000,0PASS ; BUT NEVER LET IN GO NEGATIVE

```

7934	033254	005327		DEC	(PC)+	;PASS LOOP 7
7935	033256	000001		SEOPCT	WORD	1
7936	033260	003021		BGT	\$DOAGN	
7937	033262	012737		MOV	(PC)+,0(PC)+	;YES
7938	033264	000001		SENDCT	WORD	1
7939	033266	033256		SEOPCT		
7940	033270	032777	002000 145046	BIT	\$BWI0,\$BWR	;BELL ON PASS END ?
7941	033276	001002		BNE	\$GET42	;NO
7942	033300	104401	001314	TYPE	,\$BELL	;YES
7943						
7944	033304	013700	000042	\$GET42	MOV	0*42,R0
7945	033310	001405		BEU	\$DOAGN	;GET MONITOR ADDRESS
7946	033312	000005		RESET		;NO MONITOR
7947						;CLEAR WORLD
7948	033314	004710		SENDAD	JSR	PC,(R0)
7949	033316	000240		NOP		;GO TO MONITOR
7950	033320	000240		NOP		;RESERVED FOR ACT11
7951	033322	000240		NOP		
7952						
7953	033324	000137	003034	\$DOAGN	JMP	0*NEWPAD
7954						;RETURN
7955						

7956				.SHTL	SUBR TO TEST THE CMPF INSTRUCTION	
7957						
7958	033330			CMPFI		
7959	033330	012700	000007	MOV	\$7,R0	; LOAD STMP0=6
7960	033334	014501		MOV	R0,R1	; WITH TEST DATA SETS
7961	033336	012702	001230	MOV	\$STMP0,R2	; FOR DISPLAY LATER
7962	033342	012122		MOV	(R1)+,(R2)+	
7963	033344	017002		SUB	R0,-2	
7964	033346	012737	033354 001112	MOV	\$CMPFL,\$LPERR	; ERROR LOOPING ADDRESS
7965						
7966	033354	170001		CMPFL	SETF	; F MODE
7967	033356	172715		LDF	(R0),AC3	; INITIAL AC FLOAT NUMBER
7968	033360	170165	000010	LDFPB	10(R0)	; INITIAL FPS
7969						
7970	033364	173765	000004	CMPFI	CMPF	4(R0),AC3
7971						; (MEM)-(AC3)
7972	033370	170237	002000	STFPS	FPS	; STORE FPS AFTER
7973	033374	170337	002002	STF	FEC	; STORE FEC/FEA AFTER
7974	033400	174337	001170	STF	AC3,\$REG0	; STORE AC NUMBER
7975						
7976	033404	023765	002000 000012	CMP	FPS,12(R0)	; CHECK FPS
7977	033412	001401		BEQ	650	; FPS IS OK
7978	033414	104002		ERROR	2	; FPS BAD
7979	033416	005765	000014	650:	IST	14(R0)
7980	033422	100014		BPL	660	; DOES FEC/FEA APPLY?
7981	033424	012737	033364 002014	MOV	\$CMPFI,EXPFEA	; GET EXPECTED FEA
7982	033432	143765	002002 000014	CMPB	FEC,14(R0)	; COMPARE FEC-S
7983	033440	001004		BNE	640	; NOT EQUAL
7984	033442	023737	002004 002014	CMP	FEA,EXPFEA	; COMPARE FEA-S
7985	033450	001401		BEQ	660	; FEC, FEA OK
7986	033452	104012		640:	ERROR	12
7987	033454			660:		; FEC OR FEA ARE BAD
7988						
7989	033454	023715	001170	CMP	\$REG0,(R0)	; 1ST WORD OF RESULT CHECK?
7990	033460	001004		BNE	670	; NO
7991	033462	023765	001172 000002	CMP	\$REG1,2(R0)	; 2ND WORD OF RESULT CHECK?
7992	033470	001401		BEQ	680	; ALL WORDS OK
7993	033472	104021		670:	ERROR	21
7994	033474			680:		; NUMBERS NOT EQUAL
7995						
7996	033474	000201		RTS	PC	; RETURN TO TEST CALLER
7997						
7998						
7999						
8000				.SHTL	SUBR TO TEST THE CMPD INSTRUCTION	
8001						
8002						
8003	033476			CMPDI		
8004	033476	012700	000013	MOV	\$13,R0	; LOAD STMP0=12
8005	033502	010501		MOV	R0,R1	; WITH TEST DATA SETS
8006	033504	012702	001230	MOV	\$STMP0,R2	; FOR DISPLAY LATER
8007	033510	012122		MOV	(R1)+,(R2)+	
8008	033512	017002		SUB	R0,-2	
8009	033514	012737	033522 001112	MOV	\$CMPDL,\$LPERR	; ERROR LOOPING ADDRESS
8010						
8011	033522	170011		CMPDL	SETD	; D MODE
	033524	172615		LDD	(R0),AC2	; INITIAL AC FLOAT NUMBER

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0012 033526 170165 000020          LDFFS 20(R5)          ; INITIAL FPS
0013
0014 033532 173665 000010          CMPDI: CMPD 10(R5),AC2 ; (MEM)-(AC2)
0015
0016 033536 170237 002000          STFPS FPS           ; STORE FPS AFTER
0017 033542 170337 002002          STST FEC           ; STORE FEC/FEA AFTER
0018 033546 174237 001170          STD AC2,0REG0      ; STORE AC AFTER
0019
0020 033552 023765 002000 000022          CMP FPS,22(R5)      ; CHECK FPS
0021 033560 001401          BEQ 658            ; FPS IS OK
0022 033562 104005          ERROR 5           ; FPS BAD
0023 033564 005765 000024          658: TST 24(R5)      ; DOES FEC/FEA APPLI?
0024 033570 100014          BPL 668           ; NU = SKIP TEST
0025 033572 012737 033532 002014          MOV 0CMPDI,EXPFEA  ; GET EXPECTED FEA
0026 033600 123765 002002 000024          CMPB FEC,24(R5)    ; COMPARE FEC-S
0027 033606 001004          BNE 648           ; NOT EQUAL
0028 033610 023737 002004 002014          CMP FEA,EXPFEA     ; COMPARE FEA-S
0029 033616 001401          BEQ 668           ; FEC, FEA OK
0030 033620 104015          648: ERROR 15      ; FEC OR FEA ARE BAD
0031 033622
0032
0033 033622 023715 001170          CMP 0REG0,(R5)     ; 1ST WORD OF RESULT CHECK?
0034 033626 001014          BNE 678           ; NO
0035 033630 023765 001172 000002          CMP 0REG1,2(R5)    ; 2ND WORD OF RESULT CHECK?
0036 033636 001010          BNE 678           ; NO
0037 033640 023765 001174 000004          CMP 0REG2,4(R5)    ; 3RD WORD OF RESULT CHECK?
0038 033646 001004          BNE 678           ; NO
0039 033650 023765 001176 000006          CMP 0REG3,6(R5)    ; 4TH WORD OF RESULT CHECK?
0040 033656 001401          BEQ 688           ; ALL WORDS OK
0041 033660 104022          678: ERROR 22      ; NUMBERS NOT EQUAL
0042 033662
0043
0044 033662 000207          RTS PC            ; RETURN TO TEST CALLER
0045
    
```

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0046          .SBTTL SUBR TO TEST THE ADDF INSTRUCTION
0047
0048          ADDFI:
0049 033664 012700 000011          MOV 011,R0         ; LOAD 0100-10
0050 033670 010501          MOV R0,R1         ; WITH TEST DATA SETS
0051 033672 012702 001230          MOV 01TMP0,R2     ; FOR DISPLAY LATER
0052 033676 012122          MOV (R1),(R2)+    ;
0053 033700 077002          SOB R0,-2         ;
0054 033702 012737 033710 001112          MOV 0ADDFL,0LPERA ; ERROR LOOPING ADDRESS
0055
0056 033710 170001          ADDFL: SETF        ; F MODE
0057 033712 172515          LDF (R5),AC1      ; INITIAL AC FLOAT NUMBER
0058 033714 170165 000014          LDFFS 14(R5)      ; INITIAL FPS
0059
0060 033720 172165 000004          ADDFI: ADDF 4(R5),AC1 ; (AC1)+(MEM)->AC1
0061
0062 033724 170237 002000          STFPS FPS         ; STORE FPS AFTER
0063 033730 170337 002002          STST FEC         ; STORE FEC/FEA AFTER
0064 033734 174137 001170          STF AC1,0REG0     ; RESULT OF ADDF
0065
0066 033740 023765 002000 000016          CMP FPS,16(R5)    ; CHECK FPS
0067 033746 001401          BEQ 658           ; FPS IS OK
0068 033750 104004          ERROR 4           ; FPS BAD
0069 033752 005765 000020          658: TST 20(R5)    ; DOES FEC/FEA APPLI?
0070 033756 100014          BPL 668           ; NU = SKIP TEST
0071 033760 012737 033720 002014          MOV 0ADDFI,EXPFEA ; GET EXPECTED FEA
0072 033766 123765 002002 000020          CMPB FEC,20(R5)    ; COMPARE FEC-S
0073 033774 001004          BNE 648           ; NOT EQUAL
0074 033776 023737 002004 002014          CMP FEA,EXPFEA     ; COMPARE FEA-S
0075 034004 001401          BEQ 668           ; FEC, FEA OK
0076 034006 104014          648: ERROR 14     ; FEC OR FEA ARE BAD
0077 034010
0078
0079 034010 023765 001170 000010          CMP 0REG0,10(R5)   ; 1ST WORD OF RESULT CHECK?
0080 034016 001004          BNE 678           ; NO
0081 034020 023765 001172 000012          CMP 0REG1,12(R5)   ; 2ND WORD OF RESULT CHECK?
0082 034026 001401          BEQ 688           ; ALL WORDS OK
0083 034030 104023          678: ERROR 23     ; NUMBERS NOT EQUAL
0084 034032
0085
0086 034032 000207          RTS PC            ; RETURN TO TEST CALLER
0087
0088          *****
0089          .SBTTL SUBR TO TEST THE ADDD INSTRUCTION
0090
0091          ADDDI:
0092 034034 012700 000017          MOV 017,R0         ; LOAD 0100-16
0093 034040 010501          MOV R0,R1         ; WITH TEST DATA SETS
0094 034042 012702 001230          MOV 01TMP0,R2     ; FOR DISPLAY LATER
0095 034046 012122          MOV (R1),(R2)+    ;
0096 034050 077002          SOB R0,-2         ;
0097 034052 012737 034060 001112          MOV 0ADDL,0LPERA ; ERROR LOOPING ADDRESS
0098
0099 034060 170011          ADDDI: SETD        ; D MODE
0100 034062 172415          LDD (R5),AC0      ; INITIAL AC FLOAT NUMBER
0101 034064 170165 000030          LDFFS 30(R5)      ; INITIAL FPS
    
```

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0102
0103 034070 172065 000010 ADDDI: ADD 10(R5),AC0 ; (AC0)+(MEM)->AC0
0104
0105 034074 170237 002000 STFPS FFS ; STORE FFS AFTER
0106 034100 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0107 034104 174037 001170 STD AC0,0REG0 ; RESULT OF ADD
0108
0109 034110 023765 002000 000032 CMP FFS,32(R5) ; CHECK FFS
0110 034116 001401 BEQ 656 ; FFS IS OK
0111 034120 104007 ERROR 7 ; FFS BAD
0112 034122 005765 000034 656: TST 34(R5) ; DOES FEC/FEA APPLT
0113 034126 100014 BPL 668 ; NU - SKIP TEST
0114 034130 012737 034070 002014 MOV #ADDI,EXPFEA ; GET EXPECTED FEA
0115 034136 123765 002002 000034 CMPB FEC,34(R5) ; COMPARE FEC-S
0116 034144 001004 BNE 648 ; NOT EQUAL
0117 034146 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0118 034154 001401 BEQ 666 ; FEC, FEA OK
0119 034156 104017 648: ERROR 17 ; FEC OR FEA ARE BAD
0120 034160 666:
0121
0122 034160 023765 001170 000020 CMP #REG0,20(R5) ; 1ST WORD OF RESULT CHECK?
0123 034166 001014 BNE 678 ; NO
0124 034170 023765 001172 000022 CMP #REG1,22(R5) ; 2ND WORD OF RESULT CHECK?
0125 034176 001010 BNE 678 ; NO
0126 034200 023765 001174 000024 CMP #REG2,24(R5) ; 3RD WORD OF RESULT CHECK?
0127 034206 001004 BNE 678 ; NO
0128 034210 023765 001176 000026 CMP #REG3,26(R5) ; 4TH WORD OF RESULT CHECK?
0129 034216 001401 BEQ 688 ; ALL WORDS OK
0130 034220 104024 678: ERROR 24 ; NUMBERS NOT EQUAL
0131 034222 688:
0132
0133 034222 000207 RTS PC ; RETURN TO TEST CALLER
0134
  
```

```

0135 .SBTTL SUBR TO TEST THE SUBF INSTRUCTION
0136
0137 034224 SUBFT:
0138 034224 012700 000011 MOV #11,R0 ; LOAD SIMPO=10
0139 034230 010501 MOV R0,R1 ; WITH TEST DATA SETS
0140 034232 012702 001230 MOV #TMP0,R2 ; FOR DISPLAY LATER
0141 034236 012122 MOV (R1)+(R2)+ ;
0142 034240 017002 SOB R0,-2 ;
0143 034242 012737 034250 001112 MOV #SUBFL,#LPEPR ; ERROR LOOPING ADDRESS
0144
0145 034250 170001 SUBFL: SETF ; F MODE
0146 034252 172415 LDF (R5),AC0 ; INITIAL AC FLOAT NUMBER
0147 034254 170165 000014 LDFPS 14(R5) ; INITIAL FFS
0148
0149 034260 173065 000004 SUBFI: SUBF 4(R5),AC0 ; (AC0)-(MEM)->AC0
0150
0151 034264 170237 002000 STFPS FFS ; STORE FFS AFTER
0152 034270 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0153 034274 174037 001170 STF AC0,0REG0 ; RESULT OF SUBF
0154
0155 034300 023765 002000 000016 CMP FFS,16(R5) ; CHECK FFS
0156 034306 001401 BEQ 656 ; FFS IS OK
0157 034310 104004 ERROR 4 ; FFS BAD
0158 034312 005765 000020 656: TST 20(R5) ; DOES FEC/FEA APPLT
0159 034316 100014 BPL 668 ; NU - SKIP TEST
0160 034320 012737 034260 002014 MOV #SUBFI,EXPFEA ; GET EXPECTED FEA
0161 034326 123765 002002 000020 CMPB FEC,20(R5) ; COMPARE FEC-S
0162 034334 001004 BNE 648 ; NOT EQUAL
0163 034336 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0164 034344 001401 BEQ 668 ; FEC, FEA OK
0165 034346 104014 648: ERROR 14 ; FEC OR FEA ARE BAD
0166 034350 666:
0167
0168 034350 023765 001170 000010 CMP #REG0,10(R5) ; 1ST WORD OF RESULT CHECK?
0169 034356 001004 BNE 678 ; NO
0170 034360 023765 001172 000012 CMP #REG1,12(R5) ; 2ND WORD OF RESULT CHECK?
0171 034366 001401 BEQ 688 ; ALL WORDS OK
0172 034370 104023 678: ERROR 23 ; NUMBERS NOT EQUAL
0173 034372 688:
0174
0175 034372 000207 RTS PC ; RETURN TO TEST CALLER
0176
0177 ;*****
0178 .SBTTL SUBR TO TEST THE SUBD INSTRUCTION
0179
0180 034374 SUBDT:
0181 034374 012700 000017 MOV #17,R0 ; LOAD SIMPO=16
0182 034400 010501 MOV R0,R1 ; WITH TEST DATA SETS
0183 034402 012702 001230 MOV #TMP0,R2 ; FOR DISPLAY LATER
0184 034406 012122 MOV (R1)+(R2)+ ;
0185 034410 017002 SOB R0,-2 ;
0186 034412 012737 034420 001112 MOV #SUBDL,#LPEPR ; ERROR LOOPING ADDRESS
0187
0188 034420 170011 SUBDL: SETD ; D MODE
0189 034422 172715 LDD (R5),AC3 ; INITIAL AC FLOAT NUMBER
0190 034424 170165 000030 LDFPS 30(R5) ; INITIAL FFS
  
```

```

0191
0192 034430 173365 000010 SUBDI: SUBD 10(R5),AC3 ; (AC3)-(MEM)->AC3
0193
0194 034434 170237 002000 STFPS FPS ; STORE FPS AFTER
0195 034440 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0196 034444 174337 001170 STD AC3,0REG0 ; RESULT OF SUBD
0197
0198 034450 023765 002000 000032 CMP FPS,32(R5) ; CHECK FPS
0199 034456 001401 BEQ 656 ; FPS IS OK
0200 034460 104007 ERROR 7 ; FPS BAD
0201 034462 005765 000034 658: TST 34(R5) ; DOES FEC/FEA APPLY?
0202 034466 100014 BPL 668 ; NO - SKIP TEST
0203 034470 012737 034430 002014 MOV #SUBDI,EXPFEA ; GET EXPECTED FEA
0204 034476 123765 002002 000034 CMPB FEC,34(R5) ; COMPARE FEC-S
0205 034504 001004 BNE 648 ; NOT EQUAL
0206 034506 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0207 034514 001401 BEQ 668 ; FEC, FEA OK
0208 034516 104017 648: ERROR 17 ; FEC OR FEA ARE BAD
0209 034520 668:
0210
0211 034520 023765 001170 000020 CMP #REG0,20(R5) ; 1ST WORD OF RESULT CHECK?
0212 034526 001014 BNE 678 ; NO
0213 034530 023765 001172 000022 CMP #REG1,22(R5) ; 2ND WORD OF RESULT CHECK?
0214 034536 001010 BNE 678 ; NO
0215 034540 023765 001174 000024 CMP #REG2,24(R5) ; 3RD WORD OF RESULT CHECK?
0216 034546 001004 BNE 678 ; NO
0217 034550 023765 001176 000026 CMP #REG3,26(R5) ; 4TH WORD OF RESULT CHECK?
0218 034556 001401 BEQ 688 ; ALL WORDS OK
0219 034560 104024 678: ERROR 24 ; NUMBERS NOT EQUAL
0220 034562 688:
0221
0222 034562 000207 RTS PC ; RETURN TO TEST CALLER
0223

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```

0224 .SMTL SUBR TO TEST THE MULF INSTRUCTION
0225
0226 034564 MULFI:
0227 034564 012700 000011 MOV #17,R0 ; LOAD STMP0=10
0228 034570 010501 MOV R5,R1 ; WITH TEST DATA SETS
0229 034572 012702 001230 MOV #STMP0,R2 ; FOR DISPLAY LATER
0230 034576 012122 MOV (R1)+(R2)+ ;
0231 034600 017002 SUB #0,-2 ;
0232 034602 012737 034610 001112 MOV #MULFI,0LPEHR ; ERROR LOOPING ADDRESS
0233
0234 034610 170001 MULFI: SETF ; F MODE
0235 034612 172715 LDF (R5),AC3 ; INITIAL AC FLOAT NUMBER
0236 034614 170165 000014 LOFPS 14(R5) ; INITIAL FPS
0237
0238 034620 171365 000004 MULFI: MULF 4(R5),AC3 ; (AC3)*(MEM)->AC3
0239
0240 034624 170237 002000 STFPS FPS ; STORE FPS AFTER
0241 034630 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0242 034634 174337 001170 STI AC3,0REG0 ; RESULT OF MULF
0243
0244 034640 023765 002000 000016 CMP FPS,16(R5) ; CHECK FPS
0245 034646 001401 BEQ 658 ; FPS IS OK
0246 034650 104004 ERROR 4 ; FPS BAD
0247 034652 005765 000020 658: TST 20(R5) ; DOES FEC/FEA APPLY?
0248 034656 100014 BPL 668 ; NO - SKIP TEST
0249 034660 012737 034620 002014 MOV #MULFI,EXPFEA ; GET EXPECTED FEA
0250 034666 123765 002002 000020 CMPB FEC,20(R5) ; COMPARE FEC-S
0251 034674 001004 BNE 648 ; NOT EQUAL
0252 034676 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0253 034704 001401 BEQ 668 ; FEC, FEA OK
0254 034706 104014 648: ERROR 14 ; FEC OR FEA ARE BAD
0255 034710 668:
0256
0257 034710 023765 001170 000010 CMP #REG0,10(R5) ; 1ST WORD OF RESULT CHECK?
0258 034716 001004 BNE 678 ; NO
0259 034720 023765 001172 000012 CMP #REG1,12(R5) ; 2ND WORD OF RESULT CHECK?
0260 034726 001401 BEQ 688 ; ALL WORDS OK
0261 034730 104025 678: ERROR 25 ; NUMBERS NOT EQUAL
0262 034732 688:
0263
0264 034732 000207 RTS PC ; RETURN TO TEST CALLER
0265
0266
0267
0268 .SMTL SUBR TO TEST THE MULD INSTRUCTION
0269
0270 034734 MULDI:
0271 034740 010501 MOV #17,R0 ; LOAD STMP0=10
0272 034742 012702 001230 MOV R5,R1 ; WITH TEST DATA SETS
0273 034746 012122 MOV #STMP0,R2 ; FOR DISPLAY LATER
0274 034750 017002 MOV (R1)+(R2)+ ;
0275 034752 012737 034740 001112 SUB #0,-2 ;
0276 034754 012737 034740 001112 MOV #MULDI,0LPEHR ; ERROR LOOPING ADDRESS
0277
0278 034760 170011 MULDI: SETD ; D MODE
0279 034762 172615 LDD (R5),AC3 ; INITIAL AC FLOAT NUMBER
0280 034764 170165 000030 LOFPS 30(R5) ; INITIAL FPS

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0200
0201 034770 171265 000010 MULD: MULD 10(R5),AC2 ; (AC2)*(MEM)->AC2
0202
0203 034774 170237 002000 STFPS FFS ; STORE FFS AFTER
0204 035000 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0205 035004 174237 001170 STD AC2,0REG0 ; RESULT OF MULD
0206
0207 035010 023765 002000 000032 CMP FFS,32(R5) ; CHECK FFS
0208 035016 001401 BEQ 658 ; FFS IS OK
0209 035020 104007 ERROR 7 ; FFS BAD
0210 035022 005765 000034 658: TST 34(R5) ; DOES FEC/FEA APPLY?
0211 035026 100014 BPL 608 ; NU - SKIP TEST
0212 035030 012737 034770 002014 MOV #MULD,EXPFEA ; GET EXPECTED FEA
0213 035036 123765 002002 000034 CMPB FEC,34(R5) ; COMPARE FEC-S
0214 035044 001004 BNE 648 ; NOT EQUAL
0215 035046 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0216 035054 001401 BEQ 608 ; FEC, FEA OK
0217 035056 104017 648: ERROR 17 ; FEC OR FEA ARE BAD
0218 035060 668:
0219
0220 035060 023765 001170 000020 CMP $REG0,20(R5) ; 1ST WORD OF RESULT CHECK?
0221 035066 001014 BNE 678 ; NO
0222 035070 023765 001172 000022 CMP $REG1,22(R5) ; 2ND WORD OF RESULT CHECK?
0223 035076 001010 BNE 678 ; NO
0224 035100 023765 001174 000024 CMP $REG2,24(R5) ; 3RD WORD OF RESULT CHECK?
0225 035106 001004 BNE 678 ; NO
0226 035110 023765 001176 000026 CMP $REG3,26(R5) ; 4TH WORD OF RESULT CHECK?
0227 035116 001401 BEQ 688 ; ALL WORDS OK
0228 035120 104026 678: ERROR 26 ; NUMBERS NOT EQUAL
0229 035122 688:
0230
0231 035122 000207 RTS PC ; RETURN TO TEST CALLER
0232

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0313
0314
0315 035124
0316 035124 012700 000011
0317 035130 010501
0318 035132 012702 001230
0319 035136 012122
0320 035140 077002
0321 035142 012737 035150 001112
0322
0323 035150 170001
0324 035152 170115
0325 035154 170165 000014
0326
0327 035160 174665 000004
0328
0329 035164 170237 002000
0330 035170 170337 002002
0331 035174 174237 001170
0332
0333 035200 023765 002000 000016
0334 035206 001401 BEQ 658 ; CHECK FFS
0335 035210 104004 ERROR 4 ; FFS IS OK
0336 035212 005765 000020 658: TST 20(R5) ; DOES FEC/FEA APPLY?
0337 035216 100014 BPL 608 ; NU - SKIP TEST
0338 035220 012737 035160 002014 MOV #DIVF,EXPFEA ; GET EXPECTED FEA
0339 035226 123765 002002 000020 CMPB FEC,20(R5) ; COMPARE FEC-S
0340 035234 001004 BNE 648 ; NOT EQUAL
0341 035236 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0342 035244 001401 BEQ 608 ; FEC, FEA OK
0343 035246 104014 648: ERROR 14 ; FEC OR FEA ARE BAD
0344 035250 668:
0345
0346 035250 023765 001170 000010 CMP $REG0,10(R5) ; 1ST WORD OF RESULT CHECK?
0347 035256 001004 BNE 678 ; NO
0348 035260 023765 001172 000012 CMP $REG1,12(R5) ; 2ND WORD OF RESULT CHECK?
0349 035266 001401 BEQ 688 ; ALL WORDS OK
0350 035270 104026 678: ERROR 26 ; NUMBERS NOT EQUAL
0351 035272 688:
0352
0353 035277 000207 RTS PC ; RETURN TO TEST CALLER
0354
0355
0356
0357
0358 035274
0359 035274 012700 000017
0360 035300 010501
0361 035302 012702 001230
0362 035306 012122
0363 035310 077002
0364 035312 012737 035320 001112
0365
0366 035320 170011
0367 035322 170115
0368 035324 170165 000030

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0369
0370 035330 174565 000010 DIVDI: DIVD 10(R5),AC1 ; (AC1)/(MEM)->AC1
0371
0372 035334 170237 002000 STFPB FFS ; STORE FFS AFTER
0373 035340 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0374 035344 174137 001170 STD AC1,0REG0 ; RESULT OF DIVD
0375
0376 035350 023765 002000 000032 CMP FFS,32(R5) ; CHECK FFS
0377 035356 001401 BEQ 658 ; FFS IS OK
0378 035360 104007 ERROR 7 ; FFS BAD
0379 035362 005765 000034 658: TST 34(R5) ; DOES FEC/FEA APPLY?
0380 035366 100014 BPL 668 ; NO - SKIP TEST
0381 035370 012737 035330 002014 MOV #DIVDI,EXPFEA ; GET EXPECTED FEA
0382 035376 123765 002002 000034 CMPB FEC,34(R5) ; COMPARE FEC-S
0383 035404 001004 BNE 648 ; NOT EQUAL
0384 035406 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0385 035414 001401 BEQ 668 ; FEC, FEA OK
0386 035416 104017 648: ERROR 17 ; FEC OR FEA ARE BAD
0387 035420 668:
0388
0389 035420 023765 001170 000020 CMP 0REG0,20(R5) ; 1ST WORD OF RESULT CHECK?
0390 035426 001014 BNE 678 ; NO
0391 035430 023765 001172 000022 CMP 0REG1,22(R5) ; 2ND WORD OF RESULT CHECK?
0392 035436 001010 BNE 678 ; NO
0393 035440 023765 001174 000024 CMP 0REG2,24(R5) ; 3RD WORD OF RESULT CHECK?
0394 035446 001004 BNE 678 ; NO
0395 035450 023765 001176 000026 CMP 0REG3,26(R5) ; 4TH WORD OF RESULT CHECK?
0396 035456 001401 BEQ 688 ; ALL WORDS OK
0397 035460 104020 678: ERROR 26 ; NUMBERS NOT EQUAL
0398 035462 688:
0399
0400 035462 000207 RTS PC ; RETURN TO TEST CALLER
0401
0402

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0403 ,80TTL SUBR TO TEST THE MODF INSTRUCTION, USING 2 ACCUMULATORS
0404
0405 035464 MD2FI:
0406 035466 012700 000013 MOV #13,R0 ; LOAD STMP0-12
0407 035470 010501 MOV R5,R1 ; WITH TEST DATA SETS
0408 035472 012702 001230 MOV 0STMP0,R2 ; FOR DISPLAY LATER
0409 035476 012122 MOV (R1)+,(R2)+ ;
0410 035500 017002 SOB R0,-2 ;
0411 035502 012737 035510 001112 MOV #MD2FL,0LPEMR ; ERROR LOOPING ADDRESS
0412
0413 035510 170001 MD2FL: SETF ; F MODE
0414 035512 172015 LDF (R5),AC2 ; INITIAL AC FLOAT NUMBER
0415 035514 172737 002036 LDF PREVAC,AC3 ; FOR FEC-14 TEST
0416 035520 170105 000020 LDFPB 20(R5) ; INITIAL FFS
0417
0418 035524 171065 000004 MD2FI: MODF 4(R5),AC2 ; F*AC((AC2)*(MEM))->AC2
0419 ; INT((AC2)*(MEM))->AC3
0420
0421 035530 170237 002000 STFPB FFS ; STORE FFS AFTER
0422 035534 170337 002002 STST FEC ; STORE FEC/FEA AFTER
0423 035540 174237 001170 STF AC2,0REG0 ; STORE FRAC PART
0424 035544 174337 001174 STF AC1,0REG2 ; STORE INT PART
0425
0426 035550 023765 002000 000022 CMP FFS,22(R5) ; CHECK FFS
0427 035556 001401 BEQ 658 ; FFS IS OK
0428 035560 104005 ERROR 5 ; FFS BAD
0429 035562 005765 000024 658: TST 24(R5) ; DOES FEC/FEA APPLY?
0430 035566 100014 BPL 668 ; NO - SKIP TEST
0431 035570 012737 035524 002014 MOV #MD2FI,EXPFEA ; GET EXPECTED FEA
0432 035576 123765 002002 000024 CMPB FEC,24(R5) ; COMPARE FEC-S
0433 035604 001004 BNE 648 ; NOT EQUAL
0434 035606 023737 002004 002014 CMP FEA,EXPFEA ; COMPARE FEA-S
0435 035614 001401 BEQ 668 ; FEC, FEA OK
0436 035616 104015 648: ERROR 15 ; FEC OR FEA ARE BAD
0437 035620 668:
0438
0439 ; CHECK FRACTION PART
0440 035620 023765 001170 000010 CMP 0REG0,10(R5) ; 1ST WORD OF RESULT CHECK?
0441 035626 001004 BNE 678 ; NO
0442 035630 023765 001172 000012 CMP 0REG1,12(R5) ; 2ND WORD OF RESULT CHECK?
0443 035636 001401 BEQ 688 ; ALL WORDS OK
0444 035640 104027 678: ERROR 27 ; NUMBERS NOT EQUAL
0445 688:
0446
0447 ; CHECK INTEGER PART
0448 035642 023765 001174 000014 CMP 0REG2,14(R5) ; 1ST WORD OF RESULT CHECK?
0449 035650 001004 BNE 678 ; NO
0450 035652 023765 001176 000016 CMP 0REG3,16(R5) ; 2ND WORD OF RESULT CHECK?
0451 035660 001401 BEQ 708 ; ALL WORDS OK
0452 035662 104030 698: ERROR 30 ; NUMBERS NOT EQUAL
0453 708:
0454
0455 035664 000207 RTS PC ; RETURN TO TEST CALLER
0456
0457
0458

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0459          .SBTTL  SUMR TO TEST THE MODD INSTRUCTION, USING 2 ACCUMULATORS
0460
0461 035666      MD2DI:
0462 035666 012700 000023  MOV  #23,R0          ; LOAD STMPD=22
0463 035672 010501      MOV  R5,R1          ; WITH TEST DATA BITS
0464 035674 012702 001230  MOV  #STMP0,R2       ; FOR DISPLAY LATER
0465 035700 012122      MOV  (R1)+(R2)+      ;
0466 035702 017002      SUB  R0,-2          ;
0467 035704 012737 035712 001112  MOV  #MD2DL,SLPERR  ; ERROR LOOPING ADDRESS
0468
0469 035712 170011      MD2DL:  SETD          ; D MODE
0470 035714 112415      LDD  (R5),AC0       ; INITIAL AC FLOAT NUMBER
0471 035716 172537 002036  LDD  PREVAC,AC1     ; FOR FEC=14 TEST
0472 035722 170165 000040  LDFPB 40(R5)       ; INITIAL FPS
0473
0474 035726 171465 000010  MD2DI:  MODD 10(R5),AC0 ; FHAC((AC0)*(MEM))=>AC0
0475                                     ; INT((AC0)*(MEM))=>AC1
0476
0477 035732 170237 002000  STFPS FPS          ; STORE FPS AFTER
0478 035736 170337 002002  STST FEC          ; STORE FEC/FEA AFTER
0479 035742 170037 001170  STD  AC0,REG0      ; STORE FRAC PART
0480 035746 171437 001200  STD  AC1,REG4      ; STORE INT PART
0481
0482 035752 023765 002000 000042  CMP  FPS,42(R5)    ; CHECK FPS
0483 035760 001401      BEQ  656          ; FPS IS OK
0484 035762 104010      LRRUR 10          ; FPS BAD
0485 035764 005765 000044 656:  TST  44(R5)      ; DUES FEC/FEA APPLT
0486 035770 100014      BPL  668          ; NO - SKIP TEST
0487 035772 012737 035726 002014  MOV  #MD2DI,EXPFLA ; GET EXPECTED FEA
0488 036000 123765 002002 000044  CMPB FEC,44(R5)    ; COMPARE FEC-S
0489 036006 001004      BNE  648          ; NOT EQUAL
0490 036010 023737 002004 002014  CMP  FEA,EXPFLA   ; COMPARE FEA-S
0491 036016 001401      BEQ  668          ; FEC, FEA OK
0492 036020 104020      648:  LRROR 20          ; FEC ON FEA ARE BAD
0493 036022 001401      668:
0494
0495          ; CHECK FRACTION PART OF RESULT
0496 036022 023765 001170 000020  CMP  #REG0,20(R5)  ; 1ST WORD OF RESULT CHECK?
0497 036030 001014      BNE  678          ; NO
0498 036032 023765 001172 000022  CMP  #REG1,22(R5)  ; 2ND WORD OF RESULT CHECK?
0499 036040 001010      BNE  678          ; NO
0500 036042 023765 001174 000024  CMP  #REG2,24(R5)  ; 3RD WORD OF RESULT CHECK?
0501 036050 001004      BNE  678          ; NO
0502 036052 023765 001176 000026  CMP  #REG3,26(R5)  ; 4TH WORD OF RESULT CHECK?
0503 036060 001401      BEQ  688          ; ALL WORDS OK
0504 036062 104031      678:  LRROR 31          ; NUMBERS NOT EQUAL
0505 036064 001401      688:
0506
0507          ; CHECK INTEGER PART
0508 036064 023765 001200 000030  CMP  #REG4,30(R5)  ; 1ST WORD OF RESULT CHECK?
0509 036072 001014      BNE  698          ; NO
0510 036074 023765 001202 000032  CMP  #REG5,32(R5)  ; 2ND WORD OF RESULT CHECK?
0511 036102 001010      BNE  698          ; NO
0512 036104 023765 001204 000034  CMP  #REG6,34(R5)  ; 3RD WORD OF RESULT CHECK?
0513 036112 001004      BNE  698          ; NO
0514 036114 023765 001206 000036  CMP  #REG7,36(R5)  ; 4TH WORD OF RESULT CHECK?

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0515 036122 001401      BEW  708          ; ALL WORDS OK
0516 036124 104032      698:  LRROR 32          ; NUMBERS NOT EQUAL
0517 036126 001401      708:
0518
0519 036126 000207      RTS  PC          ; RETURN TO TEST CALLER

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0520          ;SBTTL SUBR TO TEST THE MODF INSTRUCTION, USING 1 ACCUMULATOR
0521
0522          MD1FT1
0523      036130 012700 000013      MOV      #13,R0          ; LOAD STMP0=12
0524      036134 010501              MOV      R5,R1          ; WITH TEST DATA BITS
0525      036136 012702 001230      MOV      #STMP0,R2      ; FOR DISPLAY LATER
0526      036142 012122              MOV      (R1)+(R2)+    ;
0527      036144 077002              SOB      R0,-2          ;
0528      036146 012737 036154 001112      MOV      #MD1FL,SLPERR ; ERROR LOOPING ADDRESS
0529
0530          MD1FL1:
0531      036156 172715              SETF    (R5),AC3       ; F MODE
0532      036160 172637 002036      LDF     PREVAG,AC2    ; INITIAL AC FLOAT NUMBER
0533      036164 170165 000020      LDFFS  20(R5)         ; AC2 SHOULD NOT CHANGE
0534
0535          MD1FI1:
0536      036170 171765 000004      MODF   4(R5),AC3     ; FRAC((AC3)+(MEM))=>AC3
0537
0538          STFPS  FPS          ; STORE FPS AFTER
0539          STST  FEC          ; STORE FEC/FEA AFTER
0540          STT   AC3,0REG0     ; STORE FRAC PART
0541          STF   AC2,0REG2     ; STORE UNCHANGED AC3
0542
0543          CMP   FPS,22(R5)    ; CHECK FPS
0544          BEQ   658           ; FPS IS OK
0545          ERROR 5            ; FPS BAD
0546          TST  24(R5)        ; DOES FEC/FEA APPLY?
0547          BPL  668           ; NO - SKIP TEST
0548          MOV  #MD1FI,EXPFEA ; GET EXPECTED FEA
0549          CMPB FEC,24(R5)    ; COMPARE FEC-S
0550          BNE  648           ; NOT EQUAL
0551          CMP  FEA,EXPFEA   ; COMPARE FEA-S
0552          BEQ  668           ; FEC, FEA OK
0553          ERROR 15          ; FEC OR FEA ARE BAD
0554
0555          ; CHECK FRACTION PART
0556          CMP  $REG0,10(R5)  ; 1ST WORD OF RESULT CHECK?
0557          BNE  678           ; NO
0558          CMP  $REG1,12(R5)  ; 2ND WORD OF RESULT CHECK?
0559          BEQ  688           ; ALL WORDS OK
0560          ERROR 27          ; NUMBERS NOT EQUAL
0561
0562          ; CHECK UNCHANGED PART
0563          CMP  $REG2,14(R5)  ; 1ST WORD OF RESULT CHECK?
0564          BNE  698           ; NO
0565          CMP  $REG3,16(R5)  ; 2ND WORD OF RESULT CHECK?
0566          BEQ  708           ; ALL WORDS OK
0567          ERROR 30          ; NUMBERS NOT EQUAL
0568
0569          RTS  PC            ; RETURN TO TEST CALLER
0570
0571
0572
0573
0574
0575

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0576          ;SBTTL SUBR TO TEST THE MODD INSTRUCTION, USING 1 ACCUMULATOR
0577
0578          MD1DT1:
0579      036332 012700 000023      MOV      #23,R0          ; LOAD STMP0=22
0580      036336 010501              MOV      R5,R1          ; WITH TEST DATA BITS
0581      036340 012702 001230      MOV      #STMP0,R2      ; FOR DISPLAY LATER
0582      036344 012122              MOV      (R1)+(R2)+    ;
0583      036346 077002              SOB      R0,-2          ;
0584      036350 012737 036356 001112      MOV      #MD1DL,SLPERR ; ERROR LOOPING ADDRESS
0585
0586          MD1DL1:
0587      036356 170011              SETD   (R5),AC1       ; D MODE
0588      036360 172515              LDD    (R5),AC1       ; INITIAL AC FLOAT NUMBER
0589      036362 172437 002036      LDD    PREVAG,AC0     ; AC0 SHOULD NOT CHANGE
0590      036366 170165 000040      LDFFS  40(R5)         ; INITIAL FPS
0591
0592          MD1DI1:
0593      036372 171565 000010      MODD   10(R5),AC1    ; FRAC((AC1)+(MEM))=>AC1
0594
0595          STFPS  FPS          ; STORE FPS AFTER
0596          STST  FEC          ; STORE FEC/FEA AFTER
0597          STD   AC1,0REG0     ; STORE FRAC PART
0598          STD   AC0,0REG4     ; STORE UNCHANGED AC0
0599
0600          CMP   FPS,42(R5)    ; CHECK FPS
0601          BEQ   658           ; FPS IS OK
0602          ERROR 10          ; FPS BAD
0603          TST  44(R5)        ; DOES FEC/FEA APPLY?
0604          BPL  668           ; NO - SKIP TEST
0605          MOV  #MD1DI,EXPFEA ; GET EXPECTED FEA
0606          CMPB FEC,44(R5)    ; COMPARE FEC-S
0607          BNE  648           ; NOT EQUAL
0608          CMP  FEA,EXPFEA   ; COMPARE FEA-S
0609          BEQ  668           ; FEC, FEA OK
0610          ERROR 20          ; FEC OR FEA ARE BAD
0611
0612          ; CHECK FRACTION PART OF RESULT
0613          CMP  $REG0,20(R5)  ; 1ST WORD OF RESULT CHECK?
0614          BNE  678           ; NO
0615          CMP  $REG1,22(R5)  ; 2ND WORD OF RESULT CHECK?
0616          BNE  678           ; NO
0617          CMP  $REG2,24(R5)  ; 3RD WORD OF RESULT CHECK?
0618          BNE  678           ; NO
0619          CMP  $REG3,26(R5)  ; 4TH WORD OF RESULT CHECK?
0620          BEQ  688           ; ALL WORDS OK
0621          ERROR 31          ; NUMBERS NOT EQUAL
0622
0623          ; CHECK UNCHANGED PART
0624          CMP  $REG4,30(R5)  ; 1ST WORD OF RESULT CHECK?
0625          BNE  698           ; NO
0626          CMP  $REG5,32(R5)  ; 2ND WORD OF RESULT CHECK?
0627          BNE  698           ; NO
0628          CMP  $REG6,34(R5)  ; 3RD WORD OF RESULT CHECK?
0629          BNE  698           ; NO
0630          CMP  $REG7,36(R5)  ; 4TH WORD OF RESULT CHECK?
0631

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0632	036566	001401		BEO	706		; ALL WORDS OK
0633	036570	104032		696:	ERROR	32	; NUMBERS NOT EQUAL
0634	036572			706:			
0635							
0636	036572	002207		RTS	PC		; RETURN TO TEST CALLER

0637				.SBTTL	SUBR TO TEST THE LDCDF INSTRUCTION		
0638							
0639	036574			LCDFT:			
0640	036574	012700	000011	MOV	R0,R0		; LOAD STMP0=10
0641	036600	010501		MOV	R0,R1		; WITH TEST DATA SETS
0642	036602	012702	001230	MOV	08TMP0,R2		; FOR DISPLAY LATER
0643	036606	012122		MOV	(R1)+,(R2)+		
0644	036610	017002		SUB	R0,-2		
0645	036612	012737	036620	001112	MOV	0LCDFL,0LPEHR	; ERROR LOOPING ADDRESS
0646							
0647	036620	110011		LCDFL:	SETD		; D MODE
0648	036622	172537	002036	LDF	PREVAC,AC1		; PREV CONTENTS TO ACC; FOR FEC-14 TEST
0649	036626	170165	000014	LDFPS	14(R5)		; INITIAL FPS
0650							
0651	036632	177515		LCDFI:	LDCDF	(R5),AC1	; DUF([MEM])=>AC1
0652							
0653	036634	170237	002000	STFPS	FPS		; STORE FPS AFTER
0654	036640	170137	002002	STF	FEC		; STORE FEC/FEA AFTER
0655	036644	174137	001170	STF	AC1,0REG0		; STORE RESULT
0656							
0657	036650	073765	002000	000010	CMP	FPS,16(R5)	; CHECK FPS
0658	036656	001401		BEO	650		; FPS IS OK
0659	036660	104004		ERROR	4		; FPS BAD
0660	036662	005765	000020	650:	TST	20(R5)	; DUBS FEC/FEA APPLT
0661	036666	100014		660:	NPL		; NO - SKIP TEST
0662	036670	012737	036632	002014	MOV	0LCDFI,EXPFEA	; GET EXPECTED FEA
0663	036676	123765	002002	000020	CMPB	FEC,20(R5)	; COMPARE FEC=6
0664	036704	001004		BNE	648		; NOT EQUAL
0665	036706	023737	002004	002014	CMP	FEA,EXPFEA	; COMPARE FEA=8
0666	036714	001401		BEO	660		; FEC, FEA OK
0667	036716	104014		646:	ERROR	14	; FEC OR FEA ARE BAD
0668	036720			660:			
0669							
0670	036720	023765	001170	000010	CMP	0REG0,10(R5)	; 1ST WORD OF RESULT CHECK?
0671	036726	001004		BNE	670		; NO
0672	036730	023765	001172	000012	CMP	0REG1,12(R5)	; 2ND WORD OF RESULT CHECK?
0673	036736	001401		BEO	680		; ALL WORDS OK
0674	036740	104033		670:	ERROR	33	; NUMBERS NOT EQUAL
0675	036742			680:			
0676							
0677	036742	000207		RTS	PC		; RETURN TO TEST CALLER
0678							
0679							
0680							
0681				.SBTTL	SUBR TO TEST THE LDCDF INSTRUCTION		
0682							
0683	036744			LCDFT:			
0684	036744	012700	000011	MOV	R0,R0		; LOAD STMP0=10
0685	036750	010501		MOV	R0,R1		; WITH TEST DATA SETS
0686	036752	012702	001230	MOV	08TMP0,R2		; FOR DISPLAY LATER
0687	036756	012122		MOV	(R1)+,(R2)+		
0688	036760	017002		SUB	R0,-2		
0689	036766	012737	036770	001112	MOV	0LCDFL,0LPEHR	; ERROR LOOPING ADDRESS
0690							
0691	036770	110011		LCDFL:	SETD		; D MODE
0692	036772	172537	002036	LDF	PREVAC,AC2		; PREV CONTENTS TO ACC; FOR FEC-14 TEST

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8693 030775 170165 000014          LDFPS 14(R5)          ; INITIAL FPS
8694
8695 037002 177615          LCFDI: LDCFD (R5),AC2 ; FIOD(MEM)->AC4
8696
8697 037004 170237 002000          STFPS FPS             ; STORE FPS AFTER
8698 037010 170337 002002          STST FEC             ; STORE FEC/FEA AFTER
8699 037014 174237 001170          STD AC2,BREG0        ; STORE RESULT
8700
8701 037020 023765 002000 000016      CMP FPS,16(R5)        ; CHECK FPS
8702 037026 001401          BEQ 658              ; FPS IS OK
8703 037030 104004          ERROR 4              ; FPS BAD
8704 037032 005765 000020          658:  TST 20(R5)      ; DOES FEC/FEA APPLT
8705 037036 100014          BPL 668              ; NO - SKIP TEST
8706 037040 012737 037002 002014      MOV #LCFDI,EXPFLA    ; GET EXPECTED FEA
8707 037046 123765 002002 000020      CMPB FEC,20(R5)      ; COMPARE FEC-S
8708 037054 001604          BNE 648              ; NOT EQUAL
8709 037056 023737 002004 002014      CMP FEA,EXPFEA       ; COMPARE FEA-S
8710 037064 001401          BEQ 668              ; FEC, FEA OK
8711 037066 104014          648:  ERROR 14       ; FEC OR FEA ARE BAD
8712 037070          668:
8713
8714 037070 023765 001170 000004      CMP #REG0,4(R5)      ; 1ST WORD OF RESULT CHECK?
8715 037076 001014          BNE 678              ; NO
8716 037100 023765 001172 000006      CMP #REG1,6(R5)      ; 2ND WORD OF RESULT CHECK?
8717 037106 001010          BNE 678              ; NO
8718 037110 023765 001174 000010      CMP #REG2,10(R5)     ; 3RD WORD OF RESULT CHECK?
8719 037116 001004          BNE 678              ; NO
8720 037120 023765 001176 000012      CMP #REG3,12(R5)     ; 4TH WORD OF RESULT CHECK?
8721 037126 001401          BEQ 688              ; ALL WORDS OK
8722 037130 104034          678:  ERROR 34       ; NUMBERS NOT EQUAL
8723 037132          688:
8724
8725 037132 000207          RTS PC               ; RETURN TO TEST CALLER
8726

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8727          .SBTTL SUBR TO TEST THE STCDF INSTRUCTION
8728
8729 037134          SCDFI:  MOV #11,R0          ; LOAD SIMPO=10
8730 037138 012700 000011          MOV R5,R1            ; WITH TEST DATA SETS
8731 037140 010501          MOV #STMP0,R2        ; FOR DISPLAY LATER
8732 037142 012702 001230          MOV (R1)+,(R2)+      ;
8733 037146 012122          SOB R0,-2            ;
8734 037150 017002          MOV #SCDFI,$LPERH    ; ERROR LOOPING ADDRESS
8735 037152 012737 037160 001112          ;
8736
8737 037160 170011          SCDFI:  SETD (R5),AC3    ; D MODE
8738 037162 172715          LDD 14(R5)           ; INITIAL AC FLOAT NUMBER
8739 037164 170165 000014          LDFPS 14(R5)         ; INITIAL FPS
8740
8741 037170 176337 001170          SCDFI:  STCDF AC3,BREG0 ; DIOF([AC3])->MEM
8742
8743 037174 170237 002000          STFPS FPS             ; STORE FPS AFTER
8744 037200 170337 002002          STST FEC             ; STORE FEC/FEA AFTER
8745
8746 037204 023765 002000 000016      CMP FPS,16(R5)        ; CHECK FPS
8747 037212 001401          BEQ 658              ; FPS IS OK
8748 037214 104004          ERROR 4              ; FPS BAD
8749 037216 005765 000020          658:  TST 20(R5)      ; DOES FEC/FEA APPLT
8750 037222 100014          BPL 668              ; NO - SKIP TEST
8751 037224 012737 037170 002014      MOV #SCDFI,EXPFLA    ; GET EXPECTED FEA
8752 037232 123765 002002 000020      CMPB FEC,20(R5)      ; COMPARE FEC-S
8753 037240 001604          BNE 648              ; NOT EQUAL
8754 037242 023737 002004 002014      CMP FEA,EXPFEA       ; COMPARE FEA-S
8755 037250 001401          BEQ 668              ; FEC, FEA OK
8756 037252 104014          648:  ERROR 14       ; FEC OR FEA ARE BAD
8757 037254          668:
8758
8759 037254 023765 001170 000010      CMP #REG0,10(R5)     ; 1ST WORD OF RESULT CHECK?
8760 037262 001004          BNE 678              ; NO
8761 037264 023765 001172 000012      CMP #REG1,12(R5)     ; 2ND WORD OF RESULT CHECK?
8762 037272 001401          BEQ 688              ; ALL WORDS OK
8763 037274 104033          678:  ERROR 33       ; NUMBERS NOT EQUAL
8764 037276          688:
8765
8766 037276 000207          RTS PC               ; RETURN TO TEST CALLER
8767
8768
8769          ;*****
8770          .SBTTL SUBR TO TEST THE STCDF INSTRUCTION
8771
8772 037300          SCDFI:  MOV #12,R0          ; LOAD SIMPO=11
8773 037304 012700 000012          MOV R5,R1            ; WITH TEST DATA SETS
8774 037306 010501          MOV #STMP0,R2        ; FOR DISPLAY LATER
8775 037312 012122          MOV (R1)+,(R2)+      ;
8776 037314 017002          SOB R0,-2            ;
8777 037316 012737 037324 001112          MOV #SCDFI,$LPERH    ; ERROR LOOPING ADDRESS
8778
8779
8780 037324 170011          SCDFI:  SETD (R5),AC0    ; USE D MODE
8781 037326 172415          LDD 20(R5)           ; INITIAL F FLOAT NUM, FOLLOW #/JUNA
8782 037330 170165 000020          LDFPS 20(R5)         ; INITIAL FPS

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0703
0704 037334 170037 001170          STCFD AC0,8REG0      ; FTOD((AC0))=>MEM
0705
0706 037340 170237 002000          STFPS FPS           ; STORE FPS AFTER
0707
0708 037344 023765 002000 000022    CMP     FPS,22(R5)   ; CHECK FPS OK
0709 037352 001401                    BEQ     648          ; OK, BRANCH
0710 037354 104005                    BRKOR   5           ; FPS BAD
0711 037356
0712
0713 037356 023765 001170 000010    CMP     8REG0,10(R5) ; 1ST WORD OF RESULT CHECK?
0714 037364 001014                    BNE     658          ; NO
0715 037366 023765 001172 000012    CMP     8REG1,12(R5) ; 2ND WORD OF RESULT CHECK?
0716 037374 001010                    BNE     658          ; NO
0717 037376 023765 001174 000014    CMP     8REG2,14(R5) ; 3RD WORD OF RESULT CHECK?
0718 037404 001004                    BNE     658          ; NO
0719 037406 023765 001176 000016    CMP     8REG3,16(R5) ; 4TH WORD OF RESULT CHECK?
0720 037414 001401                    BEQ     668          ; ALL WORDS OK
0721 037416 104035                    EPROR   35          ; NUMBERS NOT EQUAL
0722 037420
0723
0724 037420 000207                    RTS     PC           ; RETURN TO TEST CALLER
  
```

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0805
0806
0807 037422          .SBTTL SUBR TO TEST THE LDCIF INSTRUCTION
0808 037422 012700 000005          LCIFT: MOV     R5,R0      ; LOAD STMP0=4
0809 037426 010501          MOV     R5,R1      ; WITH TEST DATA SETS
0810 037430 012702 001230          MOV     8STMP0,R2  ; FOR DISPLAY LATER
0811 037434 012122          MOV     (R1)+(R2)+ ;
0812 037436 077002          SUB     R0,=2      ;
0813 037440 012737 037446 001112    MOV     8LCIFL,8LPERH ; ERROR LOOPING ADDRESS
0814
0815 037446 170165 000006          LCIFL: LDFPS   6(R5) ; INITIAL FPS
0816
0817 037452 177215          LDCIF (R5),AC2    ; F((MEM))=>AC2
0818
0819 037454 170237 002000          STFPS   FPS       ; STORE FPS AFTER
0820 037460 174237 001170          ST     AC2,8REG0   ; STORE RESULT
0821
0822 037464 023765 002000 000010    CMP     FPS,10(R5)  ; CHECK FPS OK
0823 037472 001401                    BEQ     648          ; OK, BRANCH
0824 037474 104001                    BRKOR   1           ; FPS BAD
0825 037476
0826
0827 037476 023765 001170 000002    CMP     8REG0,2(R5) ; 1ST WORD OF RESULT CHECK?
0828 037504 001004                    BNE     658          ; NO
0829 037506 023765 001172 000004    CMP     8REG1,4(R5) ; 2ND WORD OF RESULT CHECK?
0830 037514 001401                    BEQ     668          ; ALL WORDS OK
0831 037516 104036                    EPROR   36          ; NUMBERS NOT EQUAL
0832 037520
0833
0834 037520 000207                    RTS     PC           ; RETURN TO TEST CALLER
0835
0836
0837
0838
0839
0840 037522          .SBTTL SUBR TO TEST THE LDCID INSTRUCTION
0841 037522 012700 000007          LCIDI: MOV     R7,R0      ; LOAD STMP0=6
0842 037526 010501          MOV     R5,R1      ; WITH TEST DATA SETS
0843 037530 012702 001230          MOV     8STMP0,R2  ; FOR DISPLAY LATER
0844 037534 012122          MOV     (R1)+(R2)+ ;
0845 037536 077002          SUB     R0,=2      ;
0846 037540 012737 037546 001112    MOV     8LCIDL,8LPERH ; ERROR LOOPING ADDRESS
0847
0848 037546 170165 000012          LCIDL: LDFPS   12(R5) ; INITIAL FPS
0849
0850 037552 177315          LDCID (R5),AC3    ; D((MEM))=>AC3
0851
0852 037554 170237 002000          STFPS   FPS       ; STORE FPS AFTER
0853 037560 174337 001170          STD     AC3,8REG0   ; STORE RESULT
0854
0855 037564 023765 002000 000014    CMP     FPS,14(R5)  ; CHECK FPS OK
0856 037572 001401                    BEQ     648          ; OK, BRANCH
0857 037574 104003                    BRKOR   3           ; FPS BAD
0858 037576
0859
0860 037576 023765 001170 000002    CMP     8REG0,2(R5) ; 1ST WORD OF RESULT CHECK?
  
```

```
0861 037604 001014  
0862 037606 023765 001172 000004 BNE 658 ; NO  
0863 037614 001010 CMP 0REG1,4(R5) ; 2ND WORD OF RESULT CHECK? ; NO  
0864 037616 023765 001174 000006 BNE 658 ; NO  
0865 037624 001004 CMP 0REG2,6(R5) ; 3RD WORD OF RESULT CHECK? ; NO  
0866 037626 023765 001176 000010 BNE 658 ; NO  
0867 037634 001401 CMP 0REG3,10(R5) ; 4TH WORD OF RESULT CHECK? ; ALL WORDS OK  
0868 037636 104037 BEQ 668 ; NUMBERS NOT EQUAL  
0869 037640  
0870  
0871 037640 000207 RT6 PC ; RETURN TO TEST CALLER  
0872  
0873  
0874  
0875  
0876  
0877 037642  
0878 037644 012700 000006 LCLFT: MOV 06,R0 ; LOAD 0TMP0=5  
0879 037646 010501 MOV R5,R1 ; WITH TEST DATA SETS  
0880 037650 012702 001230 MOV 0TMP0,R2 ; FOR DISPLAY LATER  
0881 037654 012122 MOV (R1)+,(R2)+ ;  
0882 037656 077002 SOB R0,-2 ;  
0883 037660 012737 037666 001112 MOV 0LCLFL,0LPEHR ; ERROR LOOPING ADDRESS  
0884  
0885 037666 170165 000010 LCLFL: LDFFS 10(R5) ; INITIAL FPS  
0886  
0887 037672 177015 LDCLF (R5),AC0 ; F1(MEM)(MEM)->AC0  
0888  
0889 037674 170237 002000 STFFS FPS ; STORE FPS AFTER  
0890 037700 174037 001170 STF AC0,0REG0 ; STORE RESULT  
0891  
0892 037704 023765 002000 000012 CMP FPS,12(R5) ; CHECK FPS OK  
0893 037712 001401 BEQ 648 ; OK, BRANCH  
0894 037714 104002 ERROR 2 ; FPS BAD  
0895 037716  
0896  
0897 037716 023765 001170 000004 CMP 0REG0,4(R5) ; 1ST WORD OF RESULT CHECK? ; NO  
0898 037724 001004 BNE 658 ; NO  
0899 037726 023765 001172 000006 CMP 0REG1,6(R5) ; 2ND WORD OF RESULT CHECK? ; NO  
0900 037734 001401 BEQ 668 ; ALL WORDS OK  
0901 037736 104040 058: ERROK 40 ; NUMBERS NOT EQUAL  
0902 037740  
0903  
0904 037740 000207 RT6 PC ; RETURN TO TEST CALLER  
0905  
0906  
0907  
0908  
0909  
0910 037742  
0911 037742 012700 000010 LCLDT: MOV 010,R0 ; LOAD 0TMP0=7  
0912 037746 010501 MOV R5,R1 ; WITH TEST DATA SETS  
0913 037750 012702 001230 MOV 0TMP0,R2 ; FOR DISPLAY LATER  
0914 037754 012122 MOV (R1)+,(R2)+ ;  
0915 037756 077002 SOB R0,-2 ;  
0916 037760 012737 037766 001112 MOV 0LCLDL,0LPEHR ; ERROR LOOPING ADDRESS
```

```
0917  
0918 037766 170165 000014 LCLDL: LDFFS 14(R5) ; INITIAL FPS  
0919  
0920 037772 177115 LDCLD (R5),AC1 ; D1(MEM)(MEM)->AC1  
0921  
0922 037774 170237 002000 STFFS FPS ; STORE FPS AFTER  
0923 040000 174137 001170 STD AC1,0REG0 ; STORE RESULT  
0924  
0925 040004 023765 002000 000016 CMP FPS,16(R5) ; CHECK FPS OK  
0926 040012 001401 BEQ 648 ; OK, BRANCH  
0927 040014 104004 ERROR 4 ; FPS BAD  
0928 040016  
0929  
0930 040016 023765 001170 000004 CMP 0REG0,4(R5) ; 1ST WORD OF RESULT CHECK? ; NO  
0931 040024 001014 BNE 658 ; NO  
0932 040026 023765 001172 000006 CMP 0REG1,6(R5) ; 2ND WORD OF RESULT CHECK? ; NO  
0933 040034 001010 BNE 658 ; NO  
0934 040036 023765 001174 000010 CMP 0REG2,10(R5) ; 3RD WORD OF RESULT CHECK? ; NO  
0935 040044 001004 BNE 658 ; NO  
0936 040046 023765 001176 000012 CMP 0REG3,12(R5) ; 4TH WORD OF RESULT CHECK? ; ALL WORDS OK  
0937 040054 001401 BEQ 668 ; NUMBERS NOT EQUAL  
0938 040056 104041 058: ERROR 41 ; NUMBERS NOT EQUAL  
0939 040060  
0940  
0941 040060 000207 RT6 PC ; RETURN TO TEST CALLER
```

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0942          .SBTTL SUBR TO TEST THE STCFI INSTRUCTION
0943
0944          SCFIT:
0945          MOV     #6,R0          ; LOAD STMP0=5
0946          MOV     R5,R1          ; WITH TEST DATA SETS
0947          MOV     #STMP0,R2      ; FOR DISPLAY LATER
0948          MOV     (R1)+(R2)+     ;
0949          SOB     R0,-2          ;
0950          MOV     #SCFIL,SLPERR   ; ERROR LOOPING ADDRESS
0951
0952          SCFIL: SETF          ; F MODE
0953          LDF     (R5),AC3        ; INITIAL AC FLOAT NUMBER
0954          LDFPS  6(R5)          ; INITIAL FPS
0955
0956          SCFII: STCFI AC3,REG0   ; I[(AC3)]->MEM
0957
0958          MOV     #0PS,REG1       ; SAVE CC=8
0959          STFPS  FPS             ; STORE FPS AFTER
0960          STST  FEC              ; STORE FEC/FEA AFTER
0961
0962          CMP     FPS,10(R5)      ; CHECK FPS
0963          BEQ     658            ; FPS IS OK
0964          ERROR  1              ; FPS BAD
0965          TST     12(R5)         ; DOES FEC/FEA APPLY?
0966          BPL     668            ; NO - SKIP TEST
0967          MOV     #SCFII,EXPFEA   ; GET EXPECTED FEA
0968          CMPB   FEC,1,(R5)      ; COMPARE FEC=8
0969          BNE     648            ; NOT EQUAL
0970          CMP     FEA,EXPFEA     ; COMPARE FEA=8
0971          BEQ     668            ; FEC, FEA OK
0972          ERROR  11             ; FEC OR FEA ARE BAD
0973          668:
0974
0975          MOV     FPS,REG2        ; GET FPS, PS CC BITS ONLY
0976          BIC     #CCONLY,REG1   ;
0977          BIC     #CCONLY,REG2   ;
0978          CMP     #REG1,REG2     ; CC=8 COPIED?
0979          BEQ     678            ;
0980          ERROR  54             ; NOT EQUAL, SIGNAL ERROR
0981          678:
0982
0983          CMP     #REG0,4(R5)     ; INTEGER RESULT CHECK?
0984          BEQ     688            ;
0985          ERROR  42             ; NOT EQUAL, SIGNAL ERROR
0986          688:
0987
0988          RTS     PC              ; RETURN TO TEST CALLER
0989
0990
0991          ;*****
0992          .SBTTL SUBR TO TEST THE STCFI INSTRUCTION
0993
0994          SCFIT:
0995          MOV     #10,R0         ; LOAD STMP0=7
0996          MOV     R5,R1         ; WITH TEST DATA SETS
0997          MOV     #STMP0,R2     ; FOR DISPLAY LATER

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```

0998          MOV     (R1)+(R2)+     ;
0999          SOB     R0,-2          ;
0999          MOV     #SCDIL,SLPERR   ; ERROR LOOPING ADDRESS
0999
0999          SCDIL: SETD          ; D MODE
0999          LDD     (R5),AC0        ; INITIAL AC FLOAT NUMBER
0999          LDFPS  12(R5)          ; INITIAL FPS
0999
0999          SCDII: STCFI AC0,REG0   ; I[(AC0)]->MEM
0999
0999          MOV     #0PS,REG1       ; SAVE CC=8
0999          STFPS  FPS             ; STORE FPS AFTER
0999          STST  FEC              ; STORE FEC/FEA AFTER
0999
0999          CMP     FPS,14(R5)      ; CHECK FPS
0999          BEQ     658            ; FPS IS OK
0999          ERROR  3              ; FPS BAD
0999          TST     16(R5)         ; DOES FEC/FEA APPLY?
0999          BPL     668            ; NO - SKIP TEST
0999          MOV     #SCDII,EXPFEA   ; GET EXPECTED FEA
0999          CMPB   FEC,16(R5)      ; COMPARE FEC=8
0999          BNE     648            ; NOT EQUAL
0999          CMP     FEA,EXPFEA     ; COMPARE FEA=8
0999          BEQ     668            ; FEC, FEA OK
0999          ERROR  13             ; FEC OR FEA ARE BAD
0999          648:
0999          668:
0999
0999          MOV     FPS,REG2        ; GET FPS, PS CC BITS ONLY
0999          BIC     #CCONLY,REG1   ;
0999          BIC     #CCONLY,REG2   ;
0999          CMP     #REG1,REG2     ; CC=8 COPIED?
0999          BEQ     678            ;
0999          ERROR  54             ; NOT EQUAL, SIGNAL ERROR
0999          678:
0999
0999          CMP     #REG0,10(R5)    ; INTEGER RESULT CHECK?
0999          BEQ     688            ;
0999          ERROR  43             ; NOT EQUAL, SIGNAL ERROR
0999          688:
0999
0999          RTS     PC              ; RETURN TO TEST CALLER
0999
0999
0999          ;*****
0999          .SBTTL SUBR TO TEST THE STCFI INSTRUCTION
0999
0999          SCFLT:
0999          MOV     #7,R0          ; LOAD STMP0=6
0999          MOV     R5,R1          ; WITH TEST DATA SETS
0999          MOV     #STMP0,R2      ; FOR DISPLAY LATER
0999          MOV     (R1)+(R2)+     ;
0999          SOB     R0,-2          ;
0999          MOV     #SCFLI,SLPERR   ; ERROR LOOPING ADDRESS
0999
0999          SCFLI: SETF          ; F MODE
0999          LDF     (R5),AC1        ; INITIAL AC FLOAT NUMBER

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```

9054 040506 170165 000010          LDPPS 10(R5)          ; INITIAL FPS
9055
9056 040512 175537 001170          SCFLI: STCFL AC1,0REG0          ; L1(AC1)->MEM
9057
9058 040516 013737 177776 001174          MOV 0*PS,0REG2          ; SAVE CC-S
9059 040524 170237 002000          STFPS FPS              ; STORE FPS AFTER
9060 040530 170337 002002          STST FEC              ; STORE FEC/FEA AFTER
9061
9062 040534 023765 002000 000012          CMP FPS,12(R5)          ; CHECK FPS
9063 040542 001401          BEQ 650                ; FPS IS OK
9064 040544 104002          ERROR 2                ; FPS BAD
9065 040546 005765 000014          650: TST 14(R5)          ; DOES FEC/FEA APPLI?
9066 040552 100014          BPL 660                ; NO - SKIP TEST
9067 040554 012737 040512 002014          MOV 0SCFLI,EXPFEA      ; GET EXPECTED FEA
9068 040562 123765 002002 000014          CMPB FEC,14(R5)        ; COMPARE FEC-S
9069 040570 001004          BNE 640                ; NOT EQUAL
9070 040572 023737 002004 002014          CMP FEA,EXPFEA        ; COMPARE FEA-S
9071 040600 001401          BEQ 660                ; FEC, FEA OK
9072 040602 104012          640: ERROR 12          ; FEC OR FEA ARE BAD
9073 040604          660:
9074
9075 040604 013737 002000 001176          MOV FPS,0REG3          ; GET FPS, PS CC BITS ONLY
9076 040612 042737 177760 001174          BIC 0CONLY,0REG2      ;
9077 040620 042737 177760 001176          BIC 0CONLY,0REG3      ;
9078 040626 023737 001174 001176          CMP 0REG2,0REG3        ; CC-S COPIED?
9079 040634 001401          BEQ 670                ;
9080 040636 104055          670: ERROR 55          ; NOT EQUAL, SIGNAL ERROR
9081 040640          670:
9082
9083 040640 023765 001170 000004          CMP 0REG0,4(R5)        ; 1ST WORD OF RESULT CHECK?
9084 040646 001004          BNE 680                ; NO
9085 040650 023765 001172 000006          CMP 0REG1,6(R5)        ; 2ND WORD OF RESULT CHECK?
9086 040656 001401          BEQ 690                ; ALL WORDS OK
9087 040660 104044          680: ERROR 44          ; NUMBERS NOT EQUAL
9088 040662          690:
9089
9090 040662 000207          RTS PC                  ; RETURN TO TEST CALLER
9091
9092
9093
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9109
  ;*****
  .SBTTL SUBR TO TEST THE STCFL INSTRUCTION
  SCFLI:
9097 040664          MOV #11,R0              ; LOAD SIMPO-10
9098 040670 010501          MOV R5,R1              ; WITH TEST DATA BITS
9099 040672 012702 001230          MOV 0TMP0,R2          ; FOR DISPLAY LATER
9100 040676 012122          MOV (R1)+(R2)+         ;
9101 040700 077002          SOB R0,-2              ;
9102 040702 012737 040710 001112          MOV 0SCDLL,0LPERR     ; ERROR LOOPING ADDR&S
9103
9104 040710 170011          SCDLL: SETD            ; D MODE
9105 040712 172615          LOD (R5),AC2          ; INITIAL AC FLOAI NUMBER
9106 040714 170165 000014          LDPPS 14(R5)          ; INITIAL FPS
9107
9108 040720 175637 001170          SCDLI: STCFL AC2,0REG0          ; L1(AC2)->MEM,MEM
9109

```

```

9110 040724 013737 177776 001174          MOV 0*PS,0REG2          ; SAVE CC-S
9111 040732 170237 002000          STFPS FPS              ; STORE FPS AFTER
9112 040736 170337 002002          STST FEC              ; STORE FEC/FEA AFTER
9113
9114 040742 023765 002000 000016          CMP FPS,16(R5)          ; CHECK FPS
9115 040750 001401          BEQ 650                ; FPS IS OK
9116 040752 104004          ERROR 4                ; FPS BAD
9117 040754 005765 000020          650: TST 20(R5)          ; DOES FEC/FEA APPLI?
9118 040760 100014          BPL 660                ; NO - SKIP TEST
9119 040762 012737 040720 002014          MOV 0SCDLI,EXPFEA     ; GET EXPECTED FEA
9120 040770 123765 002002 000020          CMPB FEC,20(R5)        ; COMPARE FEC-S
9121 040776 001004          BNE 640                ; NOT EQUAL
9122 041000 023737 002004 002014          CMP FEA,EXPFEA        ; COMPARE FEA-S
9123 041006 001401          BEQ 660                ; FEC, FEA OK
9124 041010 104014          640: ERROR 14          ; FEC OR FEA ARE BAD
9125 041012          660:
9126
9127 041012 013737 002000 001176          MOV FPS,0REG3          ; GET FPS, PS CC BITS ONLY
9128 041020 042737 177760 001174          BIC 0CONLY,0REG2      ;
9129 041026 042737 177760 001176          BIC 0CONLY,0REG3      ;
9130 041034 023737 001174 001176          CMP 0REG2,0REG3        ; CC-S COPIED?
9131 041042 001401          BEQ 670                ;
9132 041044 104055          670: ERROR 55          ; NOT EQUAL, SIGNAL ERROR
9133 041046          670:
9134
9135 041046 023765 001170 000010          CMP 0REG0,10(R5)       ; 1ST WORD OF RESULT CHECK?
9136 041054 001004          BNE 680                ; NO
9137 041056 023765 001172 000012          CMP 0REG1,12(R5)       ; 2ND WORD OF RESULT CHECK?
9138 041064 001401          BEQ 690                ; ALL WORDS OK
9139 041066 104045          680: ERROR 45          ; NUMBERS NOT EQUAL
9140 041070          690:
9141
9142 041070 000207          RTS PC                  ; RETURN TO TEST CALLER

```



```

9143          .SBTTL SUBR TO TEST THE LDEAP INSTRUCTION, F MODE
9144
9145          LEAFT:
9146          041072 012704 000010      MOV      R10,R0          ; LOAD SAMP0-7
9147          041076 010901              MOV      R0,R1          ; WITH TEST DATA SETS
9148          041100 012702 001230      MOV      0TMP0,R2       ; FOR DISPLAY LATER
9149          041104 012122              MOV      (R1)+,(R2)+   ;
9150          041106 077002              SOB      R0,-2         ;
9151          041110 012737 041116 001112  MOV      0LEXFL,0LPERR ; ERROR LOOPING ADDRESS
9152
9153          LEXFL: SETF              ; F MODE
9154          041120 172815              LDF      (R5),AC1      ; INITIAL FLOAT NUMBER
9155          041122 170165 000012      LDFFS   12(R5)         ; INITIAL FPS
9156
9157          041126 176565 000010      LEXFI:  LDEXP 10(R5),AC1 ; EXP1 MEM -> AC1
9158
9159          041132 170337 002000      STFPS   FPS           ; STORE FPS AFTER
9160          041136 170337 002002      STST    FEC           ; STORE FEC/FEA AFTER
9161
9162          041142 023765 002000 000014  CMP      FPS,14(R5)    ; CHECK FPS
9163          041150 001401              BEQ     658           ; FPS IS OK
9164          041152 104003              ERROR   3            ; FPS BAD
9165          041154 005765 000016      658:    TST 16(R5)     ; DOES FEC/FEA APPLY?
9166          041160 100014              BPL     660           ; NO - SKIP TEST
9167          041162 012737 041126 002014  MOV      0LEXFI,EXPFEA ; GET EXPECTED FEA
9168          041170 123765 002002 000016  CMPB    FEC,16(R5)    ; COMPARE FEC-S
9169          041176 001004              BNE     648           ; NOT EQUAL
9170          041200 023737 002004 002014  CMP     FEA,EXPFEA   ; COMPARE FEA-S
9171          041206 001401              BEQ     668           ; FEC, FEA OK
9172          041210 104013              648:    ERROR 13       ; FEC OR FEA ARE BAD
9173          041212              668:
9174
9175          041212 174137 001170          STF     AC1,0REG0     ; STORE RESULTANT FLOAT NUMBER
9176          041216 023765 001170 000004  CMP     0REG0,4(R5)  ; 1ST WORD OF RESULT CHECK?
9177          041224 001004              BNE     678           ; NO
9178          041226 023765 001172 000006  CMP     0REG1,6(R5)  ; 2ND WORD OF RESULT CHECK?
9179          041234 001401              BEQ     688           ; ALL WORDS OK
9180          041236 104046              678:    ERROR 46       ; NUMBERS NOT EQUAL
9181          041240              688:
9182
9183          041240 000207              PTS     PC           ; RETURN TO TEST CALLER
9184
9185          ;*****
9186          .SBTTL SUBR TO TEST THE LDEXP INSTRUCTION, D MODE
9187
9188          LEADT:
9189          041242 012700 000014      MOV      R14,R0       ; LOAD SAMP0-13
9190          041246 010901              MOV      R5,R1        ; WITH TEST DATA SETS
9191          041250 012702 001230      MOV      0TMP0,R2     ; FOR DISPLAY LATER
9192          041254 012122              MOV      (R1)+,(R2)+ ;
9193          041256 077002              SOB      R0,-2       ;
9194          041260 012737 041266 001112  MOV      0LEXDL,0LPERR ; ERROR LOOPING ADDRESS
9195
9196          LEADL: SETD              ; D MODE
9197          041270 172415              LDD     (R5),AC0     ; INITIAL FLOAT NUMBER
9198          041272 170165 000022      LDFFS   22(R5)       ; INITIAL FPS

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9199
9200          041276 176465 000020      LEXDI:  LDEXP 20(R5),AC0 ; EXP1 MEM -> AC0
9201
9202          041302 170337 002000      STFPS   FPS           ; STORE FPS AFTER
9203          041306 170337 002002      STST    FEC           ; STORE FEC/FEA AFTER
9204
9205          041312 023765 002000 000024  CMP      FPS,24(R5)    ; CHECK FPS
9206          041320 001401              BEQ     656           ; FPS IS OK
9207          041322 104006              ERROR   6            ; FPS BAD
9208          041324 005765 000026      656:    TST 26(R5)     ; DOES FEC/FEA APPLY?
9209          041330 100014              BPL     666           ; NO - SKIP TEST
9210          041332 012737 041276 002014  MOV      0LEXDI,EXPFEA ; GET EXPECTED FEA
9211          041340 123765 002002 000026  CMPB    FEC,26(R5)    ; COMPARE FEC-S
9212          041346 001004              BNE     648           ; NOT EQUAL
9213          041350 023737 002004 002014  CMP     FEA,EXPFEA   ; COMPARE FEA-S
9214          041356 001401              BEQ     668           ; FEC, FEA OK
9215          041360 104016              648:    ERROR 16       ; FEC OR FEA ARE BAD
9216          041362              668:
9217
9218          041362 174037 001170          STD     AC0,0REG0     ; STORE RESULTANT FLOAT NUMBER
9219          041366 023765 001170 000010  CMP     0REG0,10(R5)  ; 1ST WORD OF RESULT CHECK?
9220          041374 001014              BNE     678           ; NO
9221          041376 023765 001172 000012  CMP     0REG1,12(R5)  ; 2ND WORD OF RESULT CHECK?
9222          041404 001010              BNE     678           ; NO
9223          041406 023765 001174 000014  CMP     0REG2,14(R5)  ; 3RD WORD OF RESULT CHECK?
9224          041414 001004              BNE     678           ; NO
9225          041416 023765 001176 000016  CMP     0REG3,16(R5)  ; 4TH WORD OF RESULT CHECK?
9226          041424 001401              BEQ     688           ; ALL WORDS OK
9227          041426 104047              678:    ERROR 47       ; NUMBERS NOT EQUAL
9228          041430              688:
9229
9230          041430 000207              RTS     PC           ; RETURN TO TEST CALLER
9231

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9232          .SBTTL  SUBR TO TEST THE STEXP INSTRUCTION, F MODE
9233
9234          SEXPT:
9235          MOV     #5,R0          ; LOAD STMP0=4
9236          MOV     R5,R1          ; WITH TEST DATA SETS
9237          MOV     #STMP0,R2     ; FOR DISPLAY LATER
9238          MOV     (R1)+,(R2)+  ;
9239          SOB     R0,-2         ;
9240          MOV     #SEXFL,$LPERR ; ERROR LOOPING ADDRESS
9241
9242          SEXFL: SETF          ; F MODE
9243          LDF     (R5),AC2       ; INITIAL FLOAT NUMBER
9244          LOFPS  6(R5)         ; INITIAL FPS
9245
9246          SEXFI: STEXP AC2,$REG0 ; EXP: AC2 -> MEM
9247
9248          MOV     @PS,$REG1     ; GET PS RIGHT AWAY; FOR CC BITS
9249          STFPS  FPS           ; STORE FPS AFTER
9250
9251          CMP     FPS,10(R5)    ; CHECK FPS OK
9252          BEQ     648          ; OK, BRANCH
9253          ERROR  1             ; FPS BAD
9254          648:
9255
9256          MOV     FPS,$REG2     ; GET FPS, PS CC BITS ONLY
9257          BIC   $CONLY,$REG1   ;
9258          BIC   $CONLY,$REG2   ;
9259          CMP   $REG1,$REG2    ; CC-B COPIED?
9260          BEQ   658           ;
9261          ERROR 54            ; NOT EQUAL, SIGNAL ERROR
9262          658:
9263
9264          CMP     $REG0,4(R5)   ; EXP CHECK?
9265          BEQ     668          ;
9266          ERROR 50            ; NOT EQUAL, SIGNAL ERROR
9267          668:
9268
9269          RTS    PC             ; RETURN TO TEST CALLER
9270
9271          ;*****
9272          .SBTTL  SUBR TO TEST THE STEXP INSTRUCTION, D MODE
9273
9274          SEXDT:
9275          MOV     #7,R0          ; LOAD STMP0=6
9276          MOV     R5,R1          ; WITH TEST DATA SETS
9277          MOV     #STMP0,R2     ; FOR DISPLAY LATER
9278          MOV     (R1)+,(R2)+  ;
9279          SOB     R0,-2         ;
9280          MOV     #SEXDL,$LPERR ; ERROR LOOPING ADDRESS
9281
9282          SEXDL: SETD          ; D MODE
9283          LOD   (R5),AC3       ; INITIAL FLOAT NUMBER
9284          LOFPS  12(R5)        ; INITIAL FPS
9285
9286          SEXDI: STEXP AC3,$REG0 ; EXP: AC3 -> MEM
9287
  
```

```

9288          MOV     @PS,$REG1     ; GET PS RIGHT AWAY
9289          STFPS  FPS           ; STORE FPS AFTER
9290
9291          CMP     FPS,14(R5)    ; CHECK FPS OK
9292          BEQ     648          ; OK, BRANCH
9293          ERROR  3             ; FPS BAD
9294          648:
9295
9296          MOV     FPS,$REG2     ; GET FPS, PS CC BITS ONLY
9297          BIC   $CONLY,$REG1   ;
9298          BIC   $CONLY,$REG2   ;
9299          CMP   $REG1,$REG2    ; CC-B COPIED?
9300          BEQ   658           ;
9301          ERROR 54            ; NOT EQUAL, SIGNAL ERROR
9302          658:
9303
9304          CMP     $REG0,10(R5)  ; EXP CHECK?
9305          BEQ     668          ;
9306          ERROR 51            ; NOT EQUAL, SIGNAL ERROR
9307          668:
9308
9309          RTS    PC             ; RETURN TO TEST CALLER
  
```

9310  
9311  
9312 041722 010637 002012  
9313 041726 012637 002006  
9314 041732 012637 002010  
9315 041736 170237 002000  
9316 041742 170337 002002  
9317 041746 104056  
9318 041750 013746 002010  
9319 041754 013746 002006  
9320 041760 000002

.SBTTL FPP UNEXPECTED TRAP CATCHER  
FPPILT: MOV SP,FPPOSP ; SP AFTER TRAP  
MOV (SP)+,FPPOPC ; POP OLD PC FOR DISPLAY  
MOV (SP)+,FPPOPB ; POP OLD PB FOR DISPLAY  
STFPA FPB ; GET FPS  
STST FEC ; GET FEC/FEA  
ERROR 56 ; SIGNAL UNEXPECTED FPP TRAP  
MOV FPPOPB,-(SP) ; PUSH PS  
MOV FPPOPC,-(SP) ; PUSH PC  
RTI ; CONTINUE, RECOVER AT LAST TRAP ONLY

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9334 041762  
9335 041762  
9336 041762 032777 040000 137154  
9337 041770 021114  
9338  
9339 041772 000416  
9340  
9341 041774 013746 000004  
9342 042000 012737 042020 000004  
9343 042006 005737 170600  
9344 042012 012637 000004  
9345 042016 000463  
9346 042020 022626  
9347 042022 012637 000004  
9348 042026 000423  
9349 042030  
9350 042030 032777 000400 137106  
9351 042036 001404  
9352 042040 023737 001150 001102  
9353 042046 001465  
9354 042050 005737 001104  
9355 042054 001421  
9356 042056 023737 001120 001104  
9357 042064 121015  
9358 042066 032777 001000 137450  
9359 042074 001404  
9360 042076 013737 001112 001110 701  
9361 042104 000446  
9362 042106 005037 001104  
9363 042112 005037 001310  
9364 042116 000415  
9365 042120 032777 004000 137016 301  
9366 042126 001011  
9367 042130 005737 001332  
9368 042134 001406  
9369 042136 005237 001106  
9370 042142 023737 001310 001106  
9371 042150 002024  
9372 042152 012737 000001 001106 101  
9373 042160 013747 042236 001310  
9374 042166 005237 001102  
9375 042172 013737 001102 001330  
9376 042200 011037 001110

.SBTTL SCOPE HANDLER ROUTINE  
;\*\*\*\*\*  
;THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT  
;AND LOAD THE TEST NUMBER(STSTNM) INTO THE DISPLAY REG.(DISPLAY<1510>)  
;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:  
;S14=1 LOOP ON TEST  
;S11=1 INHIBIT ITERATIONS  
;S00=1 LOOP ON ERROR  
;S00=1 LOOP ON TEST IN "SLPTST"  
;CALL  
;\* SCOPE ;:SCOPE=IOT  
SCOPE:  
640:  
101 BIT \$BIT14,\$SWR ;:LOOP ON PRESENT TEST?  
BNE \$OVER ;:YES IF S14=1  
;\*\*\*\*\*START OF CODE FOR THE XOR TESTER\*\*\*\*\*  
EXTSTR: BR 60 ;:IF RUNNING ON THE "XOR" TESTER CHANGE  
;THIS INSTRUCTION TO A "NOP" (NOP=240)  
MOV \$ERRVEC,-(SP) ;:SAVE THE CONTENTS OF THE ERROR VECTOR  
MOV \$S6,\$ERRVEC ;:SET FOR TIMEOUT  
TST \$177060 ;:TIME OUT ON XOR?  
MOV (SP)+,\$ERRVEC ;:RESTORE THE ERROR VECTOR  
\$SVLAD ;:GO TO THE NEXT TEST  
561 CMP (SP)+,(SP)+ ;:CLEAR THE STACK AFTER A TIME OUT  
MOV (SP)+,\$ERRVEC ;:RESTORE THE ERROR VECTOR  
BR 78 ;:LOOP ON THE PRESENT TEST  
661;\*\*\*\*\*END OF CODE FOR THE XOR TESTER\*\*\*\*\*  
BIT \$BIT00,\$SWR ;:LOOP ON SPEC. TEST?  
BEQ 20 ;:BR IF NO  
CMP \$LPTST,\$STSTNM ;:ON THE RIGHT TEST?  
BEQ \$OVER ;:BR IF YES  
201 TST \$ERFLG ;:HAS AN ERROR OCCURRED?  
BEQ 30 ;:BR IF NO  
CMP \$ERMAX,\$ERFLG ;:MAX. ERRORS FOR THIS TEST OCCURRED?  
BHI 30 ;:BR IF NO  
BIT \$BIT09,\$SWR ;:LOOP ON ERROR?  
BEQ 40 ;:BR IF NO  
701 MOV \$LPERR,\$LPADR ;:SET LOOP ADDRESS TO LAST SCOPE  
BR \$OVER  
401 CLR \$ERFLG ;:ZERO THE ERROR FLAG  
CLR \$TIMES ;:CLEAN THE NUMBER OF ITERATIONS TO MAKE  
BR 10 ;:ESCAPE TO THE NEXT TEST  
301 BIT \$BIT11,\$SWR ;:INHIBIT ITERATIONS?  
BNE 10 ;:BR IF YES  
TST \$PASS ;:IF FIRST PASS OF PROGRAM  
BFO 10 ;: INHIBIT ITERATIONS  
INC ;:INCREMENT ITERATION COUNT  
\$TIMES,\$ICNT ;:CHECK THE NUMBER OF ITERATIONS MADE  
\$OVER ;:BR IF MORE ITERATION REQUIRED  
\$ICNT ;:REINITIALIZE THE ITERATION COUNTER  
\$TIMES,\$TIMES ;:SET NUMBER OF ITERATIONS TO DO  
\$STSTNM ;:COUNT TEST NUMBERS  
\$STSTNM,\$STSTN ;:SET TEST NUMBER IN APT MAILBOX  
MOV (SP),\$LPADR ;:SAVE SCOPE LOOP ADDRESS

9377	042204	011637	001112		MOV	(SP),0LPERR	;;SAVE ERROR LOOP ADDRESS
9378	042217	005037	001312		CLR	0ESCAPE	;;CLEAR THE ESCAPE FROM ERROR ADDRESS
9379	042214	012737	000001	001120	MOV	#1,0ERMAX	;;ONLY ALLOW ONE(1) ERROR ON NEXT TEST
9380	042222	013777	001102	136716	MOVER	0TSTNM,0DISPLAY	;;DISPLAY TEST NUMBER
9381	042230	013716	001110		MOV	0LPADR,(SP)	;;FUDGE RETURN ADDRESS
9382	042234	000002			RTI		;;FIXES PS
9383	042236	003720			0MXCNT:	2000.	;;MAX. NUMBER OF ITERATIONS

```

9384          .SBTTL  ERROR HANDLER ROUTINE
9385
9386          ;;*****
9387          ;;THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
9388          ;;SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
9389          ;;AND GO TO $TYPERR ON ERROR
9390          ;;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
9391          ;;0W15=1  HALT ON ERROR
9392          ;;0W13=1  INHIBIT ERROR TYPEOUTS
9393          ;;0W10=1  BELL ON ERROR
9394          ;;0W09=1  LOOP ON ERROR
9395          ;;CALL
9396          ;;*  ERROR  N          ;;ERROR=EMT AND N=ERROR ITEM NUMBER
9397
9398          042240          010037          002016          002032          002032
9399          042244          010137          002020          002022          002024
9400          042250          010237          002022          002024          002026
9401          042254          010337          002024          002026          002030
9402          042260          010437          002026          002030          002032
9403          042264          010537          002030          002032          002034
9404          042270          010637          002032          002034          002036
9405          042274          010737          002034          002036          002038
9406          042278          010837          002036          002038          002040
9407          042282          010937          002038          002040          002042
9408          042286          011037          002040          002042          002044
9409          042290          011137          002042          002044          002046
9410          042294          011237          002044          002046          002048
9411          042298          011337          002046          002048          002050
9412          042302          011437          002048          002050          002052
9413          042306          011537          002050          002052          002054
9414          042310          011637          002052          002054          002056
9415          042314          011737          002054          002056          002058
9416          042318          011837          002056          002058          002060
9417          042322          011937          002058          002060          002062
9418          042326          012037          002060          002062          002064
9419          042330          012137          002062          002064          002066
9420          042334          012237          002064          002066          002068
9421          042338          012337          002066          002068          002070
9422          042342          012437          002068          002070          002072
9423          042346          012537          002070          002072          002074
9424          042350          012637          002072          002074          002076
9425          042354          012737          002074          002076          002078
9426          042358          012837          002076          002078          002080
9427          042362          012937          002078          002080          002082
9428          042366          013037          002080          002082          002084
9429          042370          013137          002082          002084          002086
9430          042374          013237          002084          002086          002088
9431          042378          013337          002086          002088          002090
9432          042382          013437          002088          002090          002092
9433          042386          013537          002090          002092          002094
9434          042390          013637          002092          002094          002096
9435          042394          013737          002094          002096          002098
9436          042398          013837          002096          002098          002100
9437          042402          013937          002098          002100          002102
9438          042406          014037          002100          002102          002104
9439          042410          014137          002102          002104          002106

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```

9440 042466 02737 033314 000042      CMP      #ENDAD,0042      ;;ACT=11 AUTO-ACCEPIT
9441 042474 001001                BNE      68              ;;BRANCH IF NO
9442 042476 000000                HALT                    ;;YES
9443 042500                68:
9444 042500 000002                640: RTI                ;RETURN

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```

9445                                     ;*****
9446                                     ;SBTTL ERROR MESSAGE TYPEOUT ROUTINE (MODIFIED SIGMAC)
9447
9448
9449 ;*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
9450 ;*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE",
9451 ;*($ERRTB) THE ERROR MESSAGE, DATA HEADER, AND DATA VALUES TO PRINT.
9452 ;*THIS ROUTINE IS IDENTICAL TO THE SIGMAC ROUTINE $ERRTYP, EXCEPT THIS
9453 ;*ROUTINE PUTS A <HT> BETWEEN OCTAL TYPED DATA VALUES, SO THAT EACH
9454 ;*VALUE STARTS AT A HORIZONTAL TAB STOP. ALSO, THE DATA FORMAT
9455 ;*POINTER HAS BEEN ELIMINATED FROM THE ERROR VECTOR. THIS ROUTINE
9456 ;*ALSO ALWAYS PRINTS $TESTN AND $ERRPC AS THE FIRST TWO DATA ELEMENTS
9457 ;*(WITH APPROPRIATE HEADERS).
9458
9459 $TYPERR:
9460      TYPE      ; TYPE "HOT" OR "AKM"
9461      HOTWRM:   WORD      $CRLF      ; PTR TO MESSAGE
9462      MOV      R0,-($P)      ; SAVE P0
9463      MOV      R1,-($SP)     ; SAVE R1
9464      CLR      R0            ; PICKUP ITEM INDX
9465      BISS    000ITEMB,R0    ;
9466      BNE     10            ; IF ITEM NUMBER FROM ERROR 0,
9467                                     ; JUST TYPE PC OF ERROR
9468      MOV     $ERRPC,-($P)    ; GET ERROR PC FOR TYPEOUT
9469      TYPOC   ; TYPE OCTAL, ALL DIGITS
9470      BR     70            ; EXIT
9471      10:     DEC      R0      ; ADJUST ERROR # FOR TABLE INDEX
9472      ASL     R0          ; OF 6 BYTES/ENTRY
9473      MOV     R0,R1      ;
9474      ASL     R0          ;
9475      ADD     R1,R0      ;
9476      ADD     00ERRTB,R0  ; FORM TABLE PTR
9477      MOV     (R0)+,20    ; PICKUP "ERROR MESSAGE" PTR
9478      BEQ     30          ; SKIP TYPEOUT IF NULL
9479      TYPE   ; TYPE "ERROR MESSAGE"
9480      ,WORD  0            ; "ERROR MESSAGE" PTR HERE
9481      TYPE   ; CR & LF
9482      ,CRLF ;
9483      MOV     (R0)+,40    ; PICKUP "DATA HEADER" PTR
9484      BEQ     50          ; SKIP TYPEOUT IF NULL
9485      TYPE   ; "DATA HEADER"
9486      ,WORD  0            ; "DATA HEADER" PTR HERE
9487      TYPE   ; CR & LF
9488      ,CRLF ;
9489      MOV     000,-($P)   ; ($TESTN)
9490      TYPOC   ; OCTAL W/ LEADING ZEROS
9491      TYPE   ; <HT>
9492      MOV     100,-($P)  ; ($ERRPC)
9493      TYPOC   ; OCTAL W/ LEADING ZEROS
9494      TYPE   ; <HT>
9495      MOV     (P0),R0     ; PICKUP "DATA TABLE" PTR
9496      BEQ     70          ; EXIT IF NULL
9497      MOV     0(R0)+,-($P) ; SAVE ... FOR TYPEOUT
9498      TYPOC   ; TYPE OCTAL, ALL DIGITS
9499      TST    (R0)        ; ANOTHER NUMBER ?
9500      BEQ     70          ; NO - EXIT
9501      TYPE   ; TAB BETWEEN ELEMENTS

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9501 042654 000771          BR      68          ; LOOP ON DATA TABLE VECTOR
9502 042656 012601          MOV     (SP)+,R1    ; RESTORE R1
9503 042660 012600          MOV     (SP)+,R0    ; RESTORE R0
9504 042662 144401 001321   TYPE   ,SCLRF      ; CR & LF
9505 042666 000207          RTS     PC          ; RETURN
9506 042670 001330          .WORD  0TESTN     ;
9507 042672 001122          .WORD  0ERRPC     ;
9508 042674 000011          .ASCIZ '<HT>'      ;
9509 042676 042524 052123 021440 110: .ASCIZ "TEST 0 ERR PC"
9510 042704 042411 051122 050040          .ASCIZ "TEST 0 ERR PC"
9511 042712 004503 000          .EVEN
9512 042716

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9513          .SBTTL TYPE ROUTINE
9514
9515          ;*****
9516          ;ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
9517          ;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
9518          ;NOTE1: 0NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
9519          ;NOTE2: 0FILLC CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
9520          ;NOTE3: 0FILLC CONTAINS THE CHARACTER TO FILL AFTER.
9521          ;*
9522          ;*CALLI
9523          ;*1) USING A TRAP INSTRUCTION
9524          ;* TYPE ,MESADR ;MESADR IS FIRST ADDRESS OF AN ASCII STRING
9525          ;*OR
9526          ;* TYPE
9527          ;* MESADR
9528          ;*
9529
9530 042716 105737 001165 0TYPE: TSTB 0TPFLG ;IS THERE A TERMINAL?
9531 042722 100002          BPL     10          ;BR IF YES
9532 042724 000000          HALT          ;HALT HERE IF NO TERMINAL
9533 042726 000430          BR      30          ;LEAVE
9534 042730 010046          MOV     R0,-(SP)   ;SAVE R0
9535 042732 017600 000002          MOV     02(SP),R0 ;GET ADDRESS OF ASCII STRING
9536 042736 122737 000001 001344          CMPB   02PTENV,0ENV ;RUNNING IN APT MODE
9537 042744 001011          BNE     620        ;NO,GO CHECK FOR APT CONSOLE
9538 042746 132737 000100 001345          BITB   02PTSP00L,0ENVM ;SPOOL MESSAGE TO APT
9539 042754 001405          BEO     620        ;NO,GO CHECK FOR CONSOLE
9540 042756 010037 042766          MOV     R0,010    ;SETUP MESSAGE ADDRESS FOR APT
9541 042762 004737 043206          JSR    PC,0ATY3   ;SPOOL MESSAGE TO APT
9542 042766 000000          .WORD  0          ;MESSAGE ADDRESS
9543 042770 132737 000040 001345 620: BITB   02PTCSUP,0ENVM ;APT CONSOLE SUPPRESSED
9544 042776 001003          BNE     600        ;YES,SKIP TYPE OUT
9545 043000 112046          MOVB   (R0)+,-(SP) ;PUSH CHARACTER TO BE TYPED ONTO STACK
9546 043002 001005          BNE     48          ;BR IF IT ISN'T THE TERMINATOR
9547 043004 005726          TST    (SP)+       ;IF TERMINATOR POP IT OFF THE STACK
9548 043006 012600          MOV     (SP)+,R0   ;RESTORE R0
9549 043010 002716 000002          ADD    02,(SP)     ;ADJUST RETURN PC
9550 043014 000002          RTI          ;RETURN
9551 043016 122716 000011          CMPB   0HT,(SP)    ;BRANCH IF <HT>
9552 043022 001430          BEQ     00        ;
9553 043024 122716 000200          CMPB   0CRLF,(SP)  ;BRANCH IF NOT <CRLF>
9554 043030 001000          BNE     50        ;
9555 043032 005726          TST    (SP)+       ;POP <CR><LF> EQUIV
9556 043034 104401          TYPE   ;TYPE A CR AND LF
9557 043036 001321          SCLRF          ;
9558 043040 105037 043174          CLRB   0CHARCNT    ;CLEAR CHARACTER COUNT
9559 043044 000755          BR      20          ;GET NEXT CHARACTER
9560 043046 004737 043130          JSR    PC,0TYPEPC  ;GO TYPE THIS CHARACTER
9561 043052 123726 001164          CMPB   0FILLC,(SP)+ ;IS IT TIME FOR FILLER CHARS.?
9562 043056 001350          BNE     20          ;IF NO GO GET NEXT CHAR.
9563 043060 013746 001162          MOV     0NULL,-(SP) ;GET # OF FILLER CHARS. NEEDED
9564          ;AND THE NULL CHAR.
9565 043064 105366 000001          DECR   1(0P)       ;DOES A NULL NEED TO BE TYPED?
9566 043070 002770          BLT    00          ;BR IF NO--GO POP THE NULL OFF OF STACK
9567 043072 004737 043130          JSR    PC,0TYPEPC  ;GO TYPE A NULL
9568 043076 105337 043174          DECB   0CHARCNT    ;DO NOT COUNT AS A COUNT

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9569 043102 000770          BR      78          ;;LOOP
9570
9571          ;HORIZONTAL TAB PROCESSOR
9572
9573 043104 112716 000040 001  MOVB  8' ,(SP)          ;;REPLACE TAB WITH SPACE
9574 043110 044737 043130 901  JSR   PC,STYDEC          ;;TYPE A SPACE
9575 043114 132737 000007 043174 BITB  07,SCHARCNT          ;;BRANCH IF NOT AT
9576 043122 001372          BNE   04          ;;TAB STOP
9577 043124 005726          TST   (SP)+          ;;POP SPACE OFF STACK
9578 043126 000724          BR    28          ;;GET NEXT CHARACTER
9579 043130 105777 136022  STYPEC: TSTB  04TPS          ;;WAIT UNTIL PRINTER IS READY
9580 043134 100375          BPL   STYPEC
9581 043136 116677 000002 136014  MOVB  2(SP),08TPB          ;;LOAD CHAR TO BE TYPED INTO DATA REG.
9582 043144 122766 000015 000002  CMPB  0CR,2(SP)          ;;IS CHARACTER A CARRIAGE RETURN?
9583 043152 001003          BNE   18          ;;BRANCH IF NO
9584 043154 105037 043174  CLRB  SCHARCNT          ;;YES--CLEAR CHARACTER COUNT
9585 043160 000406          BR    STYPEX          ;;EXIT
9586 043162 122766 000012 000002 101  CMPB  0LF,2(SP)          ;;IS CHARACTER A LINE FEED?
9587 043170 001402          BEQ   STYPEX          ;;BRANCH IF YES
9588 043172 105227          INCB  (PC)+          ;;COUNT THE CHARACTER
9589 043174 000000          SCHARCNT,WORD 0          ;;CHARACTER COUNT STORAGE
9590 043176 000207  STYPEX: RTS      PC
9591

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9592          .SBTTL APT COMMUNICATIONS ROUTINE
9593
9594
9595 043200 112737 000001 043444  STY1:  MOVB  01,0FFLG          ;;TO REPORT FATAL ERROR
9596 043206 112737 000001 043442  STY3:  MOVB  01,0MFLG          ;;TO TYPE A MESSAGE
9597 043214 000403          BR    04TYC
9598 043216 112737 000001 043444  STY4:  MOVB  01,0FFLG          ;;TO ONLY REPORT FATAL ERROR
9599 043224  STYC:
9600 043224 010046          MOV   R0,-(SP)          ;;PUSH R0 ON STACK
9601 043226 010146          MOV   R1,-(SP)          ;;PUSH R1 ON STACK
9602 043230 105737 043442  TSTB  0MFLG          ;;SHOULD TYPE A MESSAGE?
9603 043234 001450          BEQ   56          ;;IF NOT: BR
9604 043236 122737 000001 001344  CMPB  0APTENV,0ENV          ;;OPERATING UNDER APT?
9605 043244 001031          BNE   36          ;;IF NOT: BR
9606 043246 132737 000100 001345  BITB  0APTSPOOL,0ENVM          ;;SHOULD SPOOL MESSAGE?
9607 043254 001425          BEQ   36          ;;IF NOT: BR
9608 043256 017600 000004          MOV   04(SP),R0          ;;GET MESSAGE ADDR.
9609 043262 002766 000002 000004  ADD   02,4(SP)          ;;BUMP RETURN ADDR.
9610 043270 005737 001324 101  TST   0MSGTYPE          ;;SEE IF DONE W/ LAST XMISSION?
9611 043274 001375          BNE   18          ;;IF NOT: WAIT
9612 043276 010037 001340          MOV   R0,0MSGADR          ;;PUT ADDR IN MAILBOX
9613 043302 105720 201  TSTB  (R0)+          ;;FIND END OF MESSAGE
9614 043304 001376          BNE   28
9615 043306 103700 001340  SUB   0MSGADR,R0          ;;SUB START OF MESSAGE
9616 043312 000700          ASR   R0
9617 043314 010037 001342          MOV   R0,0MSGGLT          ;;GET MESSAGE LGTH IN WORDS
9618 043320 012737 000004 001324  MOV   04,0MSGTYPE          ;;PUT LENGTH IN MAILBOX
9619 043326 000413          BR    58          ;;TELL APT TO TAKE MSG.
9620 043330 017637 000004 043354 301  MOV   04(SP),48          ;;PUT MSG ADDR IN JSR LINKAGE
9621 043336 002766 000002 000004  ADD   02,4(SP)          ;;BUMP RETURN ADDRESS
9622 043344 013746 177776          MOV   177776,-(SP)          ;;PUSH 177776 ON STACK
9623 043350 004737 042716          JSR   PC,0TYPE          ;;CALL TYPE MACRO
9624 043354 000000          .WORD 0
9625 043356          501
9626 043356 105737 043444 1001  TSTB  0FFLG          ;;SHOULD REPORT FATAL ERROR?
9627 043362 001416          BEQ   120          ;;IF NOT: BR
9628 043364 005737 001344          TST  0ENV          ;;RUNNING UNDER APT?
9629 043370 001413          BEQ   120          ;;IF NOT: BR
9630 043372 005737 001324 1101  TST   0MSGTYPE          ;;FINISHED LAST MESSAGE?
9631 043376 001375          BNE   110          ;;IF NOT: WAIT
9632 043400 017637 000004 001326          MOV   04(SP),0FATAL          ;;GET ERROR #
9633 043406 002766 000002 000004  ADD   02,4(SP)          ;;BUMP RETURN ADDR.
9634 043414 005237 001324          INC   0MSGTYPE          ;;TELL APT TO TAKE ERROR
9635 043420 105037 043444 1201  CLRB  0FFLG          ;;CLEAR FATAL FLAG
9636 043424 105037 043443          CLRB  0LFLG          ;;CLEAR LOG FLAG
9637 043430 105037 043442          CLRB  0MFLG          ;;CLEAR MESSAGE FLAG
9638 043434 012001          MOV   (SP)+,R1          ;;POP STACK INTO R1
9639 043436 012000          MOV   (SP)+,R0          ;;POP STACK INTO R0
9640 043440 000207          RTS   PC          ;;RETURN
9641 043442          000          SFFLG: .BYTE 0          ;;MSGG. FLAG
9642 043443          000          SFLG: .BYTE 0          ;;LOG FLAG
9643 043444          000          SFFLG: .BYTE 0          ;;FATAL FLAG
9644          043446          .EVEN
9645          000200          APTSIZE=200
9646          000001          APTENV=001
9647          000100          APTSPOOL=100

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9648

000040

APTCUP=040

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.SBTTL BINARY TO OCTAL (ASCII) AND TYPE
;*****
;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
;OCTAL (ASCII) NUMBER AND TYPE IT.
;STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
;CALL:
;*   MOV   NUM,=(SP)           ;NUMBER TO BE TYPED
;*   TYPOS ;CALL FOR TYPEOUT
;*   .BYTE N                   ;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
;*   .BYTE M                   ;M=1 OR 0
;*                                     ;;TYPE LEADING ZEROS
;*                                     ;;SUPPRESS LEADING ZEROS
;*
;STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
;STYPOS OR STYPOC
;CALL:
;*   MOV   NUM,=(SP)           ;NUMBER TO BE TYPED
;*   TYPON ;CALL FOR TYPEOUT
;*
;STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
;CALL:
;*   MOV   NUM,=(SP)           ;NUMBER TO BE TYPED
;*   TYPOC ;CALL FOR TYPEOUT
;
9674 043446 017646 000000 043671 STYPOS: MOV   0(SP),-(SP)           ;PICKUP THE NUMB
9675 043452 016637 000001 043671 MOVB  1(SP),00FILL ;LOAD ZERO FILL SWITCH
9676 043460 012637 043673 MOVB  (SP)+,00MODE+1 ;NUMBER OF DIGITS TO TYPE
9677 043464 062716 000002 ADD   #2,(SP)         ;ADJUST RETURN ADDRESS
9678 043470 000406 BR     STYPON
9679 043472 012737 000201 043671 STYPOC: MOVB #1,00FILL ;SET THE ZERO FILL SWITCH
9680 043500 012737 000006 043673 MOVB  #6,00MODE+1 ;SET FOR SIX(6) DIGITS
9681 043506 012737 000005 043670 STYPON: MOVB #5,00CNT ;SET THE ITERATION COUNT
9682 043514 010346 MOV   R3,-(SP)       ;SAVE R3
9683 043516 010446 MOV   R4,-(SP)       ;SAVE R4
9684 043520 010546 MOV   R5,-(SP)       ;SAVE R5
9685 043522 013704 043673 MOVB  #0MODE+1,R4 ;GET THE NUMBER OF DIGITS TO TYPE
9686 043526 005404 NEG   R4
9687 043530 007704 ADD   #6,R4         ;SUBTRACT IT FOR MAX. ALLOWED
9688 043534 010437 043672 MOVB  R4,00MODE ;SAVE IT FOR USE
9689 043540 013704 043671 MOVB  #0FILL,R4 ;GET THE ZERO FILL SWITCH
9690 043544 010605 000012 MOV   12(SP),R5 ;PICKUP THE INPUT NUMBER
9691 043550 005003 CLF  R3            ;CLEAR THE OUTPUT WORD
9692 043552 000105 15:  ROL  R3            ;ROTATE MSB INTO "C"
9693 043554 002404 BR   R3            ;GO DO MSB
9694 043556 006105 26:  ROL  R5            ;FORM THIS DIGIT
9695 043560 006105 PCL  R5
9696 043562 006105 ROL  R5
9697 043564 010503 MOV  R5,R3
9698 043566 006103 38:  ROL  R3            ;GET LSB OF THIS DIGIT
9699 043570 005407 043672 DECB  #0MODE ;TYPE THIS DIGIT
9700 043574 000016 BFL  R4            ;BR IF NO
9701 043576 004703 177770 BIC  #177770,R3 ;GET RID OF JUNK
9702 043602 001002 BNE  #4            ;TEST FOR 0
9703 043604 005701 TST  R4            ;SUPPRESS THIS
9704 043606 001403 RFC  #5            ;BR IF YES

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9705 043610 005201          401  INC  R4          ;;DON'T SUPPRESS ANIMORE 0'S
9706 043612 052703 000060    501  BIS  #'0,R3      ;;MAKE THIS DIGIT ASCII
9707 043616 052703 000040    501  BIS  #' ,R3      ;;MAKE ASCII IF NOT ALREADY
9708 043622 110337 043666    701  MOVB R3,R0        ;;SAVE FOR TYPING
9709 043626 110401 043666    701  TYPE  ,R0        ;;GO TYPE THIS DIGIT
9710 043632 115337 043670    701  DECB 00CNT      ;;COUNT BY 1
9711 043636 003347          601  BGT  28          ;;BR IF MORE TO DO
9712 043640 002402          601  BLT  68          ;;BR IF DONE
9713 043642 005204          601  INC  R4          ;;INSURE LAST DIGIT ISN'T A BLANK
9714 043644 000741          601  HR   28          ;;GO DO THE LAST DIGIT
9715 043646 012005          601  MOV  (SP)+,R5     ;;RESTORE R5
9716 043650 012004          601  MOV  (SP)+,R4     ;;RESTORE R4
9717 043652 012003          601  MOV  (SP)+,R3     ;;RESTORE R3
9718 043654 016006 000002 000004 601  MOV  2(SP),4(SP)  ;;SET THE STACK FOR RETURNING
9719 043662 012016          601  MOV  (SP)+,(SP)
9720 043664 000002          601  RTI          ;;RETURN
9721 043666 000          601  .BYTE 0          ;;STORAGE FOR ASCII DIGIT
9722 043667 000          601  .BYTE 0          ;;TERMINATOR FOR TYPE ROUTINE
9723 043670 000          60CNT: .BYTE 0          ;;OCTAL DIGIT COUNTER
9724 043671 000          60FILL: .BYTE 0          ;;ZERO FILL SWITCH
9725 043672 000000          60MOU0: .WORD 0          ;;NUMBER OF DIGITS TO TYPE

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9726          .SBTTL TRAP DECODER
9727
9728          ;;*****
9729          ;;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
9730          ;;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
9731          ;;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
9732          ;;GO TO THAT ROUTINE.
9733
9734 043674 010046          6TRAP: MOV  R0,-(SP)      ;;SAVE R0
9735 043676 016000 000002    6TRAP: MOV  2(SP),R0     ;;GET TRAP ADDRESS
9736 043702 005740          6TRAP: TST  -(R0)       ;;BACKUP BY 2
9737 043704 111000          6TRAP: MOVB (R0),R0     ;;GET RIGHT BYTE OF TRAP
9738 043706 006300          6TRAP: ASL  R0          ;;POSITION FOR INDEXING
9739 043710 016000 043730    6TRAP: MOV  STRPAD(R0),R0 ;;INDEX TO TABLE
9740 043714 000200          6TRAP: RTS  R0          ;;GO TO ROUTINE
9741
9742
9743          ;;THIS IS USE TO HANDLE THE "GETPRI" MACRO
9744
9745 043716 011046          6THAP2: MOV  (SP),-(SP)  ;;MOVE THE PC DOWN
9746 043720 016066 000004 000002 6THAP2: MOV  4(SP),2(SP) ;;MOVE THE PSW DOWN
9747 043726 000002          6THAP2: RTI          ;;RESTORE THE PSW
9748
9749          .SBTTL TRAP TABLE
9750
9751          ;;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
9752          ;;BY THE "TRAP" INSTRUCTION.
9753
9754          ; ROUTINE
9755          ; -----
9756 043730 043710          6TRPAD: .WORD 6THAP2
9757 043732 042716          6TRAP1: .TYPE 6THAP2 TRAP+1(104401) TTY TYPEOUT ROUTINE
9758 043734 043472          6TRAP2: .TYPOC 6THAP2 TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
9759 043736 043446          6TRAP3: .TYPOS 6THAP2 TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
9760 043740 043506          6TRAP4: .TYPON 6THAP2 TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
9761
9762

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9763          .SBTTL POWER DOWN AND UP ROUTINES
9764
9765          ;*****
9766          ;POWER DOWN ROUTINE
9767          $PWRDN: MOV    $BILLLUP,$PWRVEC    ;;SET FOR FAST UP
9768          MOV    $340,$PWRVEC+2    ;;PRIO:7
9769          MOV    R0,-(SP)    ;;PUSH R0 ON STACK
9770          MOV    P1,-(SP)    ;;PUSH P1 ON STACK
9771          MOV    R2,-(SP)    ;;PUSH R2 ON STACK
9772          MOV    R3,-(SP)    ;;PUSH R3 ON STACK
9773          MOV    R4,-(SP)    ;;PUSH R4 ON STACK
9774          MOV    R5,-(SP)    ;;PUSH R5 ON STACK
9775          MOV    $SWR,-(SP)    ;;PUSH $SWR ON STACK
9776          MOV    $P,$SAVR6    ;;SAVE SP
9777          MOV    $P,$PWRUP,$PWRVEC    ;;SET UP VECTOR
9778          HALT
9779          BR    *-2    ;;HANG UP
9780
9781          ;*****
9782          ;POWER UP ROUTINE
9783          $PWRUP: MOV    $BILLLUP,$PWRVEC    ;;SET FOR FAST DOWN
9784          MOV    $SAVR6,$P    ;;GET SP
9785          CLR    $SAVR6    ;;WAIT LOOP FOR THE TTY
9786          INC    $SAVR6    ;;WAIT FOR THE INC
9787          HNE    18    ;;OF WORD
9788          MOV    (SP),R0    ;;GET SAVED SWR OFF STACK
9789          MED    +226    ;;RESTORE SWR CONTENTS
9790          MOV    (SP)+,$SWR    ;;POP STACK INTO $SWR
9791          MOV    (SP)+,R5    ;;POP STACK INTO R5
9792          MOV    (SP)+,R4    ;;POP STACK INTO R4
9793          MOV    (SP)+,R3    ;;POP STACK INTO R3
9794          MOV    (SP)+,R2    ;;POP STACK INTO R2
9795          MOV    (SP)+,R1    ;;POP STACK INTO R1
9796          MOV    (SP)+,R0    ;;POP STACK INTO R0
9797          MOV    $PWRDN,$PWRVEC    ;;SET UP THE POWER DOWN VECTOR
9798          MOV    $340,$PWRVEC+2    ;;PRIO:7
9799          TYPE
9800          $PWRMG: .WORD    $POWER    ;;REPORT THE POWER FAILURE
9801          MOV    (PC)+,(SP)    ;;POWER FAIL MESSAGE POINTER
9802          $PWRAD: .WORD    START    ;;RESTART AT START
9803          RTI
9804          $ILLUP: HALT    ;;THE POWER UP SEQUENCE WAS STARTED
9805          BR    *-2    ;; BEFORE THE POWER DOWN WAS COMPLETE
9806          $SAVR6: 0    ;;PUT THE SP HERE
9807          $POWER: .ASCII <15><12>"POWER"
9808
9809          .EVEN

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9810          .SBTTL ERROR MESSAGES, DATA HEADERS, DATA VECTORS, ETC
9811
9812          ; MESSAGE PREFIXES
9813          $ASCHOT: .ASCII "NOT: "
9814          $ASCWRM: .ASCII "WARN: "
9815
9816          ; ERROR MESSAGES HERE
9817          $EMA: .ASCII "RECEIVED FPS IS BAD"
9818
9819          $EMBI: .ASCII "RECEIVED FEC/FEA IS BAD"
9820
9821          $EMCI: .ASCII "UNEXPECTED FLOATING POINT TRAP, IGNORED & CONTINUING"
9822
9823          $EMDI: .ASCII "CPU PS CONDITION CODES SET INCORRECTLY"
9824
9825          $EMEI: .ASCII "CMP(F/D) OPERATION - REGISTER MODIFIED AFTER EXECUTION"
9826
9827          $EMFI: .ASCII "ADD/SUB(F/D) OPERATION - RESULT INCORRECT"
9828
9829          $EMGI: .ASCII "MUL/DIV(F/D) OPERATION - RESULT INCORRECT"

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9866	044564	051120	041505	000124			
9867	044572	047515	024104	027506	EMH:	.ASCIZ	"MOD(F/D) OPERATION = FRACTIONAL RESULT INCORRECT"
9868	044600	024504	047440	042520			
9869	044606	040522	044524	047117			
9870	044614	026440	043040	040522			
9871	044622	052103	047511	040516			
9872	044630	020114	042522	052523			
9873	044636	052114	044440	041516			
9874	044644	051117	042522	052103			
9875	044652	000					
9876	044653	115	042117	043050	EMI:	.ASCIZ	"MOD(F/D) OPERATION = INTEGER RESULT INCORRECT"
9877	044660	042057	020051	050117			
9878	044666	051105	052101	047511			
9879	044674	020116	020055	047111			
9880	044702	042524	042507	020122			
9881	044710	042522	052523	052114			
9882	044716	044440	041516	051117			
9883	044724	042522	052103	000			
9884	044731	106	047514	052101	EMJ:	.ASCIZ	"FLOAT-TO-DOUBLE CONVERSION = RESULT INCORRECT"
9885	044736	052055	026517	047504			
9886	044744	041125	042514	041440			
9887	044752	047117	042526	051522			
9888	044760	047511	020116	020055			
9889	044766	042522	052523	052114			
9890	044774	044440	041516	051117			
9891	045002	042522	052103	000			
9892	045007	104	052517	046102	EMK:	.ASCIZ	"DOUBLE-TO-FLOAT CONVERSION = RESULT INCORRECT"
9893	045014	026505	047524	043053			
9894	045022	047514	052101	041440			
9895	045030	047117	042526	051522			
9896	045036	047511	020116	020055			
9897	045044	042522	052523	052114			
9898	045052	044440	041516	051117			
9899	045060	042522	052103	000			
9900	045065	106	054111	042105	EML:	.ASCIZ	"FIXED-TO-FLOATING CONVERSION = RESULT INCORRECT"
9901	045072	052055	026517	046106			
9902	045100	040517	044524	043516			
9903	045106	041440	047117	042526			
9904	045114	051522	047511	020116			
9905	045122	020055	042522	052523			
9906	045130	052114	044440	041516			
9907	045136	051117	042522	052103			
9908	045144	000					
9909	045145	106	047514	052101	EMN:	.ASCIZ	"FLOATING-TO-FIXED CONVERSION = RESULT INCORRECT"
9910	045152	047111	026507	047524			
9911	045160	043055	054111	042105			
9912	045166	041440	047117	042526			
9913	045174	051522	047511	020116			
9914	045202	020055	042522	052523			
9915	045210	052114	044440	041516			
9916	045216	051117	042522	052103			
9917	045224	000					
9918	045225	114	040517	020104	EMO:	.ASCIZ	"LOAD EXPONENT(F/D) OPERATION = RESULT INCORRECT"
9919	045232	054105	047520	042516			
9920	045240	052116	043050	042057			
9921	045246	020051	050117	051105			

9922	045254	052101	047511	020116			
9923	045262	020055	042522	052523			
9924	045270	052114	044440	041516			
9925	045276	051117	042522	052103			
9926	045304	000					
9927	045305	123	047524	042522	EMO:	.ASCIZ	"STORE EXPONENT(F/D) OPERATION = RESULT INCORRECT"
9928	045312	042440	050130	047117			
9929	045320	047105	024124	027506			
9930	045326	024504	047440	042520			
9931	045334	040522	044524	047117			
9932	045342	026440	051040	051505			
9933	045350	046125	020124	047111			
9934	045356	047503	051122	041505			
9935	045364	000124					

Address	Hex	Hex	Hex	Hex	Label	Text
9936						; DATA HEADERS HERE
9937	045366	054105	023520	004504	DHA:	.ASCII "EXP'D RCV'D"
9938	045374	041522	023520	000104		
9939	045402	054105	023520	026504	DHB:	.ASCII "EXP'D-FEC-RCV'D" EXP'D-FEA-RCV'D"
9940	045410	042506	076503	041522		
9941	045416	023520	004504	054105		
9942	045424	023520	026504	042506		
9943	045432	026501	041522	023526		
9944	045440	000104				
9945	045442	026455	042455	050130	DHC:	.ASCII "---EXPECTED--- ---RECEIVED---"
9946	045450	041505	042524	026504		
9947	045456	026455	026411	026455		
9948	045464	042522	042503	053111		
9949	045472	042105	026455	000055		
9950	045500	026455	026455	026455	DHD:	.ASCII "-----EXPECTED----- -----RECEIVED-----"
9951	045506	026455	026455	042455		
9952	045514	050130	041505	042524		
9953	045522	026504	026455	026455		
9954	045530	026455	026455	026455		
9955	045536	026411	026455	026455		
9956	045544	026455	026455	026455		
9957	045552	042522	042503	053111		
9958	045560	042105	026455	026455		
9959	045566	026455	026455	026455		
9960	045574	000055				
9961	045576	046117	020104	041520	DHF:	.ASCII "OLD PC OLD PS NEW SP FPS FLC FEA"
9962	045604	047411	042114	050040		
9963	045612	004523	042516	020127		
9964	045620	050123	020011	050106		
9965	045626	004523	043040	041905		
9966	045634	020011	042506	000101		

Address	Hex	Hex	Hex	Hex	Label	Text
9967						; DATA ADDRESS VECTOR
9968						.EVEN
9969	045642	001240	002000	000000	DTA:	.WORD \$TMP4,\$FPS,0
9970	045650	001242	002000	000000	DTB:	.WORD \$TMP5,\$FPS,0
9971	045658	001244	002000	000000	DTC:	.WORD \$TMP6,\$FPS,0
9972	045666	001246	002000	000000	DTD:	.WORD \$TMP7,\$FPS,0
9973	045672	001252	002000	000000	UTE:	.WORD \$TMP11,\$FPS,0
9974	045700	001254	002000	000000	DTF:	.WORD \$TMP12,\$FPS,0
9975	045708	001262	002000	000000	DTE:	.WORD \$TMP15,\$FPS,0
9976	045714	001272	002000	000000	DTI:	.WORD \$TMP21,\$FPS,0
9977	045722	001242	002002	002014	DTJ:	.WORD \$TMP5,\$FEC,\$EXPFEA,\$FEA,0
9978	045730	001244	002002	002014		
9979	045734	001244	002002	002014	DTK:	.WORD \$TMP6,\$FEC,\$EXPFEA,\$FEA,0
9980	045742	001244	002002	002014		
9981	045746	001246	002002	002014	DTL:	.WORD \$TMP7,\$FEC,\$EXPFEA,\$FEA,0
9982	045754	002004	002000	000000		
9983	045760	001250	002002	002014	DTM:	.WORD \$TMP10,\$FEC,\$EXPFEA,\$FEA,0
9984	045766	002004	002000	000000		
9985	045772	001254	002002	002014	DTN:	.WORD \$TMP12,\$FEC,\$EXPFEA,\$FEA,0
9986	046000	002004	002000	000000		
9987	046004	001256	002002	002014	DTO:	.WORD \$TMP13,\$FEC,\$EXPFEA,\$FEA,0
9988	046012	002004	002000	000000		
9989	046016	001264	002007	002014	DTP:	.WORD \$TMP16,\$FEC,\$EXPFEA,\$FEA,0
9990	046024	002004	002000	000000		
9991	046030	001274	002002	002014	DTP:	.WORD \$TMP22,\$FEC,\$EXPFEA,\$FEA,0
9992	046036	002004	002000	000000		
9993	046042	001234	001170	000000	DTQ:	.WORD \$TMP2,\$REG0,0
9994	046050	001240	001170	000000	DTR:	.WORD \$TMP4,\$REG0,0
9995	046056	001230	001232	001170	DTS:	.WORD \$TMP0,\$TMP1,\$REG0,\$REG1,0
9996	046064	001172	000000			
9997	046070	001232	001234	001170	DTT:	.WORD \$TMP1,\$TMP2,\$REG0,\$REG1,0
9998	046076	001172	000000			
9999	046102	001234	001236	001170	DTU:	.WORD \$TMP2,\$TMP3,\$REG0,\$REG1,0
10000	046110	001172	000000			
10001	046114	001240	001242	001170	DTV:	.WORD \$TMP4,\$TMP5,\$REG0,\$REG1,0
10002	046122	001172	000000			
10003	046126	001244	001246	001174	DTE:	.WORD \$TMP6,\$TMP7,\$REG2,\$REG3,0
10004	046134	001176	000000			
10005	046140	001230	001232	001234	DTX:	.WORD \$TMP0,\$TMP1,\$TMP2,\$TMP3
10006	046146	001236				
10007	046150	001172	001174	001174		
10008	046156	001176	000000			
10009	046162	001232	001234	001236	DTY:	.WORD \$TMP1,\$TMP2,\$TMP3,\$TMP4
10010	046170	001240				
10011	046172	001170	001172	001174		
10012	046200	001176	000000			
10013	046204	001234	001236	001240	DTZ:	.WORD \$TMP2,\$TMP3,\$TMP4,\$TMP5
10014	046212	001242				
10015	046214	001170	001172	001174		
10016	046222	001176	000000			
10017	046226	001240	001242	001244	DTAA:	.WORD \$TMP4,\$TMP5,\$TMP6,\$TMP7
10018	046230	001246				
10019	046236	001170	001172	001174		
10020	046244	001176	000000			
10021	046250	001250	001254	001254	DTAB:	.WORD \$TMP10,\$TMP11,\$TMP12,\$TMP13
10022	046256	001256				

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10023 040260 001170 001172 001174 .WORD $REG0,$REG1,$REG2,$REG3,0
10024 046266 001176 000000
10025 046272 001260 001262 001264 DTAC: .WORD $TMP14,$TMP15,$TMP16,$TMP17
10026 046300 001266
10027 046302 001200 001202 001204 .WORD $REG4,$REG5,$REG6,$REG7,0
10028 046310 001206 000000
10029 046314 001174 001172 000000 DTAD: .WORD $REG2,$REG1,0
10030 046322 001176 001174 000000 DTAE: .WORD $REG3,$REG2,0
10031 046330 002006 002010 002012 DTAK: .WORD FPP0PC,FPP0PS,FPP0SP,FPS,FLC,F&A,0
10032 046336 002000 002002 002004
10033 046344 000000
10034
10035
10036 ; THE END
10037 .PP0001 .END
  
```

```

ABASE = 000000 330
ACDW1 = 000000 330
ACDW2 = 000000 330
ACPUOP = 000000 330 345
ADD01 034074 8103# 8114
ADD0L 034060 8097 8099#
ADD0T 034031 1426 1448 1470 1492 1514 1536 1558 1580 1602 1624 1646 1668 1690
      1712 1734 1756 1778 1800 1822 8091#
ADD01 005624 1425 1430#
ADD010 006272 1579 1584#
ADD011 006344 1601 1606#
ADD012 006416 1623 1628#
ADD013 006470 1645 1650#
ADD014 006542 1667 1672#
ADD015 006614 1689 1694#
ADD016 006666 1711 1716#
ADD017 006740 1733 1738#
ADD02 006676 1447 1452#
ADD020 007012 1755 1760#
ADD021 007064 1777 1782#
ADD022 007136 1799 1804#
ADD023 007210 1821 1826#
ADD03 005750 1469 1474#
ADD04 006022 1491 1496#
ADD05 006074 1513 1518#
ADD06 006146 1535 1540#
ADD07 006220 1557 1562#
ADD01 033720 8060# 8071
ADD0L 033710 8054 8056#
ADD0I 033664 1064 10#3 1102 1121 1140 1159 1178 1197 1216 1235 1254 1273 1292
      1311 1330 1349 1368 1387 1406 8040#
ADD01 004532 1063 1060#
ADD010 005054 1196 1201#
ADD011 005112 1215 1220#
ADD012 005150 1234 1239#
ADD013 005206 1253 1258#
ADD014 005244 1272 1277#
ADD015 005302 1291 1296#
ADD016 005340 1310 1315#
ADD017 005376 1329 1334#
ADD02 004570 1082 1087#
ADD020 004534 1348 1353#
ADD021 005472 1367 1372#
ADD022 005530 1386 1391#
ADD023 005566 1405 1410#
ADD03 004620 1101 1106#
ADD04 004664 1120 1125#
ADD05 004722 1139 1144#
ADD06 004760 1158 1163#
ADD07 004816 1177 1182#
ADD08 = 000000 330
ADD09 = 000000 330
ADD010 = 000000 330
ADD011 = 000000 330
ADD012 = 000000 330
ADD013 = 000000 330
  
```

















TST102	007400	1895	1900#
TST103	007430	1904	1919#
TST104	007474	1923	1938#
TST105	007532	1942	1957#
TST106	007570	1961	1976#
TST107	007626	1980	1995#
TST11	003516	735	749#
TST110	007664	1999	2014#
TST111	007722	2018	2033#
TST112	007760	2037	2052#
TST113	010016	2056	2071#
TST114	010054	2075	2090#
TST115	010112	2094	2110#
TST116	010164	2114	2132#
TST117	010236	2136	2154#
TST12	003550	753	767#
TST120	010310	2158	2176#
TST121	010362	2180	2198#
TST122	010434	2202	2220#
TST123	010506	2224	2242#
TST124	010560	2246	2264#
TST125	010632	2268	2286#
TST126	010704	2290	2308#
TST127	010756	2312	2330#
TST13	003602	771	785#
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TST131	011102	2356	2374#
TST132	011154	2378	2396#
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TST134	011264	2422	2437#
TST135	011322	2441	2456#
TST136	011360	2460	2475#
TST137	011416	2479	2494#
TST14	003634	789	803#
TST140	011454	2498	2513#
TST141	011512	2517	2532#
TST142	011550	2536	2551#
TST143	011606	2555	2570#
TST144	011644	2574	2589#
TST145	011702	2593	2608#
TST146	011740	2612	2627#
TST147	011776	2631	2646#
TST15	003666	807	822#
TST150	012034	2650	2665#
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TST153	012166	2707	2723#
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TST155	012312	2749	2767#
TST156	012364	2771	2789#
TST157	012436	2793	2811#
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TST161	012562	2837	2855#
TST162	012634	2859	2877#
TST163	012706	2881	2899#

TST164	012760	2903	2921#
TST165	013032	2925	2943#
TST166	013104	2947	2965#
TST167	013156	2969	2987#
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TST171	013302	3013	3031#
TST172	013354	3035	3053#
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TST174	013464	3079	3094#
TST175	013522	3098	3113#
TST176	013560	3117	3132#
TST177	013616	3136	3151#
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TST201	013712	3174	3189#
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TST204	014044	3231	3246#
TST205	014102	3250	3265#
TST206	014140	3269	3284#
TST207	014176	3288	3303#
TST21	004076	886	902#
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TST213	014366	3364	3379#
TST214	014424	3383	3399#
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TST220	014674	3469	3487#
TST221	014746	3491	3509#
TST222	015020	3513	3531#
TST223	015072	3535	3553#
TST224	015144	3557	3575#
TST225	015216	3579	3597#
TST226	015270	3601	3619#
TST227	015342	3623	3641#
TST23	004202	926	942#
TST230	015414	3645	3663#
TST231	015466	3667	3685#
TST232	015540	3689	3707#
TST233	015612	3711	3729#
TST234	015664	3733	3751#
TST235	015726	3755	3771#
TST236	015770	3775	3791#
TST237	016032	3795	3811#
TST24	004244	946	962#
TST240	016074	3815	3831#
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TST242	016200	3855	3871#
TST243	016242	3875	3891#
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TST246	#16410	3935	3951#
TST247	#16452	3953	3971#
TST25	#04306	966	982#
TST250	#16514	3975	3991#
TST251	#16556	3995	4011#
TST252	#1662#	4015	4031#
TST253	#16662	4035	4051#
TST254	#16724	4055	4072#
TST255	#17006	4076	4096#
TST256	#17079	4100	4120#
TST257	#17152	4124	4144#
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TST260	#17734	4148	4168#
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TST262	#1740#	4196	4216#
TST263	#17462	4220	4240#
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TST265	#17626	4268	4288#
TST266	#17710	4292	4312#
TST267	#17772	4316	4336#
TST27	#44412	1006	1022#
TST27#	#20054	4340	4360#
TST271	#20136	4364	4384#
TST272	#2022#	4388	4408#
TST273	#20302	4412	4432#
TST274	#20384	4436	4456#
TST275	#20426	4460	4476#
TST276	#20470	4484	4496#
TST277	#20532	4508	4516#
TST3	#03262	627	641#
TST3#	#44454	1026	1042#
TST300	#20574	4520	4536#
TST3#1	#20636	4540	4556#
TST302	#20700	4560	4576#
TST3#3	#20742	4580	4596#
TST3#4	#21004	4600	4616#
TST3#5	#21046	4620	4636#
TST3#6	#21110	4640	4656#
TST3#7	#21152	4660	4676#
TST31	#04516	1046	1062#
TST31#	#21214	4680	4696#
TST311	#21256	4700	4716#
TST312	#21320	4720	4736#
TST313	#21362	4740	4756#
TST314	#21424	4760	4777#
TST315	#21506	4781	4801#
TST316	#21570	4805	4825#
TST317	#21652	4829	4849#
TST32	#04554	1066	1081#
TST32#	#21734	4853	4873#
TST321	#22016	4877	4897#
TST322	#22100	4901	4921#
TST323	#22162	4925	4945#
TST324	#22244	4949	4969#
TST325	#22326	4973	4993#

TST326	#2241#	4997	5017#
TST327	#22472	5021	5041#
TST33	#04612	1085	1100#
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TST331	#22636	5069	5089#
TST332	#22720	5093	5113#
TST333	#23002	5117	5137#
TST334	#23064	5141	5161#
TST335	#23122	5165	5180#
TST336	#23160	5184	5199#
TST337	#23216	5203	5218#
TST34	#04650	1104	1119#
TST34#	#23254	5222	5237#
TST341	#23312	5241	5256#
TST342	#2335#	5260	5275#
TST343	#23406	5279	5294#
TST344	#23444	5298	5313#
TST345	#23502	5317	5332#
TST346	#23540	5336	5351#
TST347	#23576	5355	5370#
TST35	#04706	1123	1138#
TST35#	#23634	5374	5389#
TST351	#23672	5393	5408#
TST352	#23730	5412	5428#
TST353	#23766	5432	5447#
TST354	#24024	5451	5466#
TST355	#24062	5470	5485#
TST356	#24120	5489	5504#
TST357	#24156	5508	5523#
TST36	#04744	1142	1157#
TST36#	#24214	5527	5542#
TST361	#24252	5546	5561#
TST362	#24310	5565	5581#
TST363	#24346	5585	5600#
TST364	#24404	5604	5619#
TST365	#24442	5623	5638#
TST366	#24500	5642	5657#
TST367	#24536	5661	5676#
TST37	#05002	1161	1176#
TST37#	#24574	5680	5695#
TST371	#24632	5699	5714#
TST372	#24670	5718	5733#
TST373	#24726	5737	5752#
TST374	#24764	5756	5771#
TST375	#25022	5775	5790#
TST376	#2506#	5794	5810#
TST377	#2512#	5814	5829#
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TST4#	#05040	1180	1195#
TST4#0	#25160	5833	5848#
TST4#1	#2522#	5852	5867#
TST4#2	#2526#	5871	5886#
TST4#3	#2532#	5890	5905#
TST4#4	#2536#	5909	5924#
TST4#5	#2540#	5928	5941#
TST4#6	#25434	5945	5958#

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TST411	025536	5996	6010#
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TST415	025706	6068	6082#
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TST42	005131	1218	1233#
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TST421	026050	6139	6152#
TST422	026100	6156	6169#
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TST424	026160	6190	6203#
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TST426	026244	6225	6239#
TST427	026300	6243	6257#
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TST440	026644	6406	6420#
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TST442	026730	6443	6458#
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TST444	027020	6481	6496#
TST445	027054	6500	6515#
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TST455	027402	6649	6663#
TST456	027434	6667	6681#
TST457	027466	6685	6700#
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TST516	031310	7282	7299#
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TST526	031750	7450	7467#
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2011	2028#	2030	2047#	2049	2066#	2068	2085#	2087	2105#	2107	2127#	2129
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2679#	2681	2698#	2700	2718#	2720	2740#	2742	2762#	2764	2784#	2786	2806#
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3338	3355#	3357	3374#	3376	3394#	3396	3416#	3418	3438#	3440	3460#	3462
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4733	4751#	4753	4772#	4774	4796#	4798	4820#	4822	4844#	4846	4868#	4870
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7133#	7135	7152#	7154	7171#	7173	7190#	7192	7210#	7212	7231#	7233	7252#
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\$DCNT	043670											
\$DMODE	043672											
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\$PASS	001332											
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\$POWER	044122											
\$PWAD	044110											
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\$PWHG	044104	9800#										
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\$RDUCHR	***** U	9763										
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\$REGAD	001106	278#										
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		290#	9999	10001	10007	10011	10015	10019	10023	10027	10031	10035
\$REG1	001172	281#	7991	8035	8081	8124	8170	8213	8259	8302	8348	8391
		283#	8559	8615	8672	8716	8761	8795	8829	8862	8899	8932
		285#	9008#	9026#	9028	9055	9137	9178	9221	9248#	9257#	9299
		287#	9995	9997	9999	10001	10007	10011	10015	10019	10023	10029
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\$REG12	001214	290#										
\$REG13	001216	291#										
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\$REG15	001222	293#										
\$REG16	001224	294#										
\$REG17	001226	295#										
\$REG2	001174	282#	8037	8126	8215	8304	8393	8424#	8448	8500	8541#	8565
		284#	8797	8864	8934	8975#	8977#	8978	9025#	9027#	9028	9058#
		286#	9120#	9130	9223	9256#	9258#	9259	9296#	9298#	9299	10003
		288#	10019	10023	10029	10030	10031	10032	10033	10034	10035	10036
		290#	283#	8039	8128	8217	8306	8395	8450	8502	8549	8596
		292#	8936	9075#	9077#	9078	9127#	9129#	9130	9225	9257	9299
		294#	10023	10030								
\$REG4	001200	284#	8490#	8508	8597#	8625	10027					
\$REG5	001202	285#	8510	8627	10027							
\$REG6	001204	286#	8512	8629	10027							
\$REG7	001206	287#	8514	8631	10027							
\$R2A	***** U	9763										
\$SAVRE	***** U	9763										
\$SAVP6	044120	9776#	9784	9785#	9786#	9806#						
\$SCOPE	041762	402	9334#									
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\$STUP	177777	467#										
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\$SWK	167400	1#	12	320	321	322	491	492	494	495	606	624
		2#	678	696	714	732	750	768	804	843	863	883
		3#	923	943	963	983	1003	1023	1043	1063	1083	1103
		4#	1177	1196	1215	1234	1253	1272	1291	1310	1329	1348
		5#	1425	1447	1469	1491	1513	1535	1557	1579	1601	1623
		6#	1711	1733	1755	1777	1799	1821	1844	1863	1882	1901
		7#	1977	1996	2015	2034	2053	2072	2091	2111	2133	2155
		8#	2243	2265	2287	2309	2331	2353	2375	2397	2419	2438
		9#	2514	2533	2552	2571	2590	2609	2628	264		

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3304	3323	3342	3361	3380	3400	3422	3444	3466	3488	3510	3532	3554
3576	3598	3620	3642	3664	3686	3708	3730	3752	3772	3792	3812	3832
3852	3872	3892	3912	3932	3952	3972	3992	4012	4032	4052	4073	4097
4121	4145	4169	4193	4217	4241	4265	4289	4313	4337	4361	4385	4409
4433	4457	4477	4497	4517	4537	4557	4577	4597	4617	4637	4657	4677
4697	4717	4737	4757	4779	4802	4826	4850	4874	4898	4922	4946	4970
4994	5018	5042	5066	5090	5114	5138	5162	5181	5200	5219	5238	5257
5276	5295	5314	5333	5352	5371	5390	5409	5429	5448	5467	5486	5505
5524	5543	5562	5582	5601	5620	5639	5658	5677	5696	5715	5734	5753
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6240	6258	6276	6294	6313	6331	6349	6367	6385	6403	6421	6440	6459
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6968	6987	7006	7025	7044	7063	7082	7101	7120	7139	7158	7177	7196
7216	7237	7258	7279	7300	7321	7342	7363	7384	7405	7426	7447	7468
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7739	7757	7775	7793	7811	7829	7847	7865	7883	7902	7920	7938	7956
9336	9340	9350	9351	9354	9355	9356	9363	9364	9365	9377	9380	9383
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SSWPEG 001346  
SSWHRK 000000  
STESTN 001330  
STINLS 001310  
STKB 001154  
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3431	515											
9330	9352											
3340	9375*	9506										
320*	491*	7883*	7931*	9363*	9370	9373*	9383					
2710												
2700												
296*	7961	8005	8051	8094	8140	8183	8229	8272	8318	8361	8408	8464
8525	8581	8642	8686	8732	8775	8810	8843	8860	8913	8947	8997	9047
9099	9140	9191	9237	9277	9305	10005						
297*	9995	9997	10005	10009								
1044	9983	10021										
1058	9973	10021										
1066	9974	9985	10021									
1077	9987	10021										
1088	10025											
1090	9975	10025										
1100	9989	10025										
1110	10025											
298*	9993	9997	9999	10005	10009	10013						
312*												
313*	9976											
314*	9991											
315*												
316*												
317*												
318*												
319*												
299*	9999	10005	10009	10013								
300*	9969	9994	10001	10009	10013	10017						
301*	9970	9977	10001	10013	10017							
302*	9971	9979	10003	10017								
303*	9972	9981	10003	10017								
1*	12	600	606*	609	618	624*	627	636	642*	645	654	660*
663	672	678*	681	690	696*	699	700	714*	717	726	732*	735
744	750*	753	762	768*	771	780	786*	789	798	804*	807	817

823*	826	837	843*	846	857	863*	866	877	883*	886	897	903*
906	917	923*	926	937	943*	946	957	963*	966	977	983*	986
997	1003*	1006	1017	1023*	1026	1037	1043*	1046	1057	1063*	1066	1076
1082*	1085	1095	1101*	1104	1114	1120*	1123	1133	1139*	1142	1152	1158*
1161	1171	1177*	1180	1190	1196*	1199	1209	1215*	1216	1228	1234*	1237
1247	1253*	1256	1266	1272*	1275	1285	1291*	1294	1304	1310*	1313	1323
1329*	1332	1342	1348*	1351	1361	1367*	1370	1380	1386*	1389	1399	1405*
1408	1419	1425*	1428	1441	1447*	1450	1463	1469*	1472	1485	1491*	1494
1507	1513*	1516	1529	1535*	1538	1551	1557*	1560	1573	1579*	1582	1595
1601*	1604	1617	1623*	1626	1639	1645*	1648	1661	1667*	1670	1683	1689*
1692	1705	1711*	1714	1727	1733*	1736	1749	1755*	1758	1771	1777*	1780
1793	1799*	1802	1815	1821*	1824	1838	1844*	1847	1857	1863*	1866	1876
1882*	1885	1895	1901*	1904	1914	1920*	1923	1933	1939*	1942	1952	1958*
1961	1971	1977*	1980	1990	1996*	1999	2009	2015*	2018	2028	2034*	2037
2047	2053*	2056	2066	2072*	2075	2085	2091*	2094	2105	2111*	2114	2127
2133*	2136	2149	2155*	2158	2171	2177*	2180	2193	2199*	2202	2215	2221*
2224	2237	2243*	2246	2259	2265*	2268	2281	2287*	2290	2303	2309*	2312
2325	2331*	2334	2347	2353*	2356	2369	2375*	2378	2391	2397*	2400	2413
2419*	2422	2432	2438*	2441	2451	2457*	2460	2470	2476*	2479	2489	2495*
2498	2508	2514*	2517	2527	2533*	2536	2546	2552*	2555	2565	2571*	2574
2584	2590*	2593	2603	2609*	2612	2622	2628*	2631	2641	2647*	2650	2660
2666*	2669	2679	2685*	2688	2698	2704*	2707	2718	2724*	2727	2740	2746*
2749	2762	2768*	2771	2784	2790*	2793	2806	2812*	2815	2828	2834*	2837
2850	2856*	2859	2872	2878*	2881	2894	2900*	2903	2916	2922*	2925	2936
2944*	2947	2960	2966*	2969	2982	2988*	2991	3004	3010*	3013	3026	3032*
3035	3048	3054*	3057	3070	3076*	3079	3089	3095*	3098	3108	3114*	3117
3127	3133*	3136	3146	3152*	3155	3165	3171*	3174	3184	3190*	3193	3203
3209*	3212	3222	3228*	3231	3241	3247*	3250	3260	3266*	3269	3279	3285*
3280	3290	3304*	3307	3317	3323*	3326	3336	3342*	3345	3355	3361*	3364
3374	3380*	3383	3394	3400*	3403	3416	3422*	3425	3438	3444*	3447	3460
3466*	3469	3482	3488*	3491	3504	3510*	3513	3526	3532*	3535	3540	3554*
3557	3570	3576*	3579	3592	3598*	3601	3614	3620*	3623	3636	3642*	3645
3658	3664*	3667	3680	3686*	3689	3702	3708*	3711	3724	3730*	3733	3746
3752*	3755	3766	3772*	3775	3786	3792*	3795	3806	3812*	3815	3826	3832*
3835	3846	3852*	3855	3866	3872*	3875	3886	3892*	3895	3906	3912*	3915
3926	3932*	3935	3946	3952*	3955	3966	3972*	3975	3986	3992*	3995	4006
4012*	4015	4026	4032*	4035	4046	4052*	4055	4067	4073*	4076	4091	4097*
4100	4115	4121*	4124	4139	4145*	4148	4163	4169*	4172	4187	4193*	4196
4211	4217*	4220	4235	4241*	4244	4259	4265*	4268	4283	4289*	4292	4307
4313*	4316	4331	4337*	4340	4355	4361*	4364	4379	4385*	4388	4403	4409*
4412	4427	4433*	4436	4451	4457*	4460	4471	4477*	4480	4491	4497*	4500
4511	4517*	4520	4531	4537*	4540	4551	4557*	4560	4571	4577*	4580	4591
4597*	4600	4611	4617*	4620	4631	4637*	4640	4651	4657*	4660	4671	4677*
4680	4691	4697*	4700	4711	4717*	4720	4731	4737*	4740	4751	4757*	4760
4772	4778*	4781	4796	4802*	4805	4820	4826*	4829	4844	4850*	4853	4868
4874*	4877	4892	4898*	4901	4916	4922*	4925	4940	4946*	4949	4964	4970*
4973	4988	4994*	4997	5012	5018*	5021	5036	5042*	5045	5060	5066*	5069
5084	5090*	5093	5108	5114*	5117	5132	5138*	5141	5156	5162*	5165	5175
5181*	5184	5194	5200*	5203	5213	5219*	5222	5232	5238*	5241	5251	5257*
5260	5270	5276*	5279	5289	5295*	5298	5308	5314*	5317	5327	5333*	5336
5346	5352*	5355	5365	5371*	5374	5384	5390*	5393	5403	5409*	5412	5423
5429*	5432	5442	5448*	5451	5461	5467*	5470	5480	5486*	5489	5499	5505*
5508	5518	5524*	5527	5537	5543*	5546	5556	5562*	5565	5576	5582*	5585
5595												



