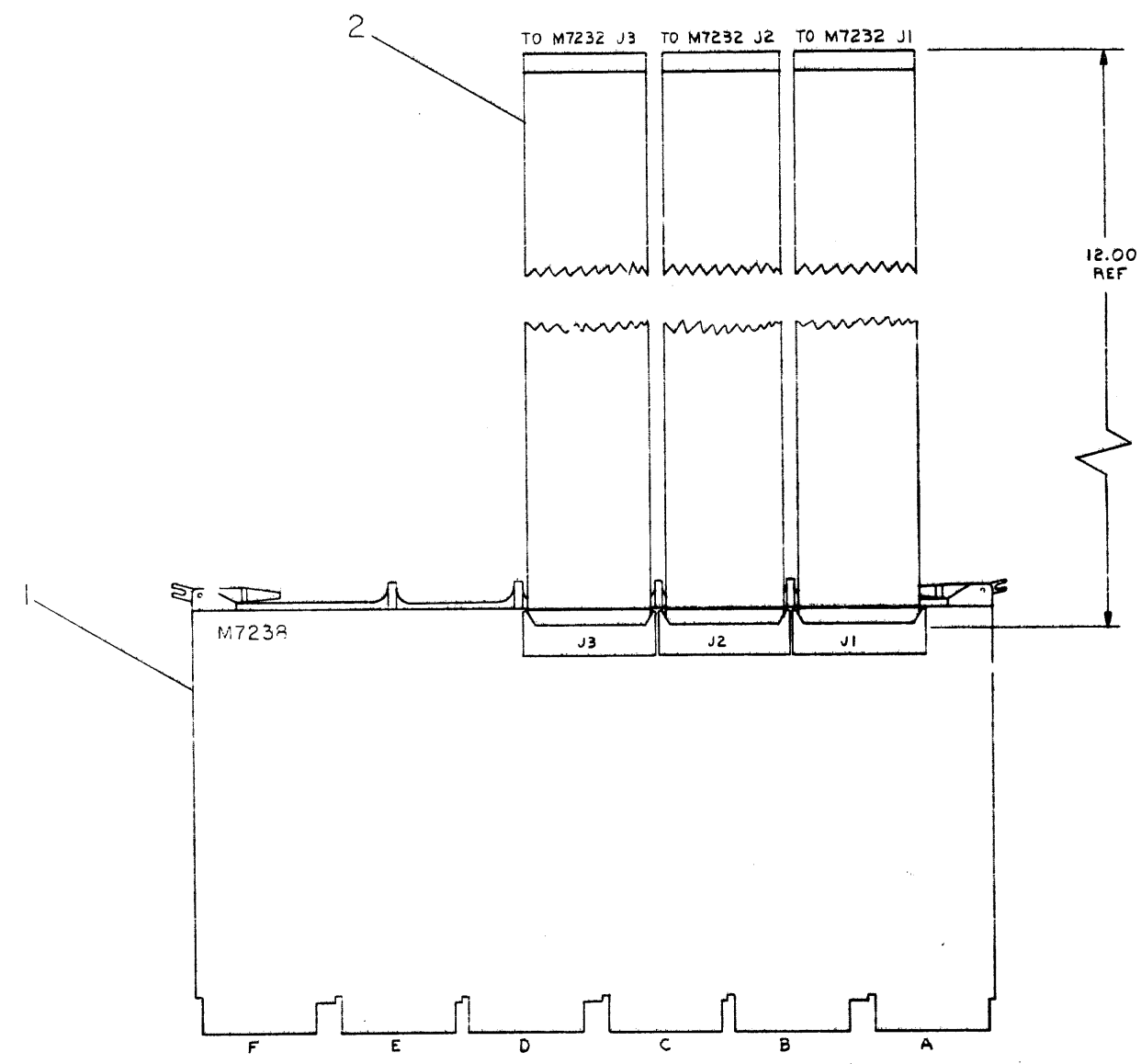




NOTES:

1. PLUG M723B INTO SLOT A-F2 OF KDII-A SEE D-MU-KDII-A-MU
2. INSTALL CABLES WITH SHIELD TOWARD M723B MODULE.
3. CUT W1 ON M723B MODULE (IR DECODE) INSTALLING KEII-E OPTION.
4. CUT W1, W2, W3 ON M723B MODULE (EIS) WHEN INSTALLING KEII-F OPTION.
5. WHEN INSTALLING KEII-E OPTION IN THE 11/40 TUCK SLACK BCØ9R-Ø1 CABLE LOOP DOWN IN FRONT OF SLOT 1 MODULE POSITION.
6. FOR DRAWING DIRECTORY INFORMATION REFER TO DRAWING \*B-DD-KEII-E



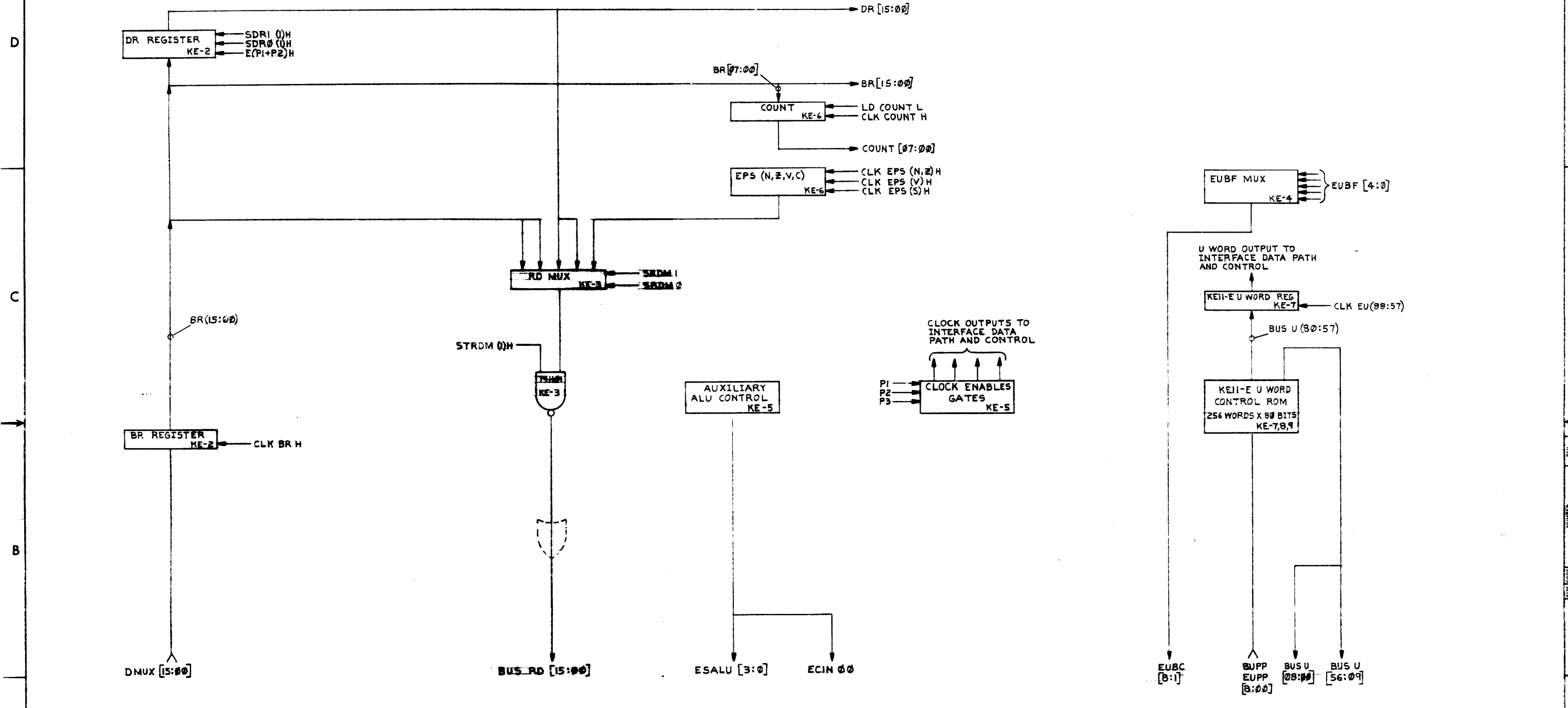
3	I/O CABLE (BCØ9R)	C-UA-BCØ9R-Ø1	2	
1	EIS BOARD	D-CS-M723B-Ø-1	1	
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.	DRN <i>[Signature]</i>	DATE 8-23-72	 digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
TOLERANCES	CHKD <i>[Signature]</i>	DATE 7-26-72		
DECIMALS	APP <i>[Signature]</i>	DATE 9-21-72		
ANGLES	PROJ. ENG. <i>[Signature]</i>	DATE 9-21-72		
.XXX = .005	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROJ. <i>[Signature]</i>	DATE 9-21-72	TITLE
.XX = .02				KEII-E ASSY
.X = .1				
MATERIAL	NEXT HIGHER ASSY.			
FINISH	B-DD-KEII-E	SIZE CODE DUA	NUMBER KEII-E-Ø	REV.
	SCALE NONE			
	SHEET 1 OF 1	DIST. 6		

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REV	
CHG	
NO	

NOTE: THIS DIAGRAM IS DRAWN SO THAT INTERFACE SIGNALS AT THE BOTTOM OF THIS SHEET LINE UP WITH THEIR RESPECTIVE SIGNALS AT THE TOP OF THE KD11-A BLOCK DIAGRAM (D-BD-KD11-A-BD)

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REV	REV
CHK	CHANGE NO.
KE11-E-00001	A
BUZY NSH	
1/2/72	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.	DRN	DATE	digital EQUIPMENT CORPORATION	
TOLERANCES	CHK'D	DATE	MAYNARD MASSACHUSETTS	
DECIMALS		DATE	TITLE	
ANGLES		DATE	KE11-E	
.XXX - .005		DATE	BLOCK DIAGRAM	
.XX - .02		DATE	(U WORD & TABLES)	
.X - .1		DATE		
REMOVE BURRS AND BREAK SPARK CORNERS SURFACE QUALITY		DATE		
MATERIAL	NEXT HIGHER ASSY.	SCALE	SIZE CODE	NUMBER
++	B-DD-KE11-E	NONE	D BD	KE11-E-BD
FINISH	SHEET	OF	DIST.	REV.
++	1	2		A

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EXPANSION U WORD

M7239 KEII-F U WORD [U(88:81)] — M7238 KEII-E U WORD [U(80:57)]

CON1		CON2		FCI		FUB		MHR			FRD		ERD		SRD		SDR		CVM			NZM		CCC			GPC			CEE		CNT		EUB					CBR																																					
CON1	CON2	FCI EUS	EUBF4	MSR	MSRI	MSRΦ	STFROM	STROM	SDDMI	SDDMΦ	SDR1	SDRΦ	SCVM2	SCVM1	SCVMΦ	SN2M1	SN2MΦ	CLK NZ	CLK V	CLK C	GPC 2	GPC 1	GPC Φ	CLK UPB	LCNT	ECNT	EUBF3	EUBF2	EUBF1	EUBFΦ	CLK BR																																													
U77(1)H	U76(1)H	U75(1)H	U74(1)H	U73(1)H	U72(1)H	U71(1)H	U70(1)H	U69(1)H	U68(1)H	U67(1)H	U66(1)H	U65(1)H	U64(1)H	U63(1)H	U62(1)H	U61(1)H	U60(1)H	U59(1)H	U58(1)H	U57(1)H	U56(1)H	U55(1)H	U54(1)H	U53(1)H	U52(1)H	U51(1)H	U50(1)H	U49(1)H	U48(1)H	U47(1)H	U46(1)H	U45(1)H	U44(1)H	U43(1)H	U42(1)H	U41(1)H	U40(1)H	U39(1)H	U38(1)H	U37(1)H	U36(1)H	U35(1)H	U34(1)H	U33(1)H	U32(1)H	U31(1)H	U30(1)H	U29(1)H	U28(1)H	U27(1)H	U26(1)H	U25(1)H	U24(1)H	U23(1)H	U22(1)H	U21(1)H	U20(1)H	U19(1)H	U18(1)H	U17(1)H	U16(1)H	U15(1)H	U14(1)H	U13(1)H	U12(1)H	U11(1)H	U10(1)H	U9(1)H	U8(1)H	U7(1)H	U6(1)H	U5(1)H	U4(1)H	U3(1)H	U2(1)H	U1(1)H

**EUB (4:1) BUT CHART**

FUBF 3	FUBF 2	FUBF 1	FUBF 0	BUT
L	L	L	L	NOOP
L	L	L	H	DIS
L	L	L	M	SOVD
L	L	L	H	BRIS
L	L	H	L	D=Φ
L	L	L	H	DRIS
L	L	H	H	NOT USED
L	L	H	H	DIV QUIT
L	H	L	L	COUNT=Φ
L	H	L	H	OVFL+UNFL+STORE *
L	H	L	H	DRΦΦ * E15
L	H	H	L	BR(Φ5:ΦΦ)
L	H	H	L	ZB * EPS (Z) *
L	H	H	H	FINSTR I *
L	H	H	H	EINSTR II
L	H	H	H	EINSTR I

\* = KEII-F

**FUBC BUT CHART (KEII-F)**  
EUBC (4:1)

STB	S2	S1	S0	FN	BUT (FUBCI)
EUBF4	EUBF2	EUBF1	EUBF0	FN	(FUBCI)
H	—	—	—	L	—
L	L	L	L	D0	ARGA
L	L	L	H	D1	MSR01
L	L	H	L	D2	ZB + EPS (Z)
L	L	H	H	D3	COUNT > 30
L	H	L	L	D4	NORMALIZED
L	H	L	H	D5	MSRΦΦ
L	H	H	L	D6	—
L	H	H	H	D7	—

**CONSTANTS (KEII-F)**

GPC=6	CON1	CON2	CONSTANT (OCTAL)
H	L	L	400
H	L	H	244
H	H	L	E
H	H	H	30
L	L	L	200

**GPC CHART**

GPC2	GPC1	GPC0	FUNCTION
0	0	0	NOOP
0	0	1	BUT (NORMALIZE) TESTS DR09 - KEII-F
0	1	0	ALLOWS ALU CONTROL AS A FUNCTION OF CERTAIN CONDITIONS RATHER THAN DIRECTLY BY THE CONTROL ROM
0	1	1	ENABLES DRIS TO PROVIDE THE CARRY-IN TO THE ALU
1	0	0	ENABLES EPS(C) TO PROVIDE THE CARRY-IN TO THE ALU
1	0	1	ENABLES MSRI TO PROVIDE LSB SHIFT INPUT TO THE DR, KEII-F
1	1	0	USED IN KEII-F. FORMS CONSTANT 2000
1	1	1	USED IN KEII-F EXECUTES A MICROPROGRAM TEST FOR BUS REQUESTS

**KEII-E KEII-F**  
ALU FUNCTIONS

ALU4	ALU3	ALU2	ALU1	ALU0	ALU Φ (FUNCTION)
L	L	L	L	L	F=A
L	L	L	H	H	F=MINUS 1 (2'S COMPLEMENT)
L	L	H	H	L	F=A MINUS B MINUS 1
L	H	L	L	H	F=A PLUS B
L	H	H	L	L	F=A PLUS B
H	L	L	L	L	F=X
H	L	L	H	H	F=LOGICAL Φ
H	H	L	H	L	F=B
H	H	H	H	L	F=AB

REVISIONS

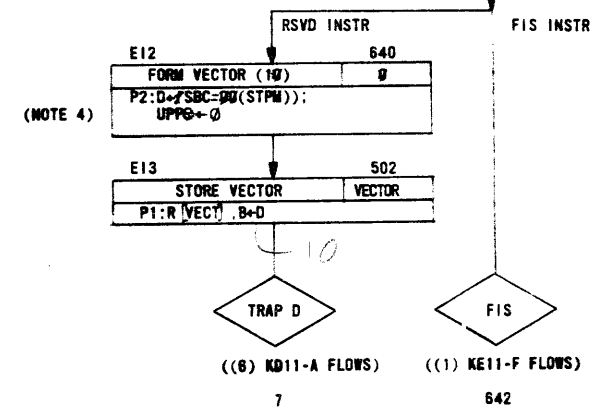
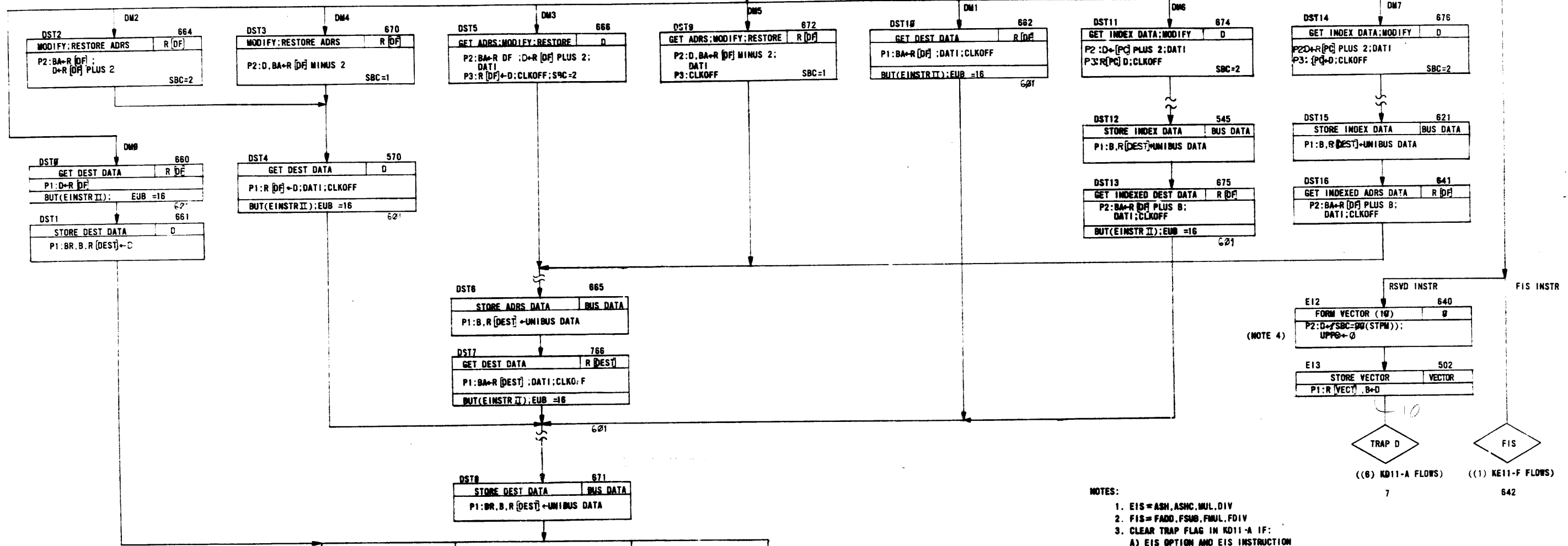
REV	CHANGE NO

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES				
TOLERANCES				
DECIMALS	ANGLES	DATE		
.XXX - .000	± 0° 30'	9-8-72		
.XXX - .00		DATE		
.X - .1		9-21-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.			
FINISH	SCALE NONE			
B-DD-KEII-E		D BD		REV. A
SHEET 2 OF 2		DIST.		

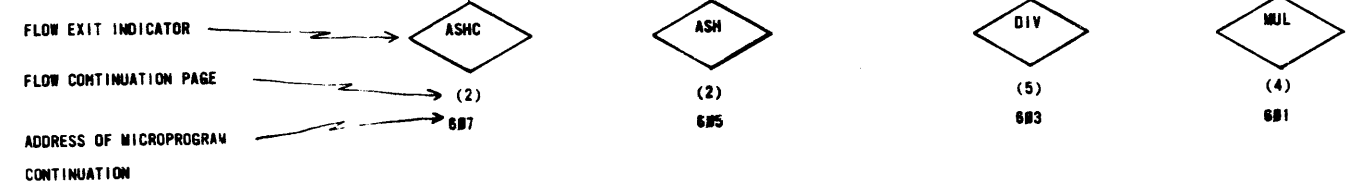
digital EQUIPMENT CORPORATION  
MAYNARD MASSACHUSETTS  
TITLE  
KEII-E  
BLOCK DIAGRAM  
(U WORD & TABLES)

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FLOW PAGE FROM WHICH THIS MICRO PROGRAM ENTRY WAS MADE ((1) KD11-A FLOW)  
 EXPANSION  
 U WORD ADDRESS 500  
 DATA DISPLAY OF CONSOLE  
 E10  
 NOTE 3  
 P1: BR+R  
 BUT(EINSTR I); EUB = 17 (NOTE 3)  
 640  
 777  
 E11  
 ZERO ETS STATUS  
 P1: EPS(N,Z,V,C)+BR  
 BASE U WORD ADDRESS FOR BRANCH



- NOTES:
- EIS = ASH, ASHC, MUL, DIV
  - FIS = FADD, FSUB, FMUL, FDIV
  - CLEAR TRAP FLAG IN KD11-A IF:
    - A) EIS OPTION AND EIS INSTRUCTION
    - B) FIS, OPTION AND EIS OR FIS INSTRUCTION
  - STILL A RESERVED INSTRUCTION. GENERATE TRAP VECTOR 10, ENABLE KD11-A ROM AND TRAP.
  - R[DF] INDICATES THE DESTINATION REGISTER SELECTED BY THE IR.
  - D IN THE DISPLAY IS THE LAST DATA LOADED INTO THE D REGISTER AND MAY NOT BE PERTINENT.
  - SELECTION OF INTERNAL REGISTERS (R[SF], R[DR], ETC.) IS RELATED TO THE ACTUAL BIT POSITIONS IN THE IR AND NOT TO THE TERMINOLOGY USED IN THE EIS INSTRUCTION FORMAT.

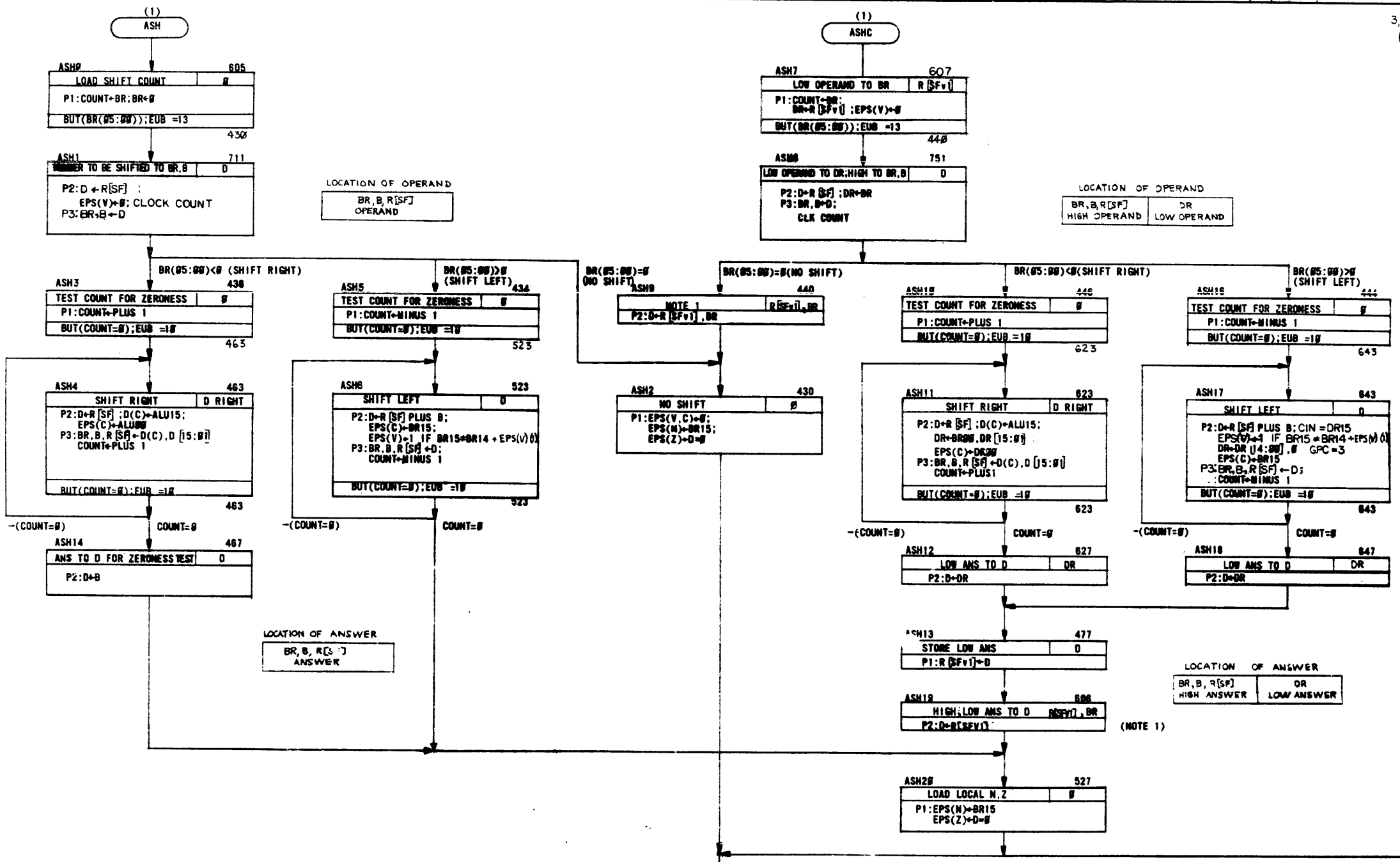


BRUNING 40 522 15840

REV	CHANGE NO	DATE	BY	CHK
1	0001	10-30-72	BUZINSKI	STOKES

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPI:		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DATE 9-18-72	digital EQUIPMENT CORPORATION	
DECIMALS	ANGLES	DATE 9-21-72	TITLE	
.XXX - .000	±0° 30'	DATE 9-21-72	FLOW DIAGRAM	
.XX - .00		DATE 9-21-72	(KE11-E DEST)	
.X - .0		DATE 9-21-72		
MATERIAL	NEXT HIGHER ASSY.	DATE 9-21-72		
FINISH	B-DD-KD11-A	DATE 9-21-72		
	SCALE			
	SHEET OF 5			
	DIST.			

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- NOTES:
1. R (XX) BR ARE OR'ED TOGETHER ON BUS RD (15:00) TO DETERMINE ZERONESS OF THE 32 BIT ANSWER.
  2. BUT (COUNT=0) IS USED TO CLOCK THE NPR & BUS REQUEST FLAGS AND TO CLEAR THE BBSY FLAG IN THE KD11-A. THIS ALLOWS NPRS TO OCCUR WITHOUT DOING A BUS DATA CYCLE IN THE KD11-A.
  3. R (SFV) INDICATES THAT THE SOURCE REGISTER ADDRESS SELECTED BY THE IR IS OR'ED WITH 1.

LOCATION OF OPERAND  
BR, B, R (SF)  
OPERAND

LOCATION OF OPERAND  
BR, B, R (SF)  
DR  
HIGH OPERAND  
LOW OPERAND

LOCATION OF ANSWER  
BR, B, R (S)  
ANSWER

LOCATION OF ANSWER  
BR, B, R (SF)  
DR  
HIGH ANSWER  
LOW ANSWER

REV	CHG	NO

8 7 6 5 4 3 2 1

ASH15 577  
LOWE EPS TO STATUS EPS(N,C)  
P2: P2(N:C) - EPS(N:C); UPPB=0

ASH21 723  
NO-OP FOR EXIT TO KD11-A  
P1: NO-OP

SERVICE C

((18) KD11-A FLOW)  
17

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPII				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	TITLE		
.XX - .005	± 0° 30'	FLOW DIAGRAM		
.XX - .02		(ASH, ASHC)		
.X - .1		(2)		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.	REV. CODE	NUMBER	REV.
FINISH		B-DD-KD11-A	DFD	KEII-E-FD
SCALE		SHEET 2 OF 5		

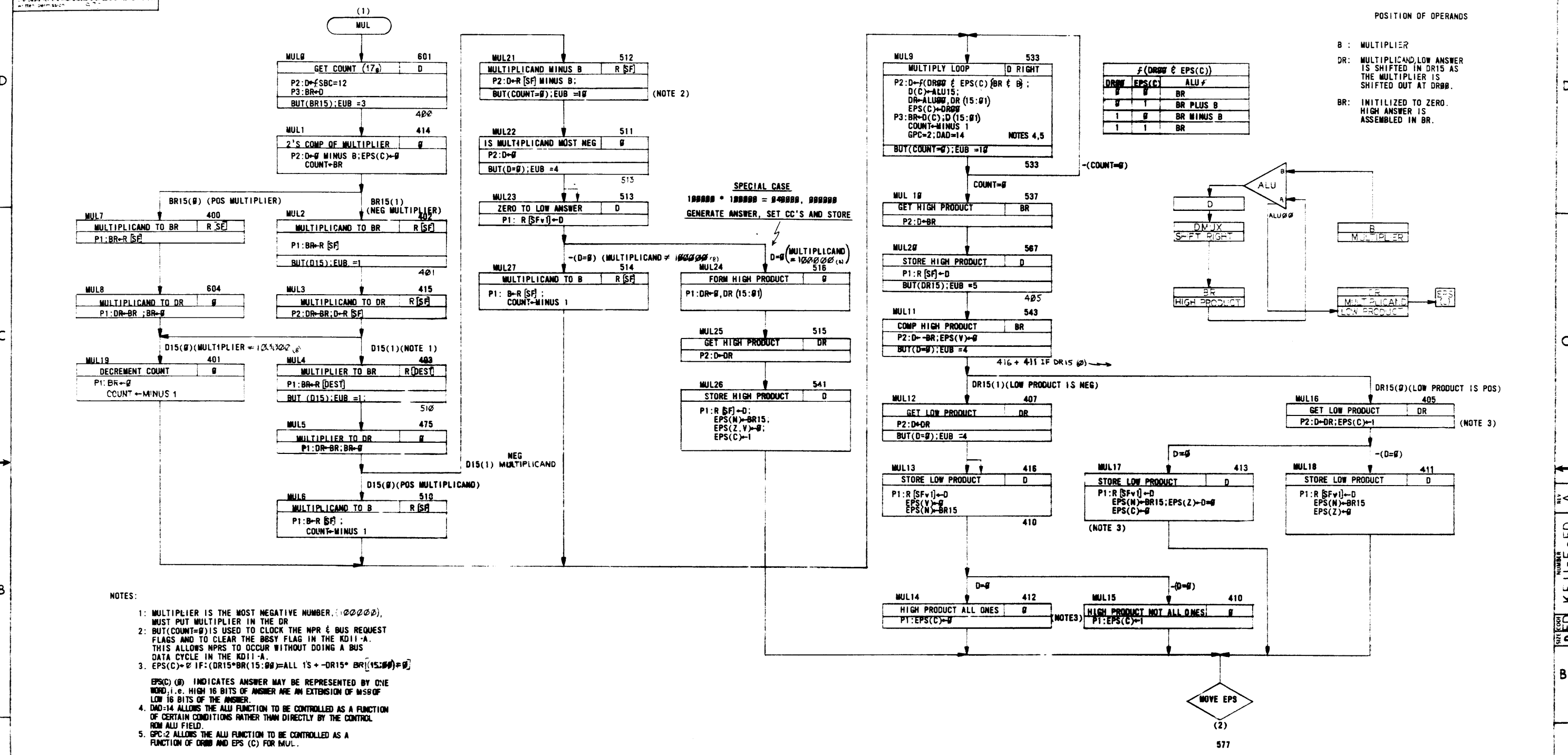
REV A KEII-E-FD

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POSITION OF OPERANDS

B : MULTIPLIER  
 DR: MULTIPLICAND LOW ANSWER IS SHIFTED IN DR15 AS THE MULTIPLIER IS SHIFTED OUT AT DR00.  
 BR: INITIALIZED TO ZERO. HIGH ANSWER IS ASSEMBLED IN BR.

DR00	EPS(C)	ALU f
0	0	BR
0	1	BR PLUS B
1	0	BR MINUS B
1	1	BR



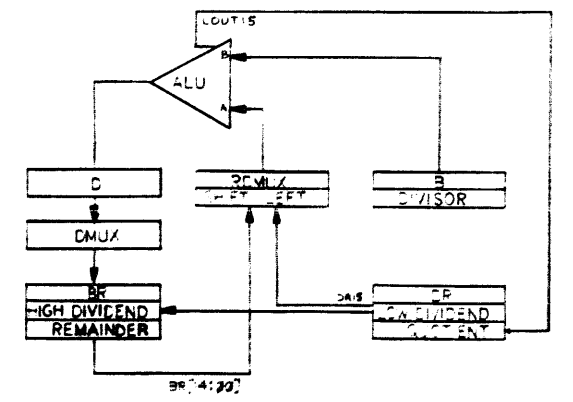
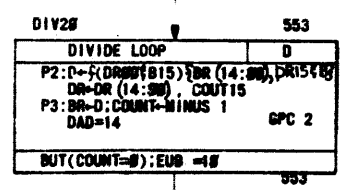
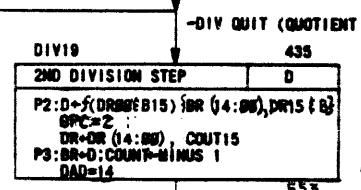
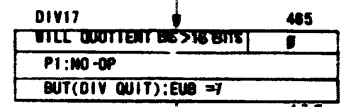
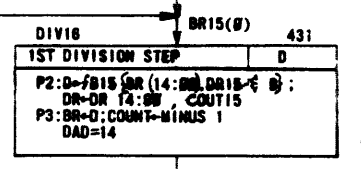
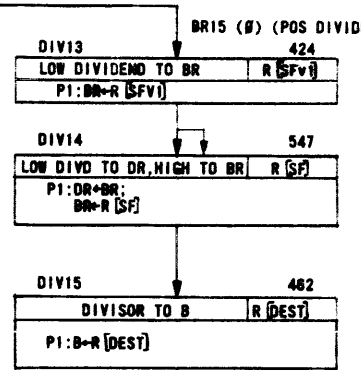
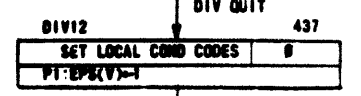
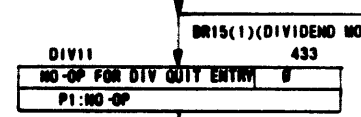
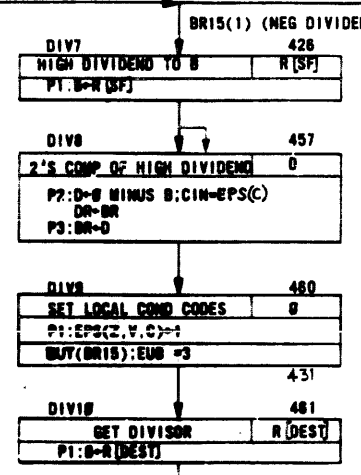
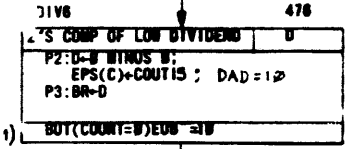
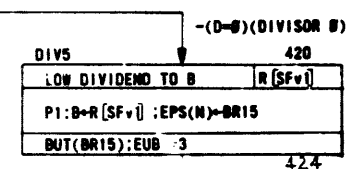
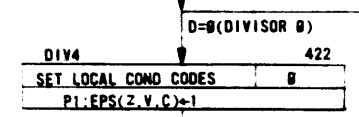
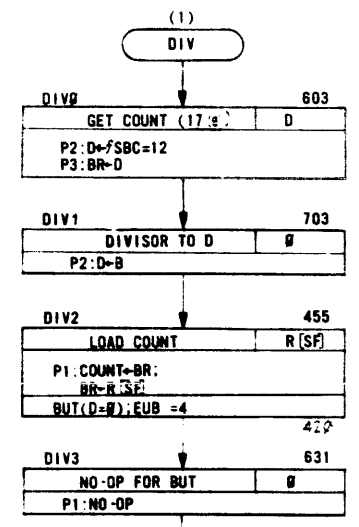
- NOTES:
- MULTIPLIER IS THE MOST NEGATIVE NUMBER. (100000). MUST PUT MULTIPLIER IN THE DR.
  - BUT(COUNT=0) IS USED TO CLOCK THE MPR & BUS REQUEST FLAGS AND TO CLEAR THE BBSY FLAG IN THE KD11-A. THIS ALLOWS NPRS TO OCCUR WITHOUT DOING A BUS DATA CYCLE IN THE KD11-A.
  - EPS(C)=0 IF: (DR15\*BR(15:00))=ALL 1'S + DR15\*BR(15:00)=0
- EPS(C) (0) INDICATES ANSWER MAY BE REPRESENTED BY ONE WORD, I.E. HIGH 16 BITS OF ANSWER ARE AN EXTENSION OF MSB OF LOW 16 BITS OF THE ANSWER.
- DAD=14 ALLOWS THE ALU FUNCTION TO BE CONTROLLED AS A FUNCTION OF CERTAIN CONDITIONS RATHER THAN DIRECTLY BY THE CONTROL ROM ALU FIELD.
  - GPC=2 ALLOWS THE ALU FUNCTION TO BE CONTROLLED AS A FUNCTION OF DR00 AND EPS (C) FOR MUL.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP11		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN [Signature]	DATE 7-12-72	digital EQUIPMENT CORPORATION	
DECIMALS ANGLES	CHK'D [Signature]	DATE 6-21-72	TITLE	
.XXX - .008 10° 30'	ENG [Signature]	DATE 7-21-72	FLOW DIAGRAM	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROG [Signature]	DATE 7-21-72	(MUL)	
MATERIAL	NEXT HIGHER ASSY.		SIZE CODE	NUMBER
	B-DD-KD:1-A		D FD	KEII-E-FC
FINISH	SCALE		SHEET	3 OF 5
			DIST	

REVISIONS  
 CHANGE NO.  
 CHK

REV A KEII-E-FC

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FB15

B15	ALU
1	BR PLUS B
0	BR MINUS B

DIV QUIT =  $(-D) \cdot DRB + B15 \cdot EPS(N) \cdot DRB + (-B15) \cdot EPS(N) \cdot DRB$

POSITION OF OPERANDS

B: DIVISOR

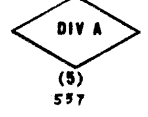
DR: INITIALLY LOADED WITH LOW DIVIDEND. LOW ANSWER IS SHIFTED INTO DRB.

BR: INITIALLY LOADED WITH HIGH DIVIDEND. REMAINDER IS ASSEMBLED IN BR.

- NOTES:
- BUT(COUNT=B) IS USED TO CLOCK THE NBR & BUS REQUEST FLAGS AND TO CLEAR THE BBSY FLAG IN THE KD11-A. THIS ALLOWS NBRs TO OCCUR WITHOUT DOING A BUS DATA CYCLE IN THE KD11-A.
  - DAD=14 ALLOWS THE ALU FUNCTION TO BE CONTROLLED AS A FUNCTION OF CERTAIN CONDITIONS RATHER THAN DIRECTLY BY THE CONTROL ROM ALU FIELD.
  - GPC=2 ALLOWS THE ALU FUNCTION TO BE CONTROLLED AS A FUNCTION OF DRB AND B15 FOR DIV.

(2) 577

DRB	B15	ALU
0	0	BR(14:00), DR15 PLUS B
0	1	BR(14:00), DR15 MINUS B
1	0	BR(14:00), DR15 MINUS B
1	1	BR(14:00), DR15 PLUS B



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
POPII				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS		ANGLES		
.XXX = .006		±0° 30'		
.XX = .02				
.X = .1				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASSY.		
		B-DD-KD11-A		
FINISH				

digital EQUIPMENT CORPORATION  
MAYNARD MASSACHUSETTS

TITLE: FLOW DIAGRAM (DIV)

SCALE: 4 OF 5  
SHEET 4 OF 5  
DIST: KE11-E-FD

REV. A

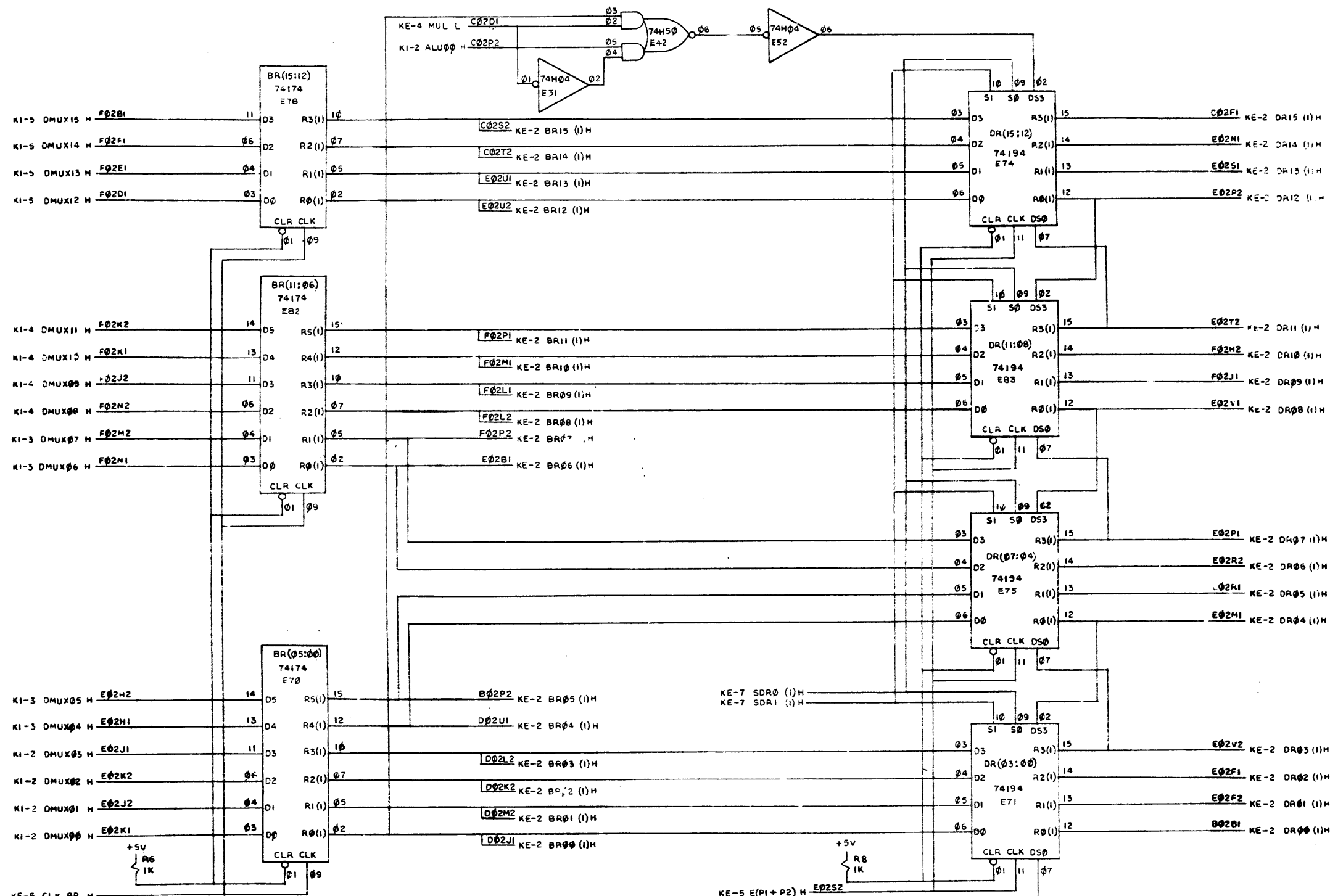
BRUNING 40-532 15840  
DEC FORM NO  
ORD 102-B







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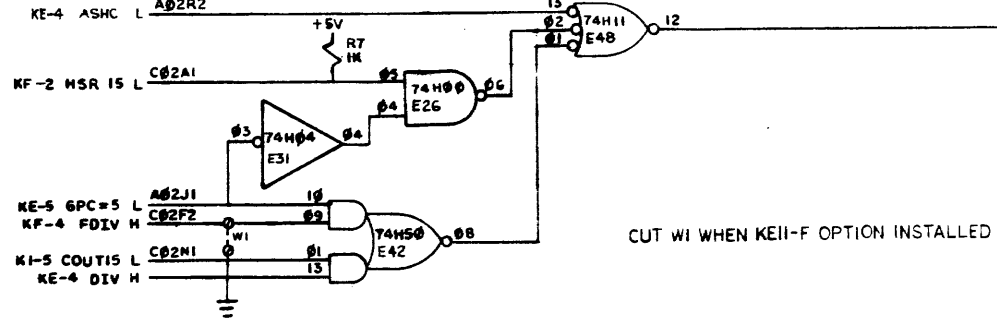


TRUTH TABLE  
74194

SI	S0	FUNCTION
0	0	NO OP
0	1	SHF RIGHT
1	0	SHF LEFT
1	1	LOAD

REVISIONS

REV	CHANGE NO



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
KD11-A		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN. <i>R. S. Smith</i> DATE 2-16-72		
DECIMALS .XXX ± .006	ANGLES ± 0° 30'	CHK'D. <i>Thompson</i> DATE 2-17-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		ENG. <i>Thompson</i> DATE 1-17-72	TITLE EIS BOARD	
MATERIAL		BR & DR	KE-2	
NEXT HIGHER ASSY.		SIZE CODE	NUMBER	REV.
KE11-E		D	CS	M7238-0-1
FINISH		SCALE	SHEET 2 OF 17	DIST.

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F02V1 BUS RD15 L  
F02V2 BUS RD14 L  
F02S2 BUS RD13 L  
F02R1 BUS RD12 L  
F02U2 BUS RD11 L  
F02U1 BUS RD10 L

F02R2 BUS RD09 L

F02S1 BUS RD08 L

F02F2 BUS RD07 L  
F02H1 BUS RD06 L  
F02C1 BUS RD05 L  
F02A1 BUS RD04 L

E02N2 BUS RD03 L

E02M2 BUS RD02 L

E02L1 BUS RD01 L

E02L2 BUS RD00 L

TRUTH TABLE			
74153			
S1	S0	RDMUX(15:00)	FN
0	0	EIS STATUS	AN
0	1	DR (15:00)	BN
1	0	BR(4:00)DR15	CN
1	1	BR (15:00)	DN

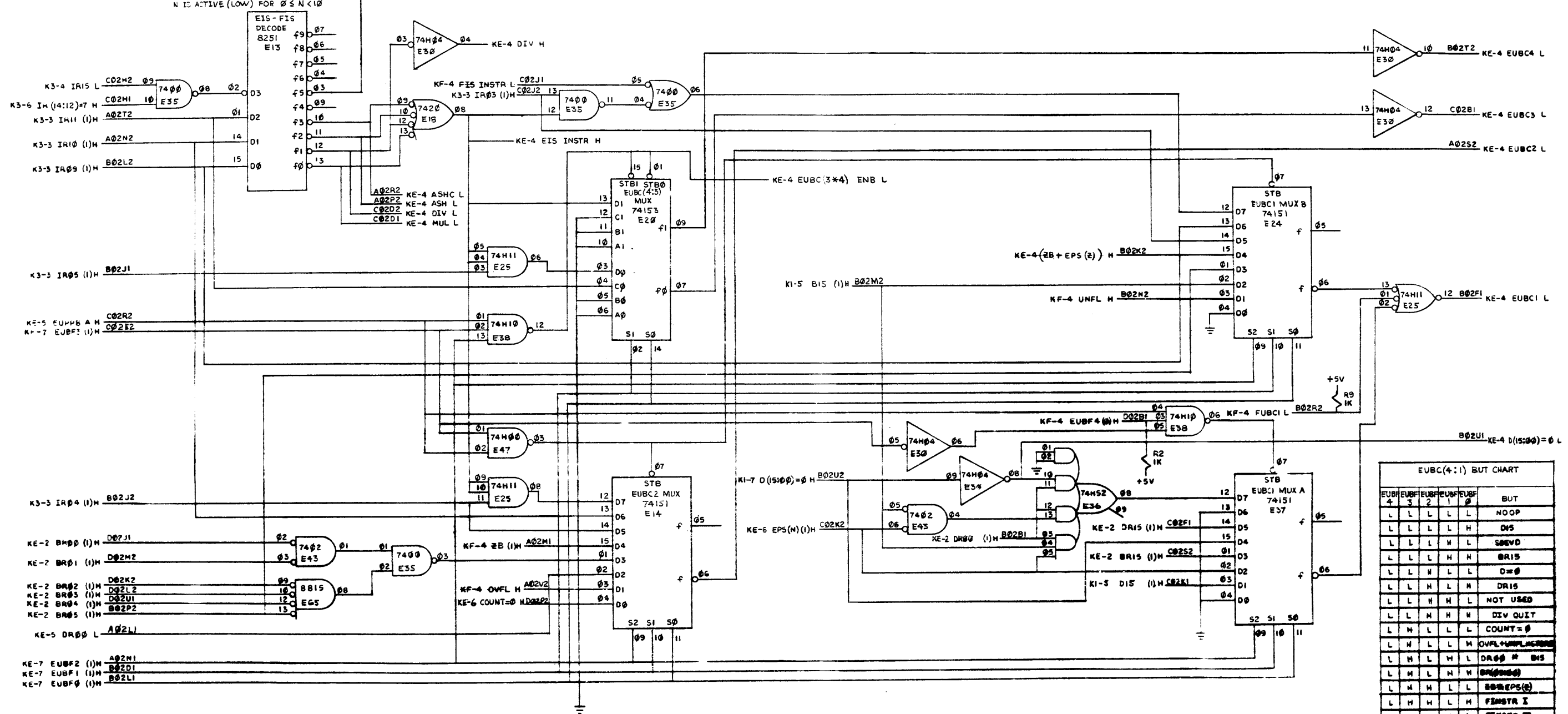
FIRST USED OR OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
K011-A				
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES.				
TOLERANCES				
DECIMALS	ANGLES	DATE 2-16-72		
.XX - .000	±0° 30'	DATE 2-17-72		
.XX - .02		DATE 2-17-72		
.X - .1		DATE 2/17/72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.	ROMUX	KE-3	
FINISH	SCALE	SIZE CODE	NUMBER	REV.
	SHEET 3 OF 17	D CS	M7238-0-1	D

REV	CHG	NO	DATE

DEC FORM NO 100-B

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DEVICE TRUTH TABLE  
S2S1  
FOR THE DECIMAL EQUIVALENCE, N, OF THE BINARY NUMBER (S2,S1,D1,D0), ONLY OUTPUT N IS ACTIVE (LOW) FOR 0 ≤ N < 10



EUBC(4:1) BUT CHART

EUB	EUBF	EUBFE	EUBFUE	EUBFUEF	EUBFUEFE	BUT
4	3	2	1	0		NOOP
L	L	L	L	L	L	D15
L	L	L	L	H	L	SBEVD
L	L	L	L	H	H	DR15
L	L	L	H	L	L	D=0
L	L	L	H	L	H	DR15
L	L	H	L	L	L	NOT USED
L	L	H	L	L	H	DIV QUIT
L	H	L	L	L	L	COUNT=0
L	H	L	L	L	H	OVFL+UNPLACED
L	H	L	L	H	L	DR00 # D15
L	H	L	H	L	L	DR00#0
L	H	H	L	L	L	SBEEPS(0)
L	H	H	L	H	L	FINSTR I
L	H	H	H	L	L	EINSTR I
L	H	H	H	H	L	EINSTR I

TRUTH TABLE 74153

STB	S2	S1	S0	FN
H	-	-	-	L
L	L	L	L	AN
L	L	L	H	BN
L	L	H	L	CN
L	H	L	L	DN

TRUTH TABLE 74151

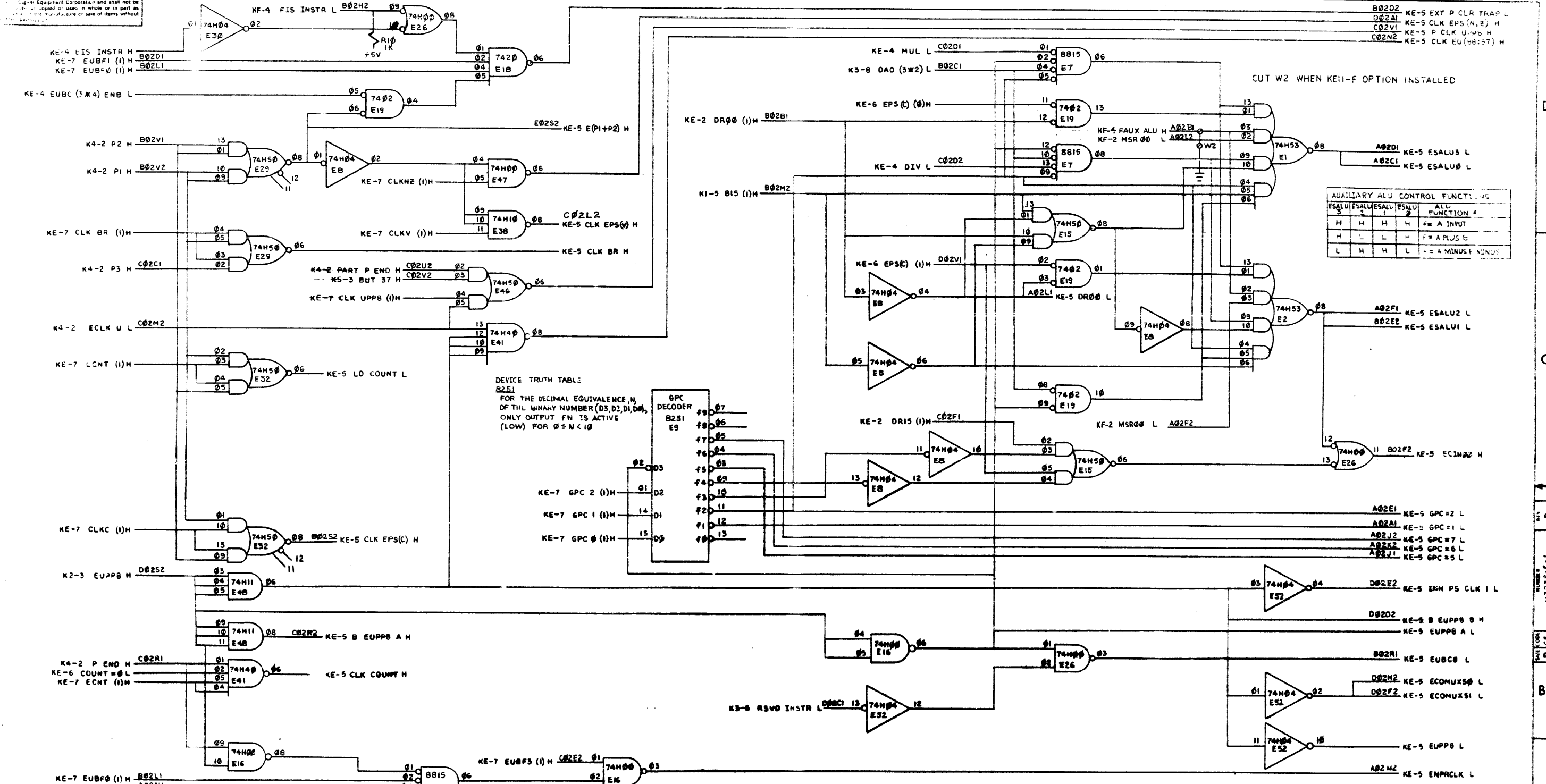
STB	S2	S1	S0	F
H	-	-	-	L
L	L	L	L	D0
L	L	L	H	D1
L	L	H	L	D2
L	L	H	H	D3
L	H	L	L	D4
L	H	L	H	D5
L	H	H	L	D6
L	H	H	H	D7

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
K011-A		PARTS LIST	
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES	DAN 2-19-72	DATE	EQUIPMENT CORPORATION
DECIMALS	0.005	DATE	
ANGLES	±0° 30'	DATE	
XXX - 000		DATE	
.XX - 02		DATE	
.X - 1		DATE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE	
MATERIAL	NEXT HIGHER ASSY.	DATE	
FINISH	SCALE	DATE	
	SHEET 4 OF 17	DIST	

REVISIONS

REV	CHANGE NO

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DEVICE TRUTH TABLE:  
B251  
FOR THE DECIMAL EQUIVALENCE, N,  
OF THE BINARY NUMBER (D3, D2, D1, D0),  
ONLY OUTPUT FN IS ACTIVE  
(LOW) FOR 0 ≤ N < 10

AUXILIARY ALU CONTROL FUNCTIONS			
ESALU1	ESALU2	ESALU3	ALU FUNCTION
H	H	H	= + A INPUT
H	L	L	= + A PLUS B
L	H	H	= - A MINUS B

REV. 1  
CHANGE NO.  
CHK

FIRST USED ON OPTION/MODEL	QTY.	DATE	DESCRIPTION	PART NO.	ITEM NO.
K011-A					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES					
DECIMALS	ANGLES	TITLE			
.XX - .02	± 0° 30'	E15 BOARD			
REMOVES BURRS AND BREAK SHARP CORNERS SURFACE QUALITY					
MATERIAL		NEXT HIGHER ASSY.		CONTROL KE-5	
FINISH		SCALE	SHEET	DIST.	
		5 OF 17			

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D

C

B

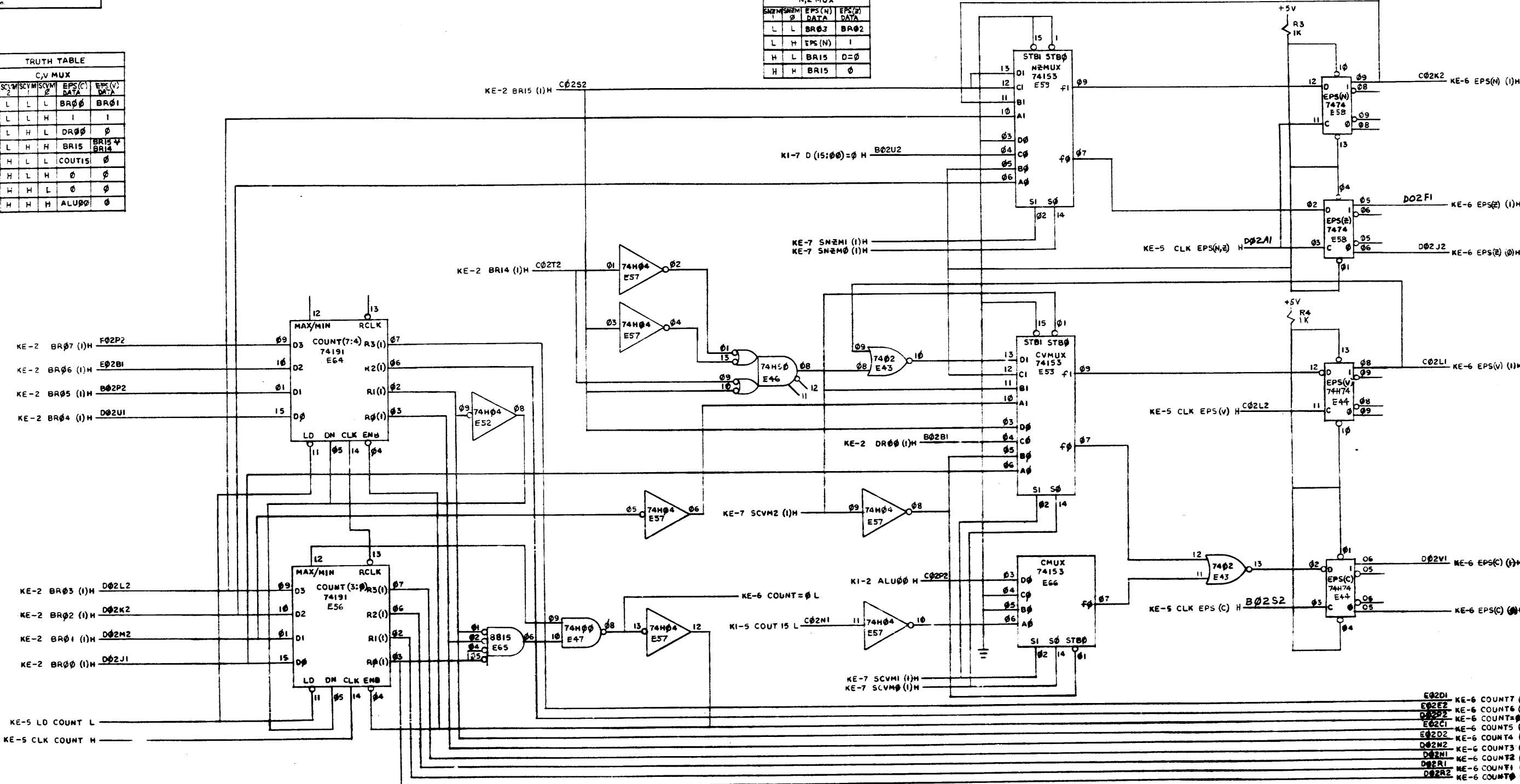
A

TRUTH TABLE				
C,V MUX				
SCVM	SCVM	SCVM	EPS(C) DATA	EPS(V) DATA
L	L	L	BR00	BR01
L	L	H	I	I
L	H	L	DR00	0
L	H	H	BR15	BR15
H	L	L	COUT15	0
H	L	H	0	0
H	H	L	0	0
H	H	H	ALU00	0

TRUTH TABLE				
N,Z MUX				
SNZM	SNZM	EPS(N) DATA	EPS(V) DATA	
L	L	BR03	BR02	
L	H	EPS(N)	I	
H	L	BR15	D=0	
H	H	BR15	0	

REV	CHG	NO

BRUNING 40-522 15840  
DEC FORM NO DED 102-B



TRUTH TABLE				
74153				
STB	SI	S0	FH	
H	-	-	L	
L	L	L	AH	
L	L	H	BH	
L	H	L	CH	
L	H	H	DH	

- E02D1 KE-6 COUNT7 (1)H
- E02E2 KE-6 COUNT6 (1)H
- D02E2 KE-6 COUNT5 (1)H
- E02C1 KE-6 COUNT4 (1)H
- E02D2 KE-6 COUNT3 (1)H
- D02M2 KE-6 COUNT2 (1)H
- D02N1 KE-6 COUNT1 (1)H
- D02R1 KE-6 COUNT0 (1)H
- D02R2 KE-6 COUNT0 (1)H

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
KD11-A				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	DATE 2-25-72		
.XXX = .003	.XX = .02	DATE 7-17-72		
.X = .1	±0°30'	DATE 7-17-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASBY.		EPS & COUNT KE-6
FINISH		SCALE	SHEET 6 OF 17	DIST.

D

C

B

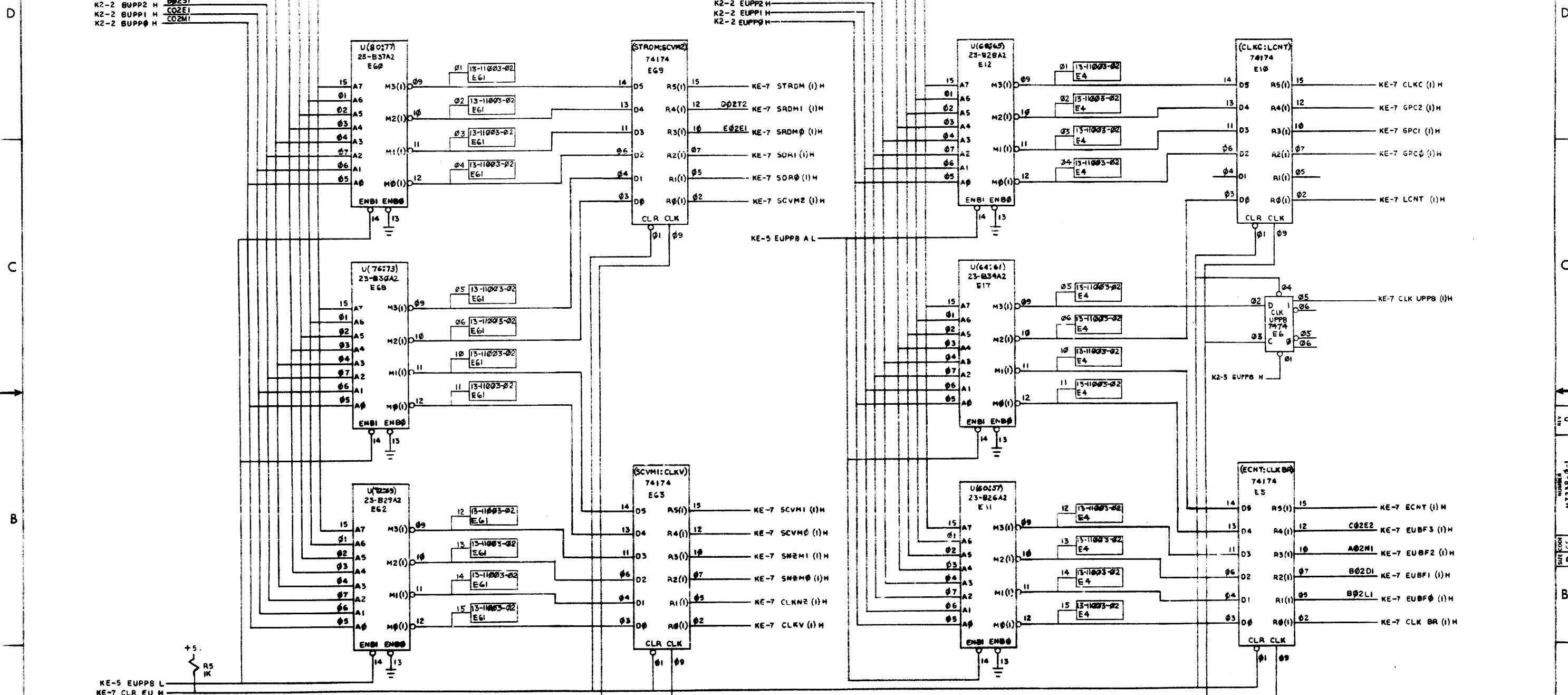
A

REV D  
M7238-0-1  
REV B  
M7238-0-1  
REV C  
M7238-0-1  
REV D  
M7238-0-1

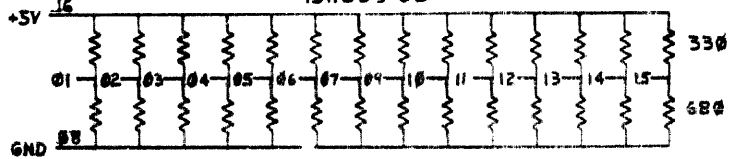
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- K2-3 BUYP7 H F02D2
- K2-3 BUYP6 H F02T2
- K2-3 BUYP5 H F02E2
- K2-3 BUYP4 H D02K1
- K2-2 BUYP3 H D02H1
- K2-2 BUYP2 H B02S1
- K2-2 BUYP1 H C02E1
- K2-2 BUYP0 H C02M1

- K2-3 EUPP7 H
- K2-3 EUPP6 H
- K2-3 EUPP5 H
- K2-3 EUPP4 H
- K2-2 EUPP3 H
- K2-2 EUPP2 H
- K2-2 EUPP1 H
- K2-2 EUPP0 H



DEVICETRUTH TABLE  
1311003-02



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.										
KD11-A														
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES														
DECIMALS	ANGLES	PARTS LIST												
.125 = .008	±0°30'	<table border="1"> <tr> <td>DATE</td> <td>2-23-72</td> </tr> <tr> <td>DATE</td> <td>8-17-72</td> </tr> <tr> <td>DATE</td> <td>8-17-72</td> </tr> <tr> <td>DATE</td> <td>8-17-72</td> </tr> <tr> <td>DATE</td> <td>8-17-72</td> </tr> </table>			DATE	2-23-72	DATE	8-17-72	DATE	8-17-72	DATE	8-17-72	DATE	8-17-72
DATE	2-23-72													
DATE	8-17-72													
DATE	8-17-72													
DATE	8-17-72													
DATE	8-17-72													
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY														
MATERIAL	NEXT HIGHER ASSY.	KE ROM WORD	KE-7											
FINISH	SCALE	SIZE CODE	NUMBER	REV										
	7 OF 17	D CS	M7238-0-1	D										

REVISIONS  
CHANGE NO.  
REV







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FLWS	STATE	ADR	CLK	CIR	WR	CB	CD	CBA	BUS	DAD	SP8	ALU	SBC	SBM	SDR	ISA	UBF	SRX	RIF	UPF
E3	MUL7	000	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	10	00	204
E3	MUL19	001	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	133
E3	MUL2	002	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	10	00	015
E3	MUL4	003	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	12	075
F5	FDV1	004	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	15	006
E3	MUL16	005	4	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	011
F5	FDV2	006	6	0	0	1	1	0	0	10	0	06	00	00	2	0	00	01	14	160
E3	MUL12	007	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	016
E3	MUL15	010	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E3	MUL18	011	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	11	01	177
E3	MUL14	012	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E3	MUL17	013	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	11	01	177
E3	MUL1	014	4	0	0	0	1	0	0	10	0	06	00	00	0	0	00	00	00	000
E3	MUL3	015	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	10	00	001
E3	MUL13	016	2	0	3	0	0	0	0	00	0	0	00	00	2	0	00	11	01	010
F3	ADD23	017	6	0	3	0	1	0	0	00	0	14	00	00	2	0	00	01	11	361
E4	DIV20	020	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	11	01	076
E5	DIV23	021	4	0	0	0	1	0	0	00	0	20	00	00	0	0	00	00	00	071
E4	DIV4	022	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E5	DIV33	023	4	0	0	0	1	0	0	00	0	20	00	00	0	0	00	00	00	070
E4	DIV13	024	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	11	01	147
E5	DIV27	025	4	0	0	0	1	0	0	00	0	11	00	00	0	0	00	00	00	101
E4	DIV7	026	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	10	00	057
E5	DIV31	027	4	0	0	0	1	0	0	10	0	06	00	00	0	0	00	00	00	103
E2	ASH2	030	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E4	DIV16	031	6	0	0	0	1	0	0	14	0	00	00	00	2	0	00	00	00	065
F5	FDV21	032	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	11	372
E4	DIV11	033	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	037
E2	ASH5	034	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	123
E4	DIV19	035	6	0	0	0	1	0	0	14	0	00	00	00	2	0	00	00	00	153
E2	ASH3	036	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	063
E4	DIV12	037	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177

NOTE:  
THE COMPLEMENT OF THE ACTUAL ROM OUTPUT FOR THE UPF, WR, SPS (HIGH 2 BITS), BUS (LOW BIT), AND CLK (LOW BIT) FIELD IS LISTED FOR CLARITY.

FLWS	STATE	ADR	CON	FC1	FUB	MHR	FRD	RD	SRD	SDR	CVM	NZM	CCC	GPC	CEE	CNT	EUB	CBR
E3	MUL7	000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
E3	MUL19	001	0	0	0	0	0	0	3	0	0	0	0	0	0	1	00	1
E3	MUL2	002	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01	1
E3	MUL4	003	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01	1
F5	FDV1	004	0	0	0	0	0	0	0	0	0	2	4	0	0	0	14	0
E3	MUL16	005	0	0	0	0	0	1	1	0	1	0	1	0	0	0	00	0
F5	FDV2	006	0	0	0	0	0	0	0	0	0	0	0	6	0	0	00	0
E3	MUL12	007	0	0	0	0	0	1	1	0	0	0	0	0	0	0	04	0
E3	MUL15	010	0	0	0	0	0	0	0	0	1	0	1	0	0	0	00	0
E3	MUL18	011	0	0	0	0	0	0	0	0	0	3	4	0	0	0	00	0
E3	MUL14	012	0	0	0	0	0	0	0	0	5	0	1	0	0	0	00	0
E3	MUL17	013	0	0	0	0	0	0	0	0	5	2	5	0	0	0	00	0
E3	MUL1	014	0	0	0	0	0	0	0	0	5	0	1	0	0	2	00	0
E3	MUL3	015	0	0	0	0	0	0	0	3	0	0	0	0	0	0	00	0
E3	MUL13	016	0	0	0	0	0	0	0	0	2	3	6	0	0	0	00	0
F3	ADD23	017	0	0	0	0	0	0	2	0	0	0	0	4	0	0	00	0
E4	DIV20	020	0	0	0	0	0	0	0	0	0	3	4	0	0	0	03	0
E5	DIV23	021	0	0	0	0	0	1	3	0	0	0	0	0	0	0	02	0
E4	DIV4	022	0	0	0	0	0	0	0	0	1	1	7	0	0	0	00	0
E5	DIV33	023	0	0	0	0	0	1	3	0	0	0	0	0	0	0	02	0
E4	DIV13	024	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
E5	DIV27	025	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
E4	DIV7	026	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E5	DIV31	027	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
E2	ASH2	030	0	0	0	0	0	0	0	0	5	2	7	0	0	0	00	0
E4	DIV16	031	0	0	0	0	0	1	2	0	0	0	0	0	0	1	00	1
F5	FDV21	032	0	0	0	0	0	0	1	0	0	0	0	0	0	1	00	0
E4	DIV11	033	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E2	ASH5	034	0	0	0	0	0	0	0	0	0	0	0	0	0	1	10	0
E4	DIV19	035	0	0	0	0	0	1	2	2	0	0	0	2	0	1	00	1
E2	ASH3	036	0	0	0	0	0	0	0	0	0	0	0	0	0	1	10	0
E4	DIV12	037	0	0	0	0	0	0	0	0	1	0	2	0	0	0	00	0

REV	
CHANGE NO.	
REVISIONS	

DRN	DATE
CHKD.	DATE
	DATE
	DATE
	DATE
	DATE

**DIGITAL EQUIPMENT CORPORATION**  
MAYNARD, MASSACHUSETTS 01901

TITLE  
**EIS BOARD**  
(ADRS 000-037)

NUMBER  
47820-01

SHEET 10 OF 17

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FLWS STATE	ADR	CLK	CIR	WH	CB	CD	CBA	BUS	DAD	SPS	ALU	SBC	SBM	SDM	SBA	UBF	BRX	RIF	UPF
E2 ASH9	040	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	11	01	030
F4 FML4	041	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	150
F5 FDV23	042	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	13	211
F4 FML3	043	6	0	3	1	1	0	0	00	0	23	00	00	2	0	00	01	15	176
E2 ASH16	044	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	243
E5 DIV25	045	6	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	053
E2 ASH10	046	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	223
E5 DIV29	047	4	0	0	0	1	0	0	00	0	11	01	17	0	0	00	00	00	104
E5 DIV37	050	4	0	0	0	1	0	0	00	0	20	00	00	0	0	00	00	00	106
E5 DIV40	051	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	054
E5 DIV35	052	4	0	0	0	1	0	0	00	0	11	01	17	0	0	00	00	00	105
E5 DIV26	053	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	10	00	177
E5 DIV41	054	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	037
E4 DIV2	055	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	10	00	231
E5 DIV42	056	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E4 DIV8	057	6	0	0	0	1	0	0	00	0	0	00	00	2	0	00	00	00	060
E4 DIV9	060	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	061
E4 DIV10	061	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	12	031
E4 DIV15	062	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	12	031
E2 ASH4	063	6	0	3	1	1	0	0	00	0	00	00	00	3	0	00	10	00	063
F2 ADD3	064	6	0	0	0	1	0	0	10	0	06	00	00	2	0	00	00	00	320
E4 DIV17	065	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	066
E4 DIV18	066	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	035
E2 ASH14	067	4	0	0	0	1	0	0	00	0	32	00	00	0	0	00	00	00	127
E5 DIV34	070	2	0	0	1	0	0	0	00	0	00	00	00	2	0	00	00	00	050
E5 DIV24	071	2	0	0	1	0	0	0	00	0	00	00	00	2	0	00	00	00	045
F2 ADD17	072	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	301
	073	0	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	000
E5 DIV39	074	6	0	3	0	1	0	0	00	0	11	01	17	2	0	00	10	00	051
E3 MUL5	075	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	110
E4 DIV6	076	6	0	0	0	1	0	0	10	0	06	00	00	2	0	00	00	00	024
E2 ASH13	077	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	11	01	206

NOTE:  
THE COMPLEMENT OF THE ACTUAL ROM OUTPUT FOR THE UPF, WR, SPS(HIGH 2 BITS), BUS (LOW BIT), AND CLK(LOW BIT) FIELD IS LISTED FOR CLARITY.

FLWS STATE	ADR	CON	FC1	FUB	MHR	FRD	ERD	SRD	SDR	CVM	NZM	CCC	GPC	CEE	CNT	EUB	CBR
E2 ASH9	040	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
F4 FML4	041	0	0	0	0	1	0	3	0	0	0	0	6	0	0	10	0
F5 FDV23	042	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00	0
F4 FML3	043	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
E2 ASH16	044	0	0	0	0	0	0	0	0	0	0	0	0	0	1	10	0
E5 DIV25	045	0	0	0	0	0	1	1	0	0	0	0	0	0	0	00	1
E2 ASH10	046	0	0	0	0	0	0	0	0	0	0	0	0	0	1	10	0
E5 DIV29	047	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
E5 DIV37	050	0	0	0	0	0	1	1	0	0	0	0	0	0	0	00	0
E5 DIV40	051	0	0	0	0	0	0	0	0	5	2	7	0	0	0	00	0
E5 DIV35	052	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
E5 DIV26	053	0	0	0	0	0	0	0	0	5	2	7	0	0	0	00	0
E5 DIV41	054	0	0	0	0	0	0	0	0	1	0	2	0	0	0	00	0
E4 DIV2	055	0	0	0	0	0	0	0	0	0	0	0	0	0	2	04	1
E5 DIV42	056	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E4 DIV8	057	0	0	0	0	0	0	0	3	0	0	0	4	0	0	00	1
E4 DIV9	060	0	0	0	0	0	0	0	0	1	1	7	0	0	0	03	0
E4 DIV10	061	0	0	0	0	0	0	2	0	0	0	0	0	0	0	00	0
E4 DIV15	062	0	0	0	0	0	0	2	0	0	0	0	0	0	0	00	0
E2 ASH4	063	0	0	0	0	0	0	0	0	7	0	1	0	0	1	10	1
F2 ADD3	064	0	0	0	3	0	0	0	0	4	0	1	0	0	0	10	1
E4 DIV17	065	0	0	0	0	0	0	0	0	0	0	0	0	0	0	07	0
E4 DIV18	066	0	0	0	0	0	0	2	0	5	0	1	0	0	1	00	0
E2 ASH14	067	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E5 DIV34	070	0	0	0	0	0	0	3	0	0	0	0	0	0	0	00	1
E5 DIV24	071	0	0	0	0	0	0	3	0	0	0	0	0	0	0	00	1
F2 ADD17	072	0	0	0	3	0	1	1	0	0	0	0	0	0	0	00	0
	073	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E5 DIV39	074	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01	1
E3 MUL5	075	0	0	0	0	0	0	0	3	0	0	0	0	0	0	00	1
E4 DIV6	076	0	0	0	0	0	0	0	0	4	0	1	0	0	0	10	1
E2 ASH13	077	0	0	0	0	0	0	3	0	0	0	0	0	0	0	00	0

REV	
CHANGE NO.	
HK	

DRN	<i>R. P. ...</i>	DATE	8-16-72
CHKD.	<i>R. P. ...</i>	DATE	8-17-72
APP.	<i>R. P. ...</i>	DATE	8-17-72
DATE		DATE	

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS 01901	
TITLE	
EIS BOARD	
(ADRS 040-077)	
REVISION	NUMBER
C ES	19836-000



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FLWS STATE	ADR	CLK	CIR	WR	CH	CD	CBA	BUS	DAD	SPS	ALU	SBC	SBM	SOM	BBA	UBF	SRX	RIF	UPF
F6 NOM14	140	6	0	3	1	1	0	0	00	0	11	01	17	2	0	00	01	15	142
E3 MUL26	141	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	10	00	177
F6 EXI0	142	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	332
E3 MUL11	143	4	0	0	0	1	0	0	00	0	20	00	00	0	0	00	00	00	005
F1 FP12	144	2	0	3	0	1	0	0	00	0	00	00	00	2	0	00	01	11	244
E1 DST12	145	2	0	3	1	0	0	0	00	0	00	00	00	1	0	00	01	12	275
F1 FP10	146	6	0	3	1	1	0	0	00	0	23	00	00	2	0	00	01	13	252
E4 DIV14	147	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	10	00	062
F4 FML5	150	6	0	3	0	1	0	0	00	0	06	00	00	2	0	00	01	15	365
F1 FP4	151	6	0	3	0	1	0	0	00	0	11	00	00	2	0	00	01	13	135
	152	0	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	000
E4 DIV20	153	6	0	0	0	1	0	0	14	0	00	00	00	2	0	00	00	00	153
F6 NOM1	154	6	0	3	0	1	0	0	00	0	11	01	17	2	0	00	01	15	324
F1 FP6	155	6	0	3	1	1	0	0	00	0	11	00	00	2	0	00	01	11	175
F6 NOM8	156	6	0	0	0	1	0	0	00	0	11	01	17	2	0	00	00	00	174
E5 DIV21	157	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	107
F5 FDV6	160	4	0	0	0	1	0	0	00	0	1	00	00	0	0	00	00	00	344
F6 EXI4	161	6	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	256
F5 FDV5	162	6	0	3	1	1	0	0	00	0	23	00	00	2	0	00	01	15	176
F2 ADD13	163	4	0	0	0	1	0	0	10	0	06	00	00	0	0	00	00	00	340
F2 ADD5	164	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	11	165
F2 ADD6	165	4	0	0	0	1	0	0	00	0	06	00	00	0	0	00	00	00	202
F5 FDV3	166	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	350
E3 MUL20	167	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	10	00	143
E1 DST4	170	3	0	3	0	0	0	1	00	0	00	00	00	2	0	00	04	00	271
F5 FDV11	171	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	10	042
F4 FML8	172	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	10	210
F5 FDV13	173	6	0	0	0	1	0	0	14	0	00	00	00	2	0	00	00	00	331
F6 NOM9	174	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	273
F1 FP7	175	4	0	0	0	1	0	0	00	0	32	00	16	0	0	00	00	00	367
F6 NOM3	176	2	0	0	0	0	1	0	06	0	00	00	00	1	0	00	04	00	220
F2 ASH15	177	6	0	0	0	0	0	0	00	7	00	00	00	0	0	00	00	00	323

NOTE:  
THE COMPLEMENT OF THE ACTUAL ROM OUTPUT FOR THE UPF, WR, SPS (HIGH 2 BITS), BUS (LOW BIT), AND CLK (LOW BIT) FIELD IS LISTED FOR CLARITY.

FLWS STATE	ADR	CON	FC1	FUB	MHR	FRD	ERD	SRD	SDR	CVH	NZH	CCC	GPC	CEE	CNT	EUB	CBR
F6 NOM14	140	0	0	0	1	0	0	0	1	0	0	0	0	0	0	00	1
E3 MUL26	141	0	0	0	0	0	0	0	1	3	5	0	0	0	0	00	0
F6 EXI0	142	0	0	0	0	1	0	2	0	0	0	0	0	0	0	00	1
E3 MUL11	143	0	0	0	0	0	1	3	0	2	0	2	0	0	0	04	0
F1 FP12	144	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00	0
E1 DST12	145	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F1 FP10	146	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00	1
E4 DIV14	147	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F4 FML5	150	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F1 FP4	151	0	0	0	0	0	0	0	4	0	1	0	0	0	0	00	0
	152	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E4 DIV20	153	0	0	0	0	0	1	2	2	0	0	0	0	2	0	10	1
F6 NOM1	154	0	0	1	0	0	0	0	0	0	0	0	1	0	0	04	0
F1 FP6	155	0	0	0	0	0	0	0	0	0	0	0	4	0	0	10	0
F6 NOM8	156	0	0	0	0	1	0	1	0	4	0	1	0	0	0	00	1
E5 DIV21	157	0	0	0	0	0	1	3	0	0	0	0	0	0	0	12	0
F5 FDV6	160	0	0	0	0	1	0	3	0	0	0	6	0	0	0	00	0
F6 EXI4	161	2	0	0	0	1	0	3	0	1	3	6	0	0	0	00	0
F5 FDV5	162	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F2 ADD13	163	0	0	0	0	0	0	0	0	2	4	0	0	2	0	00	0
F2 ADD5	164	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F2 ADD6	165	0	0	0	0	0	0	0	0	0	0	4	0	0	0	00	0
F5 FDV3	166	1	0	0	0	1	0	3	0	1	0	1	0	0	0	00	0
E3 MUL20	167	0	0	0	0	0	0	3	0	0	0	0	0	0	0	05	0
E1 DST4	170	0	0	0	0	0	0	0	0	0	0	0	0	0	0	16	0
F5 FDV11	171	0	0	0	7	0	0	0	0	0	0	0	0	0	0	00	1
F4 FML8	172	0	0	0	7	0	0	0	0	0	0	0	0	0	0	00	1
F5 FDV13	173	0	0	1	0	0	1	1	3	4	0	1	2	0	0	05	1
F6 NOM9	174	0	0	0	3	0	0	1	0	0	0	0	0	0	0	00	0
F1 FP7	175	0	0	0	0	0	0	1	0	0	0	0	0	0	0	10	0
F6 NOM3	176	0	1	0	3	0	0	1	3	0	2	4	0	0	0	00	0
F2 ASH15	177	0	0	0	0	0	1	0	0	0	0	0	0	1	0	00	0

REVISIONS	REV
CHANGE NO.	
CHK	

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASS./02148	
TITLE EIS BOARD	
(ADR 140-177)	
DRAWN <i>R. Ruelter</i>	DATE 8-10-72
CHG'D <i>J. Ryznar</i>	DATE 8-17-72
<i>J. Ryznar</i>	DATE 8-17-72
<i>[Signature]</i>	DATE 8-17-72
MEMO NO. C	NUMBER 47230-0-1
REV. B	
SHEET 13	OF 17


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FLWS	STATE	ADR	CLK	CIR	WR	CB	CD	CBA	BUS	DAD	SPS	ALU	SBC	SBM	SDM	SBA	UBF	SRX	RIF	UPF
F2	ADD39	200	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	15	072
E3	MUL0	201	6	0	0	0	1	0	0	00	0	32	12	17	2	0	00	00	00	014
F2	ADD7	202	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	11	300
E4	DIV0	203	6	0	0	0	1	0	0	00	0	32	12	17	2	0	00	00	00	303
E3	MUL8	204	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	001
E2	ASH0	205	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	311
E2	ASH19	206	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	11	01	127
E2	ASH7	207	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	11	01	351
F4	FML9	210	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	212
F5	FDV24	211	6	0	0	0	1	0	0	10	0	06	00	00	2	0	00	00	00	032
F4	FML10	212	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	13	213
F4	FML11	213	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	232
F6	NOM11	214	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	13	200
F2	ADD16	215	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	234
F6	NOM12	216	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	330
F4	FML18	217	6	0	0	0	1	0	0	00	0	00	00	00	2	0	00	00	00	330
F6	EXI7	220	5	0	0	0	1	0	3	00	0	00	00	00	0	0	00	00	00	336
E1	DST15	221	2	0	3	1	0	0	0	00	0	00	00	00	1	0	00	01	12	241
F6	EXI3	222	6	0	0	0	1	0	2	00	0	03	00	00	2	0	00	00	00	224
E2	ASH11	223	6	0	3	1	1	0	0	00	0	00	00	00	3	0	00	10	00	223
F6	EXI12	224	2	0	0	0	0	0	2	00	0	00	00	00	2	0	00	00	00	161
F2	ADD10	225	6	0	0	0	1	0	0	00	0	06	00	00	2	0	00	00	00	245
F2	ADD15	226	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	215
E2	ASH12	227	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	077
F4	FML16	230	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	11	259
E4	DIV3	231	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	020
F4	FML12	232	2	0	0	0	0	0	0	00	0	00	00	00	3	0	00	00	00	304
F3	ADD35	233	6	0	0	1	1	0	0	00	0	36	00	00	0	0	00	00	00	360
F6	NOM13	234	6	0	0	1	1	0	0	00	0	00	00	00	0	0	00	01	15	140
F1	FP9	235	4	0	0	0	1	0	0	00	0	11	00	14	0	0	00	00	00	144
F6	EXI1	236	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	00	00	254
F3	ADD25	237	2	0	0	0	0	0	0	00	0	00	00	00	2	0	00	00	00	341

NOTE:  
THE COMPLEMENT OF THE  
ACTUAL ROM OUTPUT FOR THE  
UPF, WR, SPS (HIGH 2 BITS), BUS  
(LOW BIT), AND CLK (LOW BIT)  
FIELD IS LISTED FOR CLARITY.

FLWS	STATE	ADR	CON	FC1	FUB	MHR	FF	ERD	SRD	SDR	CVM	NZM	CCC	GPC	CEE	CNT	EUB	CBR
F2	ADD39	200	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00	0
E3	MUL0	201	0	0	0	0	0	0	0	0	0	0	0	0	0	0	03	1
F2	ADD7	202	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E4	DIV0	203	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
E3	MUL8	204	0	0	0	0	0	0	0	3	0	0	0	0	0	0	00	1
E2	ASH0	205	0	0	0	0	0	0	0	0	0	0	0	0	0	2	13	0
E2	ASH19	206	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
E2	ASH7	207	0	0	0	0	0	0	0	2	0	2	0	0	0	2	13	1
F4	FML9	210	0	0	0	3	0	0	0	0	0	0	0	0	0	0	00	1
F5	FDV24	211	0	0	0	0	0	1	1	3	4	0	1	0	0	0	00	1
F4	FML10	212	2	0	0	0	0	0	3	3	0	0	0	0	0	0	00	0
F4	FML11	213	0	0	1	0	0	1	3	0	0	0	0	0	0	1	01	0
F6	NOM11	214	0	0	0	0	0	0	0	3	0	0	0	0	0	0	00	0
F2	ADD16	215	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F6	NOM12	216	0	0	1	1	0	0	0	1	0	0	0	1	0	0	04	1
F4	FML18	217	0	0	0	0	0	1	1	3	0	0	0	0	0	0	00	1
F6	EXI7	220	0	0	0	0	1	0	1	0	0	0	0	0	0	0	00	0
E1	DST15	221	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	EXI3	222	2	0	0	0	0	0	3	0	0	0	0	0	0	0	00	1
E2	ASH11	223	0	0	0	0	0	0	1	1	2	0	1	0	0	1	10	1
F6	EXI12	224	2	0	0	0	0	0	3	0	0	0	0	0	0	0	00	1
F2	ADD10	225	0	0	0	0	0	0	0	0	0	0	0	4	0	0	00	1
F2	ADD15	226	0	0	0	0	1	0	1	0	0	0	0	0	0	2	00	0
E2	ASH12	227	0	0	0	0	0	1	1	0	0	0	0	0	0	0	00	0
F4	FML16	230	0	0	0	0	0	0	1	0	0	0	0	7	0	0	10	0
E4	DIV3	231	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F4	FML12	232	0	0	0	5	0	0	0	1	0	0	0	7	0	0	10	1
F3	ADD35	233	0	0	0	0	0	1	1	0	0	0	0	0	0	0	00	0
F6	NOM13	234	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F1	FP9	235	0	0	1	0	1	0	3	0	0	0	0	0	0	0	00	0
F6	EXI1	236	1	0	0	0	1	0	3	0	0	0	0	0	0	0	11	0
F3	ADD25	237	0	0	0	0	0	0	0	0	0	0	0	7	0	1	10	0

REV.	CHANGE NO.	CHK

 <b>DIGITAL EQUIPMENT CORPORATION</b> <small>MAYNARD, MASSACHUSETTS</small>	
TITLE	
<h1>EIS BOARD</h1> <p>(ADRS 200-237)</p>	
DRN	DATE
<i>R. P. ...</i>	8-16-72
CHK'D	JATE
<i>J. ...</i>	8-17-72
DATE	DATE
<i>...</i>	8-17-72
DATE	DATE
<i>...</i>	8-17-72
SIZE CODE	NUMBER
C-66	ME638-0-1
REV.	
SHEET 16	OF 17





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FLWS	STATE	ADR	CLK	CIR	WH	CH	CD	CBA	BUS	DAD	SPS	ALU	SBC	SBM	SDM	SBA	UBF	BRX	RIF	UPF
F2	ADD2	300	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	12	064
F2	ADD18	301	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	11	521
F2	ADD4	302	6	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	13	164
E4	DIV1	303	4	0	0	0	1	0	0	00	0	32	00	00	0	0	00	00	00	055
F4	FML13	304	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	13	213
F6	NOM7	305	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	156
F4	FML14	306	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	13	277
F6	NOM5	307	4	0	0	0	1	0	0	00	0	06	00	00	0	0	00	01	15	136
F2	ADD21	310	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	15	340
E2	ASH1	311	6	0	0	1	1	0	0	00	0	00	00	00	2	0	00	10	00	030
F5	FDV19	312	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	134
F3	ADD27	313	6	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	313
F3	ADD33	314	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	334
F3	ADD32	315	6	0	0	0	1	0	0	00	0	11	00	00	2	0	00	00	00	314
F5	FDV16	316	6	0	0	0	1	0	0	00	0	00	00	00	2	0	00	00	00	333
F3	ADD30	317	6	0	0	0	1	0	0	00	0	11	00	00	2	0	00	00	00	347
F2	ADD8	320	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	10	245
F2	ADD19	321	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	11	325
F2	ADD9	322	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	10	225
E2	ASH21	323	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	017
F6	NOM4	324	2	0	0	0	0	1	0	06	0	00	00	00	1	0	00	04	00	305
F2	ADD20	325	4	0	0	0	1	0	0	00	0	00	00	00	0	0	00	01	14	310
F6	NOM2	326	6	0	3	1	1	0	0	00	0	23	00	00	2	0	00	01	15	176
F6	NOM0	327	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	154
F4	FML19	330	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	154
F5	FDV14	331	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	11	370
F6	EXI14	332	4	0	0	0	1	0	0	00	0	32	00	16	0	0	00	00	00	236
F5	FDV12	333	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	13	173
F3	ADD34	334	6	0	0	0	1	0	0	10	0	06	00	00	2	0	00	00	00	233
F3	ADD37	335	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	334
F6	EXI8	336	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	356
F5	FDV17	337	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	352

NOTE:

THE COMPLEMENT OF THE ACTUAL ROM OUTPUT FOR THE UPF, WR, SPS (HIGH 2 BITS), BUS (LOW BIT), AND CLK (LOW BIT) FIELD IS LISTED FOR CLARITY.

FLWS	STATE	ADR	CON	FC1	FUB	MHR	FRD	ERD	SRD	SDR	CVM	NZM	CCC	GPC	CEE	CNT	EUB	CBR
F2	ADD2	300	0	0	0	0	0	0	0	0	0	0	0	0	0	0	05	1
F2	ADD18	301	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10	1
F2	ADD4	302	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E4	DIV1	303	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F4	FML13	304	2	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	NOM7	305	0	0	0	1	0	0	1	1	0	0	0	0	0	0	10	0
F4	FML14	306	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00	0
F6	NOM5	307	0	0	1	2	0	0	0	2	0	0	0	5	0	0	04	0
F2	ADD21	310	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
E2	ASH1	311	0	0	0	0	0	0	0	0	2	0	1	0	0	1	00	1
F5	FDV19	312	0	0	0	0	1	0	0	0	0	0	0	0	0	0	00	1
F3	ADD27	313	2	0	0	1	0	0	1	1	0	0	0	7	0	1	10	0
F3	ADD33	314	0	0	0	0	1	0	1	3	0	2	4	0	0	0	00	0
F3	ADD32	315	0	0	0	0	0	1	1	0	0	0	0	0	0	0	10	1
F5	FDV16	316	0	0	0	0	0	1	2	2	0	0	0	0	0	0	00	1
F3	ADD30	317	0	0	0	0	1	0	1	0	4	0	1	0	0	0	00	1
F2	ADD8	320	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F2	ADD19	321	0	0	0	0	0	0	0	3	0	0	0	0	0	0	00	0
F2	ADD9	322	0	0	0	3	0	0	0	0	0	0	0	0	0	0	00	0
E2	ASH21	323	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	NOM4	324	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F2	ADD20	325	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	NOM2	326	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	1
F6	NOM0	327	0	0	0	0	0	0	0	0	0	0	0	0	0	0	04	1
F4	FML19	330	0	0	0	3	0	0	0	0	0	0	0	0	0	0	00	1
F5	FDV14	331	0	0	0	0	0	0	1	0	0	0	0	0	0	1	10	0
F6	EXI14	332	0	0	0	0	0	0	0	5	2	5	0	0	0	0	00	0
F5	FDV12	333	0	0	0	0	0	0	1	0	0	0	0	7	0	0	00	0
F3	ADD34	334	0	0	0	0	0	0	1	0	4	0	1	0	0	0	03	1
F3	ADD37	335	0	0	0	0	0	1	0	3	0	0	0	0	0	0	00	0
F6	EXI8	336	0	0	0	0	0	1	3	0	0	0	0	0	0	0	00	0
F5	FDV17	337	0	0	0	0	1	0	1	0	0	0	0	0	0	0	00	1

REV.	
CHANGE IN	
CHK	

DRN	<i>A. Pindella</i>	DATE	8-16-72
CHD	<i>J. Bussanese</i>	DATE	8-17-72
CHK	<i>J. Bussanese</i>	DATE	8-17-72
MOD	<i>J. Bussanese</i>	DATE	8-16-72

<b>DIGITAL EQUIPMENT CORPORATION</b> MAYNARD, MASSACHUSETTS	
TITLE	
EIS BOARD	
(ADRS 300-337)	
SIZE CODE	NUMBER
C CS	M7238-0-1
REV.	D
SHEET 16 OF 17	DIST.

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FLWS	STATE	ADR	CLK	CIR	WR	CB	CD	CBA	BUS	DAD	SPS	ALU	SBC	SBM	SDM	SBA	UBF	SRX	RIF	UPF
F3	ADD22	340	6	0	0	1	1	0	0	00	0	14	00	00	2	0	00	01	13	017
F3	ADD26	341	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	313
F2	ADD14	342	2	0	0	0	0	0	0	00	0	00	00	00	2	0	00	00	00	226
F3	ADD28	343	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	317
F5	FDV7	344	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	15	346
F5	FDV8	346	6	0	0	0	1	0	0	00	0	11	02	17	2	0	00	00	00	364
F3	ADD31	347	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	01	11	315
F5	FDV4	350	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	14	222
E2	ASH8	351	6	0	0	1	1	0	0	00	0	00	00	00	2	0	00	10	00	040
F5	FDV18	352	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	312
F3	BRQ0	353	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	355
F6	EXI6	354	6	0	3	0	1	0	0	10	0	06	00	00	2	0	00	04	00	007
F4	BRQ4	355	6	0	3	0	1	0	0	10	0	06	00	00	2	0	00	04	00	375
F6	EXI9	356	6	0	3	0	1	1	0	06	0	06	01	17	2	0	00	04	00	374
F3	BRQ1	357	2	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	355
F3	ADD38	360	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	327
F3	ADD24	361	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	237
F3	ADD36	362	6	0	0	1	1	0	0	00	0	06	00	00	2	0	00	00	00	335
F3	ADD29	363	2	0	0	0	0	0	0	00	0	00	00	00	2	0	00	00	00	314
F5	FDV9	364	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	01	12	267
F4	FML6	365	6	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	263
E1	D8T7	366	3	0	0	0	0	1	1	00	0	00	00	00	0	1	00	01	12	271
F1	FP8	367	2	0	3	0	0	0	0	00	0	00	00	00	2	0	00	01	15	235
F5	FDV15	370	6	0	0	0	1	0	0	00	0	11	00	00	2	0	00	00	00	316
F5	BRQ6	371	6	0	0	1	0	0	0	00	0	00	00	00	0	0	00	00	00	355
F5	FDV22	372	6	0	0	0	1	0	0	00	0	06	00	00	2	0	00	00	00	316
F2	ADD12	373	6	0	0	1	1	0	0	10	0	06	00	00	2	0	00	01	15	163
F6	EXI10	374	5	0	0	0	1	0	1	00	0	32	00	00	0	0	00	00	00	376
F6	BRQ5	375	6	0	3	0	1	0	0	00	0	06	01	17	2	0	00	01	07	017
F6	EXI11	376	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	177
E1	EI1	377	2	0	0	0	0	0	0	00	0	00	00	00	0	0	00	00	00	240

NOTE:  
THE COMPLEMENT OF THE ACTUAL ROM OUTPUT FOR THE UPF, WR, SPS (HIGH 2 BITS), BUS (LOW BIT), AND CLK (LOW BIT) FIELD IS LISTED FOR CLARITY.

FLWS	STATE	ADR	CON	FC1	FUB	MHR	FRD	ERD	SRD	SDR	CVM	NZM	CCC	GPC	CEE	CNT	EUB	CBR
F3	ADD22	340	0	0	1	0	0	0	0	0	4	0	1	0	0	1	03	1
F3	ADD26	341	0	0	0	0	0	0	0	0	0	0	0	0	0	1	10	0
F2	ADD14	342	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10	1
F3	ADD28	343	0	0	0	2	0	0	1	2	0	0	0	5	0	0	00	0
F5	FDV7	344	3	0	0	0	0	0	3	0	0	0	0	0	0	0	10	0
F5	FDV8	346	3	0	0	0	1	0	3	0	0	0	0	0	0	0	00	1
F3	ADD31	347	0	0	0	3	0	0	1	0	0	0	0	0	0	0	00	0
F5	FDV4	350	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00	0
E2	ASH8	351	0	0	0	0	0	0	3	3	0	0	0	0	0	1	00	1
F5	FDV18	352	0	0	0	0	0	0	3	0	0	0	0	0	0	0	00	0
F3	BRQ0	353	2	0	0	0	1	0	3	0	0	0	0	0	0	0	00	0
F6	EXI6	354	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F4	BRQ4	355	0	0	0	0	0	0	0	0	0	0	0	0	1	00	0	
F6	EXI9	356	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F3	BRQ1	357	2	0	0	0	1	0	3	0	0	0	0	0	0	0	00	0
F3	ADD38	360	0	0	0	1	0	0	0	1	0	0	0	0	0	0	00	0
F3	ADD24	361	0	0	1	0	0	1	2	0	0	0	0	0	0	0	02	1
F3	ADD36	362	0	0	0	3	0	0	1	0	0	0	0	4	0	0	00	1
F3	ADD29	363	0	0	0	3	0	0	0	0	0	0	0	0	0	0	10	1
F5	FDV9	364	0	0	0	0	0	0	0	0	0	0	0	0	0	2	00	1
F4	FML6	365	3	0	0	0	1	0	3	0	0	0	0	0	0	0	00	1
E1	D8T7	366	0	0	0	0	0	0	0	0	0	0	0	0	0	0	16	0
F1	FP8	367	0	0	0	0	0	0	3	0	0	2	4	0	0	0	04	0
F5	FDV15	370	0	0	0	0	0	1	1	3	4	0	1	4	0	0	10	1
F5	BRQ6	371	2	0	0	0	1	0	3	0	0	0	0	0	0	0	00	0
F5	FDV22	372	0	0	0	0	0	1	1	3	4	0	1	4	0	0	10	1
F2	ADD12	373	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01	1
F6	EXI10	374	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	BRQ5	375	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0
F6	EXI11	376	0	0	0	0	0	0	0	0	5	2	7	0	0	0	00	0
E1	EI1	377	0	0	0	0	0	0	0	0	0	0	7	0	0	0	00	0

REV.	
CHANGE NO.	
CHK	

DRN	<i>P. Corbett</i>	DATE	8-16-72
CHK'D.	<i>Thompson</i>	DATE	8-17-72
DATE	<i>8-17-72</i>	DATE	8-17-72
DATE	<i>8-17-72</i>	DATE	8-17-72
DATE	<i>8-17-72</i>	DATE	8-17-72

<b>digital EQUIPMENT CORPORATION</b> MAYNARD, MASSACHUSETTS	
TITLE	
EIS BOARD	
(ADRS 340-377)	
REV. 0	NUMBER M7238-0-1
C CS	DIST.
SHEET 17 OF 17	

