

**T & F Documents  
for  
.150" - Tape Cassette  
Unit**



## PRODUCT SPECIFICATION

### Cassette Tapes

The use of tape cassettes as a reliable data storage medium requires strict adherence to proper operator handling procedures, the control of operation environment, the control of cassette tapes in a storage library, and the control of transportation and storage environment for cassette tapes. Each procedure and control is equally important and interdependent on each other.

### Operator Handling Procedures

The tape cassette should be carefully removed from the storage container and placed opening edge down, "A" side toward operator, in the cassette drive.

Before removing a cassette from the drive, always let the unit rewind the tape to clear leader. Make sure the clear leader is in full view before replacing the cassette tape in the storage container. The tape should not be touched or handled in any way.

Do not open the cassette drive front door when tape is in motion or tape damage may occur.

### Operation Environment

It is recommended that the cassette drives be operated under the following environmental conditions.

Temperature	50 to 90° F
RH	20 to 80% (no condensation)

For additional information refer to Engineering Specifications 2046 2875, 2046 3725, and Environmental Standard B2-05. The immediate operation environment must be free of liquid or particulate contaminants such as coffee, soft drinks, cigarette or cigar ashes, paper, dust, etc.

The cassette tapes must not be stored or handled near strong magnetic fields.

### Tape Library and Storage Environment.

It is recommended that cassette tapes be stored under the following environmental conditions.

Temperature	40 - 122° F
RH	20 to 80% (noncondensing)

During storage the reels shall be secured to prevent any tendency to unwind. For storage a rigid container free from dust and moisture should be used. Containers must be stored in an area free from strong magnetic fields.

Each cassette tape should be clearly identified when put into use. Identification should consist of a date code and identification number. Master file tapes should be clearly marked for ease of identification.

A performance history should be maintained for each cassette tape noting date entering use, error history, certification and tape cleaning history, and maintenance intervals required.

Periodic visual inspection should be performed on each cassette noting nonuniform wound reels, contamination buildup on any of the open end surfaces and contamination on the external cassette surfaces that would prevent proper cassette load/unload.

Tape damage of any type that causes permanent deformation or distortion of the tape will result in unpredictable operation and the cassette tape should be discarded.

The useful life of the cassette tape should be determined for each application. In general cassette tape life is reduced more by operator handling than by cassette drive use. End of tape life can be determined by the performance history of the cassette tape. Analysis of time in service, the number of temporary errors, and visual inspection should provide guidelines in determining end of cassette tape life.

### Transportation and Storage Environment

Refer to Engineering Specifications 2046 2875, 2046 3725 and B2-05.

### OPERATIONAL

#### Recommended Use

The cassette drive subsystem is designed for horizontal or vertical mounting. Cassette tape load/unload access is gained by pressing the load/unload button at the top of the cassette drive face. The cassette tape should be gently inserted open edge down, "A" side facing the operator and the carriage closed. Tape drive motion and amount of tape on the supply reel is indicated through the front view window. Optional indicators are available, one for file protect indication and the other is externally controlled through the interface. Cassette unload should only be attempted with the drive stopped and the cassette tape in clear leader position. After use the cassette carriage should be closed to prevent accumulation of dirt and dust in the cassette drive mechanism.

Do not actuate rewind by opening and closing the carriage assembly door. Rewind mode must be actuated by the controller only. To avoid possible tape damage do not open the carriage assembly door unless tape is positioned at clear leader.

## ELECTRICAL

### I/O Signals and Levels

Signal levels are measured at the receiving end of the line under termination conditions specified in Section 6.1.5.

### Logical True

A signal level is logical true (logical 1) if it is in the range + 2.5 to + 5.5 volts. No signal shall be more positive than 5.5 volts.

### Logical False

A signal level is logical false (logical 0) if it is in the range 0 to 0.5 volts. No "false" signal level shall be more positive than 0.5 volts.

### Switching Time

Switching time is the rise or fall time of a signal, whichever is greater, as seen at the receiving end of the line under the termination conditions of Section 6.1.5.

Switching time shall not exceed .250 microsecond between the 10% and 90% points.

### Output Signal Characteristics

The output consists of DTL 944 open collector driver that has sink current capability of 40 ma.

### Input Signal Termination

The input line termination consists of  $237 \pm 5\%$  ohms to + 5 V and  $348 \pm 5\%$  ohms to ground.

### Clear Leader - BOT/EOT

A clear leader signal will be generated any time the clear leader is positioned in front of each of the photo sensors located in each tape guide.

The BOT/EOT holes are sensed by the photo sensor located between the tape supply reel and the tape head (i.e. the left sensor).

### Interface Pin Assignment

<u>Pin</u>	<u>Signal</u>	<u>Description</u>
K	$\overline{\text{TWI}}$	Tape Write Level
V	$\overline{\text{TWRL}}$	Tape Write Ready Level - File Protect
M	$\overline{\text{FDL}}$	Forward Drive Level
W	$\overline{\text{TREL}}$	Tape Ready Level
H	$\overline{\text{TWCP}}$	Tape Write Clock Pulse
U	$\overline{\text{TPRL}}$	Tape Position Ready Level
T	$\overline{\text{CLPL}}$	Clear Leader Position Level
F	$\overline{\text{TWIL}}$	Tape Write Information Level
S	$\overline{\text{TRIP}}$	Tape Read Information Pulse
N	$\overline{\text{BDL}}$	Backward Drive Level
R	$\overline{\text{TRCL}}$	Tape Read Clock Level
C	-12V DC	
E	$\overline{\text{TRWP}}$	Tape Rewind Pulse
P	$\overline{\text{CSL}}$	Cassette Select Level
B	+ 12V DC	
L	$\overline{\text{HSL}}$	High Speed Level
J	$\overline{\text{RCL}}$	Read Clipping Level
A	+ 5V DC	
X	$\overline{\text{RL}}$	Indicator Control (option)

**NOTE:** All pin numbers 1 thru 19 are ground pins. Pins H, F, S, and R are information transfer lines and are twisted pair. Twisted pair grounds should be grounded at the numbered pin opposite the lettered pin on this connector. The maximum cable length is 10 feet. The cable length may be extended to 15 feet if twisted pair wires are used on all signal lines. See Product Index listed in Par. 2.0.



### Interface Connector

The cassette uses Part No. S2041 2516 connector (AMP 583617-1 ref only).

### Input Lines to Recorder

$\overline{\text{FDL}}$  - Forward Drive Level

This line, when held "false", will cause the tape to be driven in the forward direction.

$\overline{\text{BDL}}$  - Backward Drive Level

When this line is "false" tape will be driven in the backward direction.

$\overline{\text{TWL}}$  - Tape Write Level

This line, when "False", holds the drive in write status and will permit data to be written on the tape. If this line is held "False" without having "Tape Write Clock" pulses, an erase function is performed.  $\overline{\text{TWL}}$  must be "False" when the "Forward Drive Level" is turned on and must be maintained "False" for 30 ms (until tape motion stops). Where possible the tape write level should be held in the "False" state between write operations.

Continued.

TRWP

The negative-going (leading) edge of this pulse (0.5-5  $\mu$ s) will initiate a rewind cycle in the tape drive. The rewind cycle will terminate automatically when tape is positioned at the beginning of tape - clear leader.

RCL - Read Clipping Level

When "false," this line selects the high clipping level and should be used when write verification is performed. This line should be held in a "true" state for normal reading. The clipping level should also be changed on alternate read retries after an initial read failure. This will provide the best probability for recover of recorded data.

TWIL - Tape Write Information Level

When "false" during a TWCP pulse, this line will cause a 1-bit (flux change) to be written in the data track. A "true" level (or line open) will result in a 0-bit (no flux change) at TWCP time. The TWIL line must set

Continued.

$\overline{\text{TWIL}}$  (Continued)

to the proper level one microsecond before the leading (negative going edge) of the clock pulse and must remain at that level for one microsecond after the trailing edge of the clock pulse.

$\overline{\text{TWCP}}$  - Tape Write Clock Pulse

The false level of the tape write clock pulse (0.5-5  $\mu\text{s}$ ) indicates when the write information line  $\overline{\text{TWIL}}$  is being sensed and strobes the resulting data bit into the write amplifier. The clock and, if present, the data signal changes are recorded on the tape at the positive-going (trailing edge) of the clock pulse. In dual gap machines the writing of the flux changes are delayed by approximately 50  $\mu\text{sec}$ . A clock pulse must be transmitted with each information bit.

$\overline{\text{HSL}}$

When "false," this line causes the tape to be driven at approximately 25 ips (635 mmps) in the direction determined by  $\overline{\text{FDL}}$  or  $\overline{\text{BDL}}$ . The  $\overline{\text{HSL}}$  command may be given any time before or after a  $\overline{\text{FOL}}$  or  $\overline{\text{BOL}}$  is given.

Continued.

CSL - Cassette Select Level

When "false" enable all input and output lines, except for CLPL and TREL which are enabled at all times. In multi-unit configurations a separate CSL line is provided to each unit and a separate line is provided from each unit for the CLPL and TREL signals. The CSL line is grounded for single unit configurations.

RL

Optional indicator control line.

Output Lines from Recorder/Reader

TREL - Tape Ready Level

When "false," this line indicates that a cassette is properly inserted in the recorder. The recorder is ready to accept a tape command via the interface.

TWRL - Tape Write Ready Level

When "false," this line indicates that a cassette is properly inserted in the recorder and has a write enable tab installed to allow writing on tape.

TPRL - Tape Position Ready Level

When "false," this line indicates that the tape is positioned properly and that the recorder can be operated in the write or read mode. This level

Continued.

TPRL (Continued)

will be set "false" when the tape has moved forward so that the BOT hole passes the BOT/EOT photo detector. It remains "false" until the EOT hole passes the BOT/EOT photo detector. The record being recorded at the time this level goes "true" at EOT and any additional required "end of file" record(s) must be completed within the remaining usable tape. The tape is usable to within 2.0 inches of the clear leader (trailer). In dual gap units this line is not controlled by the cassette select level and cannot be wire-ored with other units.

CLPL - Clear Leader  
Position Level

When "false," this line indicates that the tape is positioned at clear leader at the physical beginning or end of tape. Tape can be driven only in the forward direction when in a clear leader position. Should the tape be at clear leader at the end of the tape, operator intervention will be required to rewind the tape passed the clear

Continued.

CLPL (Continued)

leader. The cassette can then be re-inserted into the carriage and the tape will then automatically rewind to BOT clear leader. In dual gap units this line is not controlled by the cassette select level and cannot be wire-ored with other units.

TRIP - Tape Read Information Pulse

When "false," this line indicates that a "one" is being read for the cell period defined by TRCL. More than one pulse during any one cell period should also be interpreted as a single "one" for that particular cell. No pulse during cell time should be interpreted as a "zero." The minimum pulse width = 600 ns. The TRIP is not logically gated by the TRCL cell period in dual gap units and false levels which may occur outside the cell period are to be ignored.

TRCL - Tape Read Cell Level

This level when "false" indicates cell duration time. The negative going edge defines the beginning of cell period and the positive going trailing edge defines the end of the cell period.

Continued.

TRCL. (Continued)

One or more TRIP's during TRCL time indicates that a "one" is being read and no TRIP's indicates a "zero" read. The minimum time between cell periods = 1.5  $\mu$ s.

Input Power

The voltage and current required from the host equipment to power the cassette drive subsystem is as follows:

- + 5 volts  $\pm$  10%                    1.0 amp maximum
- + 12 volts  $\pm$  10%                    0.9 amp maximum
- 12 volts  $\pm$  10%                    0.125 amp maximum

Indicator Option

An optional indicator when provided (see Figure 1) will cause the right side of the lens above the Write Status (WS) legend to be illuminated when a cassette tape, with the write enable plug in place, is properly inserted into the carriage and the carriage is closed. With the tab removed or the carriage open, the indicator will be dark.

The left side of the lens above the "R" legend is externally controlled through pin X of the interface connector. With pin X held at or near ground, the indicator will light. The indicator

**Continued.**

**will be dark when the pin is allowed to float or is held to the +5V power supply level. The open circuit voltage of pin X is equal to the +5V power supply level. The control on pin X must be capable of sinking a maximum of 40 ma (nominally 32 ma) when grounded.**



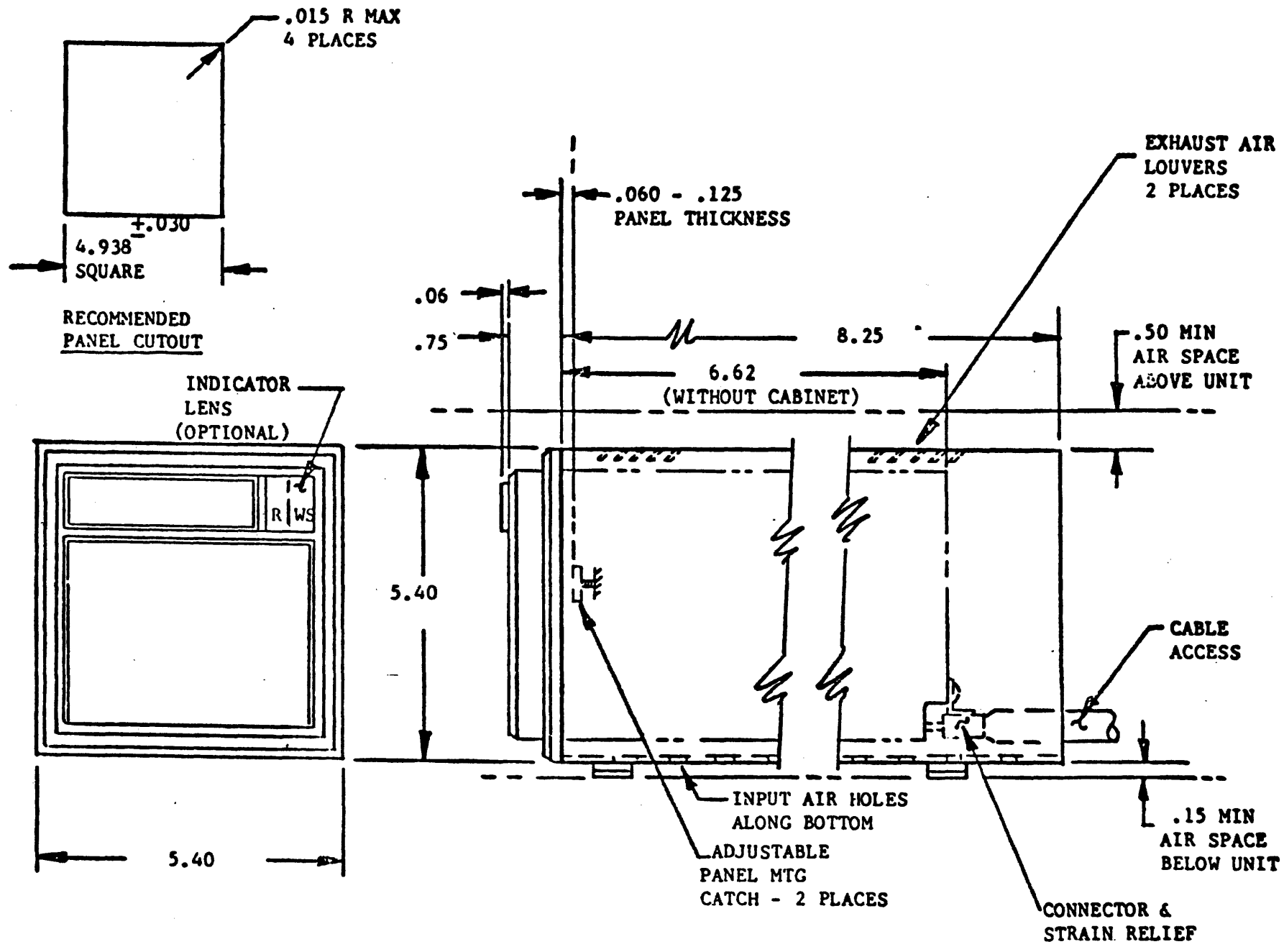


FIGURE 1

REVISIONS

REV.	DATE	BY	DESCRIPTION
A	11-25-66	WJL	INITIAL

RELEASED

ADDED NOTE 2

C

1. ADDED PINS 15(11)-(B-7)

2. CHANGED COLOR CODES: BLK WAS GRN, WHT WAS BRN, BLU WAS YEL, YEL WAS BLU (B-5)

1. 15 (B-7), 16 (B-7), 17 (B-7)

ADDED L3 & L4 (B-2)

ADDED PINS 8, 10, 16, 18 (B-7, C-7)

E

1. DELETED LED (B-2)

2. LED 3 WAS L3, LED 4 WAS L4. 18-21

3. ADDED NOTE 2

4. TITLE IS SCHEMATIC TAPE DECK, WAS SCHEMATIC TAPE DECK CASSETTE.

F

1) REVISED TO SHOW DUAL GAP CONFIG. ONLY

2) ADDED PINS L5 (C, D-4)

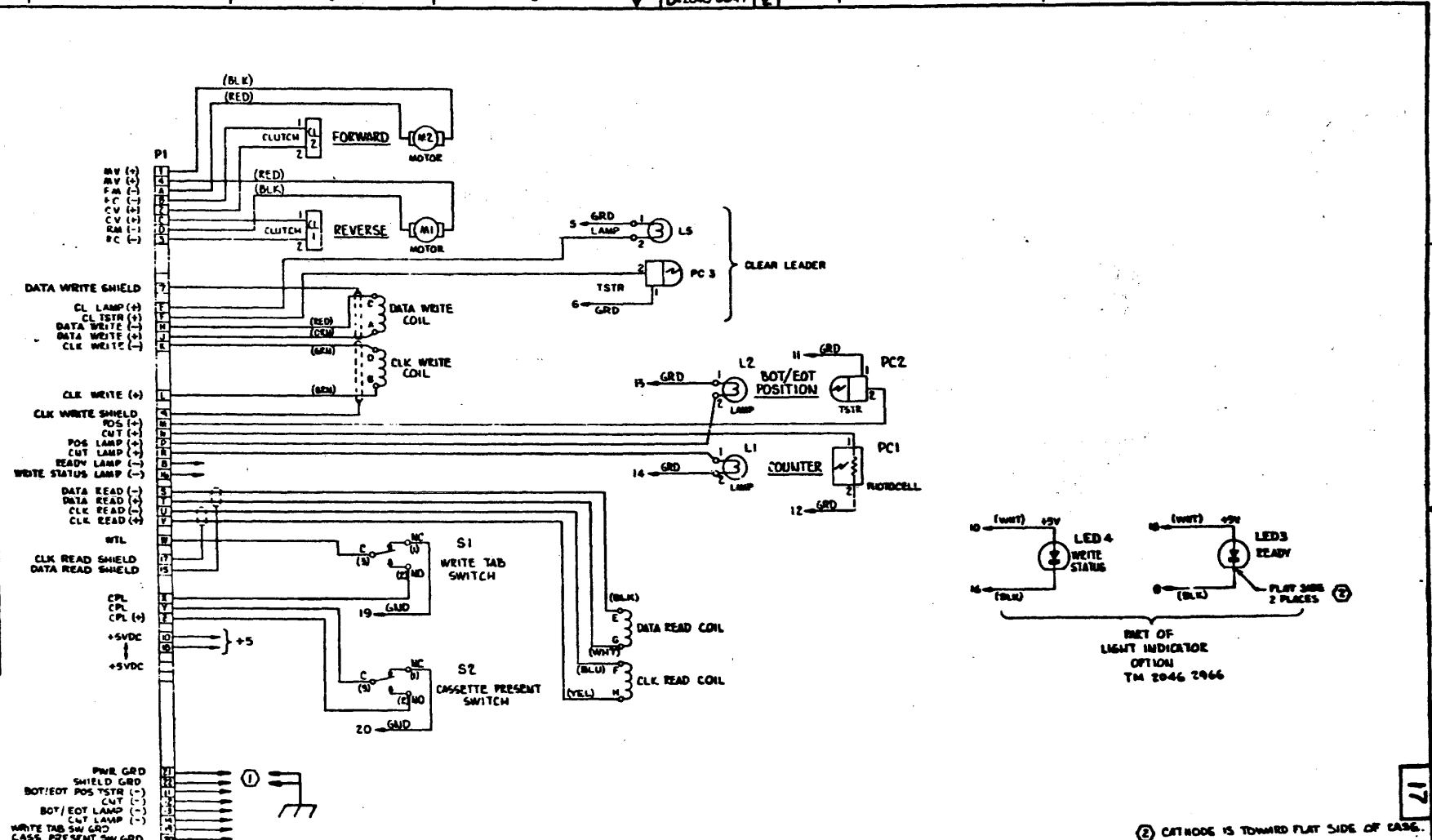
3) ADDED 'DG' TO TITLE

4) ADDED POSITIONS 11, 12, 13, 14, 19, 20, 9, E 7.

5) DELETED POSITIONS 5 & 6.

G

NOTE 1 WAS: FOR SYSTEM INSTALLATION, ONE OR BOTH WIRES FROM P-21 AND P-22 TO GND MAY BE REMOVED REFERRING TO SYSTEM REQ'YS.



NET OF LIGHT INDICATOR OPTION TM 2046 2966

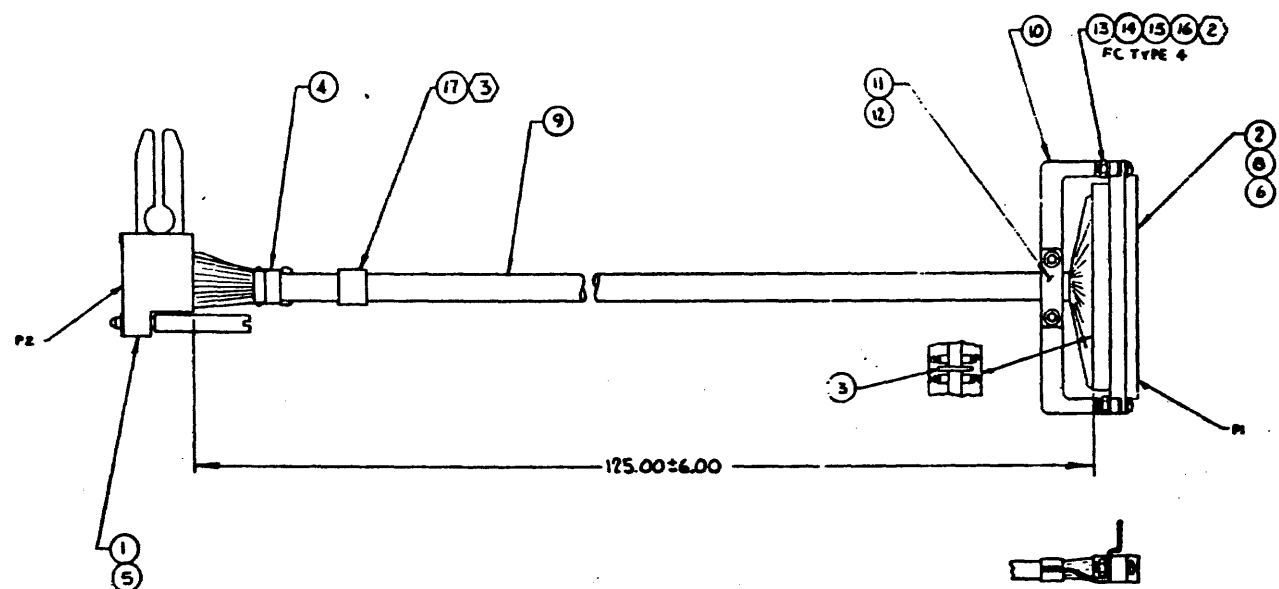
- ② CATHODE IS TOWARD FLAT SIDE OF CASE.
- ① IN PANEL MOUNTING APPLICATIONS, REMOVE THE CONNECTION FROM P-21 TO CHASSIS GROUND (E1). A SEPARATE GROUNDING JUMPER FROM CHASSIS CONNECTION E1 TO A SUITABLE PANEL GROUND MUST BE ADDED BY USER.

NOTE:

GEN. INAL. SPEC. 1150 2041 AND TITLE BLOCK LETTERS APPLY UNLESS OTHERWISE SPECIFIED (EQU.)		TELETYPE	
DATE: 11-25-66	DRAWN: WJL	CHECKED: WJL	APPROVED: WJL
TITLE: SCHEMATIC TAPE DECK, DG	PART: 1776	QTY: 1	U.S. AIR FORCE
DESIGNED BY: WJL	APPR'D BY: WJL	DATE: 11-25-66	DRAWN BY: WJL

SCHEMATIC TAPE DECK, DG D-2040 6641

SEND CHANGES TO  
USER PLANTS:



REVISIONS			
REV	BY	DATE	DESCRIPTION
A	W.B.	1/10/82	REVISED
RELEASED			
B	W.B.	1/10/82	REVISED
125.00 ± 6.00 WAS 100.00 ± 5.50 (B4) TITLE WAS "CABLE ASSY, EXT. 120 IN."			

WIRING INSTRUCTIONS						
COLOR	EPDPA			Y2D		
	TERMIN. ITEM	CONN.	PIV. NO.	TERMIN. ITEM	CONN.	PIV. NO.
RED	6	P1	A	7	P2	1
WHITE/BLACK	6	P1	I	7	P2	2
ORANGE	6	P1	B	7	P2	3
WHITE/BLACK/BLACK	6	P1	2	7	P2	4
BROWN	6	P1	C	7	P2	5
WHITE/BLACK/BROWN	6	P1	X	5	P2	10
YELLOW	6	P1	E	5	P2	15
WHITE/BLUE	6	P1	E	5	P2	16
WHITE/VIOLET	6	P1	G	5	P2	9
WHITE/GRAY	6	P1	H	5	P2	13
WHITE/BLACK/VIOLET	6	P1	Y	5	P2	8
BLUE	6	P1	J	5	P2	16
VIOLET	6	P1	K	5	P2	17
GRAY	6	P1	L	5	P2	18
WHITE	6	P1	M	5	P2	19
WHITE/BLACK/BLUE	6	P1	N	5	P2	20
WHITE/BROWN	6	P1	P	5	P2	21
WHITE/BLACK/GREEN	6	P1	R	5	P2	12
WHITE/BLACK/RED	6	P1	S	5	P2	7
WHITE/BLACK/ORANGE	6	P1	T	5	P2	11
WHITE/BLACK/YELLOW	6	P1	U	5	P2	6
WHITE/RED	6	P1	V	5	P2	22
WHITE/ORANGE	6	P1	W	5	P2	23
WHITE/YELLOW	6	P1	X	5	P2	24
WHITE/GREEN	6	P1	Y	5	P2	25

- ③ MARK ASSY NO. ON STRAP WITH "BOND-O-TOOL" STAMP.
  - ② FC "TYPE" PER SPEC 1199 2096.
  - ① INDICATED WIRES ARE TWISTED PAIR.
- NOTES: UNLESS OTHERWISE SPECIFIED

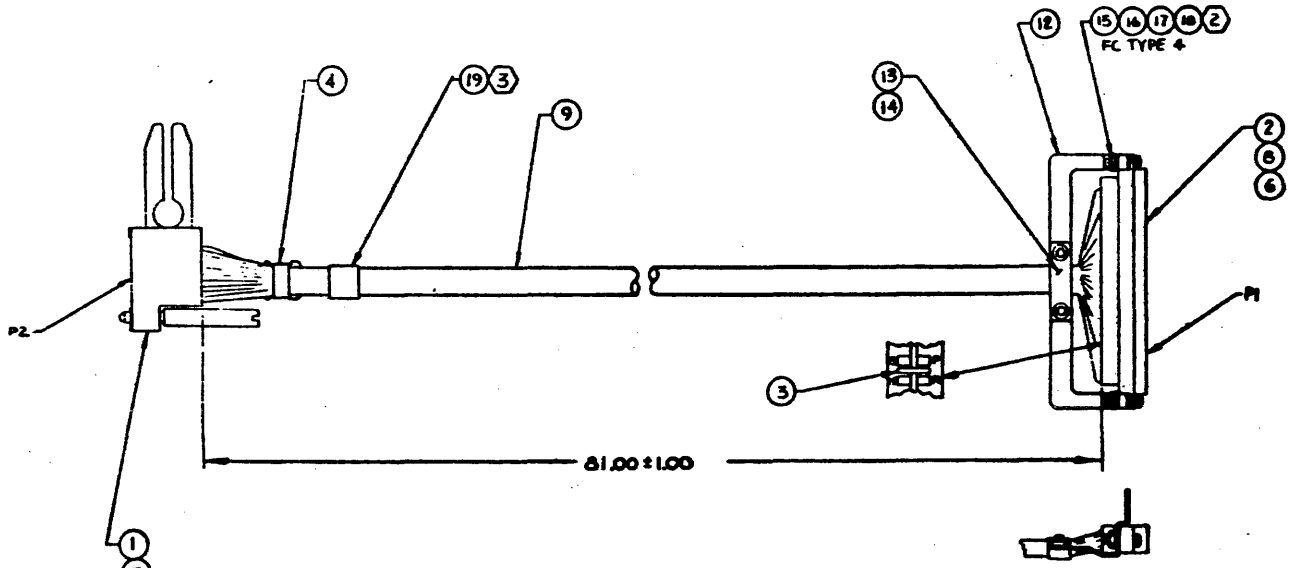
GEN. ENGR. SPEC. 1199 2096 AND TITLE BLOCK LETTERS APPLY UNLESS OTHERWISE SPECIFIED   SCALE:		Suffrage Corporation	
HOLE DIA/TOLERANCE	FINISH	TOLERANCE	WESTLAKE VILLAGE CALIFORNIA 91360
1/8" - .0005	AS MANUFACTURED	AS MANUFACTURED	WESTLAKE PLANT U.S. AMERICA
1/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3/8" - .0005	AS MANUFACTURED	AS MANUFACTURED	
1/2" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
1" - .0005	AS MANUFACTURED	AS MANUFACTURED	
1 1/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
1 1/2" - .0005	AS MANUFACTURED	AS MANUFACTURED	
1 3/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
2" - .0005	AS MANUFACTURED	AS MANUFACTURED	
2 1/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
2 1/2" - .0005	AS MANUFACTURED	AS MANUFACTURED	
2 3/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3 1/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3 1/2" - .0005	AS MANUFACTURED	AS MANUFACTURED	
3 3/4" - .0005	AS MANUFACTURED	AS MANUFACTURED	
4" - .0005	AS MANUFACTURED	AS MANUFACTURED	

Q.A. [Signature] DATE: 12-6-79  
 APPROVED: [Signature] DATE: 12-6-79

CABLE ASSY, EXTERNAL D-2046 1992 B  
 SHEET 1 OF 1

D-2046 2008 C

REVISED  
 A  
 RELEASED  
 B  
 ITEM 4 IS 38 PLACES  
 HAS 4 PLACES (CS)  
 2) 81.00±1.00 HAS  
 38.00±.50 (B)  
 3) TITLE HAS CABLE  
 ASSY INT 38 W/  
 C  
 1 ITEM (1) WAS 38 PLACES  
 (C-S)  
 2 DELETED ITEMS (1)  
 (C-4,S)  
 3 ADDED COLOR CODE TO  
 WIRE MSTR. TABLE.



WIRING INSTRUCTIONS		FROM		TO	
COLOR	TERMIN. ITEM	CONN. PIN NO.	TERMIN. ITEM	CONN. PIN NO.	
RED	G	P1	A	7	P2 1
WHITE/BLACK	G	P1	I	7	P2 2
ORANGE	G	P1	B	7	P2 3
WHITE/BLACK/BLACK	G	P1	Z	7	P2 4
BROWN	G	P1	C	7	P2 5
WHITE/BLACK/BROWN	G	P1	X	5	P2 10
YELLOW	G	P1	E	5	P2 15
WHITE/BLUE	D	P1	F	5	P2 14
WHITE/VIOLET	D	P1	V	5	P2 9
WHITE/GREY	D	P1	H	5	P2 13
WHITE/BLACK/VIOLET	D	P1	7	5	P2 8
BLUE	D	P1	J	5	P2 16
VIOLET	D	P1	K	5	P2 17
GREY	D	P1	L	5	P2 18
WHITE	D	P1	M	5	P2 19
WHITE/BLACK/BLUE	D	P1	N	5	P2 20
WHITE/BROWN	D	P1	F	5	P2 21
WHITE/BLACK/GREEN	D	P1	R	5	P2 22
WHITE/BLACK/RED	D	P1	19	5	P2 7
WHITE/BLACK/YELLOW	D	P1	S	5	P2 11
WHITE/BLACK/ORANGE	D	P1	15	5	P2 6
WHITE/RED	D	P1	T	5	P2 22
WHITE/ORANGE	D	P1	U	5	P2 23
WHITE/YELLOW	D	P1	V	5	P2 24
WHITE/GREEN	D	P1	W	5	P2 25

USE CHANGES TO  
 USER PLANTS:  
*Permitted Plans*

3 MARK ASSY NO. ON STRAP WITH "BOND-O-TOOL" STAMP.

2 FC TYPE PIN SPEC 1199 2076-

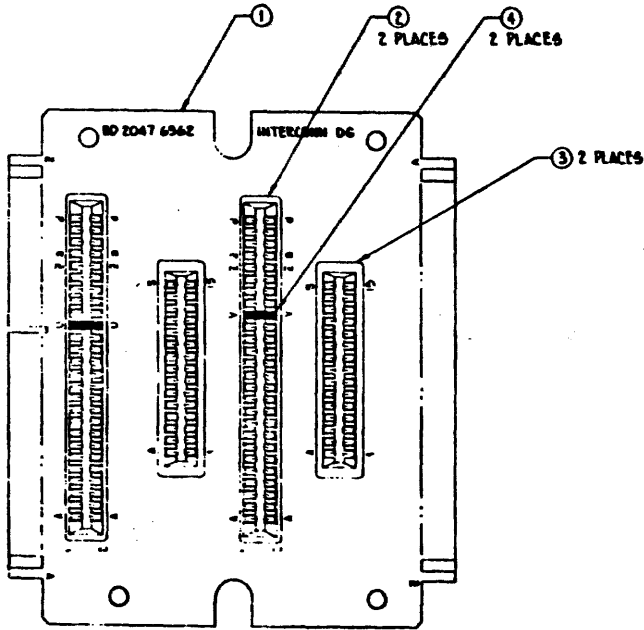
1 INDICATED WIRES ARE TWISTED PAIR.

NOTES: UNLESS OTHERWISE SPECIFIED

SEE P/L

Strathmore Corporation  
 WESTLAKE VILLAGE, CALIFORNIA 91380  
 CABLE ASSY, INTERNAL  
 D-2046 2008  
 C

REVISION  
DATE BY  
1 10/20/77  
3 10/20/77  
RELEASED



SEE P/L

D.A. *[Signature]* 8-28-77  
D.P. *[Signature]* 8-28-77

REV.	DATE	BY	DESCRIPTION
1	8-28-77	D.A.	INITIAL DESIGN
2	8-28-77	D.P.	REVISED DESIGN
3	8-28-77	D.P.	REVISED DESIGN
4	8-28-77	D.P.	REVISED DESIGN
5	8-28-77	D.P.	REVISED DESIGN
6	8-28-77	D.P.	REVISED DESIGN
7	8-28-77	D.P.	REVISED DESIGN
8	8-28-77	D.P.	REVISED DESIGN
9	8-28-77	D.P.	REVISED DESIGN
10	8-28-77	D.P.	REVISED DESIGN

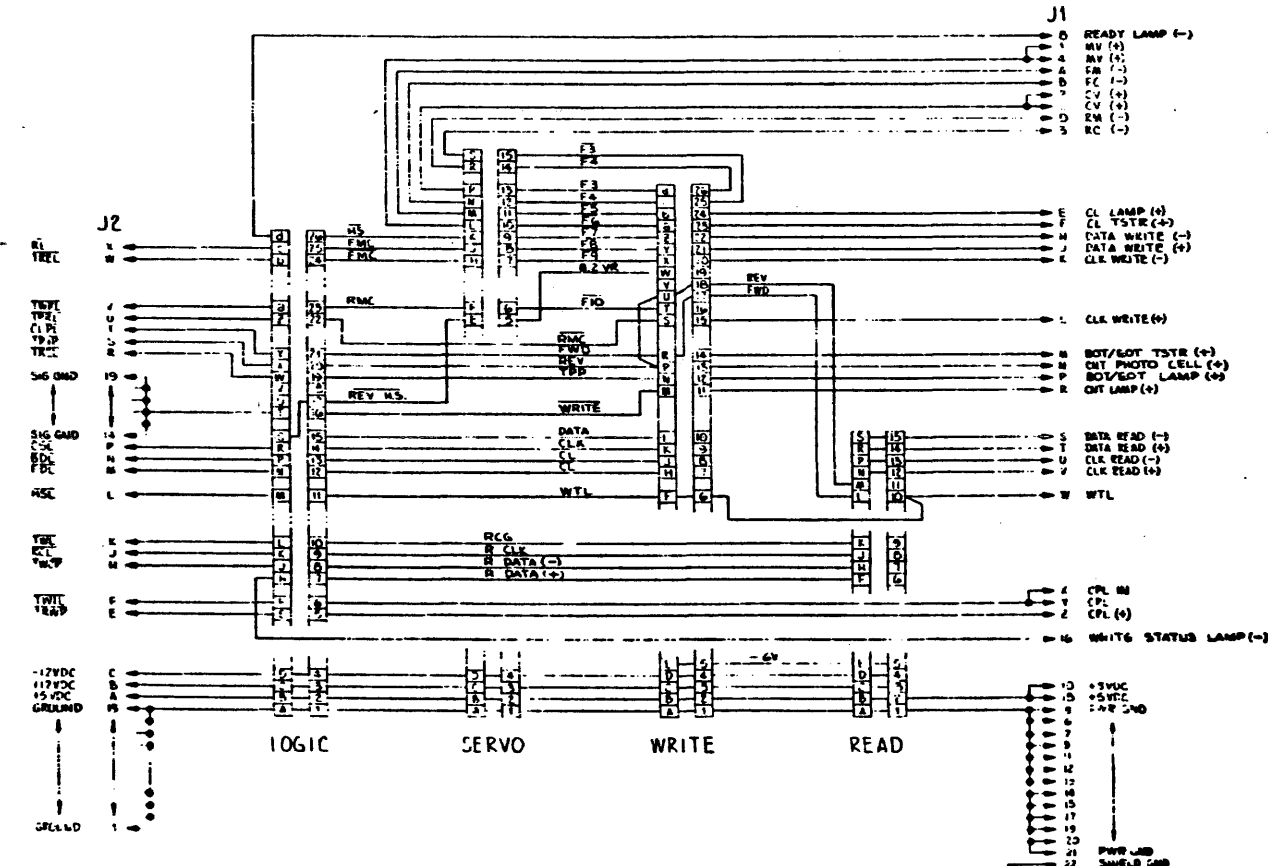
**Shaw-Walsh Corporation**

DESIGNED INCHES GROUP  
WESTLAKE VILLAGE CALIFORNIA U.S.A.

CD-INTERCONNECT DG

D-2047 6354 A

REVISED BY DATE AUTHORITY					
A 07/20/64 M.A.					
RELEASED					
RC	NO	IN	CH	BY	DATE
B	07/20/64	M.A.			
REVISIONS					
1	07/20/64	M.A.			
2	08/15/64	M.A.			
3	08/15/64	M.A.			
4	08/15/64	M.A.			
5	08/15/64	M.A.			
6	08/15/64	M.A.			
7	08/15/64	M.A.			
8	08/15/64	M.A.			
REASON FOR CHANGE BY CALLUHS					
C	08/15/64	M.A.			
CHANGE CONNECTIONS:					
DELETE					
- LOGIC PIN 22 TO LOGIC PIN 2					
- SERVO PIN 6 TO WRITE PIN 3					
- READ PIN 2 TO 24-56, 21-6					
ADD					
- LOGIC PIN 18 TO WRITE PIN 2					
- SERVO PIN 5 TO WRITE PIN 10					
- READ PIN 1 TO 24-56, 21-6					



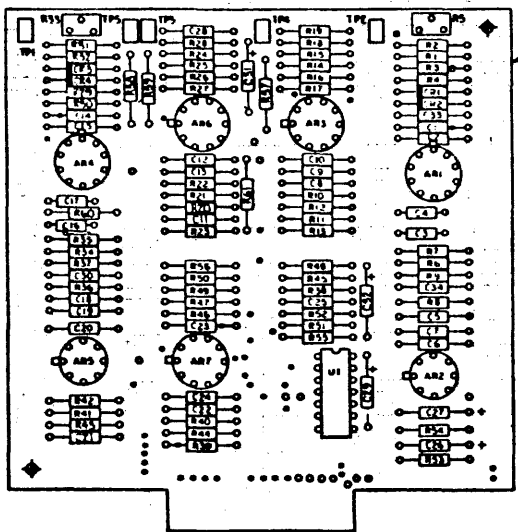
HOLE DIMENSIONS		MATERIAL	FINISH	TOLERANCES	STANDARD
SIZE	TOLERANCE				
Ø .0015	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0020	± .0005				
Ø .0025	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0030	± .0005				
Ø .0035	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0040	± .0005				
Ø .0045	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0050	± .0005				
Ø .0055	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0060	± .0005				
Ø .0065	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0070	± .0005				
Ø .0075	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0080	± .0005				
Ø .0085	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0090	± .0005				
Ø .0095	± .0005	ALUMINUM	MIL-C-13	.0005	MIL-STD-883C
Ø .0100	± .0005				

Barragato Corporation  
WYFLAKE HILL CALIFORNIA 95306  
SCHEM-INTERCONNECT DG D-2046 9527 C

D-2048 2253 A

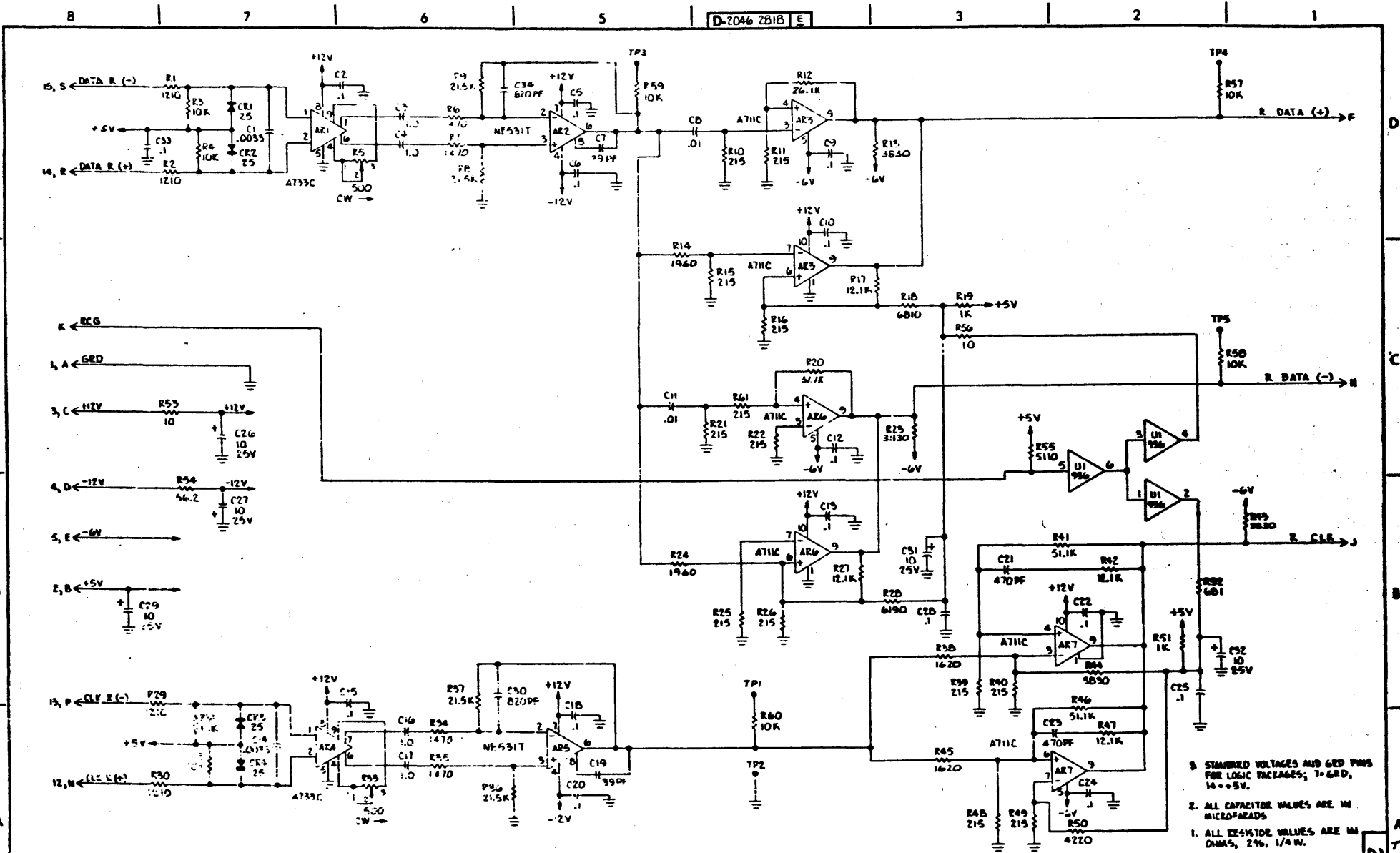
REVISIONS  
A  
RELEASED

ITEM NO	REFERENCE DESIGNATION
1	AR4
2	AR5
3	AR6
4	AR7
5	AR8
6	AR9
7	AR10
8	AR11
9	AR12
10	AR13
11	AR14
12	AR15
13	AR16
14	AR17
15	AR18
16	AR19
17	AR20
18	AR21
19	AR22
20	AR23
21	AR24
22	AR25
23	AR26
24	AR27
25	AR28
26	AR29
27	AR30
28	AR31
29	AR32
30	AR33
31	AR34
32	AR35
33	AR36
34	AR37
35	AR38
36	AR39
37	AR40
38	AR41
39	AR42
40	AR43
41	AR44
42	AR45
43	AR46
44	AR47
45	AR48
46	AR49
47	AR50
48	AR51
49	AR52
50	AR53
51	AR54
52	AR55
53	AR56
54	AR57
55	AR58
56	AR59
57	AR60
58	AR61
59	AR62
60	AR63
61	AR64
62	AR65
63	AR66
64	AR67
65	AR68
66	AR69
67	AR70
68	AR71
69	AR72
70	AR73
71	AR74
72	AR75
73	AR76
74	AR77
75	AR78
76	AR79
77	AR80
78	AR81
79	AR82
80	AR83
81	AR84
82	AR85
83	AR86
84	AR87
85	AR88
86	AR89
87	AR90
88	AR91
89	AR92
90	AR93
91	AR94
92	AR95
93	AR96
94	AR97
95	AR98
96	AR99
97	AR100
98	AR101
99	AR102
100	AR103



SEE P/L

DES. SEAL. SPEC. 1183 2043 AND TITLE BLOCK LIMITS APPLY UNLESS OTHERWISE SPECIFIED   SCALE: NONE		TOLERANCES:	
HOLE DIAMETER TOLERANCES:	DIMENSIONS:	FINISH:	TOLERANCES:
.015 - .030 .030 - .045 .045 - .060 .060 - .075 .075 - .090 .090 - .105 .105 - .120 .120 - .135 .135 - .150 .150 - .165 .165 - .180 .180 - .195 .195 - .210 .210 - .225 .225 - .240 .240 - .255 .255 - .270 .270 - .285 .285 - .300 .300 - .315 .315 - .330 .330 - .345 .345 - .360 .360 - .375 .375 - .390 .390 - .405 .405 - .420 .420 - .435 .435 - .450 .450 - .465 .465 - .480 .480 - .495 .495 - .510 .510 - .525 .525 - .540 .540 - .555 .555 - .570 .570 - .585 .585 - .600 .600 - .615 .615 - .630 .630 - .645 .645 - .660 .660 - .675 .675 - .690 .690 - .705 .705 - .720 .720 - .735 .735 - .750 .750 - .765 .765 - .780 .780 - .795 .795 - .810 .810 - .825 .825 - .840 .840 - .855 .855 - .870 .870 - .885 .885 - .900 .900 - .915 .915 - .930 .930 - .945 .945 - .960 .960 - .975 .975 - .990 .990 - 1.000	.010 .015 .020 .025 .030 .035 .040 .045 .050 .055 .060 .065 .070 .075 .080 .085 .090 .095 .100 .105 .110 .115 .120 .125 .130 .135 .140 .145 .150 .155 .160 .165 .170 .175 .180 .185 .190 .195 .200 .205 .210 .215 .220 .225 .230 .235 .240 .245 .250 .255 .260 .265 .270 .275 .280 .285 .290 .295 .300 .305 .310 .315 .320 .325 .330 .335 .340 .345 .350 .355 .360 .365 .370 .375 .380 .385 .390 .395 .400 .405 .410 .415 .420 .425 .430 .435 .440 .445 .450 .455 .460 .465 .470 .475 .480 .485 .490 .495 .500 .505 .510 .515 .520 .525 .530 .535 .540 .545 .550 .555 .560 .565 .570 .575 .580 .585 .590 .595 .600 .605 .610 .615 .620 .625 .630 .635 .640 .645 .650 .655 .660 .665 .670 .675 .680 .685 .690 .695 .700 .705 .710 .715 .720 .725 .730 .735 .740 .745 .750 .755 .760 .765 .770 .775 .780 .785 .790 .795 .800 .805 .810 .815 .820 .825 .830 .835 .840 .845 .850 .855 .860 .865 .870 .875 .880 .885 .890 .895 .900 .905 .910 .915 .920 .925 .930 .935 .940 .945 .950 .955 .960 .965 .970 .975 .980 .985 .990 .995 1.000	DESIGNED: <i>[Signature]</i> CHECKED: <i>[Signature]</i> DATE: 1/24/74	WESTLAKES PLANT WESTLAKES VILLAGE, CALIFORNIA 91385 © ASSY, NRZ READ   D-2048 2253 A

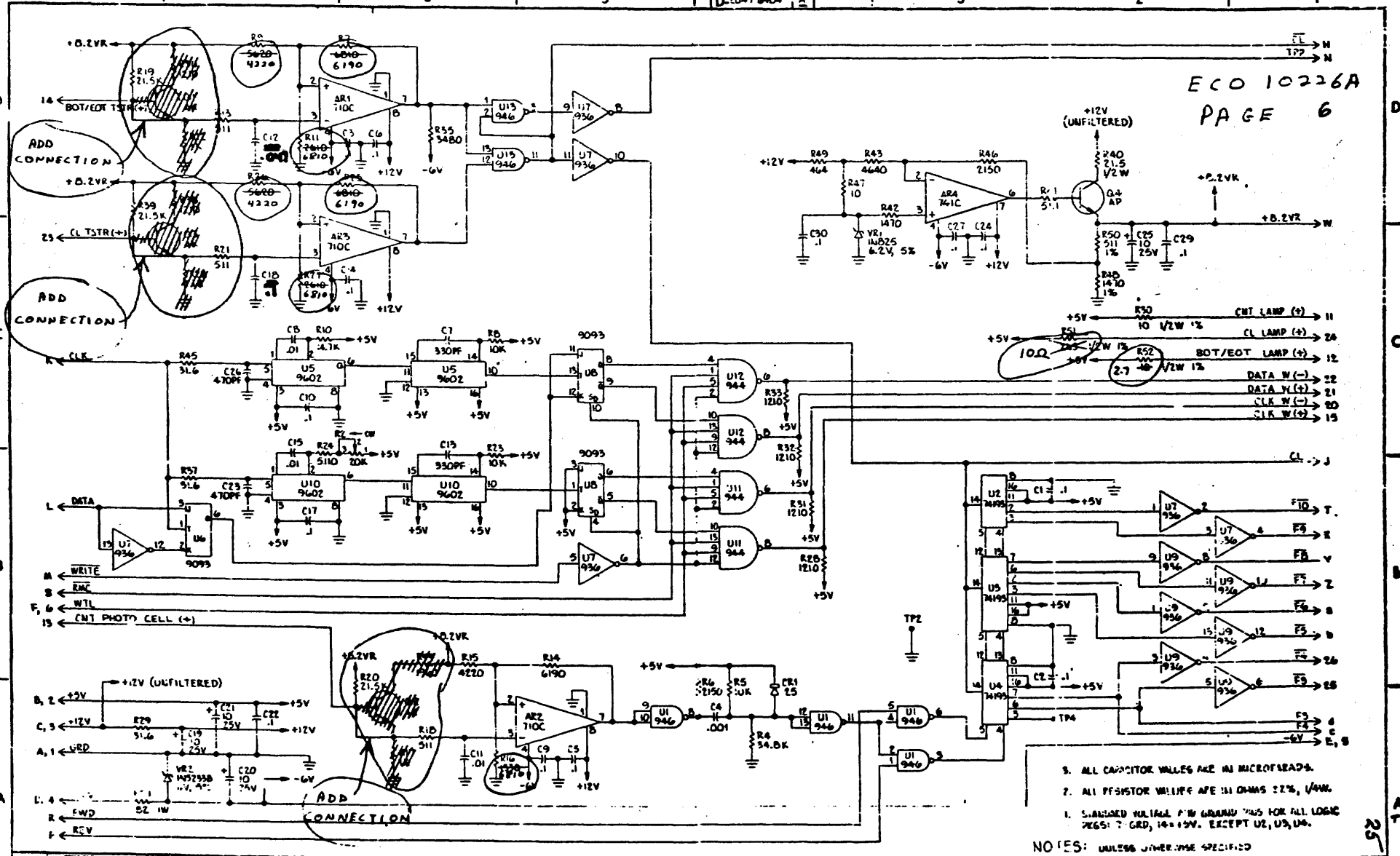


- 3. STANDARD VOLTAGES AND GND PINS FOR LOGIC PACKAGES; T-GND, 14--5V.
- 2. ALL CAPACITOR VALUES ARE IN MICROFARADS
- 1. ALL RESISTOR VALUES ARE IN OHMS, 2%, 1/4 W.

NOTES: UNLESS OTHERWISE SPECIFIED







3. ALL CAPACITOR VALUES ARE IN MICROFARADS.
2. ALL RESISTOR VALUES ARE IN OHMS 2%, 1/4W.
1. STANDARD VOLTAGE FOR GROUND TIES FOR ALL LOGIC DEVICES: 7: GRD, 14: +5V. EXCEPT U2, U3, U4.

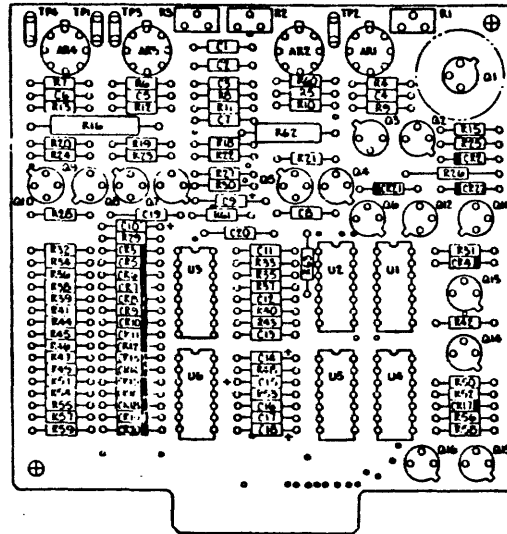
NOTES: UNLESS OTHERWISE SPECIFIED

REVISEMENTS  
 A 10000 ND  
 RELEASED - REPLACES  
 2048 5170 PER ECD 1042A

D-2026 3539 A

ITEM NO.	REFERENCE DESIGNATION
65	R6Z
66	QZ

ITEM NO.	REFERENCE DESIGNATION
2	U2, U4
3	U1
4	U3, U10
5	U5
6	AR1, AR2, AP3, AK4
7	C6
8	C1, C2, C3, C4, C5, C7, C8, C11, C12, C15, C16, C17, C19
9	C9
10	C14, C15, C18
11	CR3, CR4, CR5, CR6, CR7, CR8, CR9, CR10, CR11, CR12, CR13, CR14, CR15, CR16, CR17, CR18, CR19, CR20
12	
13	CR2, CR21, CR22
14	E1, E2, E4
15	E41
16	E45
17	E5
18	E52
19	E47
20	E44
21	E46, E59
22	E55, E57
23	E73
24	E51, E54
25	E58, E49
26	E34, E36
27	E53
28	E48, E59
29	E57, E45
30	E42, E52
31	E51, E50, E56, E58, E61
32	E27
33	E11
34	E4
35	E3
36	E35, E40, E15
37	E75, E50
38	E6
39	E13, E20
40	E12, E79
41	E7
42	E9, E19, E24, E59
43	E18
44	E11, E28
45	E10
46	E22
47	
48	E26
49	E16
50	Q4, Q6, Q7, Q7
51	Q1, Q11, Q12, Q13, Q14, Q15, Q16
52	Q5, Q8, Q10
53	Q3
54	TEST PAD FOR Q2, Q4, Q5, Q6, Q1, Q8, Q9, Q10
55	TEST PAD FOR Q1, Q3, Q11, Q12, Q13, Q14, Q15, Q16
56	TEST PAD FOR AR1, AR2, AP3, AK4
57	HEATSINK FOR Q1
58	TP1, TP2, TP3, TP4
59	E60
60	C10
61	E63
62	C20



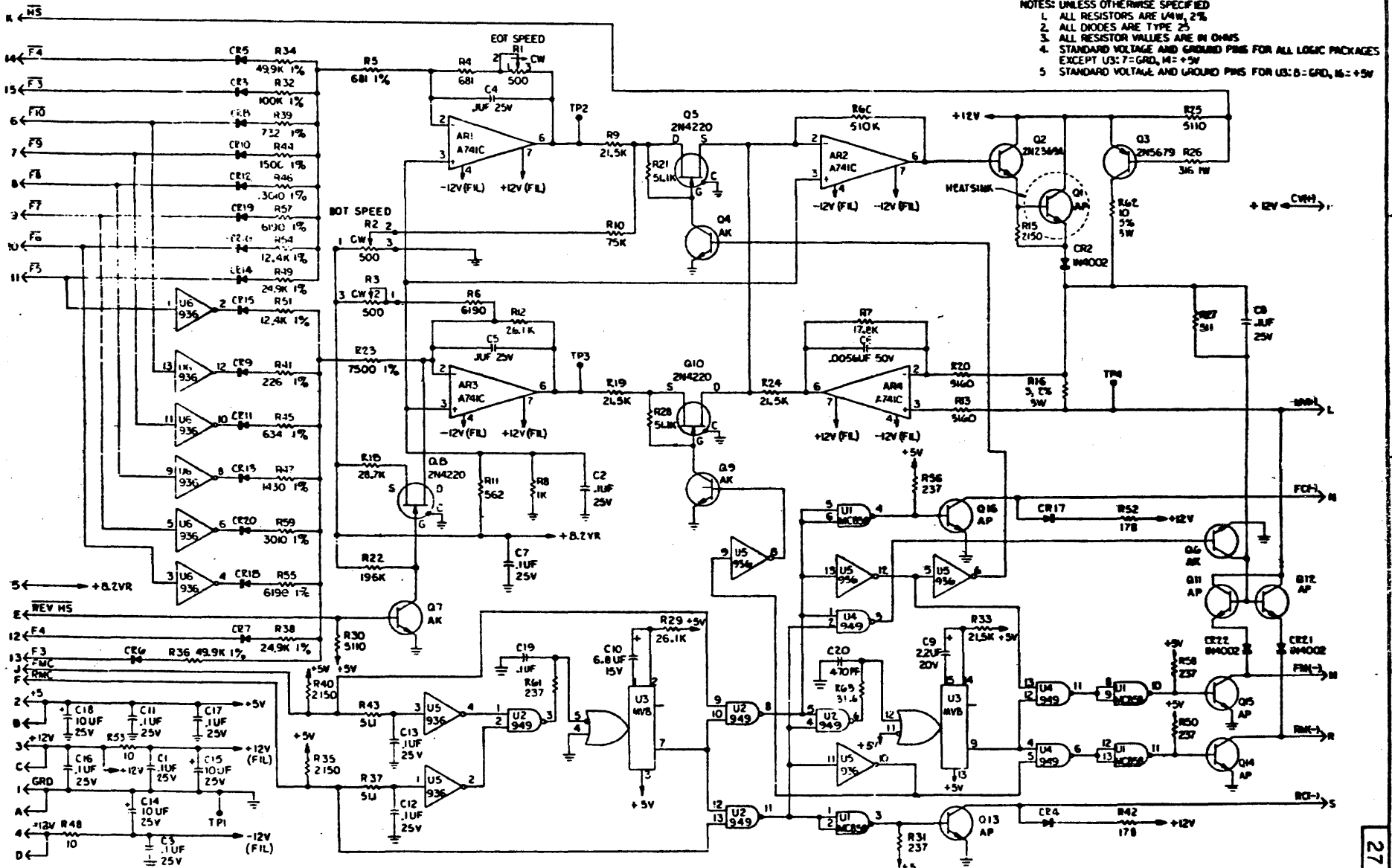
SEE P/L

DATE	BY	DESCRIPTION	APPROVED	DATE	BY	DESCRIPTION	APPROVED	DATE	BY	DESCRIPTION
6-14-74	Jerry Yonck	REVISED		6-5-74						

SUNSHINE CORPORATION		WESTLAKE PLANT	
WESTLAKE VILLAGE, CALIFORNIA 91361		U.S. AMERICA	
D-2026 3539 A		SERVO 10/30 IPS	

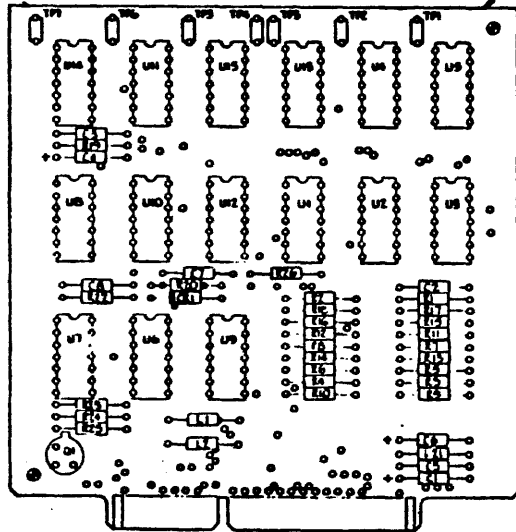
- NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE 1/4W, 2%
  2. ALL DIODES ARE TYPE 25
  3. ALL RESISTOR VALUES ARE IN OHMS
  4. STANDARD VOLTAGE AND GROUND PINS FOR ALL LOGIC PACKAGES EXCEPT U3: 7=GRD, 14=+5V
  5. STANDARD VOLTAGE AND GROUND PINS FOR U3: 8=GRD, 16=+5V



1. R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48	2. C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19	3. U1, U2, U3, U4, U5	4. AR1, AR2, AR3, AR4	5. Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11, Q12, Q13	6. TP1, TP2, TP3, TP4	7. HEATSINK	8. CR1, CR2, CR3, CR4, CR5, CR6, CR7, CR8, CR9, CR10, CR11, CR12, CR13, CR14, CR15, CR16, CR17, CR18, CR19, CR20, CR21, CR22, CR23, CR24, CR25, CR26, CR27, CR28, CR29, CR30, CR31, CR32, CR33, CR34, CR35, CR36, CR37, CR38, CR39, CR40, CR41, CR42, CR43, CR44, CR45, CR46, CR47, CR48	9. R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100	10. U6, U7, U8, U9, U10, U11, U12, U13, U14, U15, U16, U17, U18, U19, U20, U21, U22, U23, U24, U25, U26, U27, U28, U29, U30, U31, U32, U33, U34, U35, U36, U37, U38, U39, U40, U41, U42, U43, U44, U45, U46, U47, U48, U49, U50, U51, U52, U53, U54, U55, U56, U57, U58, U59, U60, U61, U62, U63, U64, U65, U66, U67, U68, U69, U70, U71, U72, U73, U74, U75, U76, U77, U78, U79, U80, U81, U82, U83, U84, U85, U86, U87, U88, U89, U90, U91, U92, U93, U94, U95, U96, U97, U98, U99, U100
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D-2047 1954 A

RELEASED



REF NO	REFERENCE DESIGNATION
3	U1, U2, U3, U11
4	U6, U7, U8, U9, U10
5	U4, U5, U13, U15
6	U12
7	U14
8	C1, C4, C6
9	C2, C3, C5, C8
10	C7
11	CE1
12	L1, L2
13	R1, R2, R3, R4, R11, R15, R16, R17
14	R5, R6, R8, R10, R12, R14, R18, R19
15	R20, R21, R26
16	R19
17	R22
18	R23, R25
19	TP1, TRW1, TP7
20	Q1
21	TEST PNT, Q1
22	R24

28

SEE P/L

Q.A. 115 (Rev. 2-22-75)  
REV. 11/1/75

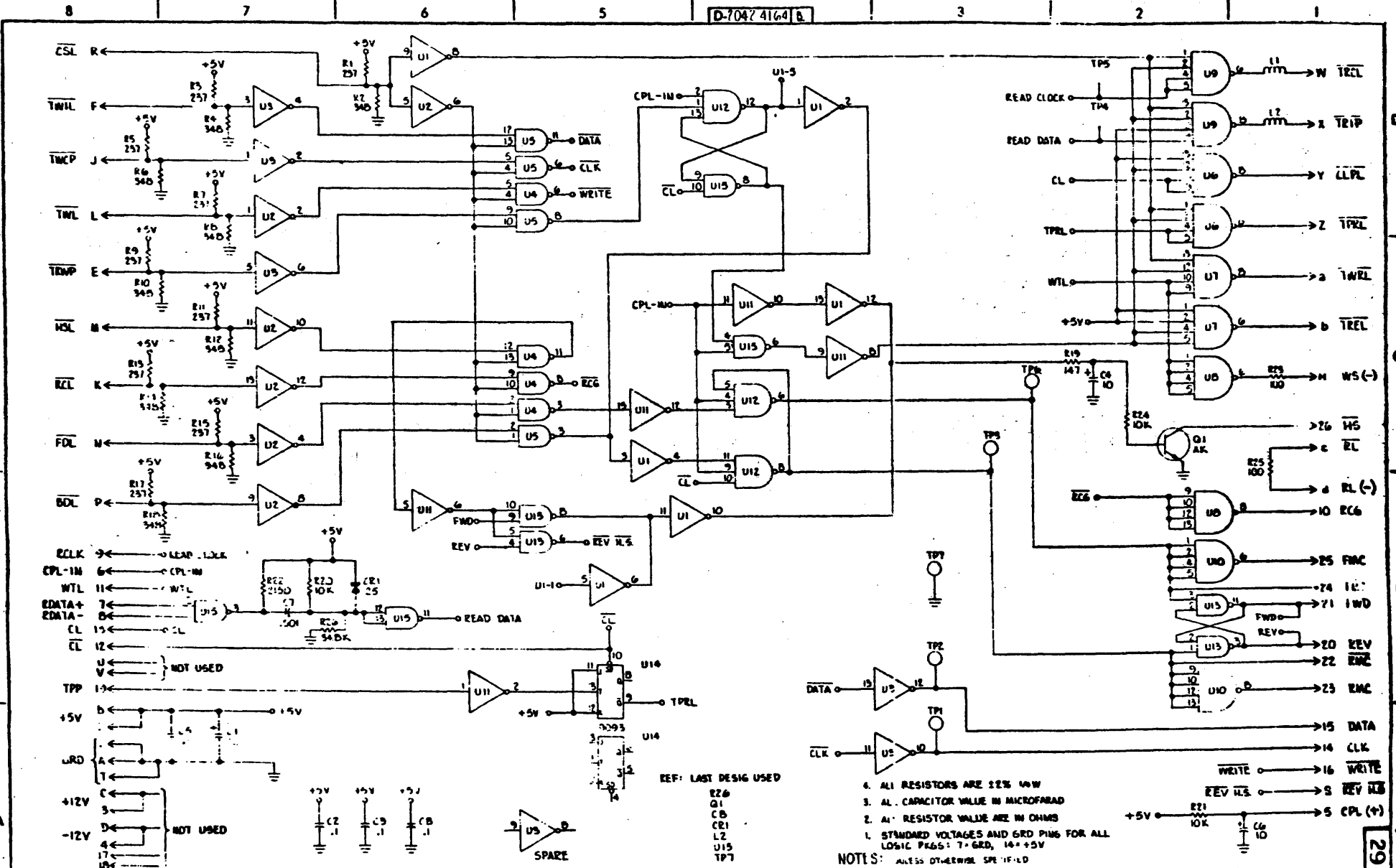
REV.	DATE	BY	CHKD.	DESCRIPTION
1	11/1/75	M.K.L.E.		ISSUED FOR FAB
2	11/1/75			
3	11/1/75			
4	11/1/75			
5	11/1/75			

**Stratronics Corporation**

DESIGNED BY: [Signature]  
 DRAWN BY: [Signature]  
 CHECKED BY: [Signature]

WESTLAKES VILLAGE, CALIFORNIA 91380

CD - LOGIC IC/90 II J D-2047 1954 A



REF: LAST DESIG USED

R22  
Q1  
C6  
C1  
L2  
U15  
TP7

- NOTES:
1. ALL RESISTORS ARE 1/2% 4W
  2. AL. CAPACITOR VALUE IN MICROFARAD
  3. A1. RESISTOR VALUE ARE IN OHMS
  4. STANDARD VOLTAGES AND GRD PINS FOR ALL LOGIC PKGS: 7+GRD, 14+5V UNLESS OTHERWISE SPE'IF-ED

REVISIONS

REV.	DATE	BY	DESCRIPTION
A	10-15-54	W.C.	RELEASED

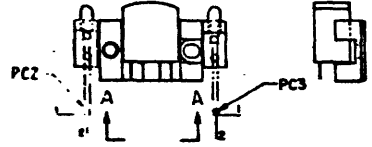
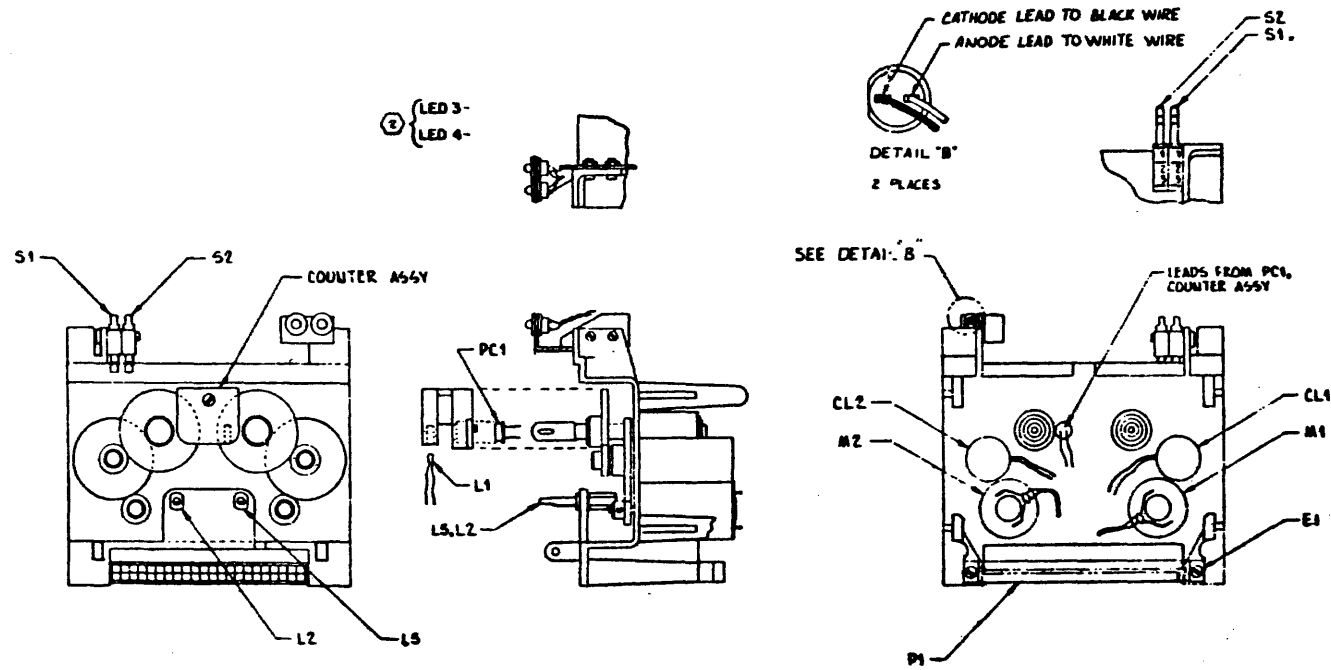
ADDED NOTE 2.

1. ADDED DETAIL "B" (B-3)  
 2. CHANGED MECHANICAL IN OF HEAD ASSY. LIST ON PHOTOCELL (B-4,7,8)  
 3. TITLE IS WIRING DIAGRAM - CASE PG. 43 - 44 - 45 - 46 - 47 - 48 - 49 - 50 - 51 - 52 - 53 - 54 - 55 - 56 - 57 - 58 - 59 - 60 - 61 - 62 - 63 - 64 - 65 - 66 - 67 - 68 - 69 - 70 - 71 - 72 - 73 - 74 - 75 - 76 - 77 - 78 - 79 - 80 - 81 - 82 - 83 - 84 - 85 - 86 - 87 - 88 - 89 - 90 - 91 - 92 - 93 - 94 - 95 - 96 - 97 - 98 - 99 - 100

4. ADDED PC3 (B-6) AND L5 (B-3) TO DIAGRAM AND WIRE LIST.

5. LED 3 AND LED 4 WERE L3 AND L4 (D-3).

6. NOTE 1 WAS FOR SYSTEM INSTALLATION AND BOTH WIRES FROM P1-E1 AND P1-E2 TO E1 MAY BE REMOVED DEPENDS ON SYSTEM REQUIREMENTS.



HEAD ASSY & POSITION PHOTOCELL  
SCALE: 2/1

FROM	TO	FROM	TO
S1-1	P1-19	LS-1	P1-3
S1-2	P1-2	LS-2	P1-E
S1-3	P1-W	PC3-1	P1-6
S2-1	P1-2D	PC3-2	P1-F
S2-2	P1-E		
S2-3	P1-V		
L1-1	P1-R		
L1-2	P1-14		
L2-1	P1-15		
L2-2	P1-P		
PC1-1	P1-N		
PC1-2	P1-12		
PC2-1	P1-11		
PC2-2	P1-M		
CL1-1	P1-C		
CL1-2	P1-3		
CL2-1	P1-D		
CL2-2	M-2		
M1-RED	P1-4		
M1-BLK	P1-D		
M2-RED	P1-A		
M2-BLK	P1-I		
E1	P1-21		
E1	P1-22		

FROM	TO	PART NO.
E-BLK	P1-5	
G-WHT	P1-7	B-2048 3363
SHIELD	P1-15	
F-R-11	P1-11	
M-A	P1-V	B-2048 3371
SHIELD	P1-11	
C-RED	P1-2	
A-ORN	P1-J	B-2048 3388
SHIELD	P1-7	
D-ORN	P1-K	
B-BRN	P1-L	B-2048 3335
SHIELD	P1-9	

① ITEMS INDICATED ARE OPTIONAL WITH THE B-46 2966.

② IN PANEL MOUNTING APPLICATIONS, STRAIN THE CONNECTION FROM P1-1 TO CVA (SEE NOTE 2). A SEPARATE GROUNDING BUSHING IS REQUIRED. CONNECTION E1 TO A SHIELD IS NOT NECESSARY. MUST BE ADDED BY USER.

NOTES:  
FOR USE WITH DUAL GAP HEAD

① EACH WIRE TO BE LUGGED SEPARATELY

- ② CAT LED3-BLK P1-B
- AN LED3-WHT P1-1B
- CAT LED4-BLK P1-16
- AN LED4-WHT P1-1D

WIRE NUMBER	WIRE COLOR	WIRE SIZE	WIRE TYPE
200	BLK	22	22
201	BLK	22	22
202	BLK	22	22
203	BLK	22	22
204	BLK	22	22
205	BLK	22	22
206	BLK	22	22
207	BLK	22	22
208	BLK	22	22
209	BLK	22	22
210	BLK	22	22
211	BLK	22	22
212	BLK	22	22
213	BLK	22	22
214	BLK	22	22
215	BLK	22	22
216	BLK	22	22
217	BLK	22	22
218	BLK	22	22
219	BLK	22	22
220	BLK	22	22

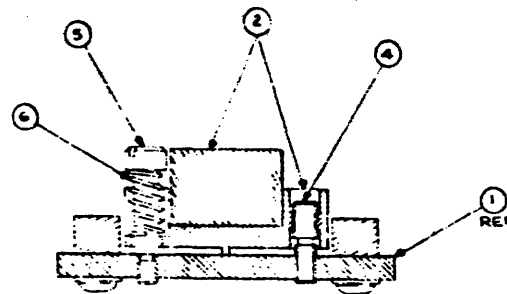
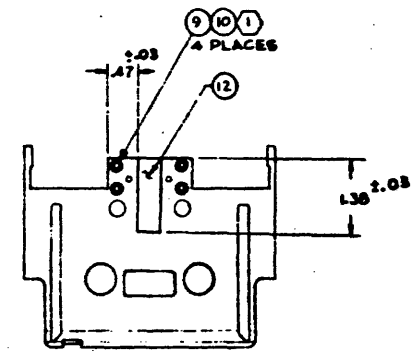
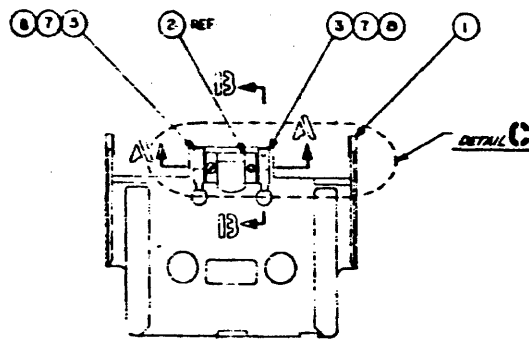
**Durrant Corporation**

BUSINESS MACHINES GROUP  
 BESTLAKE VILLAGE LAL, CHICAGO, ILL.

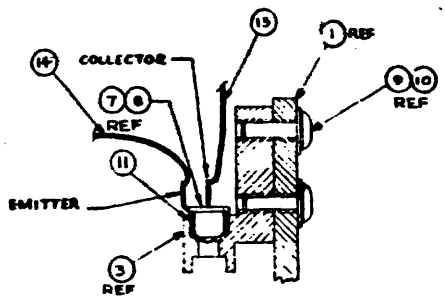
WIRING DIAGRAM - CASE PG. | D-2042 3893 E

D-20269213 A

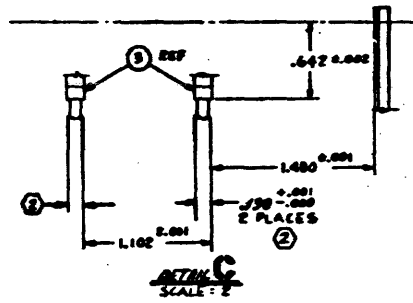
RELEASED



SECTION SCALE: 4



SECTION SCALE: 4  
2 PLACES



SCALE: 2

- NOTES:
- (2) TAPE GUIDE FINGERS OF ITEM (3) TO BE WITHIN INDICATED AMB.
  - (1) TORQUE TO 3 1/2 IN.-LBS.

SEE P/L

REV.	DATE	BY	CHKD.	DESCRIPTION	SCALE
1					
2					
3					
4					
5					
6					
7					
8					

DuPont Corporation  
 WESTLAKE VILLAGE, CALIFORNIA 91361  
 CARRIAGE, HD ASSY DG D-2026 9213 A



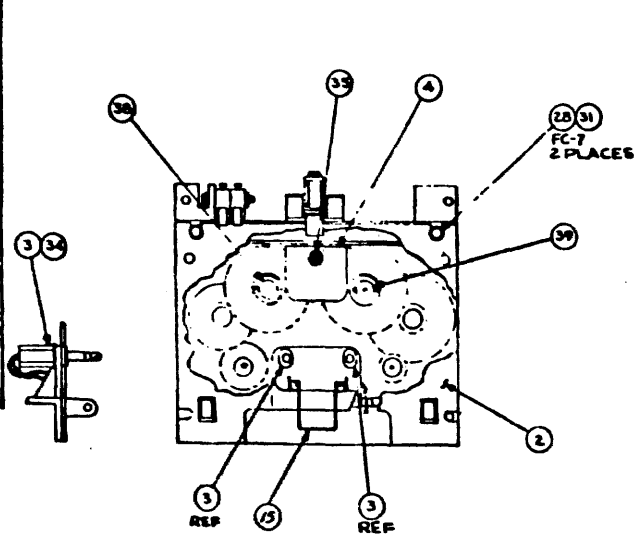
D-20462933

**RELEASED**

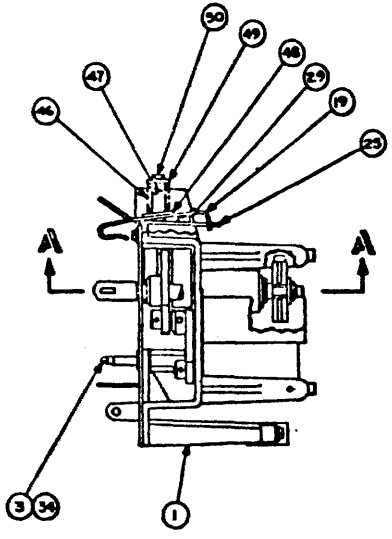
1. FC-7 2 PLACES WAB  
 FC-9 2 PLACES (D-SL)  
 & DELETED ITEM  
 CALLOUT (D-SL)

2. CHANGED VIEW (C-4  
 AND D-4) (REVISION  
 EXTENDING TO CHASSIS  
 BRACKET)

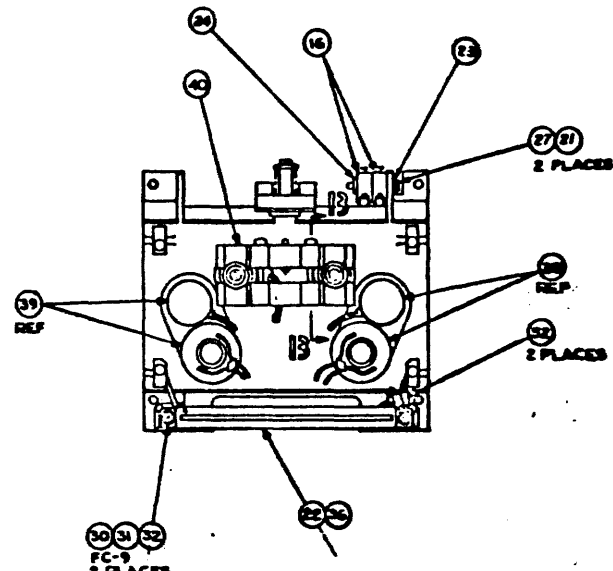
3. APPROVED FROM PSC-4  
 31 AND 32 (C-4)



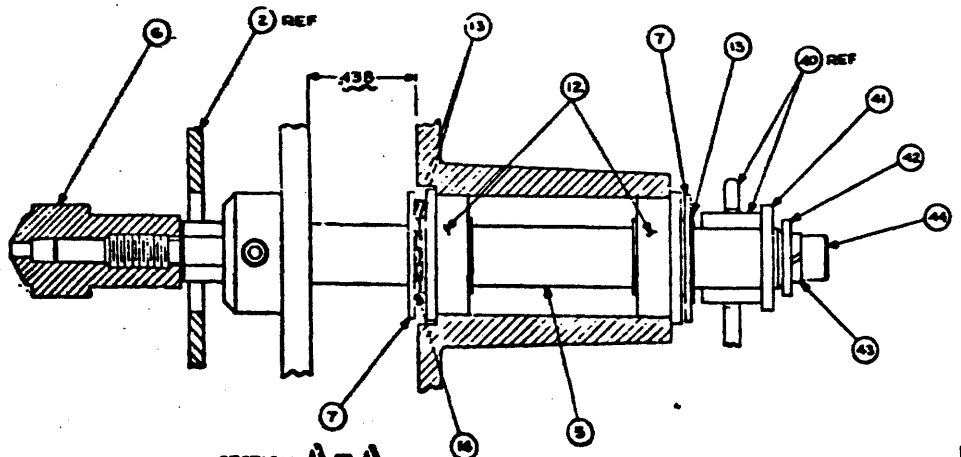
FRONT VIEW



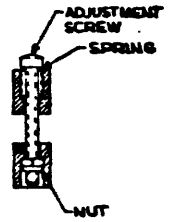
SWITCHES, ITEM 16,  
 NOT SHOWN THIS VIEW



REAR VIEW



SECTION A-A  
 SCALE: 4  
 2 PLACES



SECTION B-B  
 SCALE: 2


1. ASSEMBLE PER BURROUGHS SPEC A-1199 2096.  
 NOTE:

SEE P/L A

REV	DATE	BY	CHKD	DESCRIPTION	REASON	APPROVED
1	10-26-71	ADAMS	MARTIN	ISSUED FOR PRODUCTION		
2	11-18-71	SHAY		REVISION TO CHASSIS BRACKET		

GA: BAC (C-4)  
 MFB: C-4 (11-17)

BURROUGHS CORPORATION	WESTLAKES PLANT	U.S. AMERICA
CHASSIS ASSY, CAB M		D-2046 2933

<b>Burroughs Corporation</b> COMPUTER SYSTEMS GROUP DOWNINGTOWN PLANT DOWNINGTOWN, PENNSYLVANIA 19335		<small>PROPRIETARY TO BURROUGHS          CORP. NOT TO BE REPRODUCED OR          USED FOR MANUFACTURING          PURPOSES EXCEPT ON BURROUGHS          ORDER OR PRIOR WRITTEN          CONSENT.</small>	<b>NUMBER</b> 2608 1869	<b>REV</b> A
		<b>TITLE</b> ACU/HDB WIRE STRAP MODULE MODIFICATION SPECIFICATION		
<b>PREPARED BY</b> J. LOVRENCEVIC <i>J. Lovrenovic</i>	<b>APPROVED BY</b> L. SHAPIRO <i>LSM</i> H. B. MARX <i>HBM</i>	<b>ORIGINAL RELEASE DATE</b> 5-25-76	<b>PAGE 1 OF 4</b>	

REVISIONS			
LEVEL	DESCRIPTION	DATE	APPROVED
A	Initial Release <i>ER91REV54</i>		

2608 1869