

B 7000

MCP

EP 4055

CLASS HANDOUT

9/79

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COURSE OUTLINE

<u>DAY</u>	<u>TOPIC</u>	<u>MATERIALS</u>
1	Handouts	1. B7000 Advanced System Support Student Materials (EP 6132-1) 2. B7700 Systems Reference Manual (1060233) 3. B7000/B6000 Excerpts for Pocket Reference (1083136) 4. B7000/B6000 ESPOL Information Manual (5000094) 5. B7700 MCP Mark II.9 Student Text (EP 6132-3)
	B7700 Review	1. Start/Terminate I/O Flowchart (IO1-IO6)
	Homework Assignment	1. Hardware Quiz (HQ1-HQ3)
2	Review Hardware Quiz ESPOL	- - - - - 1. OBJECT/BRUN (OBJ1-OBJ2) 2. CODE/STUFF (CDS1-CDS2) 3. BUZZ (BZ1)
	Homework Assignment	1. ESPOL Quiz (EQ1-EQ3)
3	Review ESPOL Quiz BOOTSTRAP CODE	- - - - - 1. Disk Bootstrap Code (DBC1-DBC2) 2. CODE (C1)
	Handouts GETITGOING	1. MCP Listings* - - - - -

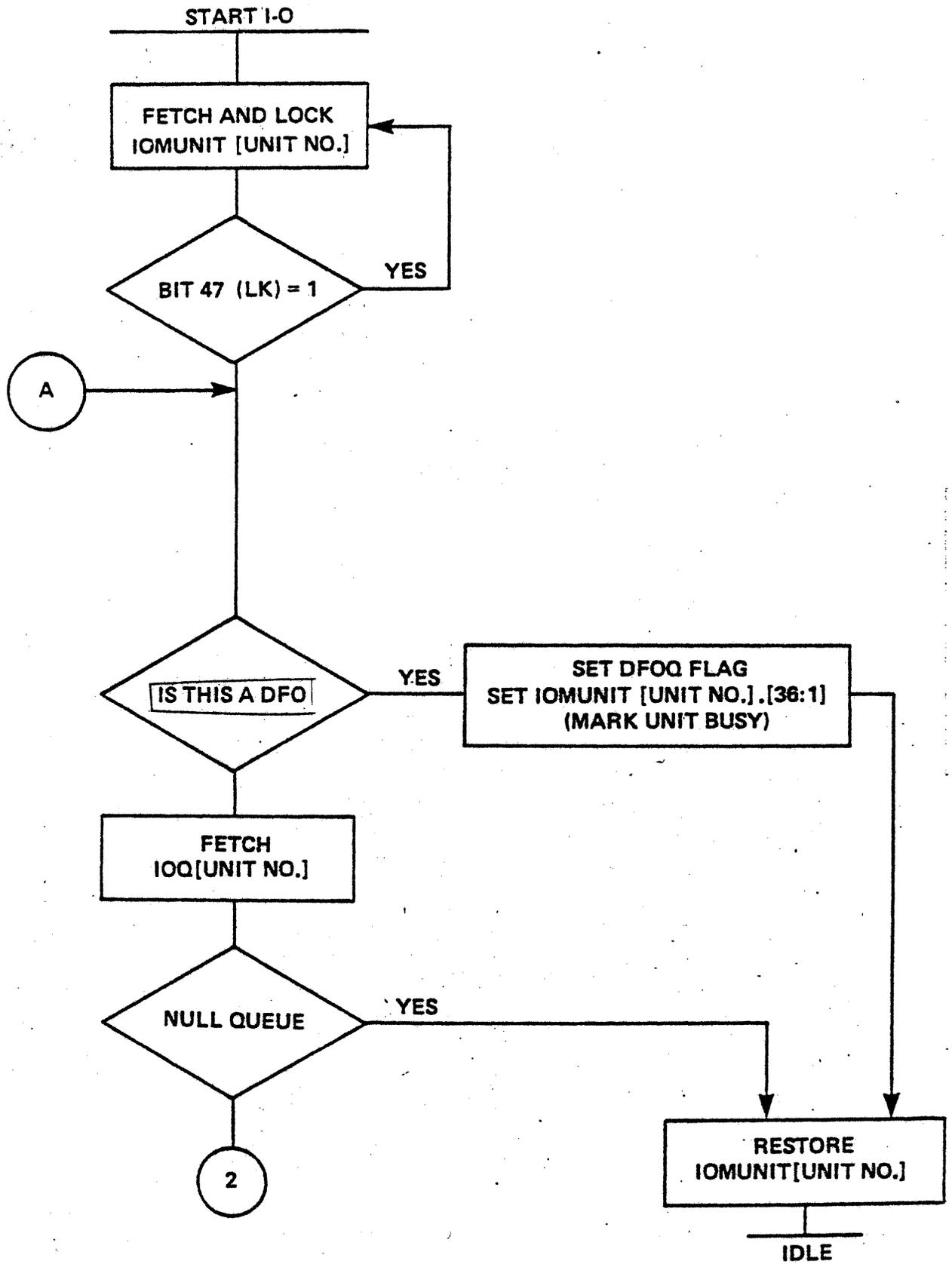
\*MCP listings include the following portions:

- GLOBALS
- FIBSTACK Procedures
- Process Control Procedures
- Event Oriented Procedures
- Process Communications Procedures
- SOPHIA
- Independent Runner Procedures
- Memory Management
- Initialization Code
- Overlay File Management
- KANGAROO, PROCESSKILL, INTERLOC
- BLOCKEXIT

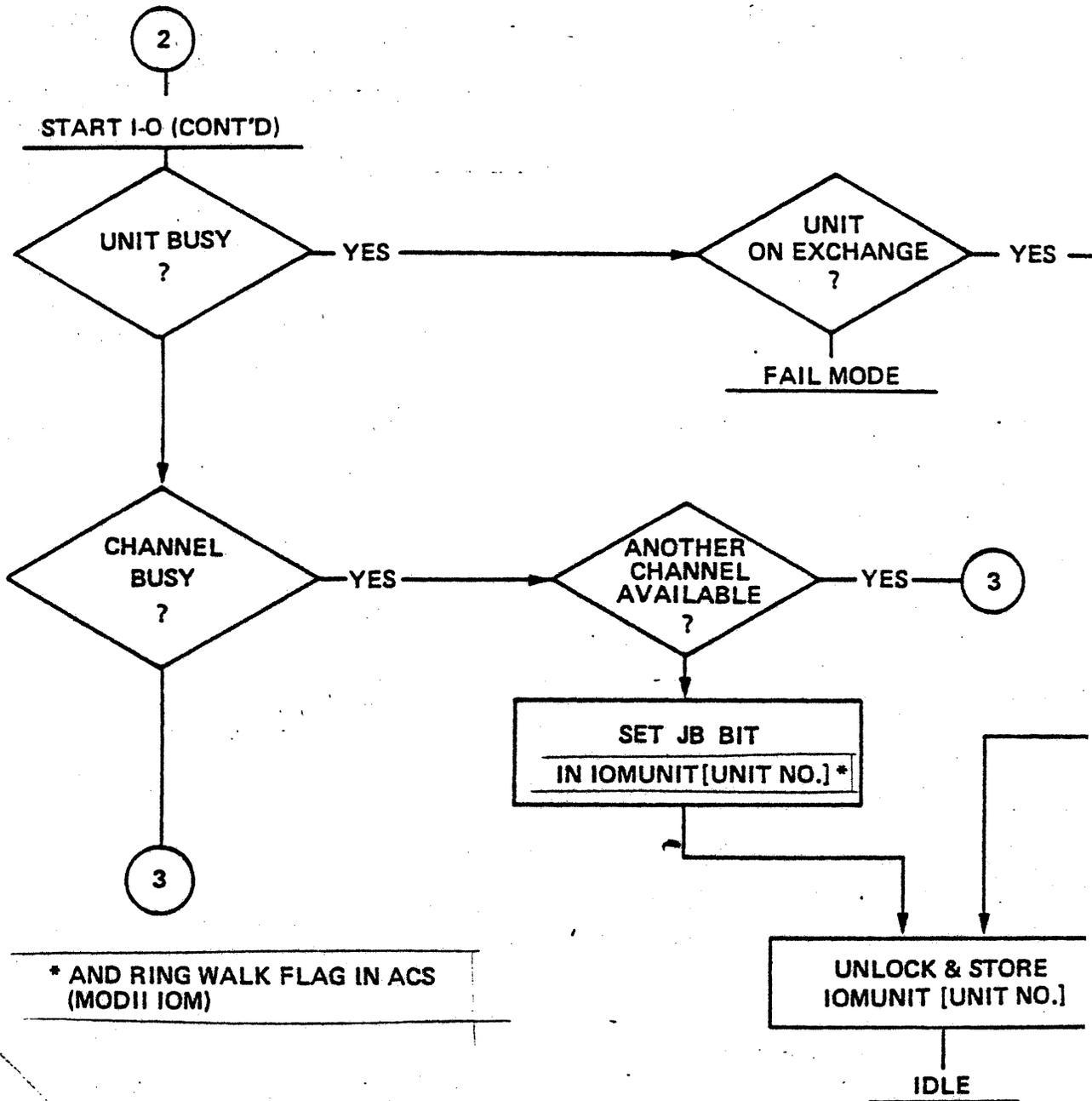


## COURSE OUTLINE (Cont.)

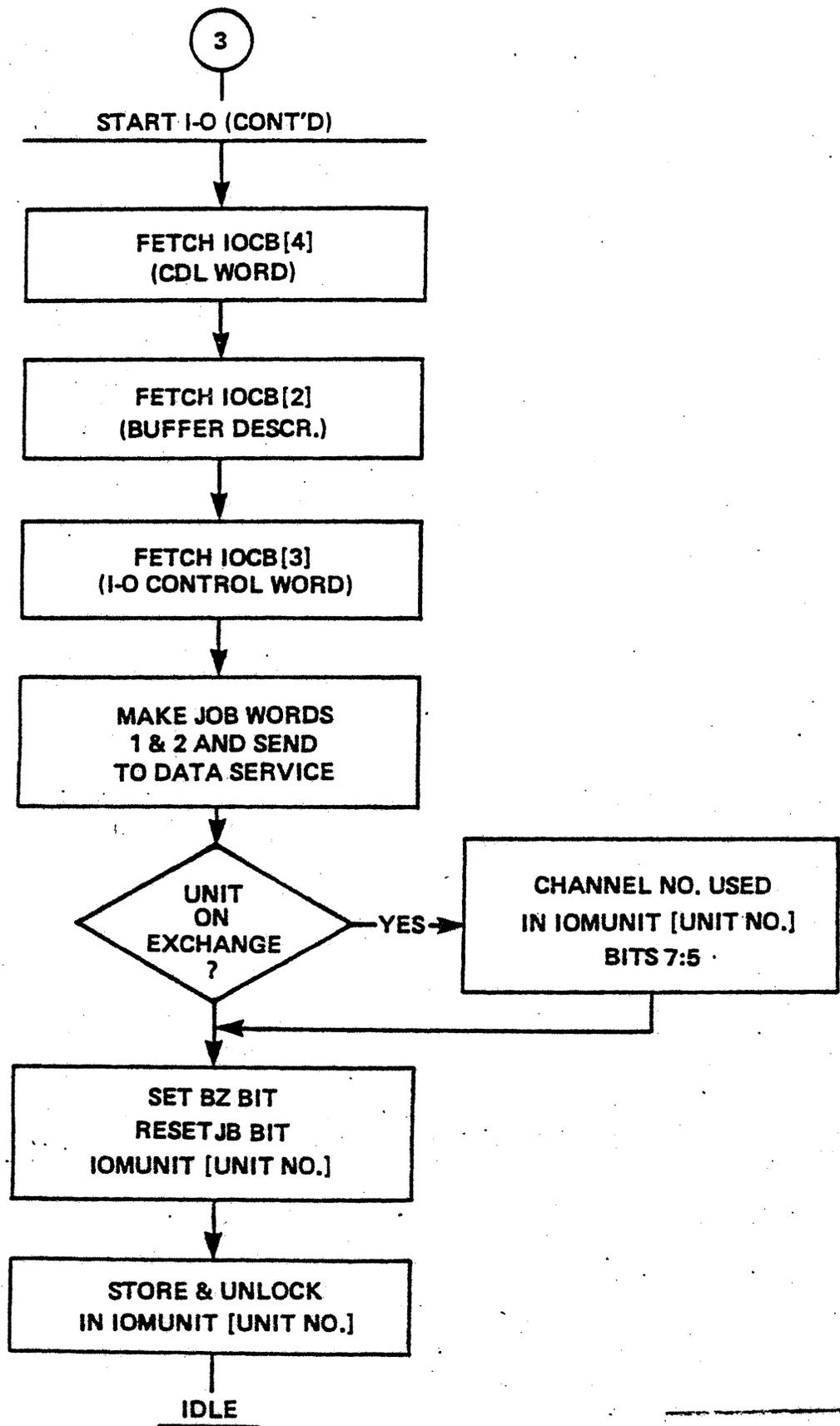
<u>DAY</u>	<u>TOPIC</u>	<u>MATERIALS</u>
7	Handout ALGOL PROCESS CODE DOCTOR INITIATE Lab (4-5 hours)	1. IPC/STUFF (IPS1-IPS3) -
8	BOJ EOJ Lab (4-5 hours)	- - - - - - - - - - - - - - -
9	KANGAROO PROCESSKILL INTERLOOPER WSSHERRIFF Lab (4-5 hours)	- -
10	Handout  FINDOLAYSPACE LOSEOLAYSPACE FIBSTACK  Handout	1. FINDOLAYSPACE/ LOSEOLAYSPACE (FLS1)    1. FIBSTACK/INFO (FSI1-FSI2) 2. FIBSTACK/INFO Program Dump (FID1-FID3)  1. INTX/X (INT1-INT3)

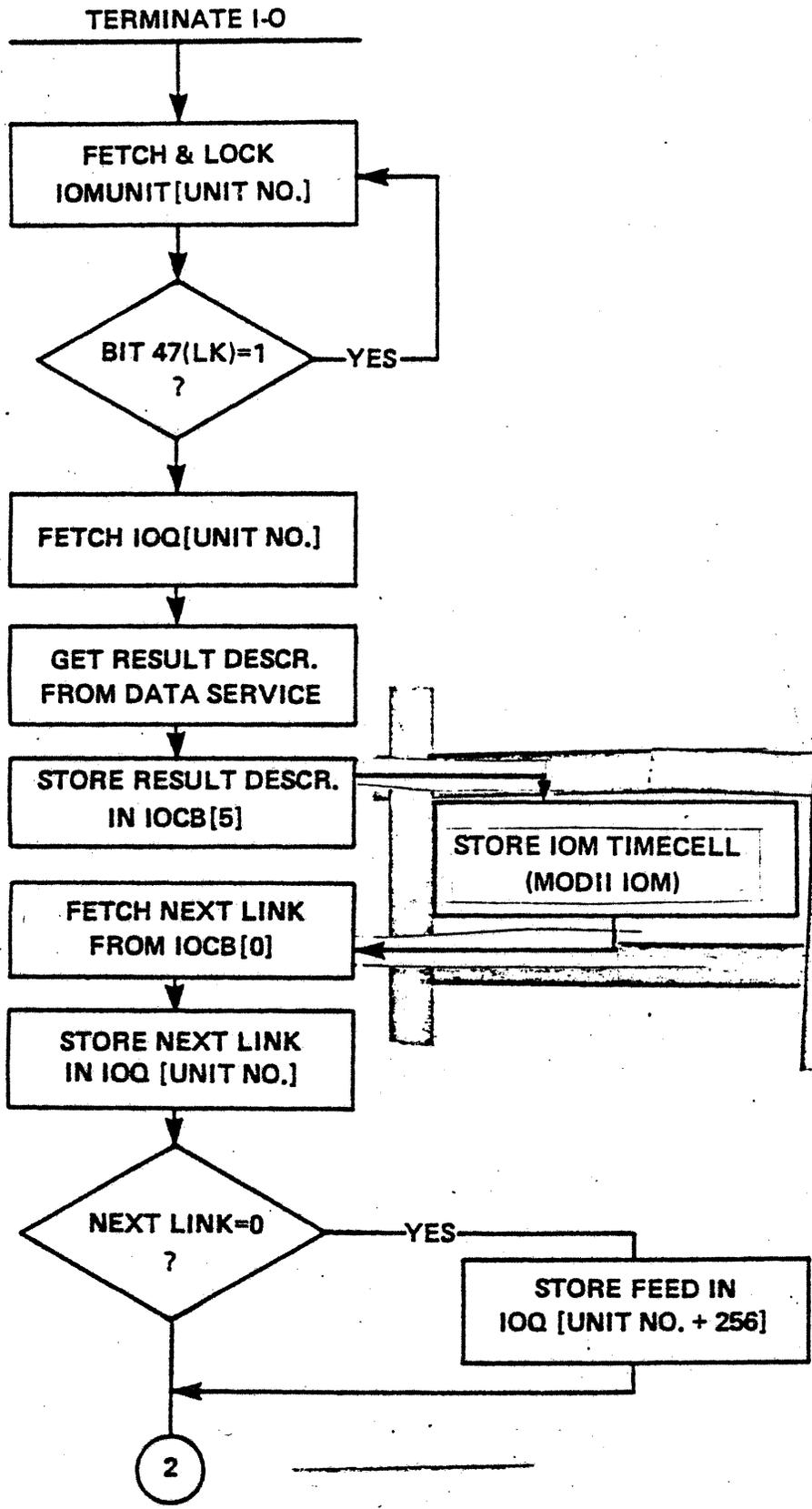


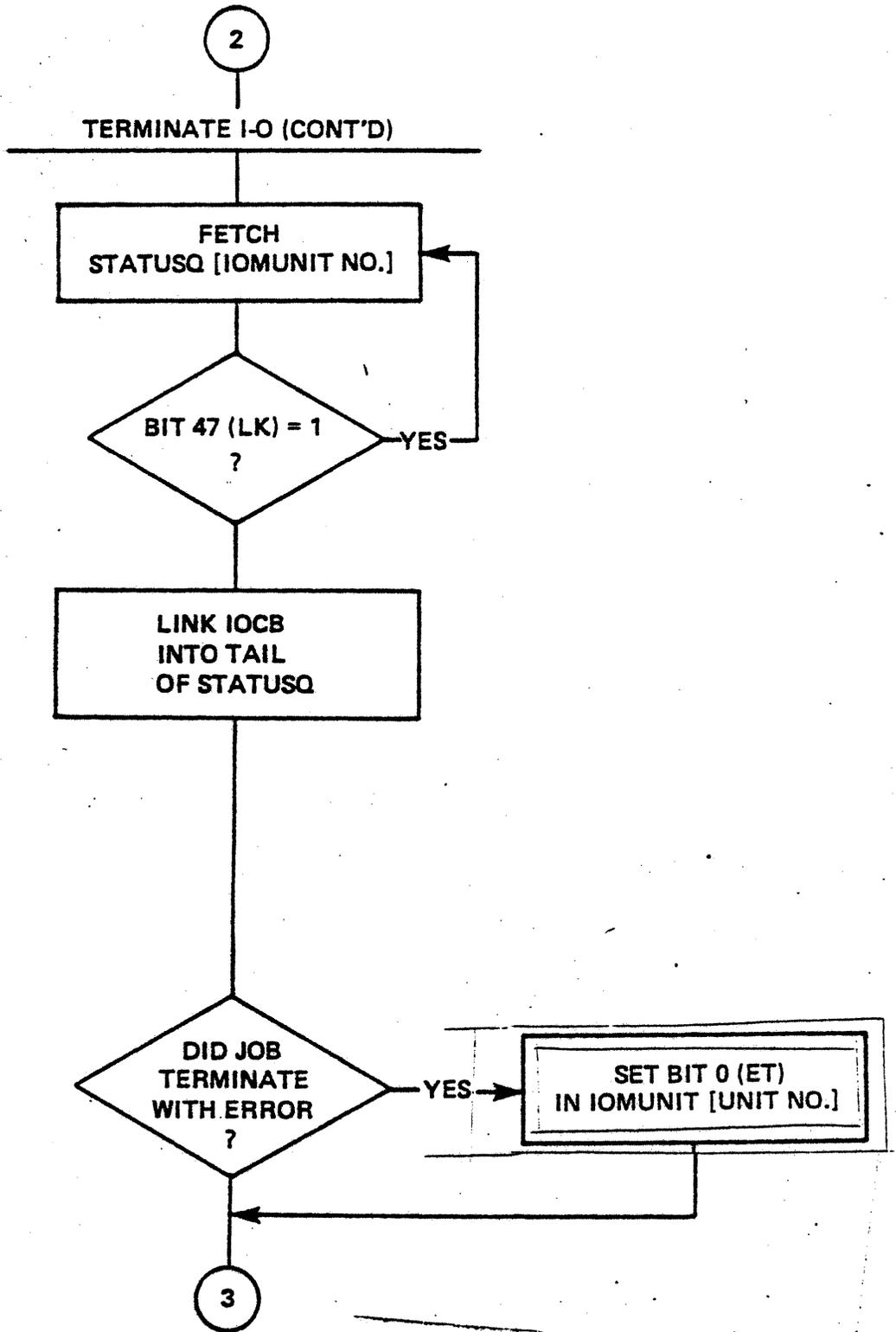
Start I/O Terminate I/O Flowchart (Sheet 1 of 6)

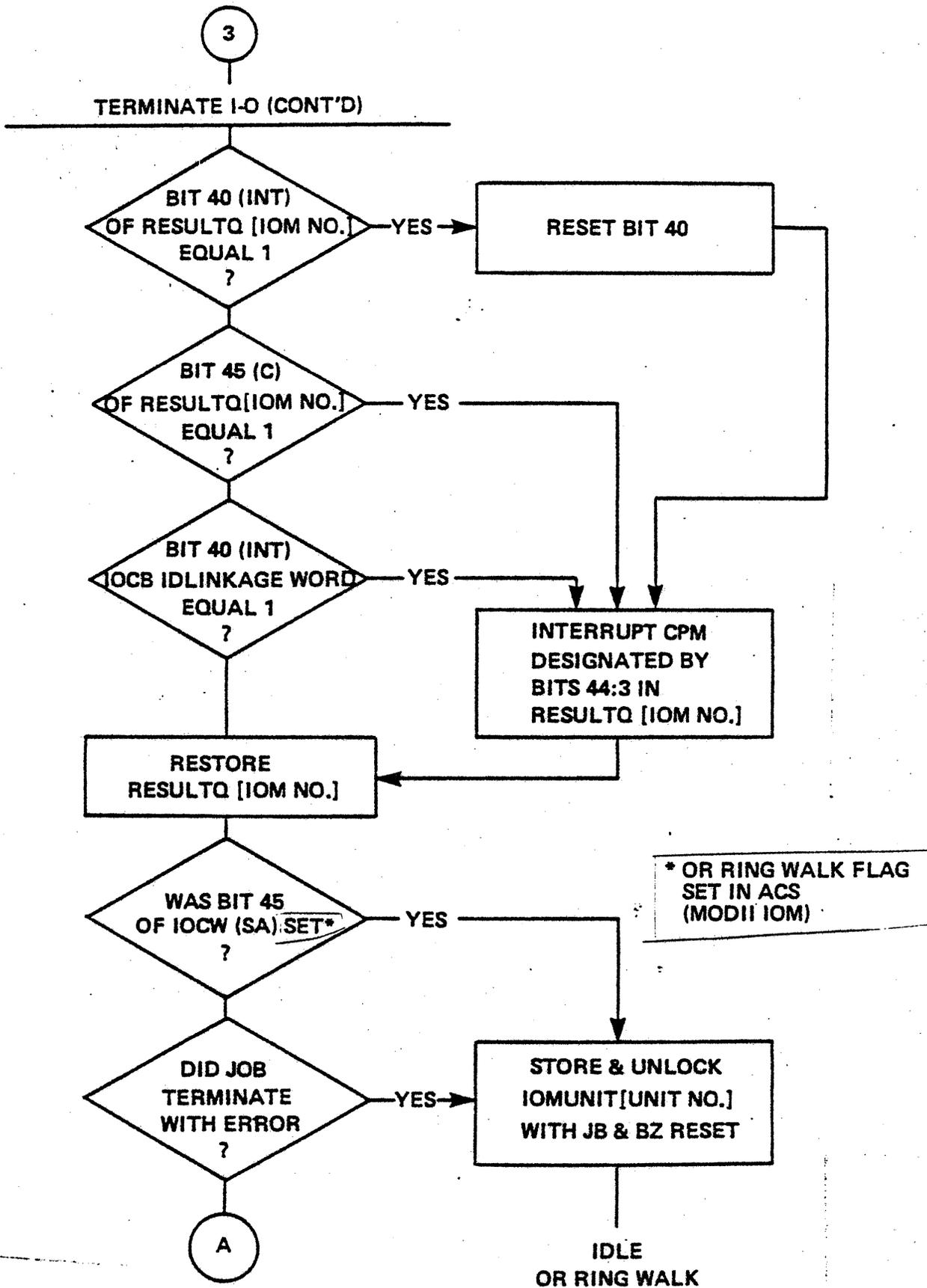


\* AND RING WALK FLAG IN ACS (MODII IOM)









## HARDWARE QUIZ

1. A B7700 System may include a maximum of \_\_\_\_\_ Central Processors and I/O Processors in any combination, and may include a maximum of \_\_\_\_\_ Memory Modules. The maximum addressable memory for a Logical system however is \_\_\_\_\_.
2. (T) (F) The B7700 Central Processors operate at a basic clock frequency of 16 Mhz and 8 Mhz; the I/O Processors and Memory operate at 8 Mhz.
3. (T) (F) Differences between the B6700 and B7700 machine language instruction sets require that B6700 programs be re-compiled to run on the B7700.
4. (T) (F) The master system clock obtains its power from the power regulators or Memory Control Module zero.
5. (T) (F) Peripheral Controllers and Disk Controllers for the B7700 are identical to B6700 Peripheral Controllers. These components are housed in B6700 style Peripheral Control Cabinets.
6. (T) (F) The Memory Limits Registers prevent memory accesses by specified requestors.
7. (T) (F) "Phasing" allows 4 words to be transferred to or from memory in parallel (one clock time).
8. (T) (F) Error check bits allow correction of all one-bit and two-bit memory errors.
9. (T) (F) A memory requestor sends an access request to all Memory Controllers on the system. Only that controller which controls the address requested will respond to the request.
10. (T) (F) If a memory requestor makes a memory request for more words than a Memory Module can send (or receive) in one "burst" the requestor must keep count of the number of words it receives (or sends) and keep repeating the access request until the word count is satisfied.
11. What is the maximum number of Peripheral Control Cabinets that can be connected to a B7700 IOM? \_\_\_\_\_
12. What is the maximum number of peripheral units that can be connected to a B7700 system? \_\_\_\_\_
13. Are DCP's included in the above count? \_\_\_\_\_

HARDWARE QUIZ (Cont)

14. Can two DFI data channels of an IOM be connected to one PC cabinet and the other two channels of the same DFI be connected to another PC cabinet? \_\_\_\_\_
15. What is the maximum number of words that an IOM will request from Memory? \_\_\_\_\_
16. Does "scan in peripheral status" use the scan bus? \_\_\_\_\_
17. When the MCP causes an IOM to interrogate peripheral status, what information is delivered to the MCP? \_\_\_\_\_
18. Where is the above information placed so that the MCP knows where it is? \_\_\_\_\_
19. When a B7700 system has more than one IOM, does the MCP have to give more than one IOM the "interrogate peripheral status" command in order to get the status of all units? \_\_\_\_\_
20. What is the significance of requestor number on the MCM? \_\_\_\_\_
21. Why is memory limited to one million words? \_\_\_\_\_
22. What is the maximum number of Peripheral Controls that can be connected to an IOM? \_\_\_\_\_
23. What is the maximum number of Peripheral Controls that can be placed in one PC cabinet? \_\_\_\_\_
24. Between what modules does the Scan Buss connect? \_\_\_\_\_
25. Does "interrogate peripheral status" make use of the "IOMUNIT" or "IOQ" arrays? \_\_\_\_\_
26. Name one thing that can cause a status change interrupt. \_\_\_\_\_
27. What prevents both the MCP and an IOM from changing an I/O queue linkage word at the same time? \_\_\_\_\_
28. What prevents both the MCP and an IOM from changing a status queue link word at the same time? \_\_\_\_\_
29. When two IOM's have paths to the same unit, which IOM will the MCP select to do an I/O operation on the unit? \_\_\_\_\_
30. When doing asynchronous I/O, how does an IOM know which CPM to interrupt? \_\_\_\_\_

HARDWARE QUIZ (Cont)

31. What would the MCP get back from a "BUZZ47" on a unit table word if an IOM has just lock fetched that word? \_\_\_\_\_  
\_\_\_\_\_
32. What prevents two CPM's from giving the same IOM different commands? \_\_\_\_\_  
\_\_\_\_\_
33. How does the MCP know whether or not an IOM has responded to the last HA command that was given to it? \_\_\_\_\_  
\_\_\_\_\_

NEW SYMBOLIC: NEWTAPE ON OLDSYMPAC.

\$\$\$ NEW LIST STACK  
\$\$\$ CODE  
\$\$\$ SIZE  
\$\$\$ BEGIN

{01:00003} = SEGMENT DESCRIPTOR

000:0000:0  
000:0000:0  
000:0000:0  
000:0000:0

9.0000 IS SEGMENT 0000

(02:00002) = R REAL R# FF  
BOOLEAN R# 1 00003000 003:0000:1  
(02:00003) = B PROCEDURE P# 00004000 003:0000:1  
(02:00004) = P PROGRAMDUMP# 00005000 003:0000:1  
00006000 003:0000:1

(01:00004) = SEPARATE INTRINSIC

003:0000:1 MKST AE  
003:0000:2 NAMC (01:00004) 6004  
003:0000:3 LFA 9202  
003:0000:4 ENTP AG  
003:0000:5 EXIT A3  
\*\*\*\*\* STACK BUILDING CODE FOR LEVEL 03 \*\*\*\*\*  
IF NOT B THEN \*\*\*\*\*  
003:0001:2 VALC (02:00003) 1003  
003:0001:4 LNOY 92

P

003:0001:4 00000000 003:0001:4  
003:0002:1 MKST AE  
003:0002:2 NAMC (02:00004) 5004

ELSE

003:0002:4 ENTR AB  
003:0003:4 BRTR-LINK 0000:0 A10003  
003:0003:5 BRUN-LINK 0300:0 A23003  
003:0003:2 VALC (02:00002) 1002  
003:0003:4 ONE 91

R := R + 1

003:0003:5 ADD 80  
003:0004:0 NAMC (02:00002) 5002  
003:0004:2 STOD 98  
003:0004:4 BRTR 0003:2 A14003

R := R + 1

003:0004:3 VALC (02:00002) 1002  
003:0004:5 ONE 91  
003:0005:0 ADD 80  
003:0005:1 NAMC (02:00002) 5002  
003:0005:3 STOD 98

END.

003:0005:4 00012000 003:0005:4  
\*\*\*\*\* STACK BUILDING CODE FOR LEVEL 02 \*\*\*\*\*  
003:0002:5 BRUN 0004:3 A26004  
003:0003:5 ZERO 80  
003:0003:0 ZERO 80

(02:00004)

003:0006:1 MPCM 9F  
003:0007:0 PUSH 00020000E0J03  
003:0008:0 BRUN 84  
003:0008:1 NVLD 0001:2  
003:0008:5 NVLD FF

\*\*\*\*\*

9.0000(0003) IS 0000 LONG

=====

NUMBER OF ERRORS DETECTED = 0  
NUMBER OF SEGMENTS = 2  
TOTAL SEGMENT SIZE = 9 WORDS  
PROGRAM SIZE = 29  
CORE ESTIMATE = 16 WORDS  
PROGRAM FILE NAME: BRUN-007700 CODE GENERATED  
STACK ESTIMATE = 7  
COMPILED BY: MNL: BRUN-007700 CODE GENERATED  
DISK SEGMENTS

=====

0.217 SECONDS ELAPSED  
0.539 SECONDS PROCESSING  
0.539 SECONDS I/O

=====

=====

=====

=====

=====

=====

=====

=====

87700 PROGRAM DUMP FOR 9RUN. (MIX-394473945, SIK-000)

MCP: SYSTEM/MCP MARK 30-071.755 INTRINSICS: SYSTEM/INTRINSICS. (LOADED) SYSTEM SERIAL: #124

CAUSE OF DUMP: PROGRAM REQUESTED 3 003:0001:1, 003:0004:3.

PROGRAM DUMP OPTIONS: (DEFAULT)

```

029A = LOSR (06A2D)
0037 (01,0002) 0 000000 000001 0P: OCT:00000000 00000001, BCL:00000001, ERC:222222, DEC:1 0003:0001:11
0036 008200 10E003 RCM: LL=03, NORMAL STATE [USER SEGMENT]
SEG DESC: 3 800000 97DF23
CODE: 3 FFAE60 04B202 >3 ABA310 03A140< 3 03AE50 04ABA2
0035 ----DI(011)=>3 C1A001 C04002 *MSCM: PREVIOUS MSCM 3 0033F D(003)=001C IN STACK 01A [USER SEGMENT] 3 0003:0004:33
0034 3 018600 40A003 RCM: LL=02, NORMAL STATE
SEG DESC: 3 800000 97DF23
CODE: 3 ABA310 03A140 3 03AE50 04ABA2 3 600410 02B10C >3 500280 1002B1< 3 805002 88A380
0033 ----DI(031)=>3 4D8002 E0C005 *MSCM: PREVIOUS MSCM 3 002E3 D(023)=002E [MCP SEGMENT] 3 0451:0000:0 (25066220)
0032 (02,0001) 7 008200 03E003 RCM: LL=02, CNTRL STATE
0031 (02,0003) 0 000000 000000 SEG DESC: 3 80003F A3BE24 3 B20280 B79587
0030 (02,0002) 0 000000 020000 CODE: 3 821482 26958C 3 002C3 D(013)=0016 IN STACK 0D9
002F 3 000000 086451 *MSCM: PREVIOUS MSCM 3 002C3 D(013)=0016 IN STACK 0D9
002E ----DI(023)=>3 CD9001 608002 RCM: LL=02, CNTRL STATE
002D 3 000000 092000 RCM: DUMMY (RUN)
002C ----DI(021)=>3 C08919 008028 *MSCM: PREVIOUS MSCM 3 0001F D(013)=919D IN STACK 008
0000 = 80SR (06793)

```

C O D E / S I M U L A T I O N

VERSION: 6.0.000 (OBJECT PROGRAM)

BEGIN REAL A(0,0), C(0,0)

A=(0, 00)  
C=(0, 01)  
DELTA=(0, 03)

WORD W, WP;

W=(0, 04)  
WP=(0, 05)  
P=(0, 05)

PROCEDURE P = WP (AA, BB);

VALUE AA, BB;  
REAL AA, BB;  
NULL;

AA=(1, 2)  
BB=(1, 3)

PROCEDURE (P1);

NULL;  
PROCEDURE (P2) (AA, BB);

VALUE AA, BB;  
REAL AA, BB;  
NULL;

AA=(1, 2)  
BB=(1, 3)

P(A, B);

005:000010 MKST (00,00005) AE 4005  
005:000011 NAME (00,00000) 0000  
005:000012 VALC (00,00001) 0001  
005:000013 ENR AB

P1 (WP);

005:000122 MKST (00,00005) AE 4005  
005:000123 NAME (00,00000) 0000  
005:000124 LDDT 958C  
005:000125 ENR AB

P1 (4\*0023\*  
TAG);

005:000222 MKST (00,00023) AE 4023  
005:000223 NAME (00,00023) AB  
005:000224 ENR AB  
005:000225 TAG;

P1 (4\*41200300020\*  
TAG);

005:000320 MKST AE  
005:000321 LDDT BE  
005:000322 LTAG 41200300020 2022000300000040

P1 (4\*0023\*  
TAG);

005:000520 ONE B1  
005:000521 STAG 9584  
005:000522 ENR AB

P2 (W) (C, D);

005:000524 MKST (00,00004) AE 4004  
005:000525 NAME (00,00000) 0000  
005:000526 VALC (00,00002) 0002  
005:000527 VALC (00,00003) 0003  
005:000528 ENR AB

P2 (WP) (C, D);

005:000722 MKST (00,00005) AE 4005  
005:000723 NAME (00,00000) 0000  
005:000724 LDDT 958C  
005:000725 VALC (00,00002) 0002  
005:000726 VALC (00,00003) 0003  
005:000727 ENR AB

P2 (4\*0003\*  
TAG, C, D);

005:000920 MKST (00,00003) AE 4003  
005:000921 NAME (00,00000) 0000  
005:000922 ZERO B0  
005:000923 VALC (00,00003) 0003  
005:000924 ENR AB

(NAME(WP)) = W;

005:000A20 MKST (00,000C5) 4005  
005:000A21 NAME (00,000C5) 4005

	005:000A:3	NAMC	(03.00004)	40D4	
	005:000A:5	LQUT		953C	
	005:000B:1	EXCH		86	
	005:000B:2	OVRO		8A	
[4"0003" & 1	TAG]:=WP?				005:000B:3
	005:000B:3	NAMC	(33.00003)	4003	
	005:000B:5	NAMC	(03.00005)	4005	
	005:000C:1	LQUT		953C	
	005:000C:3	EXCH		86	
	005:000C:4	OVRO		8A	
[4"00C0" & 1	TAG]:=A?				005:000C:5
	005:000C:5	NAMC	(33.000C0)	40C0	
	005:000D:1	VALC	(03.00000)	0000	
	005:000D:3	STDD		8E	
END.					005:000D:4
	005:000D:4	NVLD		FF	
	005:000D:5	NVLD		FF	

PCW J (0.0033) [7 000900084005]

COMPILE O. K.

CORE SIZE = 228 WORDS.

OO-STACK SIZE = (1 + 200) WORDS.

PROGRAM HAD 24 CARD IMAGES, WITH 156 SYNTACTIC ITEMS.

PROGRAM FILE NAME: CODE/STUFF COMPILED ON THE 86800 FOR THE 87800

(VERSION: 0.0.000)

NO CALLS ON BLOCKEXIT.

COMPILATION TIMES WERE:

00004.591 SECONDS ELAPSED

00000.482 SECONDS PROCESS

00000.671 SECONDS I-O

CDS2

```

BUZZ(X)
  DEXI
  NAMC X
  ONE
  RDLK
  BRFL      OUT OF LOOP - LOCKED
  ONE
  CBON
  SCRF (2)
  BRFL

```

\$ SET READLOCKTIMEOUT

```

BUZZ(X)
  DEXI
  ONE
  LT8 35 } READ UP
  RPRR   } SNR
  INSR   [29:10]
  NAMC   X
  STFF
  DUPL
  RSDN AFTER → SIRW X
  RDLK
  DUPL
  BRFL
  IMKS AFTER → FB
  NAMC (0,C2) READLOCKTIMEOUT (26642)
  RSDN AFTER → SIRW
  ZERO
  ENTR
  DUPL
  DLET → SIRW X
  DLET → IRW 0,C2
  MSCW

```

ESPOL QUIZ

1. Consider the following:

```
PROCEDURE X;  
BEGIN  
  ARRAY A [4];  
  REAL I, J, K;  
  FIELD PBITF = 47:1;  
  A: = A & 1 PBITF;  
  I: = 1;  
  J: = 2;  
  K: = 3;  
  EXIT;  
END;
```

(A) What does array A contain? \_\_\_\_\_

(B) What does DSCR for array A look like? \_\_\_\_\_  
(Remember copy DSCR action)

2. What code is generated by the following?

(A) REAL X; \_\_\_\_\_

(B) X: = EXCHANGE (TOPOFSTACK); \_\_\_\_\_

(C) EXCHANGE (TOPOFSTACK); \_\_\_\_\_

(D) TOPOFSTACK: = EXCHANGE (TOPOFSTACK); \_\_\_\_\_

(E) TOPOFSTACK: = X: = 0; \_\_\_\_\_

(F) STOP(0,X); \_\_\_\_\_

(G) EXCHANGE (TOPOFSTACK,X); \_\_\_\_\_

(H) TOPOFSTACK: = EXCHANGE (TOPOFSTACK,X) +DUPLICATE; \_\_\_\_\_

(I) TOUCH (EXCHANGE(DUPLICATE)); \_\_\_\_\_

(J) TOUCH (EVAL(TOPOFSTACK)); \_\_\_\_\_

(K) BOOLEAN B; \_\_\_\_\_

(L) X: = REAL (B); \_\_\_\_\_

(M) B: = BOOLEAN(X); \_\_\_\_\_

(N) WORD W; \_\_\_\_\_

(O) GO TO W; \_\_\_\_\_

ESPOL QUIZ (Cont.)

3. Which of the following are syntactically illegal and why?

- (A) SAVE ARRAY SA[\*]; \_\_\_\_\_
- (B) LAYOUT L(3:1: = 1, FLD = 2:1, 1:1: = 1, BZERO = 0:1: = 1); \_\_\_\_\_
- (C) FILE F(MYUSE = IN); \_\_\_\_\_
- (D) WORD W; REAL R; \_\_\_\_\_
- (E) GO TO W; \_\_\_\_\_
- (F) F.OPEN: = TRUE; \_\_\_\_\_
- (G) MYSELF.STATUS: = -1; \_\_\_\_\_
- (H) W: = 1 & 5 TAG & L(4,,\*); \_\_\_\_\_
- (I) R: = 0 & 2 TAG; \_\_\_\_\_
- (J) [4"8000OFFFOOAC" & 5 TAG]: = W; \_\_\_\_\_

4. Consider the following:

```

PROCEDURE P(A,B);
VALUE A,B;
WORD A,B;
BEGIN
    WORD W;
    PROCEDURE (Q);
    BEGIN
        A: = B;
        EXIT;
    END;
    W: = MAKEPCW(Q);
    Q [IF M[0] THEN NAME(A)
        ELSE (IF M [1] THEN NAME (B)
        ELSE NAME(W))];
END;

```

- (A) What is the effect of the above procedure? \_\_\_\_\_

5. What details must be taken care of by the stand-alone ESPOL programmer (EG., BLOCKEXIT)? \_\_\_\_\_



DISK BOOTSTRAP CODE

HARDLOAD RD - AT HA 4

16:17 - ERROR FIELD  
24:8 - UNIT NUMBER  
32:5 - CHANNEL NUMBER

MEMORY  
ADDRESS

MEMORY CONTENTS

0	0	8A0000000003
1	3	800188B00049
2	0	A0008C30001C
3	7	000682584001
4	0	000000000040
5	0	000189400040
6	0	12200000134C
7	0	510100026372
8	3	B0B095B9B234
9	3	B31E0095B9B2
A	3	35B095B9B230
B	3	B095B9B225B0
C	3	95B9AE400300
D	3	000004981B20
E	3	054000BB0007
F	3	B00004982714
10	3	0491400795BA
11	3	B5B22895B895
12	3	8F95840005A1
13	3	6012B08EB203
14	3	95B44000BAAB
15	0	055660025680

DISK BOOTSTRAP CODE

MEMORY ADDRESS	MEMORY CONTENT
0	HA WORD ZERO SYNC I/O
1	SEGMENT DESCRIPTOR ADDRESS 4"49" LENGTH 4"188B"
2	INFORMATION FOR MCP
3	PCW LL 1 CNTL 1:825:3
4	HARDLOAD R/D
5	BUFF DESCRIPTOR ADDRESS 4"40" LENGTH 4"1894"
6	IOCW
7	CDL
8	ZERO ZERO SPRR LT8 34
9	LT16 1E00 SPRR LT8 35
A	ZERO SPRR LT8 30
B	ZERO SPRR LT8 25 ZERO
C	SPRR MKST NAMC 0,3 VALC 0,0
D	VALC 0,4 FLTR 27:32:5
E	NAMC 0,0 OVRN VALC 0,7
F	ZERO VALC 0,4 FLTR 39:20:4
10	LOR NAMC 0,7 RDLK
11	DELT LT8 28 RPRR
12	INCN PAUS VALC 0,5
13	BRTR 12:3 ZERO CHSN LT8 3
14	STAG NAMC 0,0 OVRD ENTR
15	LENGTH OF SAVE STUFF AND ADDRESS OF SEGMENT DICTIONARY

C O D E  
= = = =

VERSION:0.0.000 (OBJECT PROGRAM)

BEGIN				00011000	000:0000:0
REAL X?				00012000	005:0000:0
X=(0, D0)				00013000	005:0000:0
BOOLEAN B?				00014000	005:0000:0
WORD W?				00015000	005:0000:0
W=(0, D2)				00016000	005:0000:0
X:=EXCHANGE(TOPOFSTACK);				00017000	005:0000:0
	005:0000:0	EXCH	86		
	005:0000:1	NAMC (00,00000)	4000		
	005:0000:3	STOD	8E		
EXCHANGE(TOPOFSTACK);				00018000	005:0000:4
	005:0000:4	EXCH	86		
	005:0000:5	DLET	85		
TOPOFSTACK:=EXCHANGE(TOPOFSTACK);				00019000	005:0000:0
	005:0001:0	EXCH	96		
TOPOFSTACK:=X:=?				00020000	005:0000:1
	005:0000:1	ZERO	80		
	005:0001:2	NAMC (00,00000)	4000		
	005:0001:4	STON	89		
STOP(0, X);				00021000	005:0000:5
	005:0001:5	ZERO	80		
	005:0002:0	VALC (00,00000)	0000		
	005:0002:2	EXCH	86		
	005:0002:3	HALT	DF		
	005:0002:4	DLET	85		
	005:0002:5	BLET	85		
EXCHANGE(TOPOFSTACK, X);				00022000	005:0000:0
	005:0003:0	NAMC (00,00000)	4000		
	005:0003:2	RDLK	958A		
	005:0003:4	DLET	85		
TOPOFSTACK:=EXCHANGE(TOPOFSTACK, X)+DUPLICATE;				00023000	005:0000:5
	005:0003:5	NAMC (00,00000)	4000		
	005:0004:1	RDLK	958A		
	005:0004:3	DUPL	87		
	005:0004:4	ADD	80		
TOUCH(EXCHANGE(DUPLICATE));				00024000	005:0004:5
	005:0004:5	DUPL	87		
	005:0005:0	EXCH	86		
	005:0005:1	DLET	85		
TOUCH(EVAL(TOPOFSTACK));				00025000	005:0005:2
	005:0005:2	EVAL	AC		
	005:0005:3	DLET	85		
X:=REAL(B);				00026000	005:0005:4
	005:0005:4	VALC (00,00001)	0001		
	005:0006:0	NAMC (00,00000)	4000		
	005:0006:2	STOD	8E		
B:=BOOLEAN(X);				00027000	005:0006:3
	005:0006:2	STON	89		
	005:0006:3	NAMC (00,00001)	4001		
	005:0006:5	STOD	8E		
GO TO W?				00028000	005:0007:0
	005:0007:0	NAMC (00,00002)	4002		
	005:0007:2	OBUM	AA		
END.				00029000	005:0007:3
	005:0007:3	NVLD	FF		
	005:0007:4	NVLD	FF		
	005:0007:5	NVLD	FF		

PCW 3 (0,0003) [7 000000084005]

COMPILE 0- K.

CORE SIZE = 219 WORDS.

DO-STACK SIZE = (1 + 208) WORDS.

PROGRAM HAD 17 CARD IMAGES, WITH 96 SYNTACTIC ITEMS.

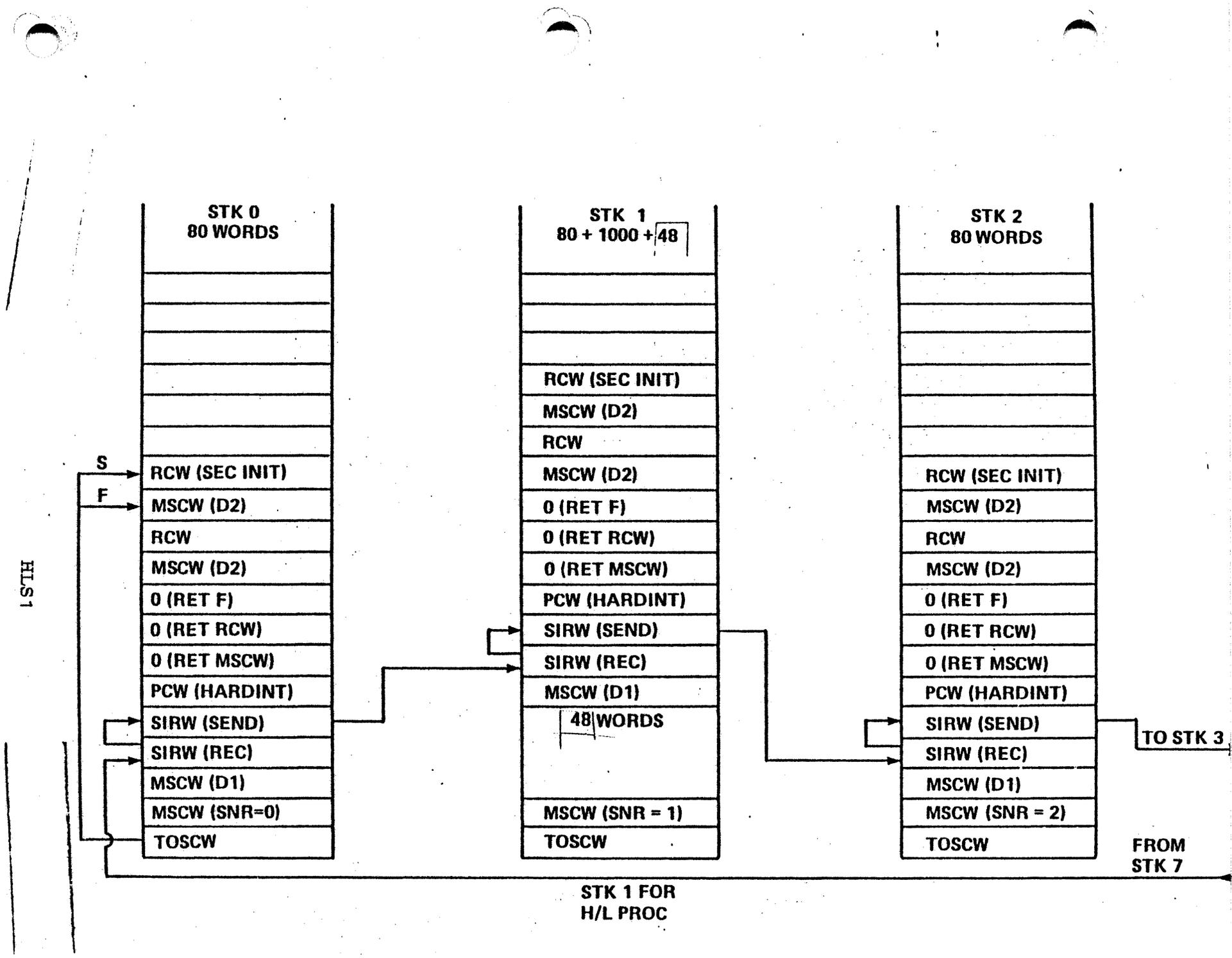
PROGRAM FILE NAME: CODE COMPILED ON THE 86800 FOR THE 87800

(VERSION: 0.0.000)

NO CALLS ON BLOCKEXIT.

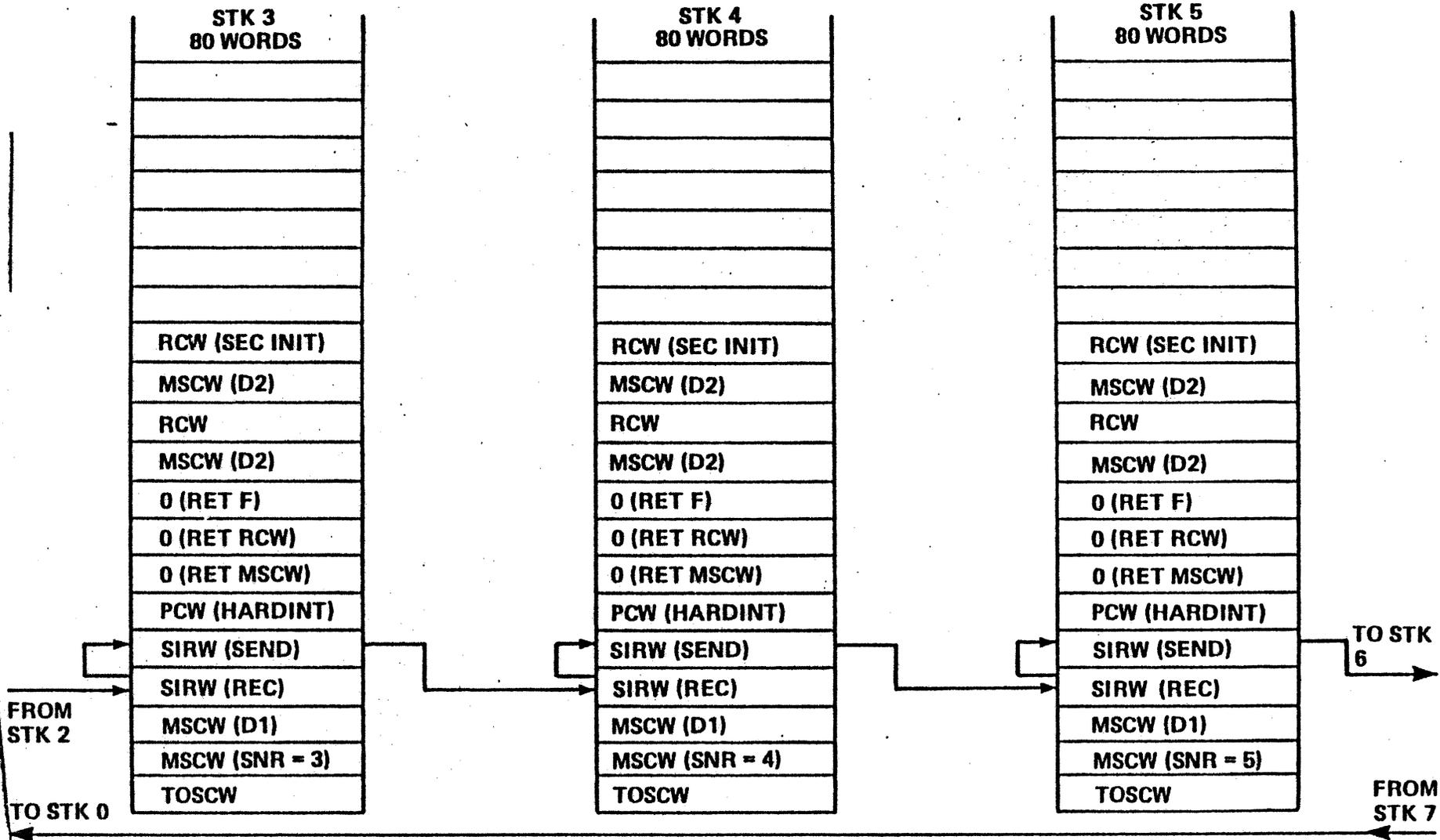
COMPILATION TIMES WERE:

00003.967 SECONDS ELAPSED  
00000.371 SECONDS PROCESS  
00000.554 SECONDS I-O

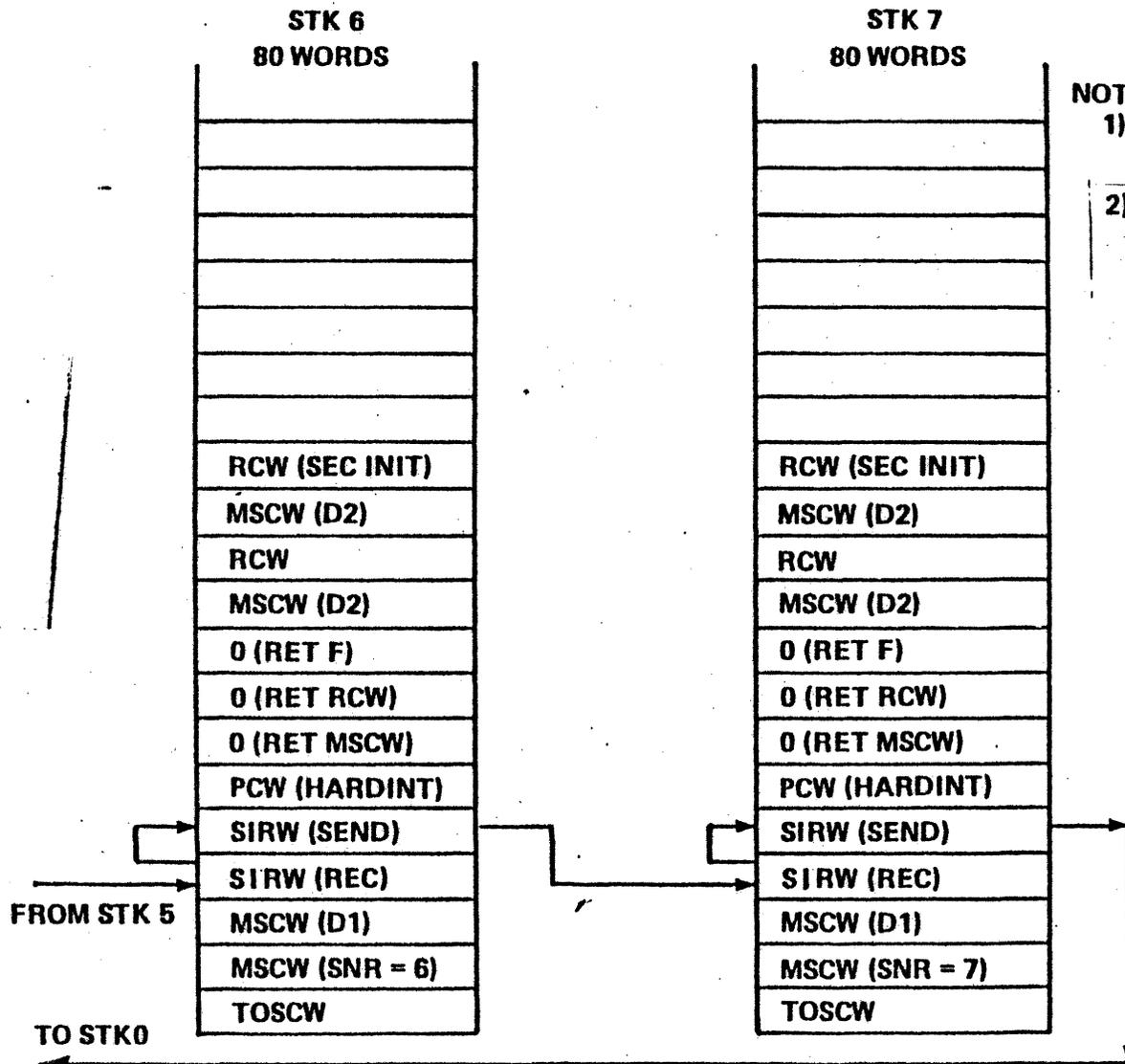


STK 1 FOR  
H/L PROC

HLS2



HLS 3



F O R K E R  
 = = = = =

VERSION:0.0.000 (OBJECT PROGRAM)

```

BEGIN
REAL A,B;
A=(0, 00)
B=(0, 01)
POINTER P;
DEFINE
STACKSIZE = 500 #,
PRIORITY = 99 #,
VISIBLE = - #,
ONEONLY = - #;
PROCEDURE MYMCP1R(AA,BB);
VALUE AA,BB;
REAL AA,BB;
NULL;
AA=(1, 2)
BB=(1, 3)
PROCEDURE MCP1RWITHLONGNAME;
MCP1RWITHLONGNAME=(0, 04)
NULL;
FORK MYMCP1R(A,B) (STACKSIZE,PRIORITY);
005:000000 MKST AE
005:000001 NAMC (00,00003) 4003
005:000002 VALC (00,00000) 0000
005:000003 VALC (00,00001) 0001
005:000101 MKST AE
005:000102 NAMC (00,000AF) 40AF
005:000104 LT16 B301F4
005:000201 LTR B263
005:000203 LTR RE
005:0003 0704E8D4C3D7 0175235065141727
005:000400 LT16 B3C9D9
005:000403 ISOL 9A0F30
005:000500 JOIN 9542
005:000502 ENTR AB
005:000503 ENTR AB
REPLACE P BY 48"11", "THEMCP1RNAMEONSP0";
005:000504 NAMC (90,00002) 4002
005:000600 LOAD RD
005:000601 LTR B288
005:000603 ISOL 9A0A30
005:000700 ONE B1
005:000701 TUNU EE
005:000702 DLET B5
=(0, 05)
005:000703 ZERO B0
005:000704 NAMC (00,00005) 4005
005:000800 INDX A6
005:000801 LTR B203
005:000803 INSR 9C2A03
FORK MYMCP1R(A,B) (ONEONLY STACKSIZE,PRIORITY,P);
005:000900 LTR B211
005:000902 TUND E6
005:000903 MKST AE
005:000904 NAMC (90,00003) 4003
005:000A00 VALC (00,00000) 0000
005:000A02 VALC (90,00001) 0001
005:000A04 MKST AE
005:000A05 NAMC (00,000AF) 40AF
005:000B01 LT16 B301F4
005:000B04 CHSN RE
005:000B05 LTR B263
005:000C01 NAMC (90,00002) 4002
005:000C03 LOAD RD
005:000C04 ZERO B0
005:000C05 ENTR AB
005:000D03 ENTR AB
  
```

FOR1

REPLACE P BY 48'09", "MCPIRNAME";

005:0000:1

005:0000:1	NAMC (00,00002)	40D2
005:0000:3	LOAD	8D
005:0000:4	LT8	8290
005:0000:9	ISOL	9A0830
005:0000:11	DNE	81
005:0000:14	TUNU	EE
005:0000:15	OLET	85
005:0000:19	LT8	8203
005:0000:22	NAMC (00,00005)	40D5
005:0000:14	INDX	A6
005:0000:15	LT8	8293
005:0010:1	INSR	9C2A93
FORK MCPIRWITHLONGNAME (STACKSIZE,VISIBLE PRIORITY,P1);		
005:0010:4	LT8	8209
005:0011:0	TUND	E6
005:0011:1	MKST	AE
005:0011:2	NAMC (00,00004)	40D4
005:0011:4	MKST	AE
005:0011:5	NAMC (00,000AF)	40AF
005:0012:1	LT16	8301F4
005:0012:4	LT8	8263
005:0013:0	CHSN	8E
005:0013:1	NAMC (00,00002)	40D2
005:0013:3	LOAD	8D
005:0013:4	ZERO	80
005:0013:5	ENTR	AB
005:0014:0	ENTR	AB
FORK MCPIRWITHLONGNAME (ONEONLY STACKSIZE,VISIBLE PRIORITY);		
005:0014:1	MKST	AE
005:0014:2	NAMC (00,00004)	40D4
005:0014:4	MKST	AE
005:0014:5	NAMC (00,000AF)	40AF
005:0015:1	LT16	8301F4
005:0015:4	CHSN	8E
005:0015:5	LT8	8263
005:0016:1	CHSN	8E
005:0016:2	LT8	8205
005:0016:4	NAMC (00,00005)	40D5
005:0017:0	INDX	A6
005:0017:1	BSET	962A
005:0017:3	ZERO	80
005:0017:4	ENTR	AB
005:0017:5	ENTR	AB

005:0010:4

005:0014:1

END.

005:001P:0  
CONSTANT POOL 3(0,0005) IS 0009 WORDS LONG

PCW 3 (0,0003) [7 000000084005]

COMPILE O. K.

CORE SIZE = 238 WORDS.

DO-STACK SIZE = (3 + 208) WORDS.

PROGRAM HAD 21 CARD IMAGES, WITH 113 SYNTACTIC ITEMS.

PROGRAM FILE NAME: FORKER COMPILED ON THE 86800 FOR THE 87800

(VERSION: 0.0.000)

NO CALLS ON BLOCKEXIT.

COMPILATION TIMES WERE:

00004.791 SECONDS ELAPSED  
00000.508 SECONDS PROCESS  
00000.686 SECONDS I-O

FOR2

ESPOL/MCP LAB

Write an ESPOL procedure to be bound to standard intrinsics. The procedure will take two parameters, a case number and an array. Thus the procedure will be declared as:

PROCEDURE ESPOLINT (CASENO, ARY)

A program has been written to drive your intrinsic. The name of this program is SYMBOL/RUNNER. A listing is attached. Change the comment line at the end of the program to call your intrinsic. Your intrinsic should do the following:

<u>CASE NO.</u>	<u>ACTION</u>
0	ARY [0] = DO ARY [1] = D1 ARY [2] = D2 ARY [3] = F ARY [4] = S ARY [5] = LOSR ARY [6] = BOSR ARY [7] = SNR
1	ARY [0] = your stack number ARY [1] = your segment dictionary stack number ARY [2] = your intrinsics stack number ARY [3] = system intrinsics stack number
2	Your program-ID (name) in ARY. Standard form is good. You must use the word in your task to get your name.
3	RCW - MSCW chain.
4	Largest available memory area and address in MEM [0] list. Place answer in ARY [0] and ARY [1], largest available memory area and address in MEM [1] list. Place answer in ARY [2] and ARY [3]. In both cases, the address must point to the start of the area.

ESPOL/MCP LAB (Cont)

CASE NO.

ACTION

5	Build a SIRW to point to your task that is, the memory area of your task. Strip tag and place in ARY [0].
6	Invoke procedure P in SYMBOL/RUN
7	The name of the intrinsics, stack form is good. You may not use the task of the intrinsics stack for this.
8	Do a disk read from the H/L unit address of 0 for 1 segment. Use disk wait.
9	Same as 8 except do a read with tags, then do a program dump. ZOT the array ARY to be sure the ALGOL program does not get tags.
10	Resize ARY to 60 words. You may not call resize and deallocate. Fix up MOM, links and copy descriptors.
11	ARY [0] = Number of tasks running. ARY [1] = Number of segment dictionaries in use.

INSTRUCTIONS

We will use CANDE. Use the following USERCODE/PASSWORD:

MCP/CLASS

Since all files will be placed under the usercode MCP, use unique names for your intrinsic and copy of RUNNER.

ESPOL/MCP LAB

All files will be on a pack called MCPCLASS.

To make your intrinsic:

- (1) Write the code. Use the following dollar options:
  - a. SINGLE STACK LIST INTRINSICS LOADINFO
  - b. The info file name is INFO.
  - c. Your procedure must end with an "END."
- (2) Your procedure must be under the node INTX/=.

To bind your intrinsic:

- (1) SO MSG. so you can see BIND.
- (2) ST BINDINT
- (3) BINDINT will do a BIND and CI.
- (4) After CI get a copy of MYPART and fix line. Compile.
- (5) Run with option fault arrays.
- (6) To fix.

Make change.

Compile.

ST BINDINT

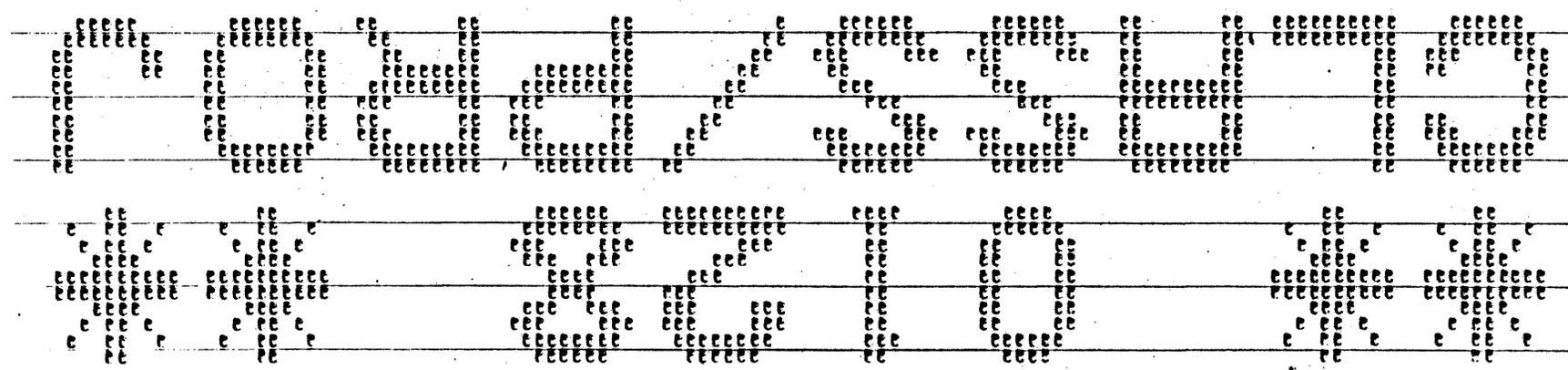
Run

RUN 1

DATE:

03:27:55, SYSTEM SERIAL: 1319, B. MCP1 SYSTEM/MCP30149, III-00-176

WORK FLOW STATEMENTS



0000100 2JOB CLASS/PROJECT

00000200 CLASS 33  
 00000300 BEGIN  
 00000400 COMPILE MY/PART ALGOL LIBRARY  
 00000500 ALGOL FILE NEWAPELITITF=SYMBOL/RUNNER1  
 00000600 DATA CARD  
 00000600 TEND JOB

JOB SUMMARY

03:06:33 80J 0120 CLASS/PROJECT  
 JOB ENTERED SYSTEM: 03:06:33  
 QUEUE: 3  
 ORIGINATING UNIT: 26  
 PRIORITY: 50

03:06:34 001 0129 SYSTEM/ALGOL ON OLDSPACK.  
 TASK TYPE: COROUTINE  
 PRIORITY: 50

03:06:42 0129 MY/PART REMOVED ON OLDSPACK PK208  
 03:06:43 0129 MY/PART REMOVED ON OLDSPACK PK208  
 03:06:43 0129 SYMBOL/RUNNER REMOVED ON OLDSPACK  
 03:06:43 0129 SYSTEM/ALGOL ON OLDSPACK  
 1.594 SEC CPU, 2.487 SECS IO  
 59 CARDS READ, 103 LINES PRINTED  
 AVERAGE CORE USAGE: CODE=57.029, DATA=42.705  
 ELAPSED TIME: 00:00:10

03:06:44 80J 0120 CLASS/PROJECT

0.105 SEC CPU, 0.254 SECS IO  
 NEW INTEGRAL CODE=0.029, DATA=0.414  
 AVERAGE CORE USAGE: CODE=81, DATA=1152  
 ELAPSED TIME: 00:00:10



RUN3

BLANK;  
FOR I:=0 STEP 1 UNTIL NUMLEFT-1 DO

3 00041000 008:0023:5  
00042000 008:0027:3

BEGIN  
REPLACE PO:PO BY POINTER(A(INX),4) FOR 12 WITH  
HEXTOEBCDIC;  
PO:=PO+1;  
INX:=+1;

4 00043000 008:002C:1  
00044000 008:002C:1  
00045000 008:002E:1  
00046000 008:002F:5

WRITE(LINE (SPACE 3), 22, OUTLINE);  
END

4 00047000 008:0031:0  
00048000 008:0032:1  
00049000 008:0032:4  
00050000 008:0039:5

END;

3 00051000 008:0039:5

FOR I:=0 STEP 1 UNTIL 11 DO

2 DUMPIT(008) IS 0051 LONG

BEGIN  
COMMENT YOURINTNAME(I,A);  
DUMPIT;  
DEALLOCATE(A);

2 00052000 003:0000:1  
00053000 003:0000:5  
00054000 003:0007:5  
00055000 003:0007:5  
00056000 003:0001:3

(01.00008) = SEPARATE INTRINSIC

END;  
END.

2 00057000 003:0033:0  
00058000 003:0005:4  
9.0000(003) IS 0012 LONG

=====  
NUMBER OF ERRORS DETECTED = 0.  
NUMBER OF SEGMENTS = 9. TOTAL SEGMENT SIZE = 143 WORDS. CORE ESTIMATE = 996 WORDS. STACK ESTIMATE = 18  
PROGRAM SIZE = 58 CARDS, 284 SYNTACTIC ITEMS, 18 DISK SEGMENTS.  
PROGRAM FILE NAME: MY/PART. R7700 CODE GENERATED.  
COMPILATION TIME = 5.834 SECONDS ELAPSED; 1.087 SECONDS PROCESSING; 1.600 SECONDS I/O.  
=====

## MEMORY MANAGEMENT

Available memory areas are linked to each other. There are two chains for memory areas: one list is searched if the memory area can be overlaid; the other list is searched if the area is a save area. Memory management will try to keep all save areas at one end of memory (low memory address). Overlay areas are at the other end of memory (high memory address). This is done to ease memory checkerboard.

The start link for the overlay memory chain is MEM(0). The start link for the save memory chain is MEM(1). The MEM(0) list is in AGE order, i.e., last area forgotten is first in list. The MEM(1) is in size order. The order is smallest to largest. A memory area can be in one or both lists. If both areas (high area and low area) around an area to be placed in a memory chain are save, the area is linked into the save chain. If both areas are overlay, link new area into overlay chain. If low area is save and high area is overlay, link into both chains. If low area is overlay and high area is save, link into overlay (see Figure MM-1). Memory is always in the largest chunk; that is, there will never be two available areas back to back.

There will be 4 links around all memory areas. The link names for available areas are: AVAILA, AVAILB, AVAILY, and AVAILZ. The link names for in use areas are: LINKA, LINKB, LINKC, and LINKZ (see Figure MM-2 thru MM-7).

Consider the memory list in Figure MM-8. Assume we want 25 words of overlay memory. We will do a LLLU on MEM(0) looking for a size 25. We will stop at 30. This area is delinked from both memory chains.

The MCP will put a large number in the size field of the last link to memory. Thus, LLLU will never return A -1.

MEM[0] LIST FOR OVERLAYABLE MEMORY, ORDERED ON AGE (NEWEST FIRST).  
 MEM[1] LIST FOR SAVE MEMORY, ORDERED ON SIZE (SMALLEST FIRST).

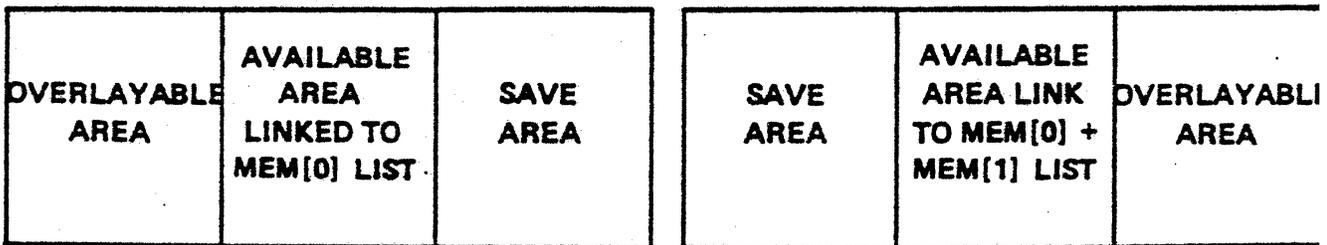
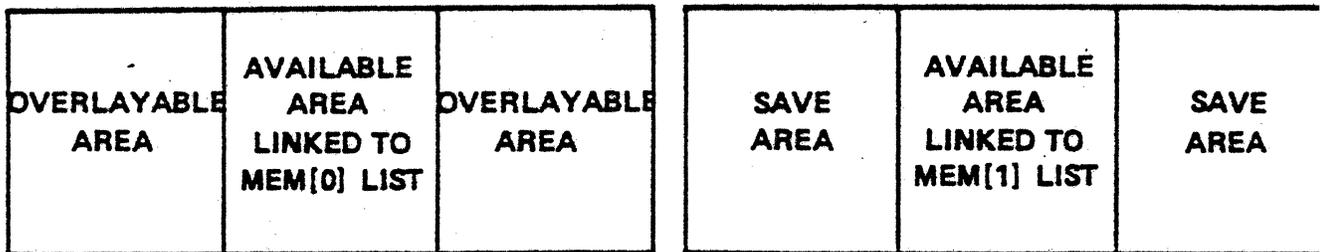


Figure MM-1. Available Memory Area Lists

AVAILA TAG 1

ST											
US	S	I	Z	E	F	SA	A	D	D	R	E
								S	S	F	
						IN					

[47:1] STOFFERF [22:1] SAVEDF  
 [46:1] USABLEF [20:1] INUSEF  
 [43:20] SIZEF [19:20] ADDRESSF

STOPPERF ON IF LAST AREA. STOPS LLLU  
 USABLEF OFF IN LAST AREA.  
 ADDRESSF LINK TO NEXT MEMORY AREA.  
 POINTS TO AVAILA. NOT IN SIZE ORDER.  
 SIZEF SIZE OF AREA INCLUDES LINKS

AVAILB TAG 0

							A	D	D	R	E
								S	S	F	

[19:20] ADDRESSF

ADDRESSF IS ADDRESS OF LINK WHICH  
 POINTED TO MY AVAILA WORD. THAT  
 IS, A BACK LINK.

Figure MM-2. Memory Links Around Available Areas  
 Linked to MEM[0] (Sheet 1 of 2)

**AVAILY TAG 0**

							A	D	D	R	E
								S	S	F	

[19:20] ADDRESSF

ADDRESSF IS ADDRESS OF LINK WHICH POINTED TO MY AVAILZ WORD. THAT IS, A BACK LINK. ADDRESSF USED ONLY IF THIS AREA IS IN MEM[1] CHAIN ALSO.

NOTE: AVAILABLE MEMORY IS BETWEEN AVAILB AND AVAILY.

**AVAILZ TAG 1**

ST											
US	S	I	Z	E	F	SA	A	D	D	R	E
								S	S	F	
						IN					

[47:1] STOPPERF [22:1] SAVEDF  
 [46:1] USABLEF [20:1] INUSEF  
 [43:20] SIZEF [19:20] ADDRESSF

SIZEF SIZE OF AREA INCLUDES LINKS ADDRESSF LINK TO NEXT MEMORY AREA. POINTS TO AVAILZ. USED ONLY IF THIS AREA IS IN MEM[1] CHAIN ALSO.

Figure MM-3. Memory Links Around Available Areas Linked to MEM[0] (Sheet 2 of 2)

**AVAILA TAG 1**

ST											
US	S	I	Z	E	F	SA	A	D	D	R	E
							S	S	F		
						IN					

[47:1] STOPPERF      [22:1] SAVEOF  
 [46:1] USABLEF      [20:1] INUSEF  
 [43:20] SIZEF      [19:20] ADDRESSF

SIZEF INCLUDES LINKS  
 ADDRESSF LINK TO NEXT MEMORY AREA.  
 POINTS TO AVAILA. USED ONLY IF THIS  
 AREA IS IN MEM[0] CHAIN ALSO.

**AVAILB TAG 0**

							A	D	D	R	E
							S	S	F		

[19:20] ADDRESSF

ADDRESSF IS ADDRESS OF LINK WHICH  
 POINTED TO MY AVAILA. THAT IS, A  
 BACK LINK. USED ONLY IF THIS AREA  
IS IN MEM[0] CHAIN ALSO.

Figure MM-4. Memory Links Around Available Areas  
 Linked to MEM [1] (Sheet 1 of 2)

**AVAILY TAG 0**

							A	D	D	R	E
								S	S	F	

[19:20] ADDRESSF  
 ADDRESSF IS ADDRESS OF LINK WHICH  
 POINTED TO MY AVAILZ. THAT IS,  
 A BACKLINK.

NOTE: AVAILABLE MEMORY IS  
 BETWEEN AVAILB AND AVAILY.

**AVAILZ TAG 1**

ST											
US	S	I	Z	E	F	SA	A	D	D	R	E
								S	S	F	
						IN					

[47:1] STOPPERF [22:1] SAVEOF  
 [46:1] USABLEF [20:1] INUSEF  
 [43:20] SIZEF [19:20] ADDRESSF

SIZEF INCLUDES LINKS  
 ADDRESSF LINK TO NEXT MEMORY  
 AREA. POINTS TO AVAILZ WORD.  
 IN ORDER OF INCREASING SIZE.

Figure MM-5. Memory Links Around Available Areas  
 Linked to MEM [1] (Sheet 2 of 2)

**LINKA TAG 6**

OL						ST					
CF	S	I	Z	E	F	CS	A	D	D	R	E
TS						SV		S	S	F	
PS						IN					

[47:2] OVERLAYCF      [23:1] STICKYF  
 [45:1] TEMPSAVF      [22:1] CURSAVF  
 [44:1] PRECURSAVF    [21:1] SAVEF  
 [43:20] SIZEF          [20:1] INUSEF  
                                  [19:20] ADDRESSF

OVERLAYCF WILL BE 3.  
 SIZEF INCLUDES LINKS.  
 ADDRESSF IS ADDRESS OF MOM.

**LINKB TAG 7**

OL						D					
CF			U	S	A	E	A	D	D	R	E
S	T	K	G	E	F	L		S	S	F	
N	R	F									

[47:2] OVERLAYCF      [23:4] DELTAF  
 [45:10] STKNRF        [19:20] ADDRESSF  
 [35:12] USAGEF

STKNRF STACK NUMBER OF MOM  
 USAGEF USAGE OF AREA (STACK, FIB, DUPEVEC)  
 DELTAF NUMBER OF SLOP (WASTE) WORDS  
 ADDRESSF DISK ADDRESS FOR OVERLAY

Figure MM-6. Memory Links Around Inuse Areas (Sheet 1 of 2)

**LINKC TAG 3**


USED TO STORE RCW OF WHERE  
PBIT OCCURED OR  
USED TO HOLD IOCW

NOTE: INUSE AREA IS BETWEEN  
LINKC AND LINKZ

**LINKZ TAG 3**

SI						DP						
	S	I	Z	E	F	CS	A	D	D	R	E	
						SV		S	S	F		
						IN						

[47:1] SAVINGF [21:1] SAVEF  
[43:20] SIZEF [20:1] INUSEF  
[23:1] DELTAPF [19:20] ADDRESSF  
[22:1] CURSAVF

SAVINGF USED WHEN SAVING MODS  
DELTAPF IF THERE ARE SLOP (WASTE) WORDS  
ADDRESSF USED FOR MEMORY PRIORITY

Figure MM-7. Memory Links Around Inuse Areas (Sheet 2 of 2)

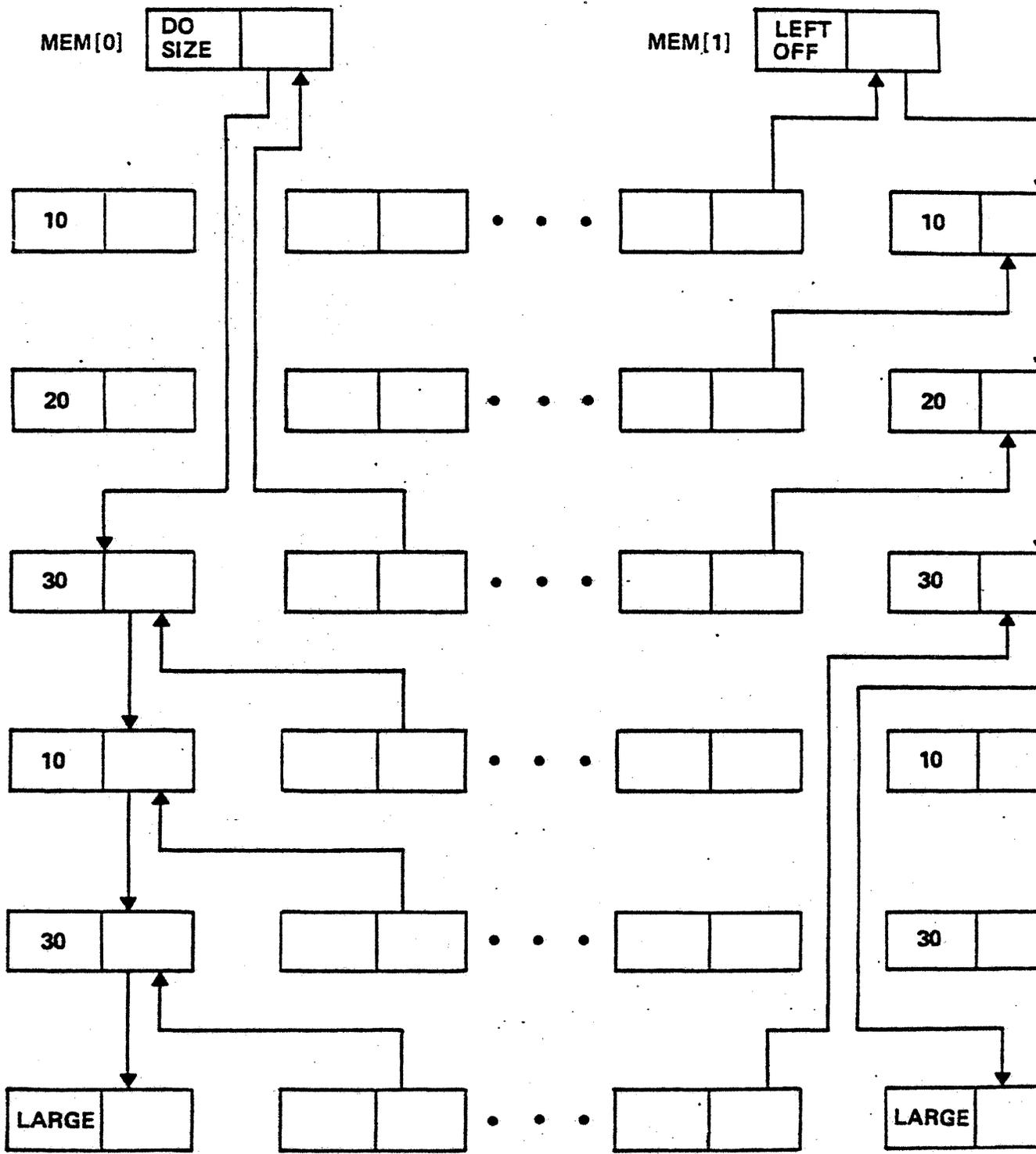


Figure MM-8. Memory Linked Lists (Available Lists)

GETSPACE (22067000)

GETSPACE(

NUMBER OF WORDS I WANT,  
STACK NUMBER OF MOM,  
USAGEL (See Below)  
MEMORY ADDRESS OF MOM)

GETSPACE RETURNS MEMORY ADDRESS OF AREA. IF A 0 IS RETURNED,  
WE DID NOT GET ANY MEMORY.

USAGEL CONTAINS:  
NOZEROXINGALLOWEDF = 28:1

THERE WILL BE NO COPY DESCRIPTOR TO THIS AREA. DO NOT  
NEED TO DO A STACK SEARCH WHEN YOU OVERLAY.

---

ODDBALLF = 27:4.

---

THE USE OF THE AREA. SEE VALUES AT 03215000.

SAVEF = 21:1

GIVE ME SAVE MEMORY.

ADDRESSF = 19:20

DISK ADDRESS OF WHERE TO OVERLAY THIS AREA IF KNOWN.

OLAYEXITF = 47:1

USED AS LOCAL IN GETSPACE. SEE GETSPACE.

DONTLOSEMEF = 35:1

IF NO MEMORY AVAILABLE, DON'T OVERLAY TO GET ME SOME.  
I DO NOT WANT TO LOSE CONTROL OF PROCESSOR.

ICANDOWITHOUTITF = 34:1

IF NO MEMORY AVAILABLE EVEN AFTER YOU TRY OVERLAY,  
RETURN TO ME.

I WONTTELLIFYOUWONTTELL = 33:1

IF NO MEMORY AVAILABLE EVEN AFTER OVERLAY JUST WAIT IN  
GETSPACE. THAT IS, WHEN YOU RETURN I WILL HAVE MEMORY.  
IF THERE IS NO MEMORY DO NOT CALL DEFUNCT.

ITSNOTDOORDIEF = 32:1

NOT USED.

GETSPACE (Cont.)

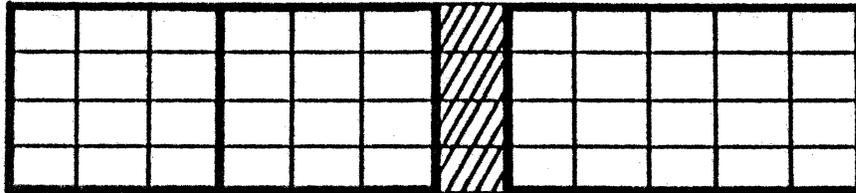
UNTOUCHABLEF = 31:1  
UNTOUCHEDF = 30:1

I WILL NEVER WRITE IN THIS AREA... DO NOT NEED TO WRITE  
THIS AREA OUT TO DISK TO OVERLAY. EXAMPLE: CODE, VALUE  
ARRAY.

NOTAGSALLOWEDF = 29:1

NO MIXED TAGS IN THIS AREA. IT'S NOT A WORD ARRAY.  
WHEN YOU OVERLAY ARRAY DO NOT NEED TO DO A WRITE WITH  
TAGS.

**GETAVAILA TAG 5**

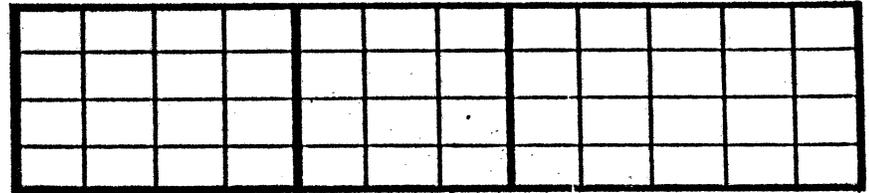


[47:12]  
FOR LINKF  
ROW  
RELATIVE  
ADDRESS.  
AREA IN  
FRONT OF  
ME IN  
THIS ROW

[35:12]  
BACKLINKF  
ROW  
RELATIVE  
ADDRESS.  
AREA BEHIND  
ME IN THIS  
ROW

[19:20] ADDRESSF  
BACKLINK.  
TO SMALLER  
AVAILB. MEM  
ADDR.

**GETAVAILB TAG 5**



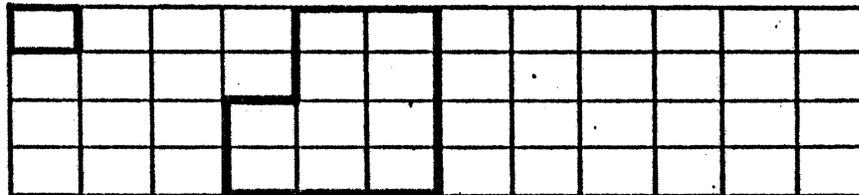
[47:16]  
AREALENGTHF  
LENGTH OF  
AREA.  
INCLUDE  
LINKS

[31:12]  
ROWNUMF  
WHICH  
ROW WE  
ARE IN

[19:20] ADDRESSF  
LINK TO NEXT  
LARGER AREA.  
TO AVAILB.

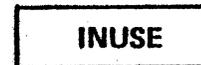
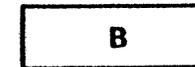
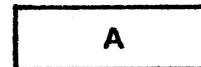
GFL 1

**INUSE LINK TAG 7**



[47:1]  
PBITF  
IF THERE  
IS A SLOP  
WORD

[33:10]  
MSGLENGTHF  
SIZE OF  
AREA INCLUDE.  
THIS WORD



I P C / S T U F F

```

(01-00002) = B-3000
(01-00003) = SEGMENT DESCRIPTOR
TASK_ID 003:00000 NVLD FF 0001000 07:00000
(02-00002) = F 1 0000 IS SEGMENT 0000
REAL X,Y 0002000 003:00001
(02-00003) = X 0003000 003:00001
(02-00004) = Y 0004000 003:00001
(02-00005) = P 0005000 003:00001
EXTERNAL;
(01-00004) = SEGMENT DESCRIPTOR P IS SEGMENT 00004
PROCEDURE P1 0006000 003:00001
BEGIN 0007000 003:00001
REAL A,B 0008000 003:00001
(01-00005) = SEGMENT DESCRIPTOR P1 IS SEGMENT 00005
(03-00002) = A 0001000 005:00001
(03-00003) = B 0002000 005:00001
A:=A+1;
0:=0+1;
END;
***** STACK BUILDING CODE FOR LEVEL 03 *****
005:00001 M00P FE
005:00002 BRUN 42002
005:00003 VALC (03-00002) 3002
005:00004 ONE 81
005:00005 ADD 80
005:00006 NAMC (03-00002) 7002
005:00007 ST00 32
005:00008 VALC (03-00003) 3003
005:00009 ONE 81
005:00010 ADD 80
005:00011 NAMC (03-00003) 7003
005:00012 ST00 92
005:00013 EXIT 03
005:00014 ZERO 90
005:00015 7680 91
005:00016 PUSH 92
005:00017 NVLD 93
005:00018 NVLD 94
005:00019 NVLD 95
005:00020 NVLD 96
005:00021 NVLD 97
PROCEDURE P2(A,B)
VALUE A;
REAL A,B;
BEGIN
A:=A+1;
0:=0+1;
END;
(02-00007) = P2 0001000 005:00001
(03-00002) = A 0001300 003:00001
(03-00003) = B 0001400 003:00001
0001500 003:00001
A:=A+1;
0:=0+1;
END;
(01-00008) = SEGMENT DESCRIPTOR P IS SEGMENT 00008
PROCEDURE P3 0001600 003:00001
BEGIN 0001700 003:00001
REAL A,B 0001800 003:00001
(01-00009) = SEGMENT DESCRIPTOR P3 IS SEGMENT 00009
(03-00002) = A 0001900 005:00001
(03-00003) = B 0002000 005:00001
A:=A+1;
0:=0+1;
END;
***** STACK BUILDING CODE FOR LEVEL 03 *****
005:00001 VALC (03-00002) 3002
005:00002 ONE 81
005:00003 ADD 80
005:00004 NAMC (03-00002) 7002
005:00005 ST00 82
005:00006 NAMC (03-00003) 7003
005:00007 EVAL AC
005:00008 VALC (03-00002) 3002
005:00009 ONE 81
005:00010 ADD 80
005:00011 ST00 92
005:00012 EXIT 03
005:00013 ZERO 90
005:00014 7680 91
005:00015 PUSH 92
005:00016 NVLD 93
005:00017 NVLD 94
005:00018 NVLD 95
005:00019 NVLD 96
005:00020 NVLD 97

```

IPSS2

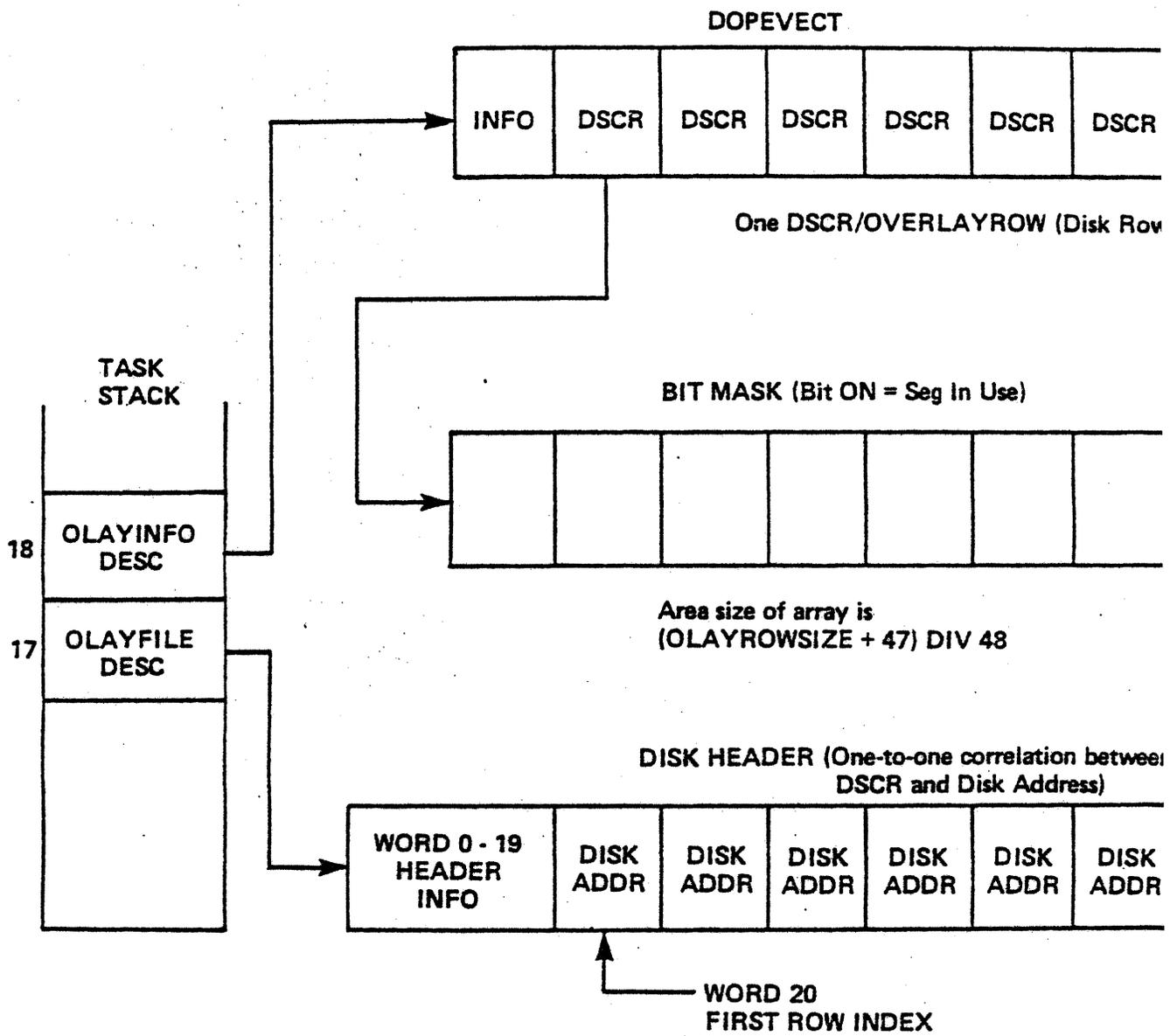
REPLACE T-NAME BY "SYSTEM/PATCH."		2	0019000	003:0002:5
(01.00006) = SEPARATE INTRINSIC	003:0002:5 MKST			AF
	003:0003:0 NAMC (01.00006)			6076
	003:0003:2 NAMC (02.00002)			5092
	003:0003:4 STFF			AF
(01.00007) = POOL DATA DESCRIPTOR	003:0003:5 ZERO			90
	003:0004:0 ZERO			90
	003:0004:1 NAMC (01.00007)			6097
	003:0004:3 INDX			46
	003:0004:4 BSET			962A
	003:0005:0 ENTR			AH
RUN P (T)			00021000	003:0005:1
(01.00008) = SEPARATE INTRINSIC	003:0005:1 MKST			AE
	003:0005:2 NAMC (01.00008)			6098
	003:0005:4 LTH			8202
	003:0006:0 NAMC (02.00005)			5005
	003:0006:2 STFF			4F
	003:0006:3 NAMC (02.00002)			5092
	003:0006:5 STFF			AF
	003:0007:0 NAMC (02.00000)			5000
	003:0007:2 STFF			4F
	003:0007:3 ENTR			AH
CALL PL (T)			00021000	003:0007:4
	003:0007:4 MKST			AE
	003:0007:5 NAMC (01.00008)			6098
	003:0008:1 ONE			81
	003:0008:2 NAMC (02.00006)			5006
	003:0008:4 STFF			4F
	003:0008:5 NAMC (02.00002)			5092
	003:0009:1 STFF			4F
	003:0009:2 NAMC (02.00000)			5000
	003:0009:4 STFF			4F
	003:0009:5 ENTR			AH
PROCESS P2(X,Y) (T)			00022000	003:000A:0
	003:000A:0 MKST			AE
	003:000A:1 NAMC (01.00008)			6098
	003:000A:3 ZERO			90
	003:000A:4 NAMC (02.00007)			5007
	003:000B:0 STFF			4F
	003:000B:1 VALC (02.00003)			1093
	003:000B:3 NAMC (02.00004)			5004
	003:000B:5 STFF			4F
	003:000C:0 NAMC (02.00002)			5002
	003:000C:2 STFF			4F
	003:000C:3 NAMC (02.00000)			5000
	003:000C:5 STFF			4F
	003:000D:0 ENTR			AH
END.			00023000	003:000D:1
(01.00009) = SEPARATE INTRINSIC	003:000D:1 MKST			AE
	003:000D:2 NAMC (01.00009)			6009
	003:000D:4 ENTR			AH
	003:000D:5 EXIT			A3

STACK BUILDING CODE FOR LEVEL 02

(02-00002)	003:0003:0	LI0	02F0
	003:0003:2	LI0L	941E30
	003:0003:5	LI0G	9205
	003:0003:4	LI0D	9584
	003:0003:4	LI0D	30
(02-00005)	003:0003:5	LI0A	9F
	003:0011:0	MPCN	490000000004 (3*200000000000004)
(02-00006)	003:0011:0	MPCN	9F
	003:0012		00020020E005
(02-00007)	003:0013:0	MPCN	9F
	003:0014		00020000E003
	003:0015:0	LI16	940J30
	003:0015:3	LI0L	8206
	003:0016:0	LI0	9584
	003:0016:2	LI0G	42A002
	003:0017:1	LI0M	FF
	003:0017:2	LI0D	FF
	003:0017:3	LI0D	FF
	003:0017:4	LI0D	FF
	003:0017:5	LI0D	FF

B-0000(003) IS 0018 LONG  
DATA IS 0003 LONG

=====  
 NUMBER OF ERRORS DETECTED = 0  
 TOTAL SEGMENT SIZE = 33 WORDS. CORE ESTIMATE = 45 WORDS. STACK ESTIMATE = 12  
 NUMBER OF SEGMENTS = 7  
 TOTAL SYNTACTIC ITEMS = 14 DISK SEGMENTS.  
 PROGRAM SIZE = 23 CARDS. 91 SYNTACTIC ITEMS. 14 DISK SEGMENTS.  
 PROGRAM FILE NAME: IBC/STUFF.87700 CODE GENERATED.  
 CORRELATION TIME = 4.104 SECONDS ELAPSED. 0.683 SECONDS I/O.  
 =====



INFO (03111)  
INUSEROWS  
ROWSIZE

DISK ADDR (070281)  
UNIT NUMBER  
SEG NUMBER

HEADER INFO (07)  
ROWSIZE  
NUMBER OF ROWS

FINDOLAYSPACE/LOSEOLAYSPACE

FLS1

F I B S T A C K / I N F O

```

(01,00002) = BEGIN
(01,00003) = SEGMENT DESCRIPTOR
(02,00002) = A
(02,00003) = FUNNY STR
(02,00004) = DSK
BLOCKSIZE=60}}

DATA POOL AT (02,00004):
0000 010000000000
0001 02010103C4E2
0002 020000000000
0003 031004030801
0004 031402031214
0005 031164030E1E
0006 030E3C000000

REPLACE POINTER(A) BY "HI THERE";
WRITE(DSK,30,A)}

(01,00004) = SEPARATE INTRINSIC
PROGRAMDUMP(ARRAYS,FILES)}
(01,00005) = SEPARATE INTRINSIC
END.
(01,00006) = SEPARATE INTRINSIC
***** STACK BUILDING CODE FOR LEVEL 02 *****
(02,00002)

```

FSI 1



```

03:000E:0          LT8          92FD
03:000E:1          LSH          9ALEX0
03:000E:2          LT8          9205
03:000E:3          STAG         9534
03:000F:1          = = = = =
03:000E:11         NAMC      (.01.00000) 6000
03:000E:15         STFF      AF
03:0010:0         BSET      9600
03:0010:1         = = = = =
03:0010:2         LT4R      BE
03:0011          270000740001 (3=1160000035000001*)
03:0012:0          LT8          9205
03:0012:2          STAG         9594
03:0012:4          LT4R      BE
03:0013          80000002800 (3=400000000024000*)
03:0014:0          LT8          9206
03:0014:2          STAG         9594
03:0014:4          BRUN         030011
03:0015:1          NVLD        FF
03:0015:2          NVLD        FF
03:0015:3          NVLD        FF
03:0015:4          NVLD        FF
03:0015:5          NVLD        FF

```

0.00000000 IS 0016 LONG

```

=====
NUMBER OF ERRORS DETECTED = 0
NUMBER OF SEGMENTS = 0
TOTAL SEGMENT SIZE = 29 WORDS
CORE ESTIMATE = 871 WORDS
STACK ESTIMATE = 5
PROGRAM SIZE = 8 CARDS
58 SYNTACTIC ITEMS, 11 DISK SEGMENTS.
PROGRAM FILE NAME = P19STAG/INFO.8700
CODE GENERATED:
COMPILE TIME = 1.127 SECONDS
ELAPSED: 0.547 SECONDS
PROCESSING: 0.547 SECONDS
I/O.
=====

```

IOMASK: 0 000000 000000  
 AREADESC: 5 000003 006F22  
 TIMECELL: 0 000000 000000  
 EVNT: 5 E10000 006F0C  
 FIBDESC: 5 C00003 70740F  
 BFFREVENT: 0 000000 000000  
 BFFREVENT2: 0 000000 000000  
 IOAL: 0 000459 904599  
 IOAW: 0 000000 000000  
 ACTUALBLOCK: 2 400000 000001  
 ACTUALKEY2: 0 000000 000000  
 DUPLCOPY: 0 000000 000000  
 JUNK1: 0 000000 000000  
 IOCHP: 0 030000 000351

USER DATA (SHOWN BELOW IN HEX AND CORRESPONDING GRAPHICS)  
 00(0000) 000000 000000 000000 000000 000000 000000 000000 000000 000000 /??/

12(JC) 5 C00003 004599 BUFFDESC  
 13(OD) 0 000000 000000 STDAREA  
 14(OE) 5 000001 120001 FMTBUFFDESC  
 15(OF) 0 000000 000000 FMTLOCK  
 16(10) 0 000000 000000 FILIOTIME  
 17(11) 7 408000 008633 PWRITES (66703500)  
 18(12) 7 408000 00876B PREADS (70039500)  
 19(13) 7 408000 00876C PWRITEN (70102000)  
 20(14) 7 408000 00876D PREADN (70150500)  
 21(15) 7 408000 00876E PSECK (70192200)  
 22(16) 7 408000 008723 PREADR (67735100)  
 23(17) 7 408000 008739 PSEARCH (68333000)  
 24(18) 7 408000 0086AB PLCKER (66625400)  
 25(19) 7 408000 008735 PRELEASE (68133000)  
 26(1A) 7 408000 0086DF PMVEDUT (67195500)  
 27(1B) 7 408000 0086E0 PMOVEIN (67199000)  
 28(1C) 7 408000 008746 PWATT (68799500)  
 29(1D) 7 408000 008779 PFLOAT (71611230)  
 30(1E) 0 00F900 330038 PCHCONTROL (66703500) (66643500) (67578000)  
 31(1F) 0 000000 00001E OFFSET  
 32(20) 0 000000 000000 RECORDCOUNT  
 33(21) 0 000000 000000 BLOCKCOUNT  
 34(22) 0 400000 000001 LOWER  
 35(23) 0 400000 000001 UPPER  
 36(24) 0 000000 00001E MINRECSZ  
 37(25) 0 000000 00001E RECSIZE  
 38(26) 0 000000 000011 I  
 39(27) 0 000000 000000 I  
 40(28) 0 000000 000000 AEXP  
 41(29) 1 419900 000000 DHEADER

00(0000) 0 000000 600000 0 001000 002800 0 040000 000000 0 003000 000000 0 241817 BRJA21  
 5(0005) 0 300014 000064 0 001358 000000 0 000000 013582 0 000000 000000 0 000000 000000  
 10(000A) 0 000001 000000 0 000000 000000 0 000000 000000 0 000000 000000 0 000000 000000  
 15(000F) 0 000000 000000 0 000000 000000 0 000000 000000 0 000000 000000 0 000000 000000  
 40(0028) 0 070201 03C4E2 0 020000 000000 0 000000 000000 0 000000 000000

42(2A) 0 000000 000000 FIBEOF  
 43(2B) 0 000000 000000 ACTNUM  
 44(2C) 0 000000 000000 SBLOCKING  
 45(2D) 0 000000 000000 SIGINF0  
 46(2E) 0 000000 000000 SOFFSET  
 47(2F) 0 000000 000000 CURRENTBLOCK  
 48(30) 2 000000 000000 FILEEVENT1  
 49(31) 2 000000 000000 FILEEVENT2  
 50(32) 0 000000 000000 OUTPUTTRANSLATION  
 51(33) 0 000000 000000 INPUTTRANSLATION  
 52(34) 0 000000 000000 USERROUTINES  
 53(35) 0 000000 000000

0031 (02-0003) 1 4EE001 602000 SIPM: OFFSET=0016 (0016+0000) IN STACK DEC  
 0030 (02-0002) 5 800001 E2265C DESC (PPRESENT-MON): DATA, LENGTH= 30  
 002F 0 (0000) 0 CRC940 E3C8C5 0 09C500 000000 0 000000 000000 0 000000 000000 0 000000 000000  
 002F 6(0006) 0 000000 000000 THRU 29(0010)  
 002F 3 000000 088451 RCW: LL=02, CNTRL STATE: EMCP SEGMENT 2 0451000000 (25066220)  
 SEG DESC: 3 80003E 438E24  
 CODE: >3 823482 269588 3 820280 879587  
 002E ----D(0?)=>3 CEE001 600002 \*MSCW: PREVIOUS MSCW 3 00203 D(01)=0316 IN STACK DEC

FID2

002D ----D102J--> } 000000 002000 RCHS DUNNY (RUN)  
002C ----D102J--> } 008919 008028 \*MSCH: PREVIOUS MSCH 2 0001; D101J=919D IN STACK 00R  
0000 = 80SR (06793)

```

1      $SET INTRINSICS LIST STACK LINEINFO
5      $LOADINFO
10000  PROCEDURE FCKINT(CASENO,ARY);
10100  VALUE CASENO,ARY;
10200  REAL CASENO;
10300  $PRAY ARY[*];
10400  BEGIN
10500  PROCEDURE P;
10600  NULL;
10700  WORD WP = P,4;
10800  WORD ARRAY STK[*];
10900  REAL INX,I,J,K;
11000  CASE CASENO OF
11100  BEGIN
11200  BEGIN % CASE 0 - THE REGISTERS
11300  ARY[0]:=D;
11400  ARY[1]:=O;
11500  ARY[2]:=D;
11600  ARY[3]:=F;
11700  ARY[4]:=S;
11800  ARY[5]:=LQSR;
11900  ARY[6]:=BQSR;
12000  ARY[7]:=SNR;
12100  END;
12200  BEGIN % CASE 1 - STACK NUMBERS
12300  ARY[0]:=SNR;
12400  ARY[1]:=(W:=WORDSTACK[SNR,STUFFITMSCW]).SEGDICTF;
12500  ARY[2]:=M[F],STKNRF;
12600  ARY[3]:=INTRINSICSTKNUM;
12700  END;
12800  BEGIN % CASE 2 - PROGRAM ID
12900  STK:=WORDSTACK[SNR,TASKDESC];
13000  I:=STK[MYNAME].[15:16];
13100  REPLACE POINTER(ARY[0],8) BY POINTER(STK[I],8)
13200  FOR STK[I].TOTALCHRSF;
13300  END;
13400  BEGIN % CASE 3 - RCW MSCW CHAIN
13500  STK:=STACKVECTOR[SNR];
13600  INX:=F-BQSR; % MY MSCW
13700  I:=-1;
13800  DO
13900  BEGIN
14000  ARY[I:=++I]:=STK[INX+1] & SETTAG(0); % RCW
14100  ARY[I:=++I]:=STK[INX] & SETTAG(0); % MSCW
14200  END
14300  UNTIL INX:=--STK[INX].DFF LSS MSCWOFEOJ;
14400  END;
14500  BEGIN % CASE 4 - MEMORY AREAS
14600  DISALLOW;
14700  TOUCH(ARY[0]);
14800  SUZZ(MEMLOCK);
14900  W:=M[I:=MEM[0].ADDRESSF];
15000  DO
15100  BEGIN
15200  IF W.SIZEF GTR INX THEN
15300  BEGIN
15400  J:=I;
15500  INX:=W.SIZEF;
15600  END;
15700  END
15800  UNTIL BOOLEAN((W:=M[W.ADDRESSF]).STOPPERF);
15900  ARY[0]:=INX-4; % SUBTRACT OFF THE LINKS
16000  ARY[1]:=J+2; % THE START OF THE AREA
16100  ARY[3]:=M[I:=LISTLOOKUP(4*7FFFFFFF,M,MEMBASE+1)].SIZEF-4;
16200  ARY[2]:=I-ARY[3]-1; % TO MEMORY AREA;
16300  UNLOCK(MEMLOCK);
16400  END;
16500  END;
16600  BEGIN % CASE 5 - STPW TO TASK AREA
16700  ARY[0]:=0 & STUFFDIRM(C,(W:=WORDSTACK[SNR,TASKDESC]).[19:4]+
16800  PSEUDOSTACKBASE,W);
16900  END;
17000  BEGIN % CASE 6 - PROCEDURE CALL
17100  WP:=6 & STUFFDIRM(C,SNR,F-M[F].DFF-BQSR);
17200  P;
17300  END;
17400  BEGIN % CASE 7 - INTRINSICS NAME
17500  PROCURE(MISSINGINTRINSICEVENT);
17600  REPLACE POINTER(ARY[0],8) BY POINTER(MCPINFO[105],8) FOR
17700  MCPINFO[105].TOTALCHRSF;
17710  LIBEPATE(MISSINGINTRINSICEVENT);

```

```

17800 END;
17900 BEGIN Z CASE 8 - DISK READ
18000 DISKWAIT(
18100   ARY, Z BUFFER
18200   -1, Z INX TO IOCW (LINKC)
18300   30, Z WORD COUNT
18400   ) & DISKADDRESSL(HLUNIT), Z ADDRESS AND UNIT NUMBER
18500   DISKREAD); Z HIGH ORDER BITS OF IOCW
18600 END;
18700 BEGIN Z CASE 9 - DISK READ WITH TAGS
18800 DISKWAIT(ARY,-1,30, & DISKADDRESSL(HLUNIT),DISKREADTAGS);
18900 PROGRAMDUMP(MCPREQUEST A*HAYS);
19000 REPLACE POINTER(ARY) BY 0 FOR ARY.LENGTHF OVERWRITE;
19100 END;
19200 BEGIN Z CASE 10 - RESIZE ARY TO 60 WORDS
19300 MAKEPRESENTANDSAVE(ARY); Z THE EASY WAY CUT
19400 I:=ARY.ADDRESSF; Z THE AREA - DO NOT USE ARY AGAIN
19500 INX:=M[I-3].ADDRESSF; Z MOM ADDRESS
19600 STK:=STACKVECTOR(SNR);
19700 J:=K:=F-BOSR; Z SEARCH BELOW ME ONLY
19800 W:=I & 1 PBITF & 5 TAG;
19900 WHILE K:=MASKSEARCH(W,4"FFFF" & 1 PBITF & 7 TAG,STK[K]) GTR
20000   MSCWOFEOJ DO
20100   BEGIN
20200     IF STK[K].IBITF THEN
20300       STK[K]:=+ & 3 PBITF & INX ADDRESSF
20400     ELSE
20500       STK[K]:=+ & 0 PBITF & 60 LENGTHF & INX ADDRESSF;
20600     K:=+1;
20700   END;
20800 K:=J;
20900 W:=INX & 5 TAG;
21000 WHILE K:=MASKSEARCH(W,4"FFFF" & 1 PBITF & 7 TAG,STK[K]) GTR
21100   MSCWOFEOJ DO
21200   BEGIN
21300     IF NOT STK[K].IBITF THEN
21400       STK[K]:=+ & 60 LENGTHF;
21500     K:=+1;
21600   END;
21700 FORGETSPACE(I);
21800 DISALLOW;
21900 I:=GETSPACE(60,SNR,0 & USAGEL(,1),INX);
22000 M[INX]:=ARY:=I & 1 PBITF & 60 LENGTHF & 5 TAG;
22100 ARY:=+;
22200 REPLACE POINTER(ARY) BY 0 FOR 60 OVERWRITE;
22300 END;
22400 BEGIN Z CASE 11 - STACK INFORMATION
22500 I:=MAXSTACKS-1;
22600 PROCURE(PROCESSCHANGELOCK);
22700 WHILE I:=MASKSEARCH(0 & 1 STACKKINDF,DUPLICATE,STACKINFO[I])
22800   GEO MCPSTACK DO
22900   BEGIN
23000     IF J:=STACKKIND(I) EQL TASKSTACK THEN
23100       INX:=+1
23200     ELSE IF J EQL SEGDICTSTACK THEN
23300       K:=+1;
23400     I:=+1;
23500   END;
23600 LIBERATE(PROCESSCHANGELOCK);
23700 ARY[0]:=INX;
23800 ARY[1]:=K;
23900 END;
24000 BEGIN Z CASE 12 - CODE FILE NAME
24100 STK:=STACKINWORDSTACK(SNR,STUFFITMSCH].SEGDICTF,+); Z D1 STACK
24200 STK:=MISTKICODEFILEDESC]; Z HEADER
24300 REPLACE POINTER(ARY[0],8) BY HEADERTITLE(STK) FOR
24400 STK[HEADERSIZE(STK)].TOTALCHRSF;
24500 END;
24600 END;
24700 EXIT; Z DONT LET BLOCKEXIT SEE ANY STRANGE MONS IN OUR STACK
24800 END.

```

TNTX/X (Sheet 2 of 9)

GENERAL  
INFORMATION

(FIXED  
LENGTH)

ROW ADDRESS  
WORDS

(VARIABLE  
LENGTH)

FILE NAME

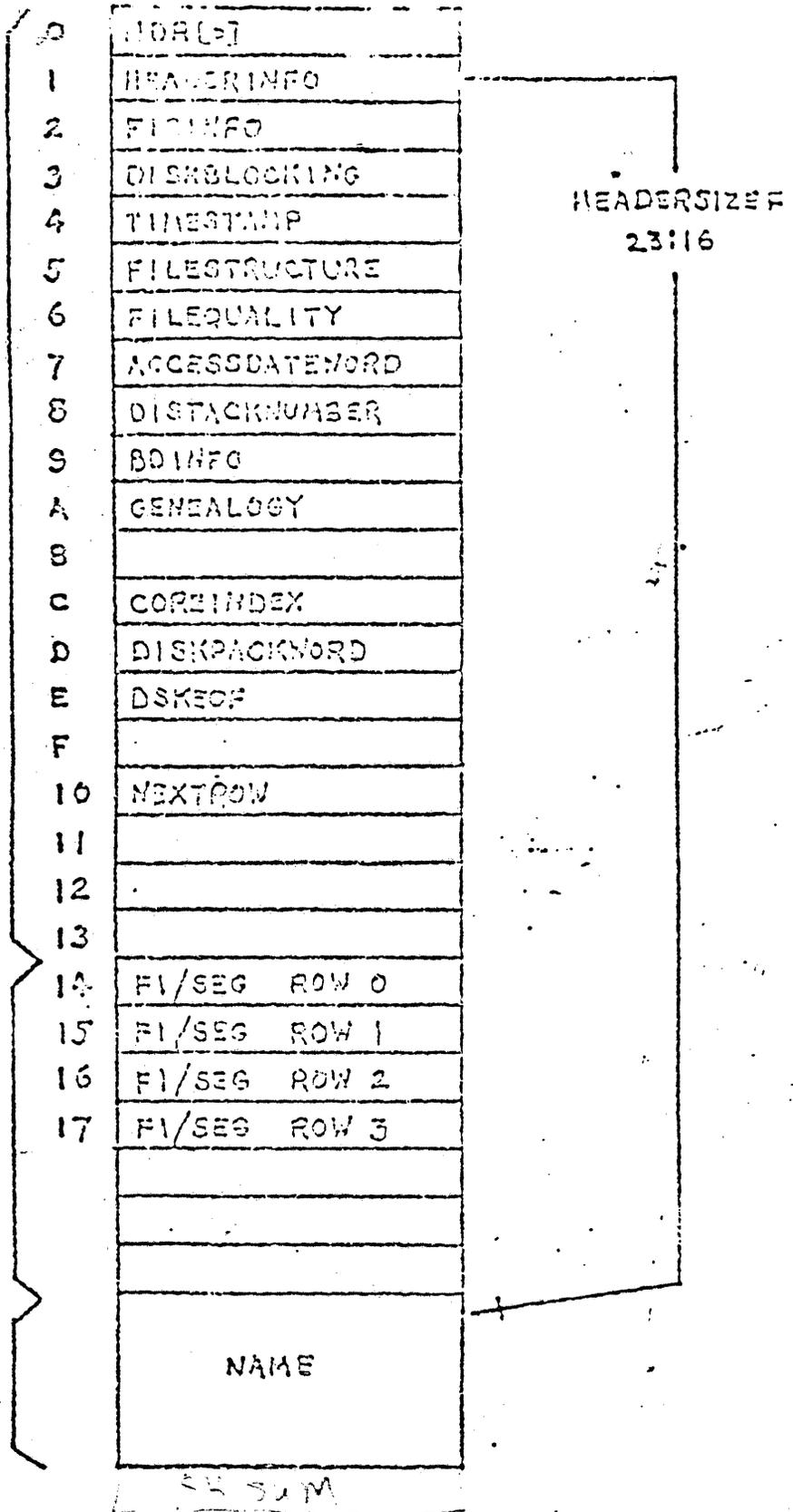


FIGURE 3. HEADER-NAME BLOCK.

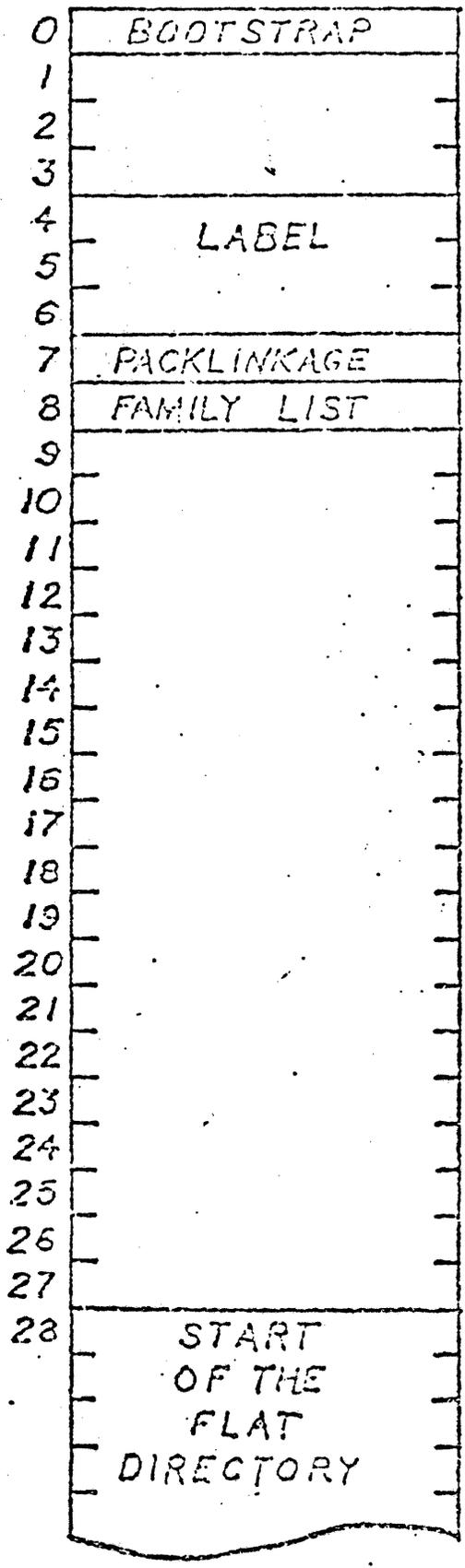


FIGURE 1 HALT/LOAD DISK LAYOUT

SEGMENT 23  
OF H/L EU.

0	FLAT HDR	0
1		1
4	AUDIT	4
5		5
	MCPINFO	
14		E
15		F
	UNITADDL	
23		17
24	HDR FOR SYSTEM DIRECTORY 001	18
59		38
60	HDR FOR FIRST BACKUP DIRECTORY	3C
95		5F
96	HDR FOR SECOND BACKUP DIRECTORY	60
131		83
132		84
	HEADERS	
599		257

DECIMAL ↗

↖ HEX

FIGURE 2 FIRST ROW OF THE  
FLAT DIRECTORY

PACK ACCESS STRUCTURE (PAST)

THE PAST HAS A FIXED SIZE OF 75 BLOCKS, EACH OF 8 DISK SEGMENTS. TO FIND AN ENTRY IN THE PAST FOR THE FAMILY, A BLOCK IS CALCULATED BASED ON AN ARITHMETIC MANIPULATION OF THE CHARACTERS IN THE FAMILY NAME (IE. HASHED). THIS ENTRY CONTAINS POINTERS IDENTIFYING THE SECTION OF THE FAST WHICH IS APPLICABLE TO THE FAMILY. A SIMPLE SEARCH IS THEN PERFORMED ON THE PAST BLOCK UNTIL THE ENTRY WITH THE GIVEN FAMILY NAME IS FOUND. GIVEN BELOW IS AN EXAMPLE PAST ENTRY. NOTE THAT ALL PAST, AS WELL AS FAST BLOCKS, ARE CHARACTER ORIENTED (1440 CHARACTERS PER BLOCK).

CHARACTER -----	CONTENT -----	COMMENT -----
00	01	PNUM - NUMBER OF FAMILY MEMBERS.
01	04	
02	C4	
03	C9	
04	E2	
05	D2	PNAME - FAMILY NAME. FIRST CHARACTER IS THE NUMBER OF CHARACTERS IN THE NAME.
06	00	
07	00	
08	20	
09	18	PSERIAL - SERIAL NUMBER IF PACK AND UNIT NUMBER IF HPT.
10	BB	
11	58	
12	08	PTIMESTAMP - TIME THIS ENTRY WAS MADE.
13	70	
14	34	PFASTIX - BEGINNING AND ENDING FAST BLOCKS FOR THIS FAMILY (BEGINNING OF FILE RELATIVE).
15	00	
16	09	
17	40	
18	00	
19	95	

BLKW[0]

0

1

2

THIS WORD HAS THE SAME

3

FORMAT AS WORD 0 OF A HEADER.

4

5

6

BLOCKNOLOC, THIS BLOCK'S NUMBER.

7

8

9

STARTBLKLOC

10

11

12

FIRSTCHARLOC, FIRST VALID CHARACTER IN THIS BLOCK.

13

14

LASTCHARLOC, LAST VALID CHARACTER IN THIS BLOCK.

15

16

CONTINUATION LENGTH

17

18

19

PFIRSTAVAILCHAR, THE LOWEST AVAILABLE CHARACTER THAT MAY BE USED.

.

.

.

.

1434

1435

1436

CHECKSUMCHARS

1437

1438

1439

FIGURE 4 PACK ACCESS STRUCTURE (PAST) BLOCK.

BLKW[0]

CHECKSUM CHARS

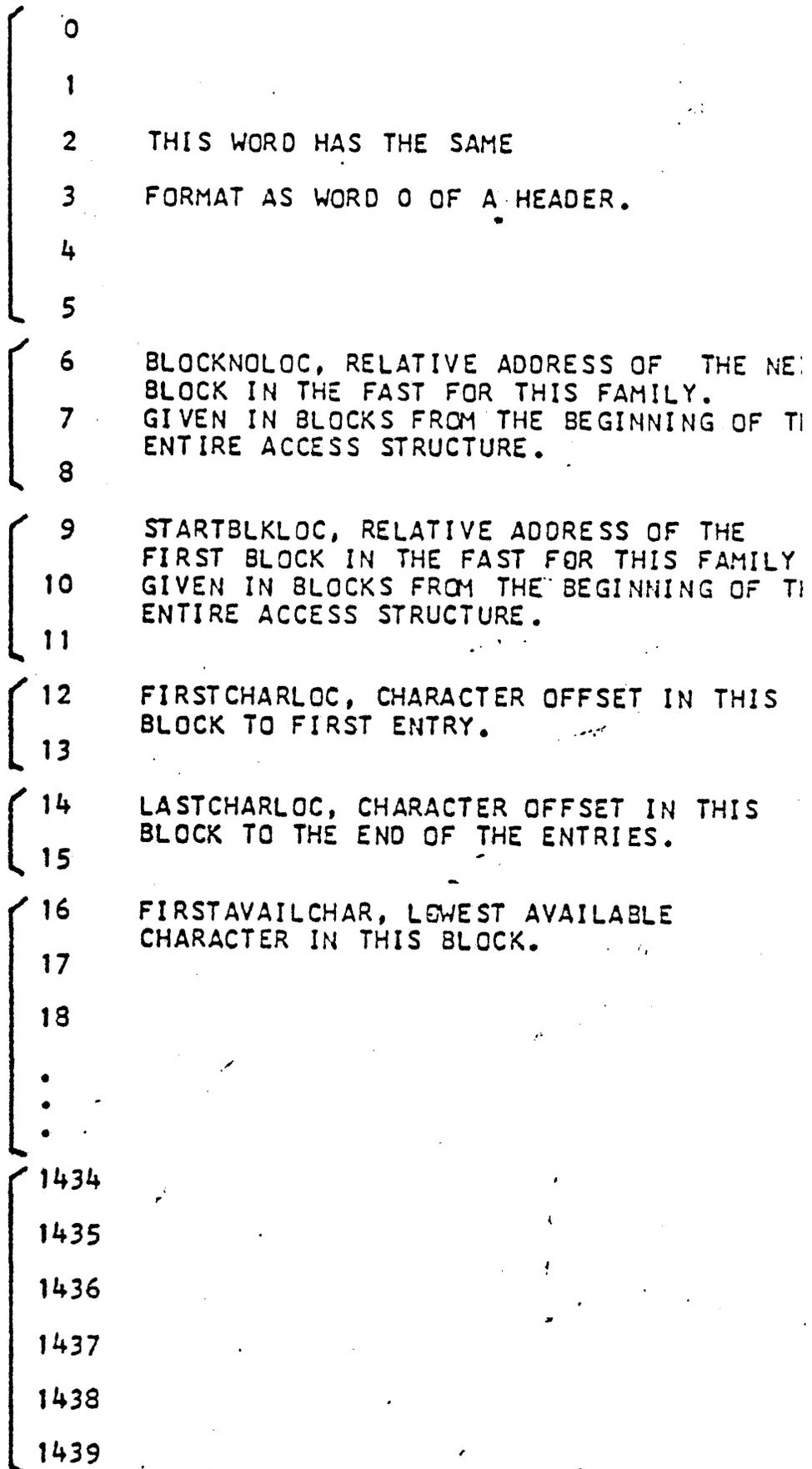


FIGURE 5 FILE ACCESS STRUCTURE (FAST).

AN EXAMPLE FAST ENTRY FOR FILE C HAS BEEN GIVEN BELOW:

<u>CHARACTER</u>	<u>CONTENT</u>	<u>COMMENT</u>
00	27 }	POINTS TO A FILE AND HAS NO BROTHER. THERE IS 1 CHARACTER IN THE FILE'S NAME. NAME OF THE FILE IS 8"C" HEADER IS AT OFFSET 4"FOO" IN THE FLAT DIRECTORY AND IS 32 WORDS LONG.
01	01 }	
02	C3 }	
03	00 }	
04	40 }	
05	0F }	
06	00 }	

THE WAY THE ABOVE INFORMATION WAS OBTAINED WAS BY USE OF THE INFORMATION PROVIDED BELOW:

CHARACTER 0 IN A FAST ENTRY IS CALLED THE "ID" CHARACTER AND HAS THE FOLLOWING FORMAT:

7:1 BROTHERF, ON IF THIS FILE HAS A BROTHER.

6:2 IDTYPEF, BROKEN DOWN AS FOLLOWS:

6:1 DIR, ON IF THIS ENTRY POINTS TO A DIRECTORY ENTRY.

5:1 FYLE, ON IF THIS ENTRY POINTS TO A FILE.

4:5 ENTRYL, LENGTH OF THIS ENTIRE ENTRY IN CHARACTERS.  
*INCLUSIVE!*

CHARACTER 1 IN A FAST ENTRY IS THE LENGTH OF THE NAME IN CHARACTERS. (*NOT SELF INCLUSIVE*)

B	E
D	E
F	P
	G
	A

CHARACTERS 2 THROUGH N ARE THE NAME. "N" REPRESENTS THE LAST CHARACTER IN THE NAME.

IF THE FYLE BIT IS ON, THE FOUR CHARACTERS FOLLOWING THE NAME ARE IN THE FORMAT GIVEN BELOW:

31:11 FILELENGTHF, LENGTH OF THE HEADER IN WORDS.

20:21 FILELOCF, POINTS TO THE HEADER IN THIS FAMILY'S FLAT DIRECTORY AND IS GIVEN IN SEGMENTS RELATIVE TO THE BEGINNING OF THE FLAT DIRECTORY.

IF THE THE FYLE BIT AND THE DIR BIT HAD ALSO BEEN ON THEN THE FOUR CHARACTERS DESCRIBED IMMEDIATELY ABOVE WOULD HAVE BEEN FOLLOWED BY TWO CHARACTERS OF THE FOLLOWING FORMAT (NOTE THAT THIS IS NOT THE CASE IN THE EXAMPLE ABOVE):

15:16 DIRDELFP, POINTER TO THE NEXT LEVEL AND IS GIVEN IN CHARACTERS RELATIVE TO THE BEGINNING OF ~~THE BLOCK.~~  
THIS ENTRY

IF THE DIR BIT HAD BEEN ON AND THE FYLE BIT HAD NOT BEEN ON THEN THE "4" CHARACTERS DESCRIBED ABOVE WOULD HAVE NOT EXISTED IN THE ENTRY AND IN THIS SPECIFIC ENTRY, THERE WOULD HAVE BEEN ONLY 5 CHARACTERS.

FIGURE 5 SHOWS HOW AN ENTIRE FAST BLOCK IS LAYED OUT. NOTICE ITS SIMILARITY TO THE PAST BLOCK LAYOUT OF FIGURE

ABSOLUTE SEGMENTS 4 AND 5 CONTAIN THE LABEL ON DISK UNITS. THIS LABEL IS ORIGINALLY SET UP BY THE SYSTEM LOADER IN THE PROCEDURE "WRITEHPTLBL" AND CONTAINS THE FOLLOWING INFORMATION:

8"VOL1"	% VOLUME 1 LABEL
UNIT FOR 6 DIGITS	% SERIAL NUMBER IF PACK AND
	% UNIT NUMBER IF DISK
8" "	
8"DISK	% FAMILY NAME
8"67"	% INTERCHANGE CODE
8"0" FOR 1	% USAGE CODE
8" " FOR 6	% RESERVED
8" " FOR 14	% OWNER FIELD
8" " FOR 28	% RESERVED
8" "	% INDICATES THAT THIS IS THE
	% BASE UNIT
8"VOL2"	% VOLUME 2 LABEL
MCPINFO[1] FOR 5 DIGITS	% INITIALIZATION DATE
8"67MC"	% INITIALIZING THE SYSTEM
MARK	
LEVEL	
MCPINFO[7].SEGADDRESSF FOR 8 DIGITS	% ABSOLUTE SEGMENT OF THE
	% START OF THE FLAT DIRECTORY.
8"00000002"	% ABSOLUTE STARTING SEGMENT OF
	% THE MASTER AVAILABLE TABLE.
8"00000000"	
8"0" FOR 20	% RESERVED
8"1" FOR 1	% IN USE FLAG
8"0" FOR 70	% RESERVED

MISCELLANEOUS INFORMATION.

ABSOLUTE SEGMENT 7 ON DISK IS ORIGINALLY BUILT BY THE SYSTEM  
LOADER IN THE PROCEDURE "WRITEHPTLBL." ITS WORDS CONTAIN THE FOLLOWI  
INFORMATION

WORD -----	CONTENTS -----
0	BASE UNIT
1	TIME OF DAY
2	DATE (FROM MCPINFO[1])
3	SITE NUMBER
4	INITIALIZED TO A VALUE OF 1 BY THE LOADER
28	INITIALIZED TO A VALUE OF 4A3 (DEFAULTRESERVEDHLDISKSIZE) BY THE LOADER
29	INITIALIZED TO 5 (CURLABLEVEL BY THE LOADER

WORD 0

47:16 MARKERF Records state of this reader.  
States are:

HEADERMARK \_\_\_\_\_ 4"3F3F" header of file

INUSEMARK \_\_\_\_\_

AVAILMARK \_\_\_\_\_ 4"3C3C" not in use area within header block. Will be used if system needs to enter file header.

BADAREAMARK \_\_\_\_\_ 4"3A3A" not in use area that the MCP will not use as a fault was detected in directory header that used to reside at that disk address.

MCPUSEMARK \_\_\_\_\_ 4"3939" in use file by the MCP and it is not to be removed. What would happen if MCP file header is removed?

Note that the MARKF is so arranged that it easily can be searched for with a SCAN pointer expression UNTIL construct. Also note that single bit error can be detected.

31:11 HDRBIØCKLENGTHE Size of this header block. Points to next header block.

20:21 HDRLØCATIONF Link back to the Access Structure for a redundancy check.

Available areas are specified as a length of 1.  
Individual Job description headers not in system director nor access structure.

THE MCP INFORMATION TABLE, MCPINFO, IS ORIGINALLY CREATED BY THE SYSTEM LOADER AND MAINTAINED BY THE MCP. THIS TABLE IS LOCATED IN A FIXED LOCATION IN THE FIRST ROW OF THE FLAT DIRECTORY AND ITS CONTENTS ARE SHOWN BELOW. IN THE LOADER, THE FIXED LOCATION OF MCPINFO IS REFERRED TO AS "MCPINFOLOC."

WORD  
-----

INFORMATION  
-----

0	HEADER[0] WORD FORMAT. WILL CONTAIN THE MCP USE MARK IN THE MARKERF, THE BLOCK LENGTH IN WORDS IN THE HDRBLOCKLENGTH AND SEGMENT INDEX INTO THE FIRST ROW OF THE FLAT DIRECTORY WHERE THIS TABLE IS FOUND (A SELF POINTER).
1	DATE
2	NOT USED. USE TO BE THE OLD CORE FACTOR USED IN MEMORY MANAGEMENT.
3	SIZE OF THE MCP'S NAME.
4	SIZE OF THE INTRINSIC FILE'S NAME.
5	CREATION DATE OF THE MCP FILE.
6	NOT USED. USE TO BE THE NUMBER OF PSEUDO-READERS.
7	DISK ADDRESS OF THE DIRECTORY HEADER. THIS LOCATION IS LEFT NEGATIVE BY THE SYSTEM LOADER AS NOTE TO THE MCP IF A COLD START IS BEING PERFORMED
8	DIRECTORY ROW SIZE. 600 SEGMENTS BY DEFAULT.
9	6"MCPINFO" USED BY THE LOADER OR THE MCP AS A PARTIAL WAY OF VALIDATING THE EXISTANCE OF THE MCPINFO TABLE.
10	NUMBER OF AUTO PRINTERS PRESENTLY RUNNING ON THE SYSTEM.
11	ENDING ADDRESS OF THE FIRST OVERLAY ROW.
12	HEADER SIZE OF THE DISK DIRECTORY.

MCPINFO (CONTINUED)

14 SYSTEM SERIAL NUMBER.  
15 THE CURRENT SUMLOG NUMBER.  
16 OVERLAY ROW SIZE.  
17 MEMORY DUMP DISK SPACE.  
18 LAST MIX NUMBER USED.  
19 SETTING OF THE PRESENT SYSTEM RUN TIME OPTIONS.  
20 DLAYGOAL. MAY BE CHANGED WITH THE "SF" ODT MESSA  
21 AVAILMIN. MAY BE CHANGED WITH THE "SF" ODT MESSA  
22 FACTOR. MAY BE CHANGED WITH THE "SF" ODT MESSAG  
24-27 DATACOM FILES PREFIX ID. "SYSTEM" BY DEFAULT BU  
MAY BE CHANGED WITH THE DC <PREFIX> <ETX> OOT  
MESSAGE.

29-31 K6 FILES PPEFIX

32 -----RSPINFO-----

37 - DCPINFO.

38 MCPINFOLEVEL.

39 MEMORY PRIORITY FACTOR. MAY BE CHANGED WITH THE  
"SF" OOT MESSAGE.

40 RESERVING RESTART MASK.

41 SYSTEM/SUMLOG IAD ADDRESS.

42 SYSTEM/SUMLOG IAD ADDRESS.

43 SYSTEM/SUMLOG ROW LENGTH.

44 LOAD LEVEL FOR MULTIPLEXORS  
45-52 -----RESERVED FOR 87700-----

## MCPINFO (CONTINUED)

53-58	SUBSTITUTE BACKUP INFO FROM THE "SB" MESSAGE.
60-104	BASE MCP CODE FILE NAME IN STANDARD FORM.
101-104	DM MONITOR ID.
105-147	SYSTEM/INTRINSICS ID.
148-179	SYSTEM SUPERVISOR ID.
180	CATALOG FAMILY SERIAL NUMBER.
181-184	CATALOG FAMILY TITLE.
272-280	MPX3 RESERVED PATH BITS
281-283	SYSTEM/SWAPDISK PACKNAME.
284-286	MEMORY DUMP DISK ALLOCATIONS.
287-289	CM # (MEMONLY) MCP CODE FILE HEADER DISK ADDRESSES.
290-292	MCP CODE FILE HEADER DISK ADDRESSES.
293-295	OVERLAY ROW ADDRESSES.
300	CHECKSUM.

CREATION OF A LARGE SYSTEMS HALT LOAD PACK WITH THE CM METHOD  
=====

1. Mount the pack that is to become a H/L pack on a drive.
2. RC the pack with a name of DSK.

RC PKNN NAME=DSK

3. Copy in all SYSTEM files.

COPY & COMPARE = FROM SYSTEM TO DSK(PACK)

4. CM to the MCP. This will write the BOOTSTRAP and transfer some MCP structures from the current H/L unit to the new H/L unit. These structures include MCPINFO and unit table.

CM SYSTEM/MCP ON DSK

5. Change the name of the pack to DISK.

LB PKNN NAME=DISK

6. After the label change, halt system or remove pack.

7. To bring up system on new H/L unit, power off all packs except the new H/L unit.

8. Change H/L "POINTER" (BLOCK (B6700,B6800), IOM CARD (B7000), BOOTSTRAP via SOFTCON (B5900,B6900)) to point to new H/L unit.

9. Load the system. At some point STARTSYSTEM will hang in the waiting entries. It will be looking for the CATALOG FAMILY. STARTSYSTEM should be IL'ed to the new H/L unit.

<STARTSYSTEM MIX NUMBER>ILPK NN

10. Another CM should be done to the currently running MCP. This will allow DUMPANALYZER to see the MCP properly.

CM SYSTEM/MCP

11. To allow a USERDATAFILE (USERCODE file) to be created one must force the MCP forget the serial number of the old H/L unit. Failure to do this step will result in the MCP trying to place the USERDATAFILE on the old H/L unit. In the following procedure it is assumed the site does not have a packed named "QQQSSSJJJ":

DL USERDATA ON QQQSSSJJJ  
DL USERDATA ON DISK

12. One can now make a USERDATAFILE.

B O O T S T R A P   C O D E

ADDRESS MEMORY		CONTENTS MEMORY	COMMENTS
000	0	8A0000000003	HOME ADDRESS WORD. SYNC I/O.
001	3	8000FEC00040	SEGMENT DESCRIPTOR.
002	0	A800B000001C	INFORMATION FOR MCP.
003	7	000000084001	PCW: LL 1, CNTL, 1:000:0
004	0	000000000040	FOR HARDLOAD RESULT DESCRIPTOR.
005	0	0000FEC00040	BUFFER DESCRIPTOR.
006	0	121000000000	IOCW. READ FORCE TAG.
007	0	510100382875	CDL.
008	3	B0B095B9B234	ZERO, ZERO, SPRR, LT8 34
009	3	B31E0095B9B2	LT16 1E00, SPRR, LT8 35
00A	3	35B095B9B230	ZERO, SPRR, LT8 30
00B	3	B095B9B225B0	ZERO, SPRR, LT8 25, ZERO
00C	3	95B9AE400300	SPRR, MKST, NAMC 0, 3
00D	3	000004981B20	VALC 0, 0, VALC 0, 4, FLTR 27:32:5
00E	3	054000BB0007	NAMC 0, 0, OVRN, VALC 0, 7
00F	3	B00004982714	ZERO, VALC 0, 4, FLTR 39:20:4
010	3	0491400795BA	LOR, NAMC 0, 7, RDLK
011	3	B5B22895B895	DELT, LT8 28, RPRR
012	3	8F95840005A1	INCN, PAUS, VALC 0, 5
013	3	6012B08EB203	BRTR 12:3, ZERO, CHSN, LT8 3
014	3	95B44000BAAB	STAG, NAMC 0, 0, OVRD, ENTR

HARDLOAD RESULT DESCRIPTOR AT HOME ADDRESS[4]

[16:17] ERROR FIELD  
 [24:08] UNIT NUMBER  
 [32:05] CHANNEL NUMBER

REVIEW QUESTIONS  
-----  
SUBROUTINE EXECUTION  
-----

A PROCEDURE IS DECLARED AT LEXICOGRAPHIC LEVEL 3 AND CALLED AT LEXICOGRAPHIC LEVEL 5. WHAT DISPLAY REGISTER POINTS TO THE RSCW FOR THIS PROCEDURE. D3

*addressing environment*

IN A MARK STACK CONTROL WORD, THE DISPLACEMENT FIELD ( BITS [ 35:16 ] ) IS RELATIVE TO BASE.

WHAT PROCESSOR REGISTER CONTAINS THE MEMORY ADDRESS OF THE MOST RECENTLY BUILT MARK STACK CONTROL WORD. F

THE "MOVE TO STACK" MACHINE INSTRUCTION CAUSES THE CENTRAL PROCESSOR TO ACCESS A DATA DESCRIPTOR AT  $D[0] + 2$ .

TO WHAT DOES THIS DATA DESCRIPTOR POINT. STACK VECTOR AREA

SEGMENT DESCRIPTORS CONTAINED IN THE USER PROGRAM SEGMENT DICTIONARY ARE ADDRESSED RELATIVE TO WHAT PROCESSOR REGISTER. D1

GIVEN THE FOLLOWING RETURN CONTROL WORD: 3 500621 894000  
WHERE WILL THE PROCEDURE EXIT TO.

	RSC			PIR			NUL			SD1					
0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0
0	1	0	0	1	0	0	0	0	1	0	0	1	0	0	1
1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	1
1	1	0	0	0	0	1	0	1	0	0	0	1	0	0	1

SEGMENT NUMBER 4

PIR 215

PSR 3

LEXICOGRAPHIC LEVEL 5

NORMAL OR CONTROL Control

Identify What is located D0 + 3. *Hardware Interrupt*

REVIEW QUESTIONS  
-----  
MACHINE LANGUAGE  
-----

- (F) (F) MORE THAN ONE PROCESS CAN USE THE SAME SEGMENT DICTIONARY.
- (T) (F) ONE PROCESS MAY ACCESS MORE THAN ONE SEGMENT DICTIONARY.  
INTRINSICS, LIBRARIES, MCP
- (T) (F) EACH CODE SEGMENT OF A PROGRAM MAY BE DESCRIBED BY SEVERAL SEGMENT DESCRIPTORS.
- (T) (F) A CODE SEGMENT MAY BE REFERENCED BY SEVERAL PROGRAM CONTROL WORDS, EACH OF WHICH GIVE AN ENTRY POINT INTO THE CODE.
- (T) (F) THE LEXICOGRAPHIC LEVEL AT WHICH A PROCEDURE WILL EXECUTE DEPENDS ON THE LEXICOGRAPHIC LEVEL OF THE CALLING ROUTINE.
- (T) (F) A PROCEDURE RUNNING AT LEXICOGRAPHIC LEVEL THREE MAY DECLARE A PROCEDURE WHICH WILL RUN AT LEXICOGRAPHIC LEVEL THREE. EXPLAIN.

EXPLAIN THE FOLLOWING PROC REGISTERS

- $S_T$  - Points to the top of stack in memory
- $F_T$  - Points to current logical bottom of stack, <sup>highest</sup> MSCW
- $D_0$  - Points to the bottom of MCP, MSCW in it
- $D_1$  - Points to segment dictionary of MCP
- $D_2$  - Points to global variables
- BOSR - hardware register,
- LOS R
- PSR - offset in current code word
- PIR - offset to current word
- SDI -

Stack overflow  
if exceeds LOSR

A PROGRAM ABORTED AT 2B : 26D : 3

DI PIR PSR

*J.H.*

JUST BEFORE THE PROGRAM ABORTED, D[1] WAS SET TO 113D7, AND  
D[2] WAS SET TO 110BF.

THE CONTENTS OF SEVERAL MEMORY LOCATIONS ARE GIVEN BELOW:

ADDRESS

01843	0	E53100	652780	0	1EF50B	01FA31	0	DABDAB	0ABD0
113E3	5	080000	2407EB	3	800000	F15522	3	000000	00050
113F5	3	800001	122FA1	5	08001B	A40870	3	800002	22150
113FB	5	880008	70C441	3	000001	0006A1	3	800000	016A4
113FE	3	800000	F1420E	3	800002	B15FE2	3	800001	315F0
11401	3	800003	013FF0	3	800028	A13C8A	3	800001	115CF
11404	3	000000	0005C3	5	080001	7403B2	3	000000	0006A
110C4	5	800001	0110A4	0	000000	00Q213	0	528000	04000
130B9	3	41ED8D	958E60	3	04B99A	2E01A0	3	218B20	0Z9A1
13EF6	3	38A610	038810	3	075005	A4700A	3	88A262	40B38
14210	3	C83453	880985	3	BFFFFFF	FFFFFF	3	5695A3	986C3
16A4A	3	B20380	958C95	3	B9AE60	2EB208	3	ABA3A3	423F2

DETERMINE WHY THE PROGRAM ABORTED

DI  
+ SDI  
SEG DESC.

113D7  
2B  
~~1141A~~  
11402

13C8A ADDRESS OF SEG DESC.  
36D PIR  
13EF7 SEG DESC.

AD IS INDEX + LOAD VALUE

1) Describe the function of each of the following words:

IRW (Indirect Reference word).

SIW (Step Index word).

Data Desc:

SCW (Software Control word).

PCW (Program control word).

MSCW (Mark Stack Control word).

RCW (Return Control word).

TOSCW (Top of Stack Control word).

Segment Desc:

SIRW (Stacked Indirect Reference word).

2) Create machine operator mnemonics for the following algol constructs:

$A = (1, 2)$ ;  $B = (2, 3)$ ;  $C = (3, 4)$ ;  $D = (2, 5)$ ;  
 $P = (2, 6)$ .

$A := B * B + L$ ;

$[A + 2[B]] := 5$

$[L] := D[L, I]$ ;

what algol constructs correspond to these code strings:

a) MAMC 2, 2; VALC 2, 3; MAMC 2, 4; ONE; MXCV;  
ADD; MAMC 2, 5; ONE; MXCV; ONE; MXCV; ADD;  
DOD.

b) MEST; MAMC 2, 6; VALC 3, 2; VALC 2, 3; ENTR  
MEST; MAMC 2, 6; VALC 3, 2; VALC 2, 3; ENTR; STOD

3)  $DZ = 22\phi\phi$ ,  $SNR = 2$ ;  $F = 4\phi 2\phi$

$22\phi\phi = 3$   $4\phi\phi\phi\phi\phi\phi\phi\phi\phi\phi\phi$

$22\phi 2 = 5$   $8\phi\phi\phi 2\phi\phi\phi 1\phi\phi\phi$

$1\phi\phi\phi = 5$   $8\phi\phi\phi 4\phi\phi\phi 2\phi\phi\phi$

$1\phi\phi 1 = 5$   $8\phi\phi\phi 4\phi\phi\phi 3\phi\phi\phi$

$1\phi\phi 2 = 5$   $8\phi\phi\phi 4\phi\phi\phi 4\phi\phi\phi$

$1\phi\phi 3 = 5$   $8\phi\phi\phi 4\phi\phi\phi 5\phi\phi\phi$

$1\phi\phi 4 = 5$   $3\phi\phi\phi 4\phi\phi\phi 6\phi\phi\phi$

$3\phi 1\phi = 3$   $8\phi\phi\phi 2\phi\phi\phi 4\phi\phi\phi$

$4\phi 1\phi = 3$   $8\phi\phi\phi 1\phi\phi\phi 8\phi\phi\phi$

$4\phi 2\phi = 3$   $8\phi\phi\phi 1\phi\phi\phi 8\phi\phi\phi$

$5\phi 1\phi = 3$   $8\phi\phi\phi 2\phi\phi\phi 4\phi\phi\phi$

$6\phi 1\phi = 3$   $8\phi\phi\phi 1\phi\phi\phi 8\phi\phi\phi$

The processor executes an exit operator. What values are contained in  $DZ$ ,  $DL$ , and  $D\phi$  after the operator completes?

4) What do you want to learn as a result of attending this class?

How to answer all these questions correctly  
I want to be able to ~~read~~ understand dumps better.

REVIEW QUESTIONS  
-----  
SUBROUTINE EXECUTION  
-----

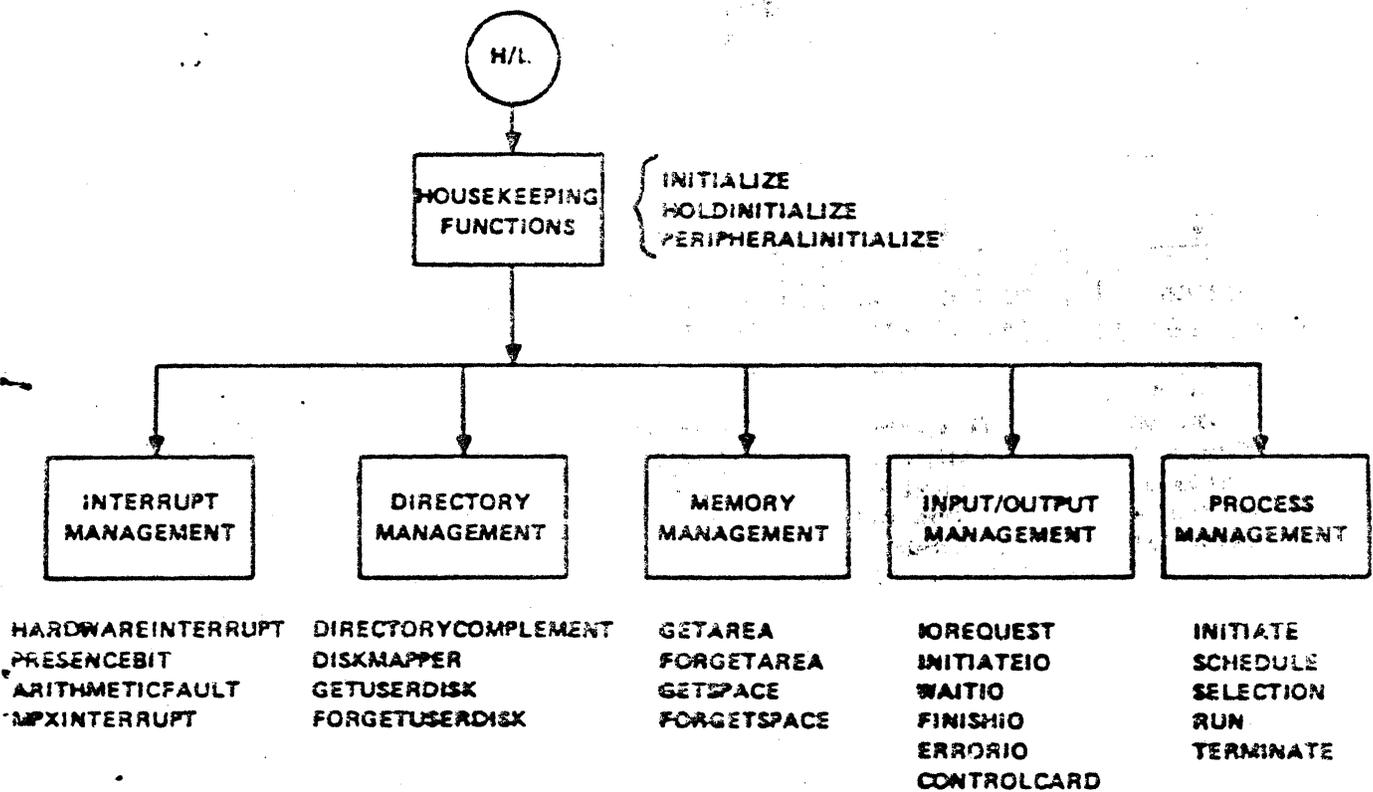
Self

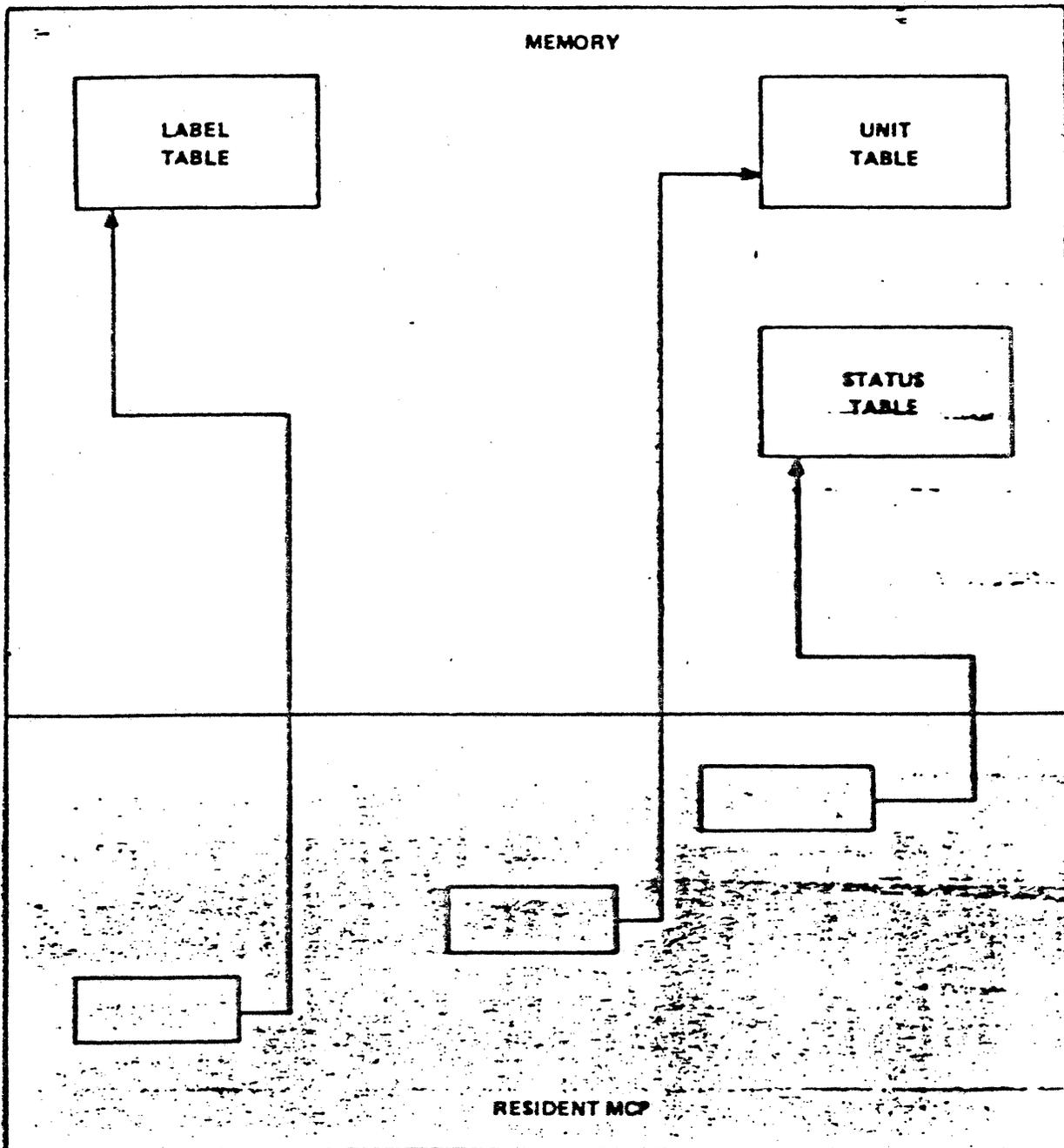
- (1) (F) DISPLAY REGISTERS CONTAIN AN INDEX VALUE WHICH, WHEN ADDED TO THE CONTENTS OF THE "BOSR" REGISTER, LOCATES A MARK STACK CONTROL WORD.
- (1) (F) THE D[2] REGISTER POINTS TO THE SAME LOCATION AS DOES THE BOSR REGISTER.
- (1) (F) THE TOP-OF-STACK CONTROL WORD IS BUILT BY THE "MOVE TO STACK" MACHINE INSTRUCTION.
- (1) (F) A STUFFED INDIRECT REFERENCE WORD MAKES IT POSSIBLE TO ADDRESS MEMORY LOCATIONS WHICH ARE NOT WITHIN ANY STACK.  
*True by way → PROCESS*
- (1) (F) MARK STACK CONTROL WORDS AND RETURN CONTROL WORDS ALWAYS OCCUR IN PAIRS. EXPLAIN.  
*Generally they do, but not always*
- (1) (F) WHEN EXITING A PROCEDURE, IF BIT 13 OF THE RETURN CONTROL WORD IS A ONE, D[1] IS SELECTED AS THE SEGMENT DICTIONARY BASE REGISTER. OTHERWISE, D[0] IS SELECTED.
- (1) (F) DISPLAY REGISTER ONE ALWAYS POINTS TO A PROGRAM SEGMENT DICTIONARY. EXPLAIN.
- (1) (F) THE "STACK VECTOR" IS AN ARRAY OF DATA DESCRIPTORS, EACH OF WHICH DESCRIBES AN AREA OF MEMORY ALLOCATED AS A STACK OR SEGMENT DICTIONARY. ALL STACKS AND SEGMENT DICTIONARIES, INCLUDING THE MCP SEGMENT DICTIONARY, ARE THUS DESCRIBED.
- (1) (F) THE MEMORY ADDRESS OF THE STACK VECTOR DESCRIPTOR DEPENDS ON THE SETTING OF THE D[0] REGISTER.
- (1) ( ) THE "F" REGISTER OF A CENTRAL PROCESSOR POINTS TO THE LAST MARK STACK CONTROL WORD IN THE STACK WHICH IS ACTIVE ON THAT PROCESSOR.

THE FIRST WORD ( WORD ZERO ) OF A PROCESS STACK <sup>SHOULD</sup> CONTAINS EITHER \_\_\_\_\_ TOSCW <sup>NOT ACTIVE</sup> OR Processor ID \_\_\_\_\_

THE SECOND WORD ( WORD ONE ) CONTAINS MSCW \_\_\_\_\_  
EXPLAIN.

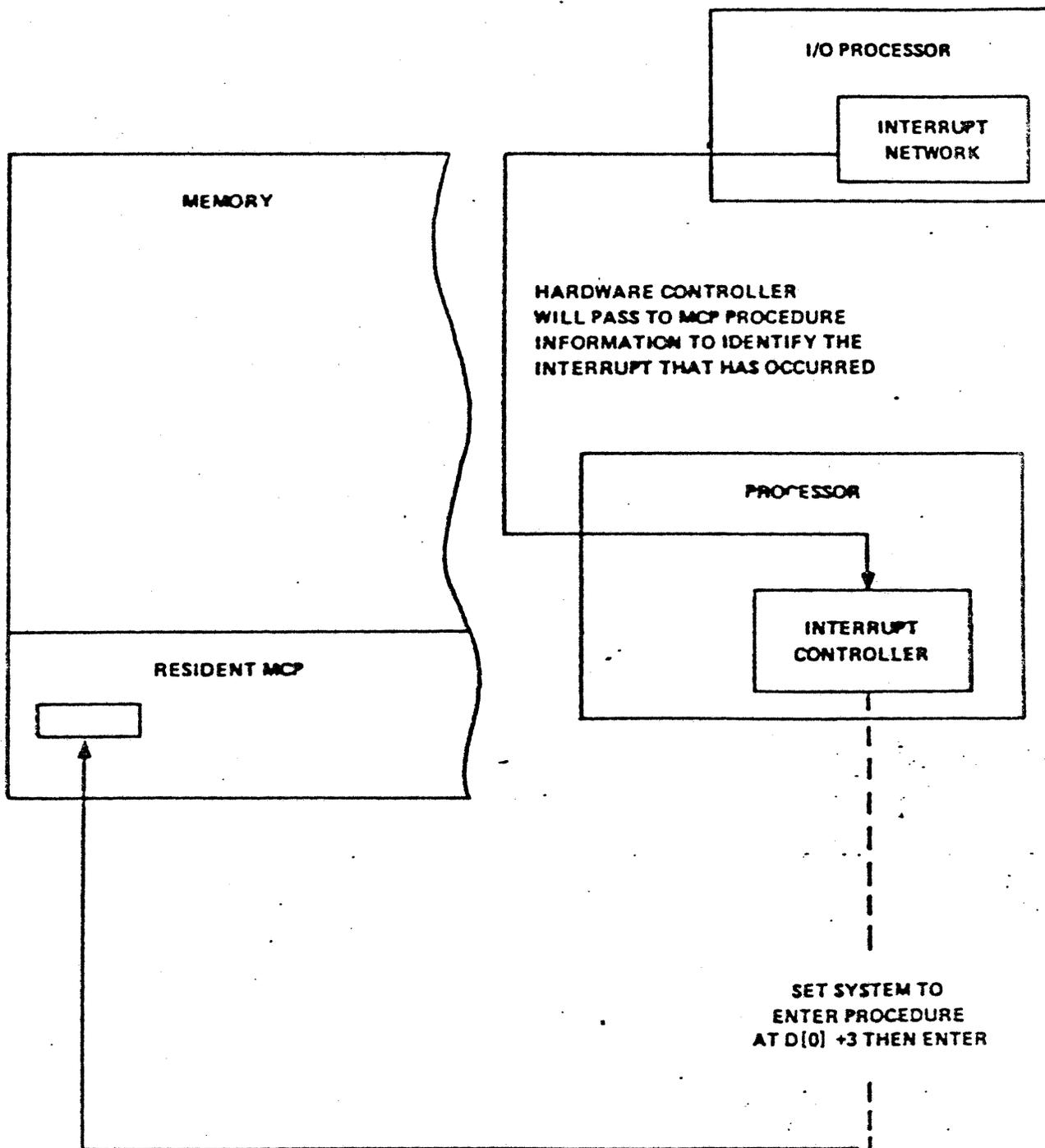
# MCP ORGANIZATION



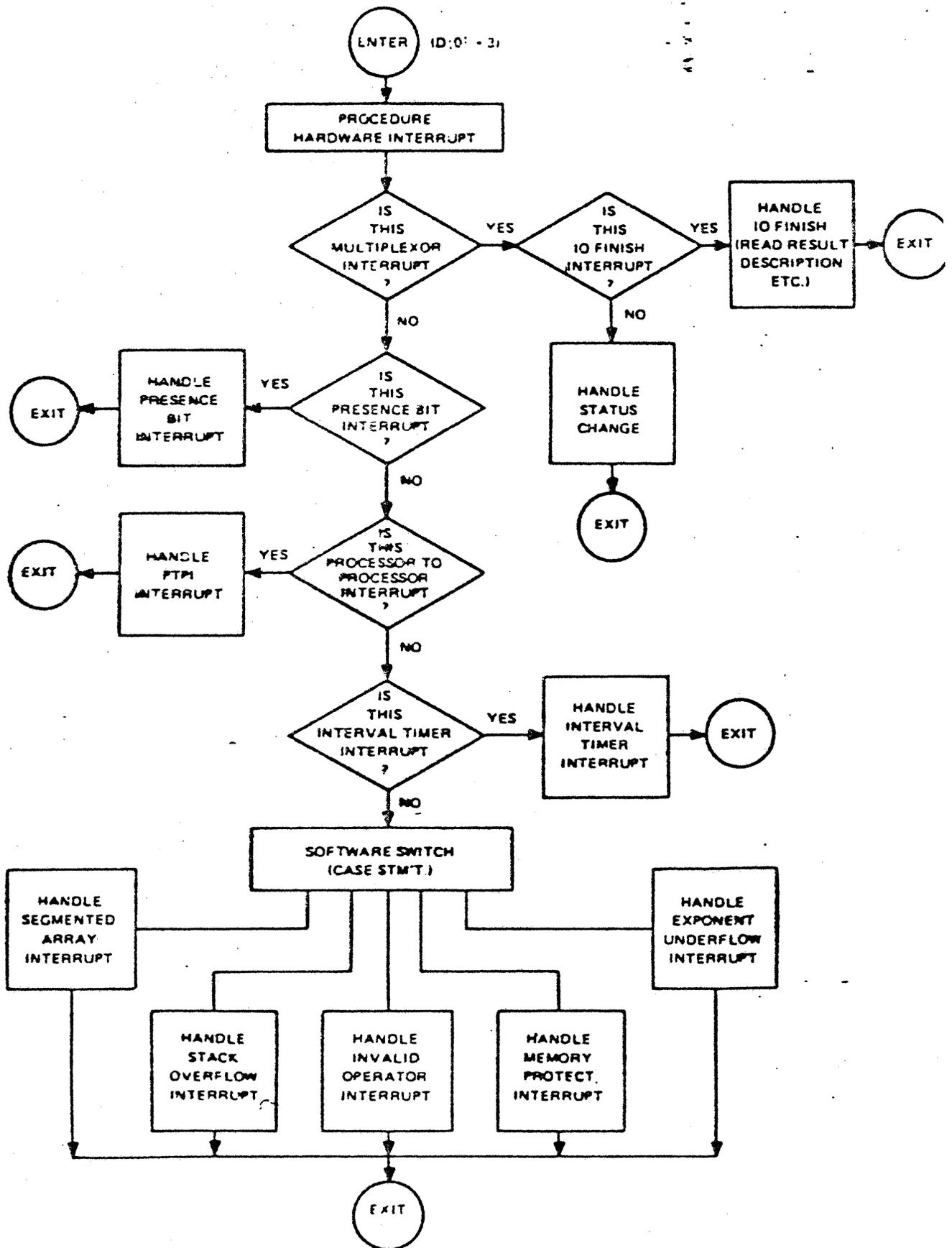


SOME TABLES USED BY MCP FOR RESOURCE ALLOCATION

EXTERNAL TYPE  
INTERRUPT  
I.E.  
(IOFINISHED)



# HARDWARE INTERRUPT FLOW DIAGRAM

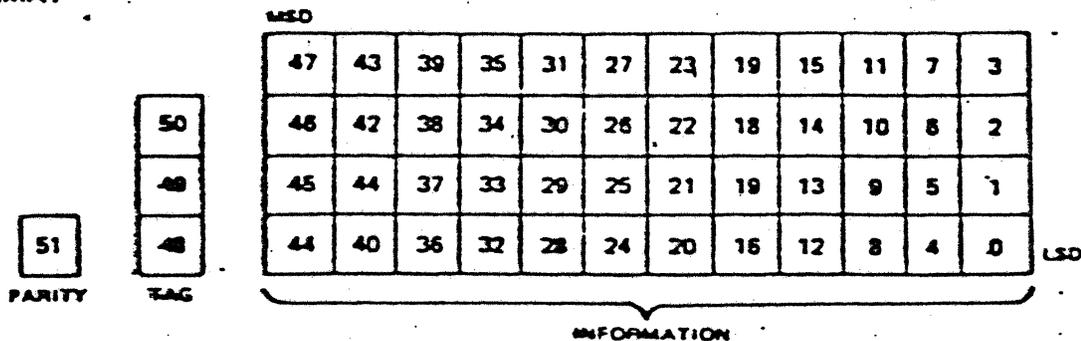


## PRIORITY

F Field	What
47:1	Invisible Independent Runner
46:2	Special Independent Runner (Autobackup, DCC=3, SWAPPER=2)
44:1	MCP code owns a soft lock
42:2	MCS or user special (MCS=2, CP=1)
39:1	Task being DSed
38:1	Interactive Swap task
37:1	WFL JOB
36:7	Declared Priority
29:6	Fine Priority
23:4	Stack Status
	Hex: F    Alive
	E    Ready
	D    Ready Swapped
	C    Asleep
	9    Holding
	8    Waiting
	7    Frozen
	6    To be continued
	5    Unemployed
	4    Selected
	3    Scheduled
	2    Dying
	1    Being set up
19:20	Link to next element in the READY Queue (MCP stack ends the chain of "ready words")

Fine priority ::= 63 \* (MYPROCESSTIME + INTEGER(.075 sec)) DIV  
 (MYPROCESSTIME + MYIOTIME + INTEGER(.150 sec))

### HEXADECIMAL FORMAT





## System Initialization

There are three distinct types of initialization available on the B 6800. They are the Cold Start, the Cool Start and the Halt/Load. Each has a special function.

The Cold Start is used when the system is first installed. It assumes that nothing is on the system. The Cold Start requires:

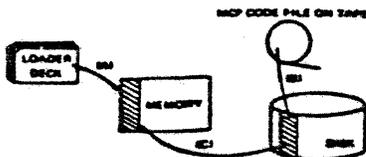
MCP object file on a library tape,  
System loader object deck.

By setting the "card load select" button on the console and then pressing the "load" button, the hardware will execute a hardwired series of bootstrap instructions to read a card into memory and then transfer control to the machine coded instructions. This loaded bootstrap program reads in an object deck (about 50 cards produced by the ESPOL compiler) and then transfers control to this program. This program, known as the utility loader (UTIL-LOADER), reads a parameter card containing a tape name and a file name. UTILLOADER then searches the system to find which units are tape units and reads the tape labels. On finding a matching file name, it then determines if it is a library tape and if so, searches the directory on the tape. Finding the designated file, it loads the program LOADER into memory and transfers control to the program. (UTILLOADER also can load other ESPOL programs used for testing and maintenance.)

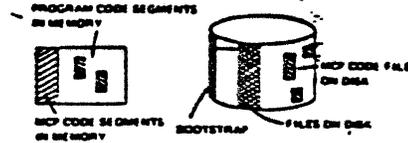
The LOADER program can perform many functions, the most important of which is loading the MCP from disk, tape, or disk pack. Its functions are specified by parameter cards.

After loading the MCP, the LOADER program initializes a disk directory and loads the primary initialized portion of the MCP into memory. Then it transfers control to this new program, i.e., the MCP. The MCP then begins its own initialization.

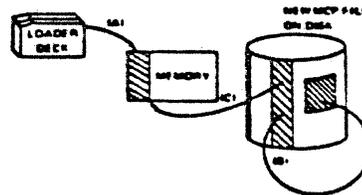
A Cold Start forces loss of any files on the disk family where the MCP is loaded.



The Cool Start operation on the B 6800 reloads the MCP code file on disk but leaves the remaining files intact. Bootstrap information, including the address of the MCP code file, is contained in the lowest address disk segment.



If the MCP code file becomes corrupted or overwritten, the same loader deck can reload the MCP from tape to disk, and if necessary, move the location of the code file on disk. A Cool Start can load a new MCP code file from disk by specifying the name of the file without disturbing the disk directory.



A Cool Start from disk can be done through the MCP by a CM (change MCP) message. In both cases, the MCP performs the initialization routine. No disk files are lost during this procedure; only MCP reinitialization of memory tables occurs. It is often used just to change MCPs.

The Halt/Load is the most common initialization, accomplished by:

- Pressing the HALT button on the console.
- Pressing the LOAD button, with the "card load select" button off.

The hardware reads the bootstrap segment at the lowest disk address, fetches the current MCP code file and the MCP reinitializes its tables. This process takes only seconds to perform. All of Main Memory is tested in the process, and any modules failing the test are deleted by the MCP from the configuration.

## Recovery Aspects

### HALT/LOAD RECOVERY

All tasks are aborted during a Halt/Load condition and memory is reinitialized. Jobs and job queues reside on disk and therefore are still present. By default the job is restarted at the last point where no task was running. Let us examine a job/task structure which is interrupted by a Halt/Load.



During normal operation the system uses the MCP code file on A. Code moves from A into

memory and the bootstrap for a Halt/Load points to A. If disk pack A fails, however, the bootstrap is altered to Halt/Load to disk pack B; the system automatically uses the MCP code file on B. Using the same principle, one can duplicate the directory and the access structure on the same families.