

TABLE 1A: JP1 JUMPER SETTING

JP1 Jumper Status	IDE/RAID Mode	Note
IN	RAID MODE	
OUT	IDE MODE	

TABLE 1B: JP2 JUMPER SETTING

JP2 Jumper Status	BA5 ENABLE/DISABLE	Note
IN	DISABLE	
OUT	ENABLE	

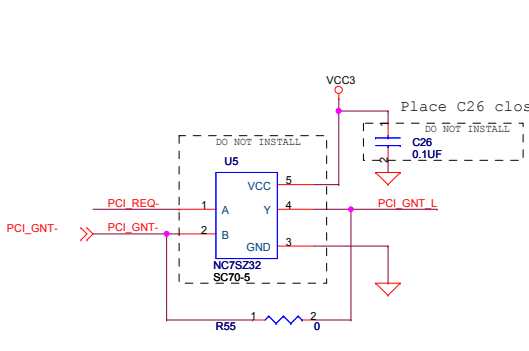
TABLE 2: DEVICES SUPPORTED IN LOCATION U4

MEMORY TYPE	FLASH MEMORY	FLASH MEMORY
FLASH MEMORY MODELS [5V]	AMD AM29F010B 1Mbit [128k x 8]	AMD AM29F040B 4Mbit [512k x 8]
NOTE	DEFAULT	

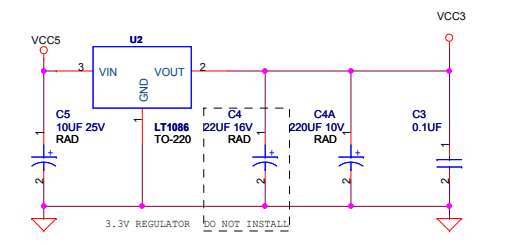
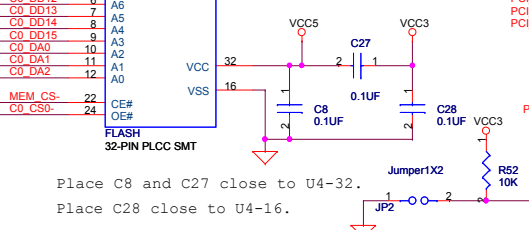
NOTES: Unless Otherwise Specified:

1. All components are subject to change.
2. All resistors and capacitors are in surface mount 0805 package.
3. All resistors are 5%, 1/8 watt.
4. All capacitors are 20%, 50 volt ceramic.
5. The following resistors are 1%, 1/10 watt: R4,R5,R6,R16 and R26.
6. The following capacitors are 5%, 50 Volt, NPO: C1,C2.
7. Circuit board layout and component selection are critical to the proper operation of the PLL. Refer to the PCI-680 Product Specification, PLL Chapter, for the circuit board layout and component selection requirements.
8. If the PCI bus provides 3.3 Volt power, and if it is desired to use this power, then locations R32 and R33 must be populated with zero ohm resistors, and U2 must not be populated. Only one power path (U2 or R32,R33) can be populated at a time.

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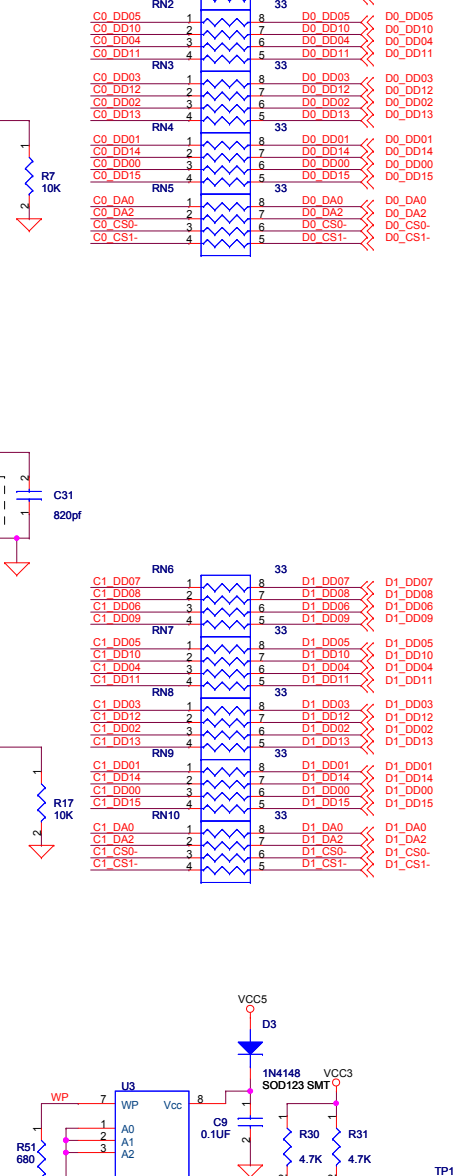
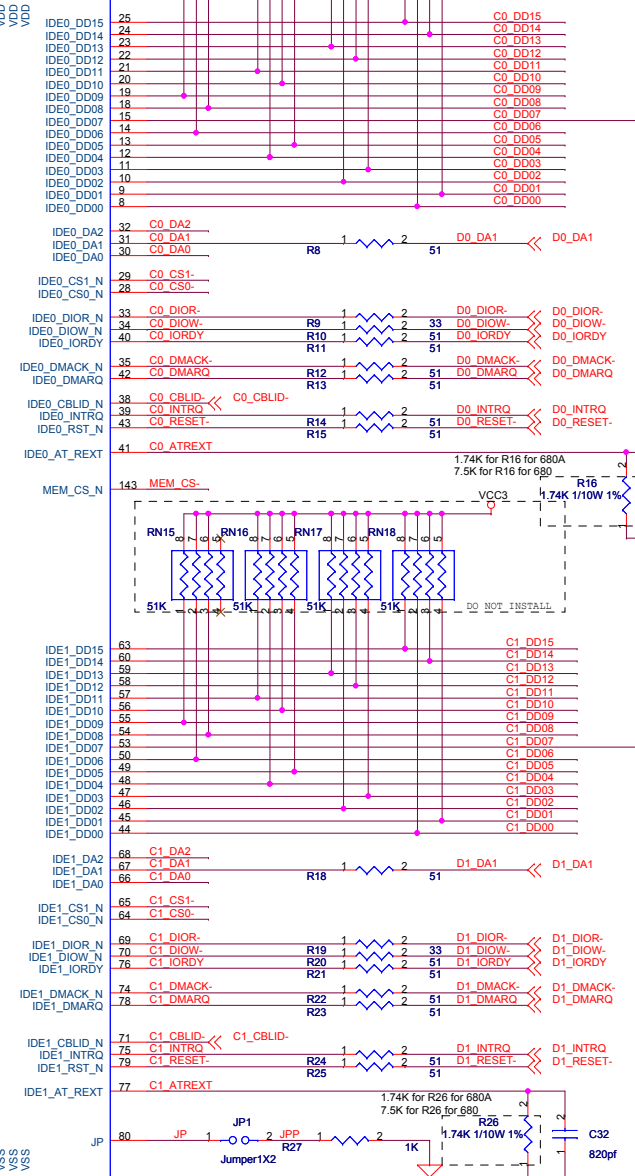


C0_DD00	1	A18/NC	07	21	C1_DD07
C0_CS1-	31	WE#	06	20	C1_DD06
C0_DD01	2	A17/NC	05	19	C1_DD05
C0_DD02	2	A16	04	18	C1_DD04
C0_DD03	3	A15	03	17	C1_DD03
C0_DD04	29	A14	02	16	C1_DD02
C0_DD05	28	A13	01	15	C1_DD01
C0_DD06	4	A12	00	14	C1_DD00
C0_DD07	25	A11			
C0_DD08	23	A10			
C0_DD09	26	A9			
C0_DD10	27	A8			
C0_DD11	3	A7			
C0_DD12	6	A6			
C0_DD13	7	A5			
C0_DD14	8	A4			
C0_DD15	9	A3			
C0_DA0	10	A1			
C0_DA1	11	A1			
C0_DA2	12	A0			
MEM_CS-	22	CE#			
C0_CS0-	24	OE#			

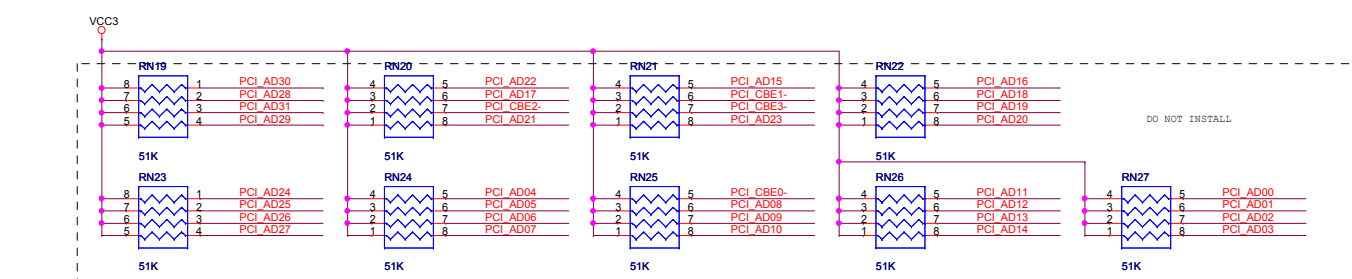
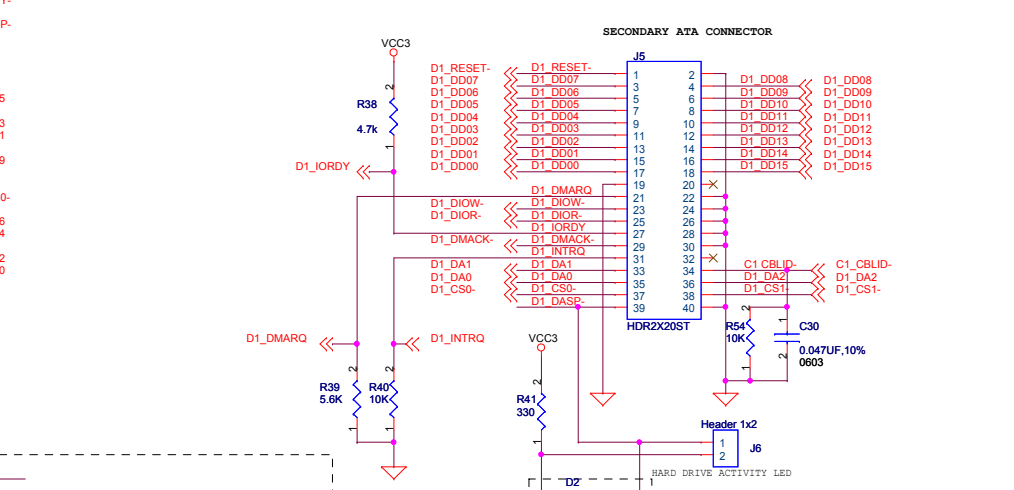
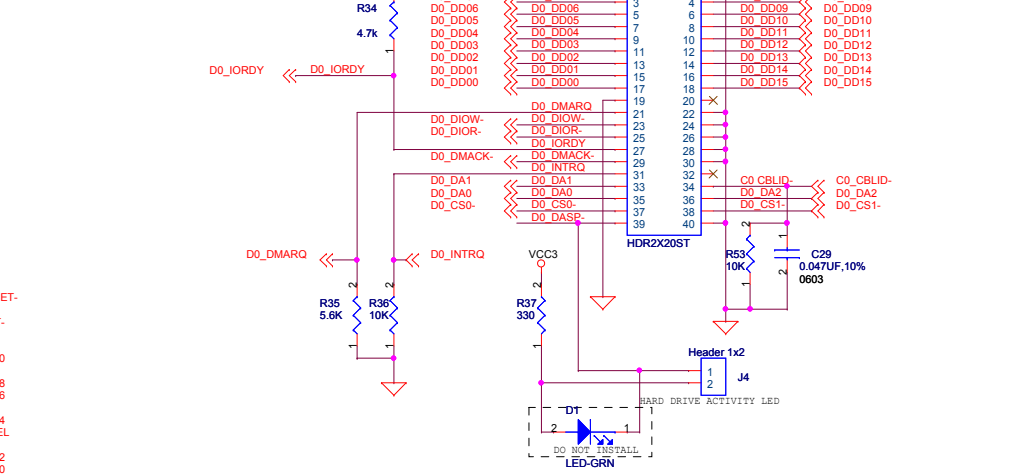
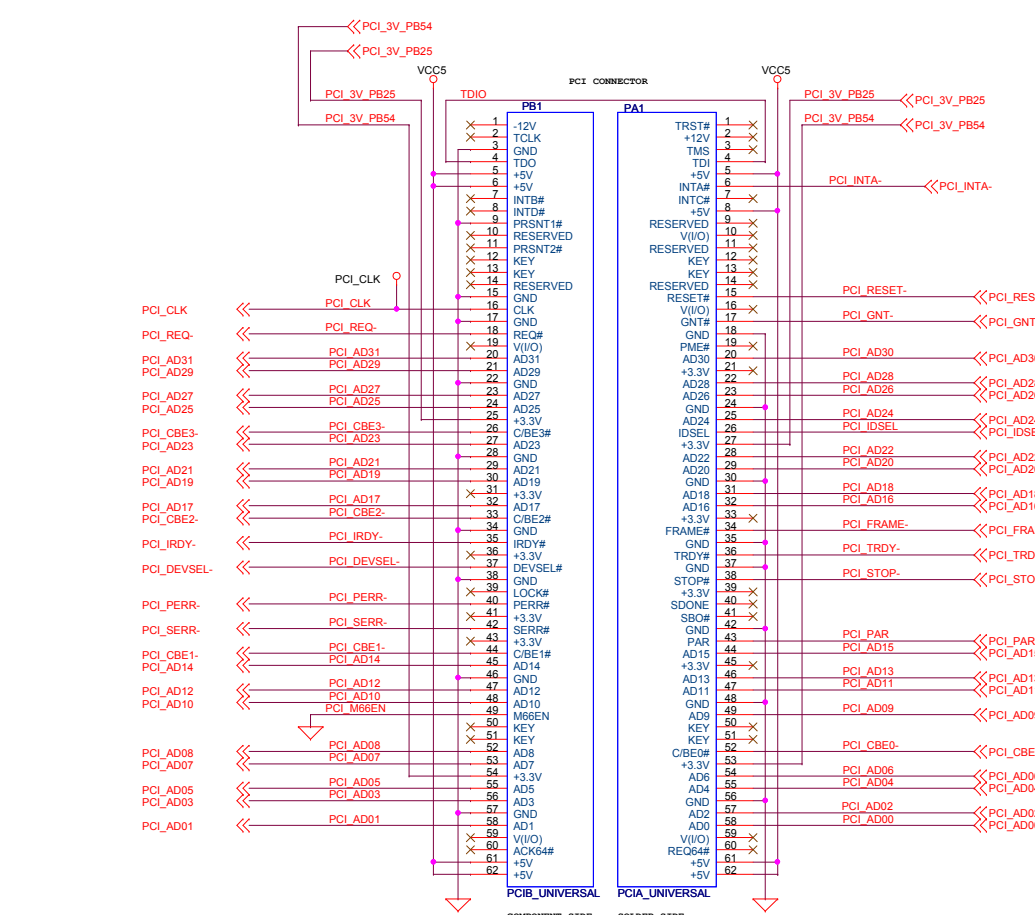
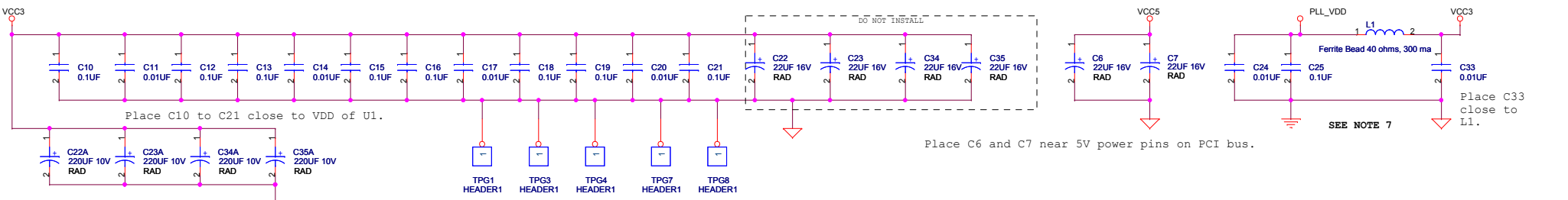


PCI_AD31	PCI AD31	81	PCI_AD31	PCI AD31	
PCI_AD30	PCI AD30	82	PCI_AD30	PCI AD30	
PCI_AD29	PCI AD29	83	PCI_AD29	PCI AD29	
PCI_AD28	PCI AD28	84	PCI_AD28	PCI AD28	
PCI_AD27	PCI AD27	85	PCI_AD27	PCI AD27	
PCI_AD26	PCI AD26	86	PCI_AD26	PCI AD26	
PCI_AD25	PCI AD25	87	PCI_AD25	PCI AD25	
PCI_AD24	PCI AD24	88	PCI_AD24	PCI AD24	
PCI_AD23	PCI AD23	89	PCI_AD23	PCI AD23	
PCI_AD22	PCI AD22	90	PCI_AD22	PCI AD22	
PCI_AD21	PCI AD21	91	PCI_AD21	PCI AD21	
PCI_AD20	PCI AD20	92	PCI_AD20	PCI AD20	
PCI_AD19	PCI AD19	93	PCI_AD19	PCI AD19	
PCI_AD18	PCI AD18	94	PCI_AD18	PCI AD18	
PCI_AD17	PCI AD17	95	PCI_AD17	PCI AD17	
PCI_AD16	PCI AD16	96	PCI_AD16	PCI AD16	
PCI_AD15	PCI AD15	97	PCI_AD15	PCI AD15	
PCI_AD14	PCI AD14	98	PCI_AD14	PCI AD14	
PCI_AD13	PCI AD13	99	PCI_AD13	PCI AD13	
PCI_AD12	PCI AD12	100	PCI_AD12	PCI AD12	
PCI_AD11	PCI AD11	101	PCI_AD11	PCI AD11	
PCI_AD10	PCI AD10	102	PCI_AD10	PCI AD10	
PCI_AD09	PCI AD09	103	PCI_AD09	PCI AD09	
PCI_AD08	PCI AD08	104	PCI_AD08	PCI AD08	
PCI_AD07	PCI AD07	105	PCI_AD07	PCI AD07	
PCI_AD06	PCI AD06	106	PCI_AD06	PCI AD06	
PCI_AD05	PCI AD05	107	PCI_AD05	PCI AD05	
PCI_AD04	PCI AD04	108	PCI_AD04	PCI AD04	
PCI_AD03	PCI AD03	109	PCI_AD03	PCI AD03	
PCI_AD02	PCI AD02	110	PCI_AD02	PCI AD02	
PCI_AD01	PCI AD01	111	PCI_AD01	PCI AD01	
PCI_AD00	PCI AD00	112	PCI_AD00	PCI AD00	
PCI_CBE3-	PCI CBE3-	91	PCI_CBE3-	PCI CBE3-	
PCI_CBE2-	PCI CBE2-	103	PCI_CBE2-	PCI CBE2-	
PCI_CBE1-	PCI CBE1-	114	PCI_CBE1-	PCI CBE1-	
PCI_CBE0-	PCI CBE0-	126	PCI_CBE0-	PCI CBE0-	
PCI_PAR	PCI PAR	113	PCI_PAR	PCI PAR	
PCI_PERR-	PCI PERR-	111	PCI_PERR-	PCI PERR-	
PCI_SERR-	PCI SERR-	112	PCI_SERR-	PCI SERR-	
PCI_FRAME-	PCI FRAME-	104	PCI_FRAME-	PCI FRAME-	
PCI_TRDY-	PCI TRDY-	106	PCI_TRDY-	PCI TRDY-	
PCI_IRDY-	PCI IRDY-	105	PCI_IRDY-	PCI IRDY-	
PCI_STOP-	PCI STOP-	110	PCI_STOP-	PCI STOP-	
PCI_DEVSEL-	PCI DEVSEL-	107	PCI_DEVSEL-	PCI DEVSEL-	
PCI_IDSEL	PCI IDSEL	92	PCI_IDSEL	PCI IDSEL	
PCI_REQ-	PCI REQ-	136	PCI_REQ-	PCI REQ-	
PCI_GNT_L	PCI GNT L	137	PCI_GNT_L	PCI GNT L	
PCI_INTA-	PCI INTA-	138	PCI_INTA-	PCI INTA-	
PCI_RESET-	PCI RESET-	141	PCI_RESET-	PCI RESET-	
PCI_BAS_EN	PCI BAS EN	139	PCI_BAS_EN	PCI BAS EN	
PCI_CLK	PCI CLK	140	PCI_CLK	PCI CLK	
PLL_VDD	PLL VDD	2	PLL_VDD	PLL VDD	
PLL_CPBIAS	PLL CPBIAS	3	PLL_CPBIAS	PLL CPBIAS	
PLL_VCOBIAS	PLL VCOBIAS	4	PLL_VCOBIAS	PLL VCOBIAS	
PLL_LOOPFLT	PLL LOOPFLT	5	PLL_LOOPFLT	PLL LOOPFLT	
PLL_GND	PLL GND	6	PLL_GND	PLL GND	
TEST_MODE	TEST MODE	7	TEST_MODE	TEST MODE	
SCAN_EN	SCAN EN	142	SCAN_EN	SCAN EN	

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