

## SCSI Applications (monolithic ICs)

Device	Type	Package		Description	Features
		Number of pins and configuration	Drawing number		
LC8945	QIP PLCC <sup>v</sup>	48A 68	3052A 3132	SCSI (Small Computer System Interface) controller	5 Mbits/s maximum asynchronous transfer rate, unbalanced cable, 20 MHz clock

## CD-ROM/CD-I Applications (monolithic ICs)

Device	Type	Package		Description	Features
		Number of pins and configuration	Drawing number		
LC8951	QIP	80A	3044B	CD-ROM/CD-I decoder	4.5 to 5.5 V operation, 2.3 Mbytes/s maximum data transfer rate, real-time error correction, CD-ROM (mode 1) and CD-I (mode 2: form1 and form2)
LC8953	QIP	160E	3153	CD-ROM/CD-I, 68000 MPU peripheral interface	Address decoder, interrupt control, serial interface, on-chip DMA circuit
LC8955	QIP	80A	3044B	CD-I format level A, B, C stereo/mono playback	On-chip DAC interface circuit, ideal for use with an LC8951
LC8956*	SQFP QIP	100E 100E	3181 3151	CD-ROM XA interface	Single-chip ADPCM (LC8955) and (LC89510)
LC89510*	QIP	80E	3174	CD-ROM/CD-I high-performance error correction LSI	On-chip 12-byte FIFO and 8 Kbit SRAM
LC89515*	QIP	80E	3174	CD-ROM/CD-I error correction LSI	On-chip 12-byte FIFO
LC89560*	SQFP QIP	100 100E	3181 3151	CD-ROM XA interface	LC8956 subcode interface
LC89581 <sup>s</sup>	QIP	80A	3044B	CD-ROM encoder	CD-ROM/CD-I error detection and error correction (EDC and ECC)
LC89582 <sup>s</sup>	QIP	48E	3156	ATIP data demodulation and error checking	Automatic transfer of ATIP data to an LC89583 CD encoder
LC89583*	QIP	100E	3151	MO and WO CD encoder	CD-formatted EFM serial data real-time conversion, on-chip ATIP synchronizer, automatic ATIP data input circuit

## Modem (monolithic ICs)

Device	Type	Package		Description	Features
		Number of pins and configuration	Drawing number		
LC8920 <sup>v</sup>	QIP	100E	3151	9600 bps modem for facsimile machines	G2, G3 and CCITT V.29, V.27ter, T.30, T.3, T.4 standards supported

## Facsimile Machine Applications (monolithic ICs)

Device	Type	Package		Description	Features
		Number of pins and configuration	Drawing number		
LC8210 <sup>v</sup>	QIP	64E	3150	Facsimile machine, digital copier and scanner signal processor	8-bit A/D converter (5 MHz maximum), black-and-white level correction, $\gamma$ correction
LC8211 <sup>v</sup>	QIP	100E	3151	Facsimile machine, digital copier and scanner IF processor	Imageable area division, dither processing, error diffusion processor, binary processing
LC8213 <sup>v</sup>	QIP	80E	3174	G3/G4 facsimile machine image data compression/expansion processor	MH, MR and MMR supported, high-speed coding and decoding, built-in DMAC, line skip coding
LC8220 <sup>v</sup>	QIP	160E	3153	Color stills image data compression/expansion processor	JPEG baseline standard supported, multi-input format, RGB/YUV switchable, marker code processing
LC8935	QIP	80E	3174	Facsimile machine transmit/receive signal processor	Bit clock playback, frame synchronization and synchronization protection, descrambling and de-interleaving, BCH error correction

## FDD/HDD Applications (monolithic ICs)

Device	Type	Package		Description	Features
		Number of pins and configuration	Drawing number		
LA6511	SIP	10H	3024A	Single-bridge BTL driver	2.0 A maximum output current, mute function, thermal protection circuit
LB1656	DIP	16F	3054A	2-phase stepper motor driver for FDDs	12 V/5 V output power transistor, power save circuit, thermal protection circuit
LB1656M	MFP	16FS	3097	2-phase stepper motor driver for FDDs	Miniflat package version of the LB1656
LB1657M	MFP	16FS	3097	2-phase stepper motor driver for FDDs	12 V/5 V output power transistor, power save circuit, thermal protection circuit

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