

PROGRAMMABLE DELAY LINES

SERIES TT3 3 BIT TTL



- Total delay times from 14ns to 287ns
- Incremental delays of 1ns to 40ns
- 7ns zero step delay
- 16-pin DIP package



ELECTRICAL CHARACTERISTICS

I _{IH} Logic "1" Input Current	:50uA max
I _{IL} Logic "0" Input Current	:-2mA max
V _{OH} Logic "1" Output Voltage	:2.4V min
V _{OL} Logic "0" Output Voltage	:0.4V max
V _{IH} Logic "1" Input Voltage	:2.0V min
V _{IL} Logic "0" Input Voltage	:0.8V max
T _A Operating Temperature	:0°C to 70°C
N _H Fanout "1" Output	:20 TTL Load
N _L Fanout "0" Output	:10 TTL Load
Supply Current	:85mA Typ.

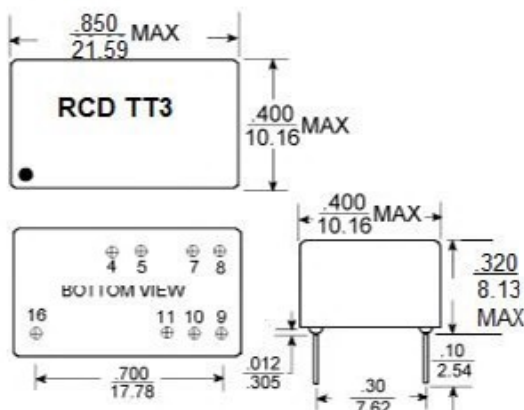
INPUT PULSE TEST CONDITION

Pulse Voltage	:3V
Pulse Width	:3Td
Duty Cycle	:1000ns
Pulse Rise Time	:2ns(0.75 to 2.4V)
Time Delay Measured	:@1.5V level
Supply Voltage V _{cc}	:5.0±0.25Vdc

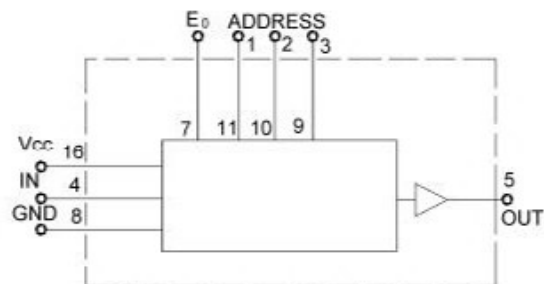
ELECTRICAL SPECIFICATIONS

Program Pins	0	1	2	3	4	5	6	7	Tol. Of Prog. Delay	Delay Time Per. Step	Delay Time Step Tol.	Maximum Delay Nominal Ref.IN
Part numbers	1	0	1	0	1	0	1	0	1	1	1	1
TT3-014	7±1ns	1	2	3	4	5	6	7	±0.5ns	1ns	±0.3ns	14ns
TT3-021	7±1ns	2	4	6	8	10	12	14	±0.6ns	2ns	±0.4ns	21ns
TT3-028	7±1ns	3	6	9	12	15	18	21	±0.7ns	3ns	±0.5ns	28ns
TT3-035	7±1ns	4	8	12	16	20	24	28	±0.9ns	4ns	±0.5ns	35ns
TT3-042	7±1ns	5	10	15	20	25	30	35	±1ns	5ns	±0.5ns	42ns
TT3-049	7±1ns	6	12	18	24	30	36	42	±1.2ns	6ns	±0.6ns	49ns
TT3-056	7±1ns	7	14	21	28	35	42	49	±1.4 ns	7ns	±0.7ns	56ns
TT3-063	7±1ns	8	16	24	32	40	48	56	±1.6 ns	8ns	±0.8ns	63ns
TT3-070	7±1ns	9	18	27	36	45	54	63	±1.8 ns	9ns	±0.9ns	70ns
TT3-077	7±1ns	10	20	30	40	50	60	70	±2.0 ns	10ns	±1ns	77ns
TT3-091	7±1ns	12	24	36	48	60	72	84	±2.0 ns	12ns	±1.2ns	91ns
TT3-112	7±1ns	15	30	45	60	75	90	105	±4.0 ns	15ns	±1.5ns	112ns
TT3-147	7±1ns	20	40	60	80	100	120	140	±5.0 ns	20ns	±1.5ns	147ns
TT3-287	7±1ns	40	80	120	160	200	240	280	±6.0 ns	40ns	±2ns	287ns

DIMENSIONS



PIN CONNECTIONS



Dimensions: inches/mm. Unless otherwise specified, all tolerances are ±.010/±0.25