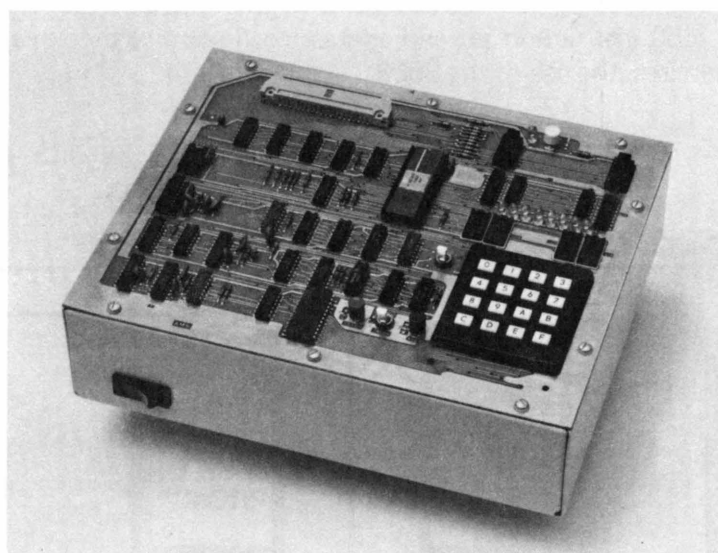# Intersil

TED Module

Preliminary Description
Intersil TED Module for 2650 Users

(TRAINING, EVALUATION, & DEVELOPMENT MODULE)
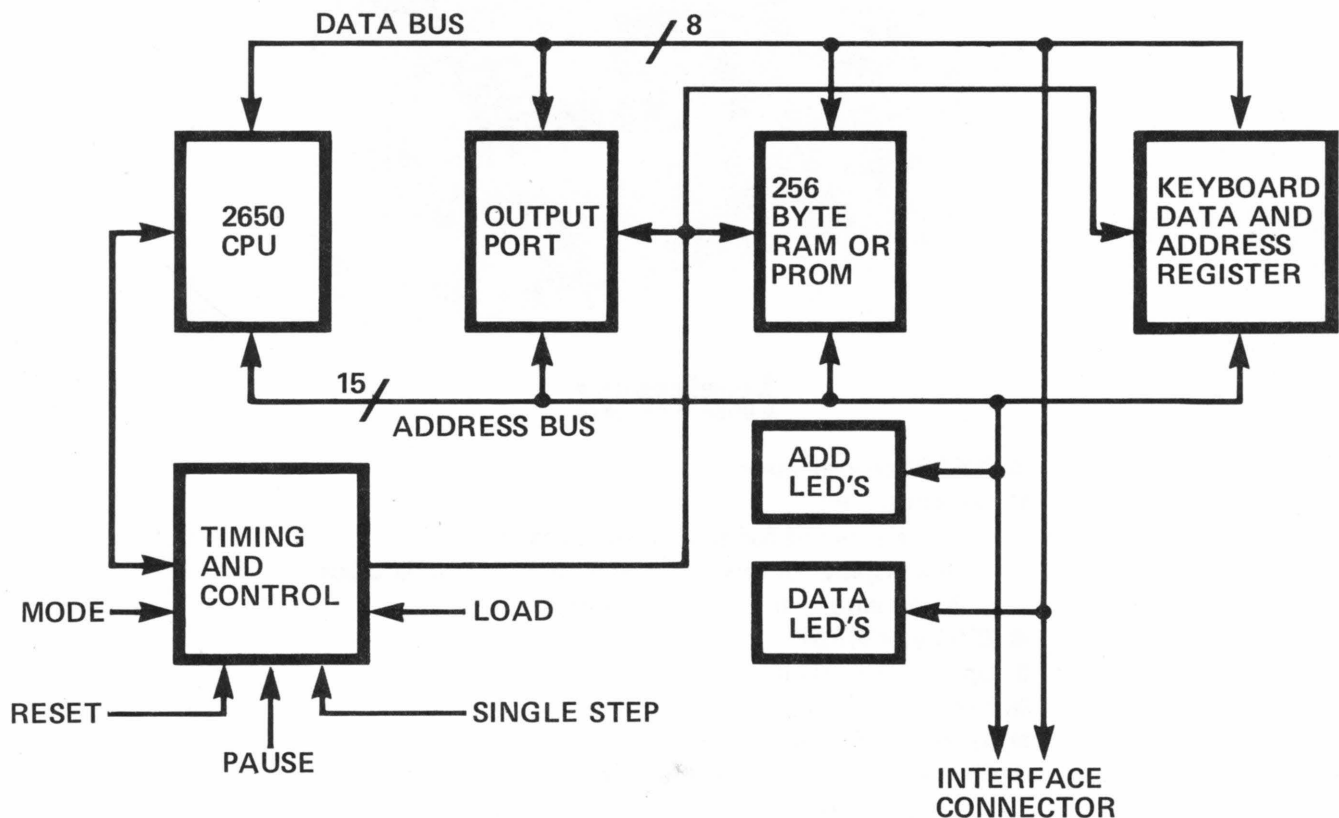

Features

- ■ **2650 Microprocessor**
- ■ **Self-contained**
    Hex Keyboard for address/data entry
    Hex display for low order address byte and data bus.
    Power supply for 117 VAC operation
- ■ **256 bytes RAM**
- ■ **Optional PROM in place of RAM**
- ■ **8 bit output port**
- ■ **Sense and Flag indicators**
- ■ **Connector for external interface**
    Address bus
    Data bus
    Control bus
    5 VDC @ .5 A
- ■ **Crystal Oscillator optional**
- ■ **Single Step, Pause or Run Modes**
- ■ **Self teaching Guide**

Description

The TED module provides a low cost, self contained system to permit the user to become familiar with the 2650 by actually working and running simple programs as taught in the accompanying self-teaching guide. Then, as real program development requirements occur, additional RAM and/or ROM cards may be interfaced, along with peripherals such as teletype and CRT displays.

A block diagram of the TED module (figure 1) shows the straight forward bus oriented organization. The simplicity of the system, due in part to the static nature of the 2650 CPU chip, is shown in the photograph.

The TED self teaching guide, included with the unit, provides a thorough introduction to the 2650 instruction set and addressing flexibility by explaining, complete with student exercises, the use of the 2650.

(FIGURE 1)

T.E.D. BLOCK DIAGRAM